

A 4–18-GHz Reconfigurable RF MEMS Matching Network For Power Amplifier Applications

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ABSTRACT: We have developed a novel reconfigurable matching network based on the loaded-line technique. The network is composed of N -switched capacitors ($N = 4-8$) with a capacitance ratio of 4–5:1 and is suitable for power amplifiers at 4–18 GHz, or as an impedance tuner for noise parameter and load-pull measurements at 10–28 GHz. The networks are very small, and offer better performance than double or triple stub matching networks. Extensive loss analysis indicates that the 8-element network has a loss of 0.5 dB at 4–12 GHz, and less than 1.5 dB at 18 GHz, even when matching a 10Ω output impedance to a 50Ω load. As expected, the 4-element matching network has about half the loss of the 8-element network, but with much less impedance coverage. Both networks were simulated and measured in high VSWR conditions and can handle at least 500 mW of RF power at 4–18 GHz. The application areas are in phased array antennas, reconfigurable power amplifiers, and wideband noise-parameter and load-pull measurement systems. © 2004 Wiley Periodicals, Inc. *Int J RF and Microwave CAE* 14: 356–372, 2004.

Keywords: RF MEMS; reconfigurable networks; impedance tuner; matching network; loaded-line; phased arrays; load-pull; noise parameters

I. INTRODUCTION

Power amplifiers (PAs) with 1–2-W output power and a 5-V power supply have an output impedance of 10Ω – 20Ω , and are mismatched in 50Ω systems. Therefore, it is imperative that a low-loss impedance matching network be used to ensure optimum power transfer between the PA and the antenna (Fig. 1). The matching network should be flexible enough to accommodate a changing load impedance or a changing PA output impedance. This is a common problem, for example, in multiband wireless networks where the antennas are placed close to walls or obstacles. An-

other application is in phased arrays where the active input antenna impedance changes with scanning angles and operating frequency. Also, the PA output impedance can change versus frequency due to gain degradation over extended operation times.

A standard narrowband matching network is the CLC network with variable capacitors [1]. Such a network can match a wide range of impedances, but suffers from a narrowband response (single or dual-frequency bands). For wider frequency coverage, one can use reconfigurable networks based on the double-stub or triple-stub tuners [2, 3]. These networks offer excellent performance, but are quite large and employ a large number of MEMS switches (or varactors). Also, depending on the design, they can have a substantial loss component (0.5–3-dB loss), thus resulting in a reduced Smith chart coverage.

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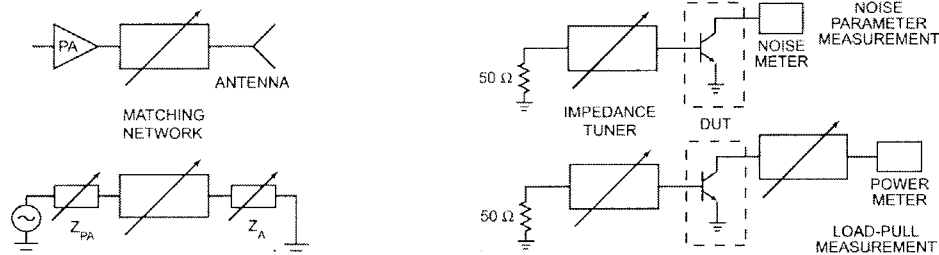


Figure 1. A reconfigurable matching network used between a power amplifier and a radiating antenna, or as an impedance tuner in noise or load-pull measurements.

In this work, we present a novel matching network capable of very wideband PA matching (4–18 GHz), and with low loss. The network is based on a loaded transmission-line with N switched capacitors. The article analyses in detail the loss of matching networks in non-50 Ω systems, taking in to consideration that they are measured solely using 50 Ω S -parameter setups. This work also presents simulations and measurements of the power handling and intermodulation distortion of the novel matching network. The reconfigurable network can also be used in noise and load-pull measurement setups. In this case, the network loss is less of a concern, and the important characteristic is that a large range of impedance be synthesized at the transistor input. This article shows that the proposed network can work over a 3:1 frequency range for these applications.

II. DESIGN AND FABRICATION

A. Design of the Reconfigurable Loaded-Line

It is well known that the phase velocity and impedance of a transmission line can be changed by capacitive loading. This fact has been used in distributed transmission-line (DMTL) MEMS phase shifters [1, 4, 5] and in tunable filters [6]. The loaded-line idea can also be applied to reconfigurable matching networks: a transmission-line is loaded with N -switched MEMS capacitors, each having a capacitance C_U (up-state position) and C_D (down-state position), and the line impedance is chosen to be approximately 50 Ω when the MEMS capacitors are in the up-state position (Fig. 2). The capacitance ratio is chosen anywhere from 3:1 to 8:1, depending upon the design. When a MEMS capacitor is actuated to the down-state position, the loading on the t-line increases, thus resulting in a localized low impedance and a high effective dielectric constant (around the region of the MEMS capacitor). Since the loading is increasingly

capacitive with more MEMS switches being actuated, the network will transform a 50 Ω –60 Ω load into a low-input impedance (or vice versa, due to reciprocity), and the higher the capacitive loading, the lower the input impedance. A 4- to 8-element design results in 16 to 256 different impedance points on the Smith chart, and the distributed nature of the design ensures wideband-frequency coverage. Also, at high frequency and due to the varying electrical length of the loaded t-line, when the switched capacitors are actuated, the impedance coverage covers the entire Smith chart and not only the low impedance region.

Many different parameters can be optimized in the loaded-line network. In general, a larger number of switched capacitors yield more wideband operation and better Smith chart coverage. However, this also results in increased component size and loss, thus the smallest N must be chosen to meet the specific application. The highest frequency of operation is limited by the Bragg frequency (see below) of the loaded line, and the lowest impedance which can be matched to 50 Ω is dependent on the loading capacitance (see section III). A closed-form design technique has not been found, because the location, electrical length, and loaded-line impedance change with the actuation of the MEMS switched capacitors; therefore, Agilent ADS [7] is used extensively for choosing C_U , C_D , s , and the number of capacitors. The design is amazingly robust, and a capacitance or a separation change of $\pm 30\%$ will not affect the performance much, especially if $N = 6$ –8.

B. Basic Analysis and Model Fitting

The impedance (Z_L) and effective dielectric constant ($\epsilon_{\text{reff-L}}$) of a capacitively loaded t-line is calculated using [8]:

$$K = 1 + \frac{cZ_0C}{s\sqrt{\epsilon_{\text{reff}}}}$$

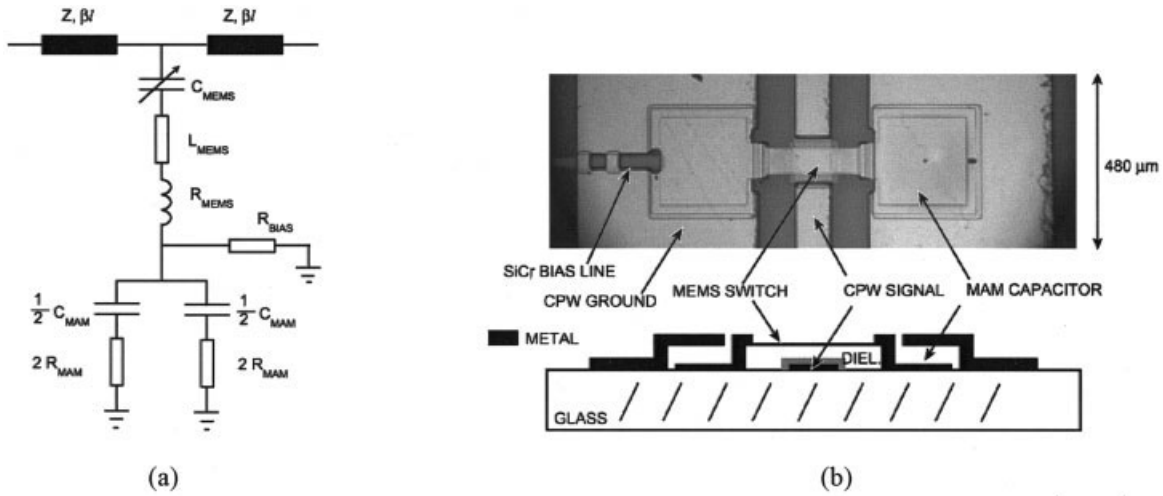


Figure 2. (a) Equivalent circuit, and (b) picture and cross-sectional view of a switched MEMS capacitor. The CPW signal line is DC grounded. This represents one cell in the reconfigurable loaded-line matching network.

$$\begin{aligned} \epsilon_{\text{reff}_L} &= \epsilon_{\text{reff}} K, \\ Z_L = Z_U, Z_D &= Z_0 \sqrt{K}, \end{aligned} \quad (1)$$

where c is the speed of the light in free space, C is the capacitance of the switched capacitor (C_U, C_D), ϵ_{reff} is the effective dielectric constant of the unloaded line, and s is the period of the loading structure ($s = 480 \mu\text{m}$). In this design (Fig. 3), the loaded line characteristics are $Z_U = 45 \Omega$ and $\epsilon_{\text{reff}_U} = 10$, and $Z_D = 23 \Omega$ and $\epsilon_{\text{reff}_D} = 38.2$, when the MEMS switches are in the up-state and down-state positions, respectively. The impedance in the up-state was designed to be 50Ω but is lower since the MEMS bridges are curved down resulting in a larger loading capacitance.

The Bragg frequency is calculated using [4]:

$$f_B = \frac{cZ_L}{\pi s Z_0 \sqrt{\epsilon_{\text{reff}}}} \quad (2)$$

and is 62.8 GHz and 32.2 GHz in the up-state and down-state positions, respectively. In general, it is good to have a relatively high Bragg frequency in order to obtain good operation over a wide-frequency range. The measured and simulated S -parameters for

the 8-element matching network are shown in Figure 4, and Agilent ADS is used for fitting the values of the switched MEMS capacitor (see Fig. 2(a) and Table 1).

C. Fabrication

The MEMS switched capacitor is fabricated using a series combination of a capacitive RF MEMS switch (with a capacitance ratio of 10–30:1) and fixed metal-insulator-metal (MIM) or metal-air-metal (MAM) capacitors. The down-state capacitance is mostly determined by the MIM (or MAM) capacitor (300–900 fF for X-band operation), while the up-state capacitance is set by the MEMS switch (70–80 fF). In this work, MAM capacitors are used for high- Q performance. Figure 2 shows a fabricated MEMS switched capacitor, and is based on a coplanar waveguide (CPW) transmission line with dimensions 100/100/100 μm on a 500 μm glass substrate (see Table 1). The dimensions of the MEMS switch are 280 $\mu\text{m} \times 80 \mu\text{m}$ and the area of each MAM capacitor is 240 $\mu\text{m} \times 235 \mu\text{m}$. The MEMS bridge is fabricated using a 0.8 μm thick gold membrane suspended 1.1 μm above the t-line, and a 3500 Å silicon-nitride dielectric inter-layer. The fabrication process is based on standard

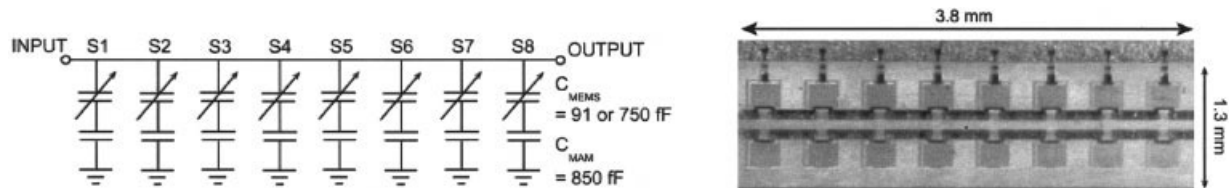


Figure 3. Circuit diagram and picture of the eight-element reconfigurable matching network.

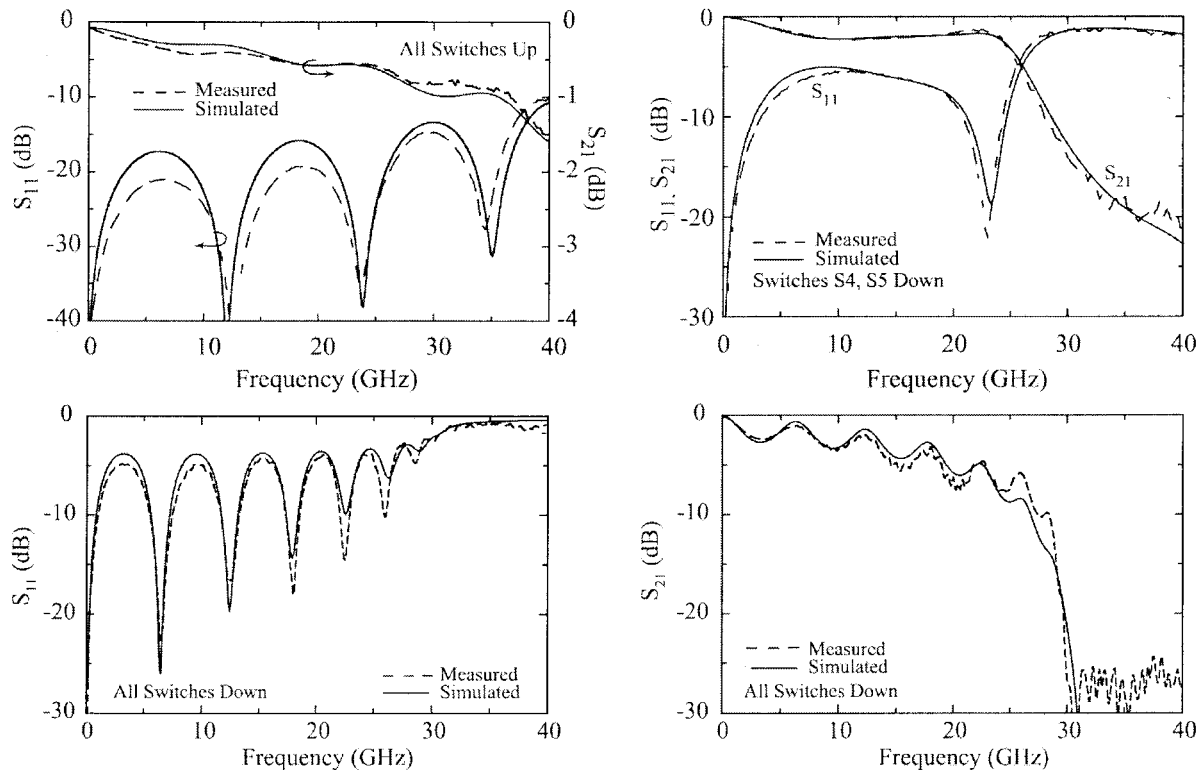


Figure 4. Measured and simulated S -parameters for the 8-switched capacitor reconfigurable MEMS network with different switch combinations.

techniques and is described in [9]. Polymethylmethacrylate (PMMA) is used as a sacrificial layer under the switches and MAM capacitors, and is patterned using oxygen plasma in a reactive ion etcher (RIE). The circuit is electroplated to 3 μm -thick (except the MEMS bridges) to reduce the t-line and MAM capacitor loss. SiCr bias lines with resistance of 700 Ω /

TABLE I. Measured T-Line Values From the TRL Calibration (Top), Fitted Values for the Switched MEMS Capacitor (Middle), and Loaded-Line Characteristics (Bottom)

ϵ_r	4.6
Z_0 (Ω)	86.2
ϵ_{reff}	2.72
α (dB/cm), 10/20 GHz	0.35/0.52
C_{MEMS} : C_{UP} (fF)	91
C_{MEMS} : C_{DOWN} (fF)	750
R_{BIAS} (k Ω)	>3
L_{MEMS} (pH)	9.5
$R_{\text{MEMS}} + R_{\text{MAM}}$ (Ω)	0.6
C_{MAM} (fF)	850
s (μm)	480
Z_U (Ω), ϵ_{reff_U}	45, 10
Z_D (Ω), ϵ_{reff_D}	23, 38

square are used for actuating the MEMS switch. The measured pull-down voltage was 15–17 V, and an actuation voltage of 35 V was used for obtaining an excellent metal-to-dielectric coverage in the down-state position. The estimated spring constant is $k = 40\text{--}50$ N/m from the standard pull-down equation [10]. The estimated residual stress is $\sigma = 50\text{--}60$ MPa using the formulas in [10].

The measured capacitance ratio of the RF MEMS switch is only 8:1 due to its low height and thick silicon-nitride layer (see Table 1), and the fixed MAM capacitor is 850 fF. This results in $C_U = 81.2$ fF, $C_D = 398$ fF and a capacitance ratio 4.9:1. The quality factor of the MEMS switched capacitor is calculated using $Q = (2\pi f C (R_{\text{MEMS}} + R_{\text{MAM}}))^{-1}$ and at 12 GHz results in $Q_U = 272$ ($C_U = 81.2$ fF, $X = -j163\Omega$) and $Q_D = 56$ ($C_D = 398$ fF, $X = -j33.3\Omega$) in the up-state and down-state positions, respectively.

III. IMPEDANCE COVERAGE

A. Simulations and Measurements

The measured and simulated input impedances of the 8-element matching network are presented in Figure 5

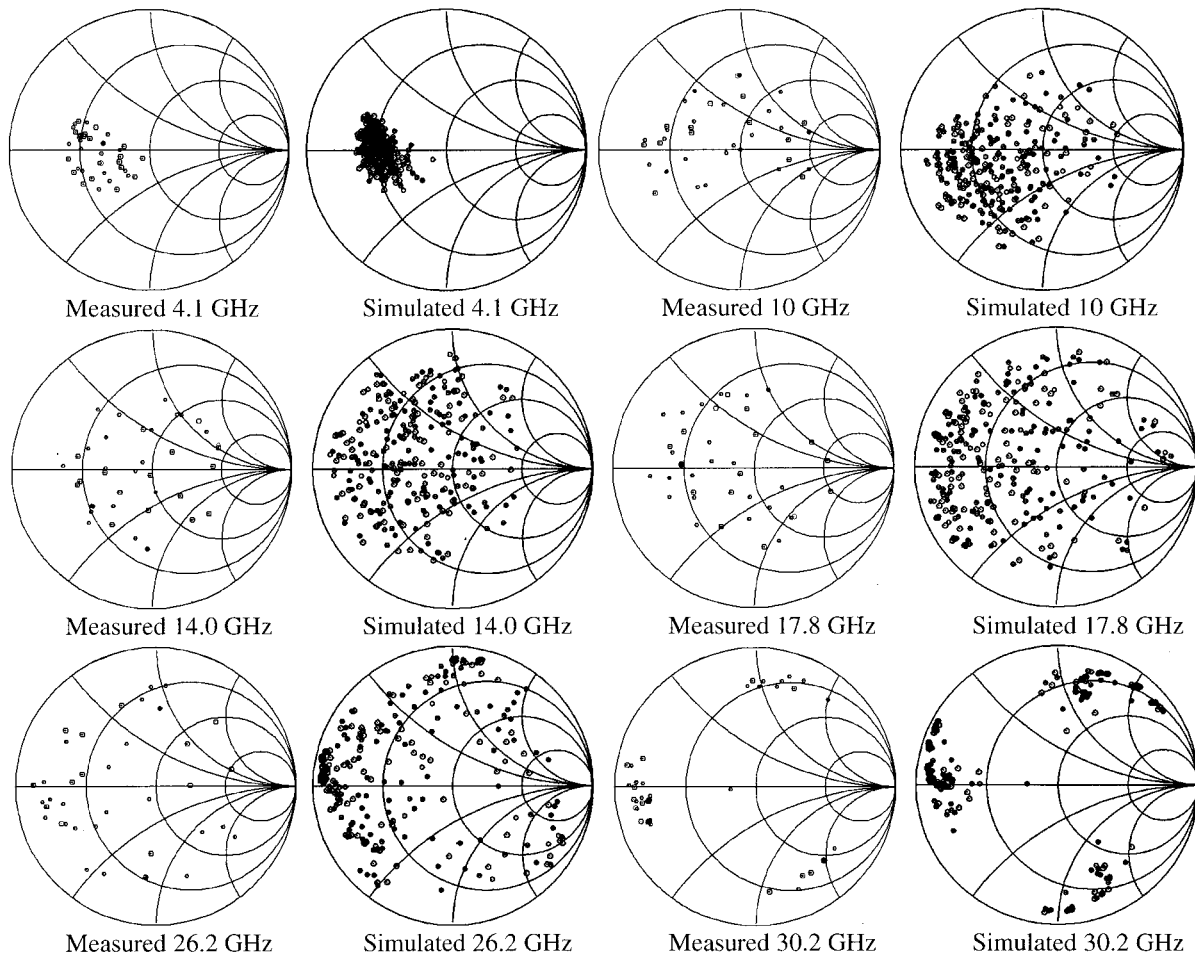


Figure 5. Measured (35) and simulated (256) impedances of the 8-element reconfigurable MEMS matching network. The single-cell values are given in Table I.

at 4–30 GHz for a 50Ω load. The measurements were done on 35 switch settings out of 256 possible combinations, and the results match the simulated results very well. Notice that at 4–8 GHz, the electrical length of the loaded t-line is quite short and the mapped input impedance lies in the low-impedance region of the Smith chart. However, above 8 GHz, the loaded-line is long enough to transform a 50Ω load into a wide coverage of the Smith chart. Still, the loaded-line design is better suited for low-impedance matching as is evident from a study of the impedance loci at 4–30 GHz (as compared with the coverage at the 20Ω and 200Ω areas).

B. Discussion of Impedance Coverage

Effect of the MEMS Bridge Height. The height of the MEMS bridge is usually the most sensitive part in the fabrication process since the value of the fixed MIM or MAM capacitor is achieved with accuracy.

Therefore, the impedance coverage of the 8-element matching network is simulated with a fixed MAM capacitor of 850 fF and a varying up-state MEMS capacitance. It is clear that the impedance coverage of the 8-element network does not change significantly if the MEMS up-state capacitance (and capacitance ratio) is changed from 34 fF to 170 fF (Fig. 6).

Effect of Loading Capacitance. The total down-state capacitance controls the lowest input impedance that can be achieved with a 50Ω load. Figure 7(a) presents the simulations with $C_D = 200$ fF ($C_{D-MEMS} = 750$ fF, $C_{MAM} = 275$ fF), $C_D = 400$ fF ($C_{D-MEMS} = 750$ fF, $C_{MAM} = 850$ fF) and $C_D = 900$ fF ($C_{D-MEMS} = 1300$ fF, $C_{MAM} = 2900$ fF). The other switch and line parameters are the same as Table 1. For $C_D = 900$ fF and $s = 480$ μm , the minimum input impedance is plotted up to 20 GHz due to Bragg frequency limitations, since above this frequency, the circuit results in very poor coverage of the Smith chart. On the other

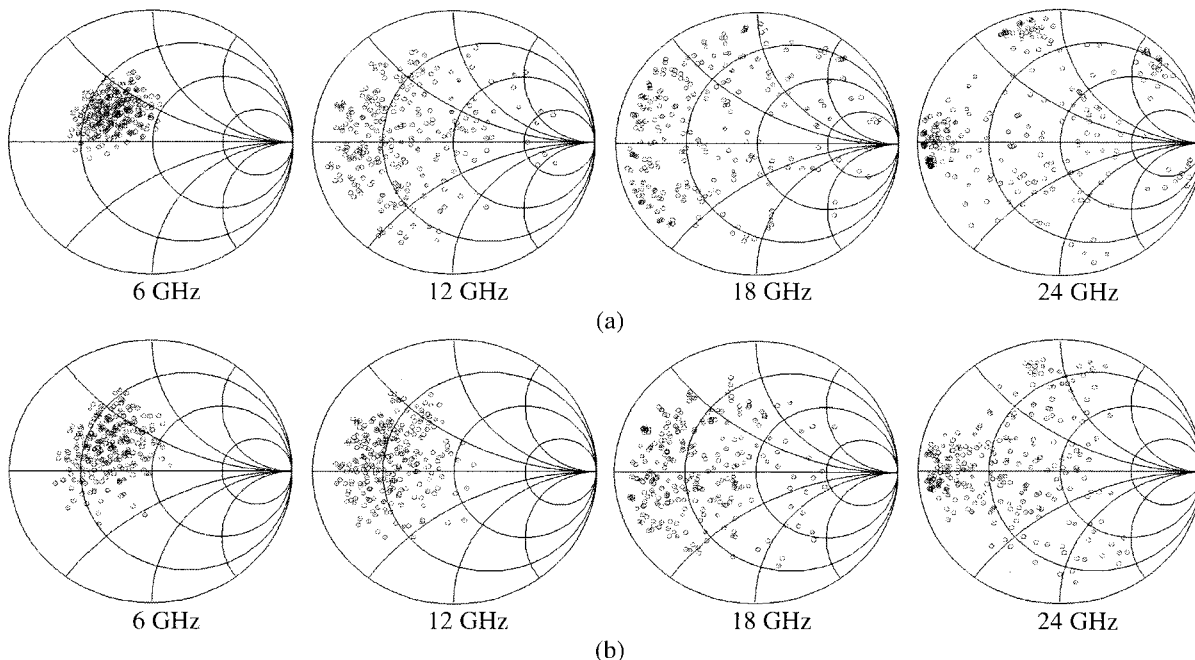


Figure 6. Simulated impedance coverage for the 8-element reconfigurable matching network with loading-capacitance ratios of (a) 2.8:1 ($C_{U-MEMS} = 171$ fF, $C_{D-MEMS} = 750$ fF, $C_{MAM} = 850$ fF) and (b) 12.2:1 ($C_{U-MEMS} = 34$ fF, $C_{D-MEMS} = 750$ fF, $C_{MAM} = 850$ fF).

hand, for $C_D = 200$ fF and $s = 480$ μm , the circuit results in excellent coverage of the Smith chart up to 36 GHz, but at the expense of a higher input impedance at 6–12 GHz. It is clear that for 6–24 GHz applications, a down-state capacitance of 400–600 fF for $s = 480$ μm is the best choice for low impedance matching and good Smith chart coverage.

Effect of Switched-Capacitor Separation. Figure 7(b) presents the effect of the period s between the switched capacitors for $C_D = 400$ fF. Again, for $s = 900$ μm , the results are plotted to 20 GHz due to Bragg frequency limitations (the circuit is electrically large), and for $s = 225$ μm , one can extend the operation of the circuit up to 40 GHz. It is seen that the separation does not affect the minimum achievable input impedance at 6–24 GHz, but a closer look at the Smith chart indicates that the best coverage occurs for $s = 300$ – 600 μm with $C_D = 400$ fF in the 6–24-GHz frequency range.

In general, many solutions in the s and C_D plane will lead to acceptable results in any frequency band.

IV. LOSS ANALYSIS AND CASE STUDIES

The reconfigurable circuit results in a very high reflection coefficient for many different switch combi-

nations; therefore, S_{21} is not the correct measure of the circuit loss. The loss of any mismatched circuit is defined as:

$$\text{Loss} = 1 - |S_{11}|^2 - |S_{21}|^2 \quad (3)$$

and takes into account the loss in the CPW line, MEMS bridges, and MAM capacitors, and the radiation loss. The loss for different switch combinations can be easily obtained from the S -parameter measurements and from the circuit model of Figure 2(a) and Table 1, as shown in Figure 8). The peaks in the loss analysis are due to the low-impedance t-line section, which is synthesized in the reconfigurable circuit ($Z_D = 23$ Ω , $\epsilon_{\text{reff}_D} = 38.2$, $N^*s = N^*480$ μm) when one or several switched capacitors are actuated. This low impedance section is bound by 45 Ω t-line sections on either side, and will resonate when its effective length is $n\lambda_g/2$ (half-wave resonator), where λ_g is the guided wavelength in the resonator. For 2, 4, or 8 cascaded switched capacitors, the first resonant frequencies occur at 25.3, 12.6, and 6.3 GHz, respectively (Fig. 8), and is independent of the location of the switched capacitors. The simulated loss using the circuit model agrees very well with the values obtained from the measured S -parameters (Fig. 8). This allows us to use the circuit model for accurate loss analysis with non-50 Ω loads.

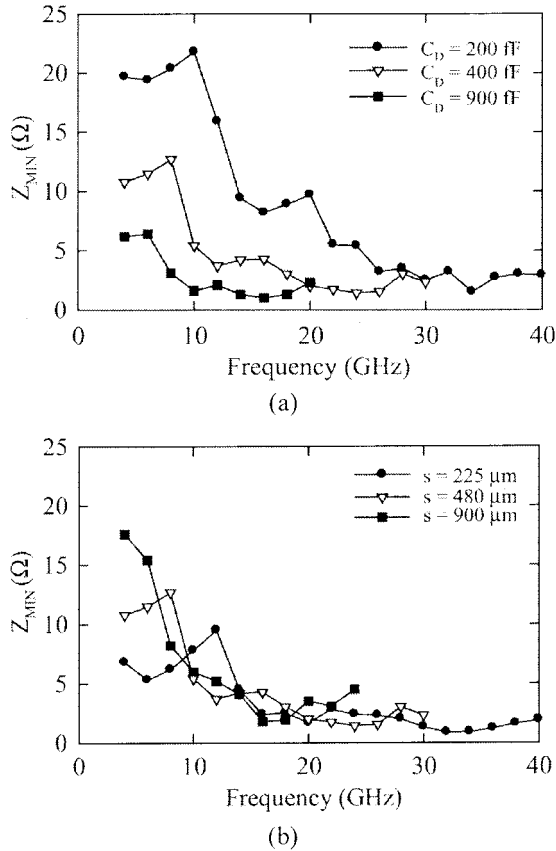


Figure 7. Simulated minimum input impedance for the 8-element matching network with a 50Ω load: (a) with different down-state capacitances and $s = 480 \mu\text{m}$; (b) with different separation distances and $C_D = 400 \text{ fF}$.

A. Case Study: 10Ω and 20Ω Loads

The reconfigurable loaded-line network was terminated with 10Ω and 20Ω loads and matched to 50Ω at 6, 12, 18, and 24 GHz (see Figs. 9 and 10). It is evident that several switch combinations result in a good match at each frequency (Tables 2 and 3). Note that as the frequency increases, the circuit becomes electrically larger and results in a narrower bandwidth (a percentage of the center frequency). The loss increases significantly above 12 GHz, and becomes -2 dB (20Ω case) and -3 dB (10Ω case) at 24 GHz. The reason is that the circuit is electrically lossy, since the down-state capacitor Q is only 28 and the t-line loss is 0.56 dB/cm at 24 GHz. If operation at 18–24 GHz is needed, then one should employ a much smaller down-state capacitor and a smaller separation s , thus resulting in a more compact circuit. However, if wide-band operation is needed (6–24 GHz), then it would be best to use a quartz substrate (instead of a glass substrate) with a lower t-line loss. Also, the use of thicker gold plating for the bridges and MAM capac-

itors can decrease the switched capacitor resistance to 0.3Ω (instead of the current value of 0.6Ω). The loss of the reconfigurable circuit with a 10Ω load is presented in Table 4 for different t-line loss and device resistance values.

The input-reflection coefficient and loss can also be obtained from the measured S -parameters under different switch combinations (Fig. 11). It is seen that the experimental data agrees quite well with the circuit model of Figure 10, and again shows that the network is excellent up to 18 GHz. The network results in high matching loss for the 10Ω case at 24 GHz, but this can be solved as discussed above.

Case Study: 200Ω Load

The 8-element reconfigurable matching network was primarily designed to match low output-impedance amplifiers to 50Ω . However, the circuit can also be used to match high-impedance components at 10–28 GHz (Fig. 12). The simulated input impedances at the input of the 8-element matching network with a 200Ω load are shown in Figure 12. A detailed analysis indicates that the bandwidth and loss (which are not presented here) are comparable to the 10Ω case.

B. Case Study: 10Ω PA to Varying Antenna Loads (10Ω – 200Ω)

An interesting case for power amplifiers is when the load impedance (such as the antenna) varies over a wide impedance range. The matching network still needs to be used when connecting a 10Ω PA to a 10Ω load due to its finite length and non- 10Ω impedance. The reconfigurable network is versatile enough that it can easily render such a match possible with several switch combinations, under virtually any load condition (Table 5, Figs. 13 and 14) but at the expense of a reduced bandwidth for the 10Ω – 200Ω case.

The above case studies prove that the loaded-line reconfigurable network is an extremely versatile design, and is capable of a wide range of impedance matching (even a 20:1 case) over a large frequency band without any excessive loss. Although we have used solely real impedances, the reconfigurable matching network has virtually the same performance for complex loads on the same VSWR circles, and this will not be repeated here for conciseness.

V. INTERMODULATION MEASUREMENTS

The intermodulation analysis of MEMS switches, varactors, and tunable filters is presented in [11] and will

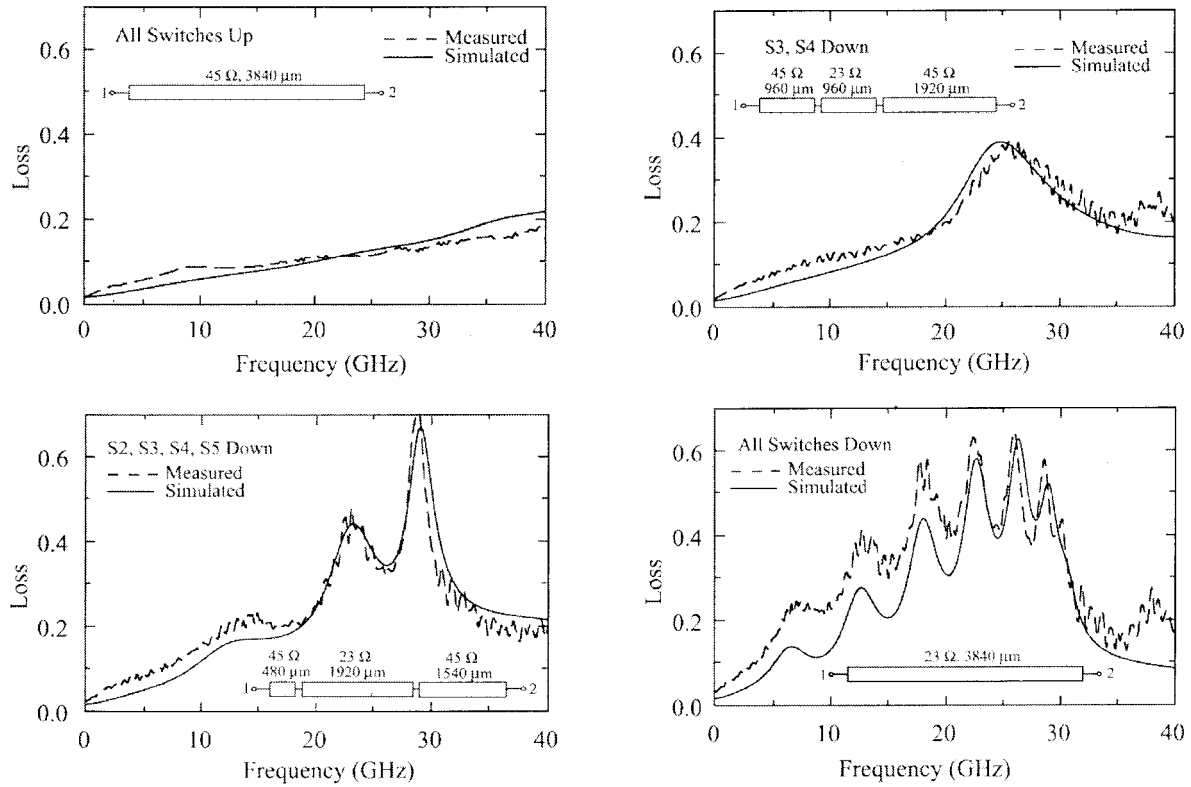


Figure 8. Loss of the 8-element matching network obtained from measured *S*-parameters and from the circuit model for different switch combinations.

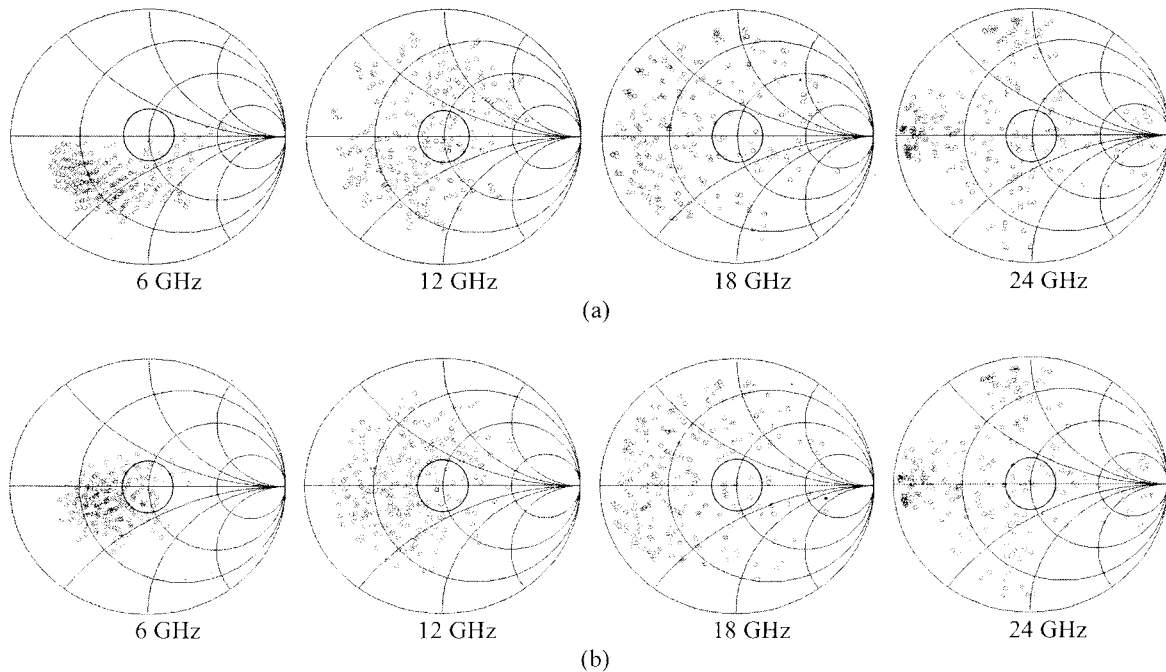


Figure 9. Simulated impedances seen at the input of the 8-element matching network with (a) 10Ω and (b) 20Ω load (Smith chart is based on 50Ω). The circle shows the -15-dB matching area.

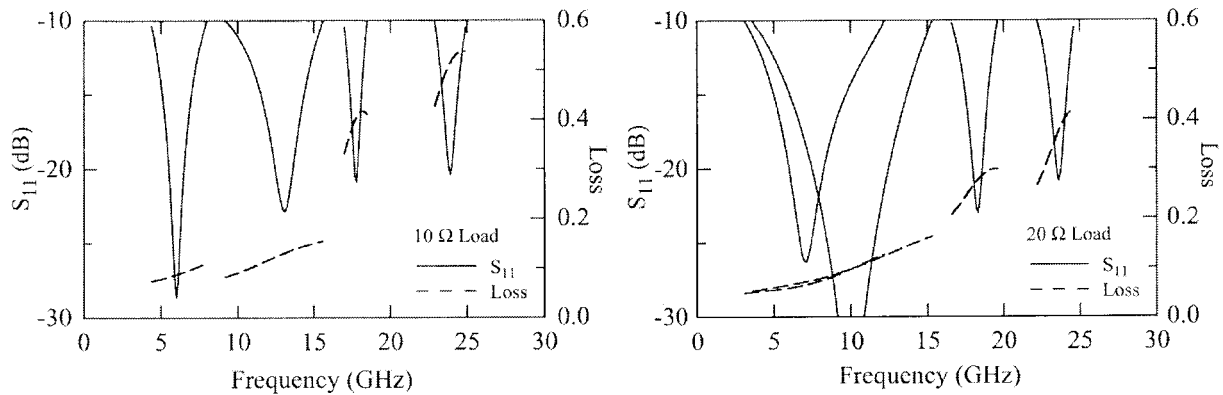


Figure 10. Simulated input reflection coefficient and loss of the 8-element matching network at 6, 12, 18, and 24 GHz with (a) 10 Ω and (b) 20 Ω loads matched to 50 Ω . The specific switch combinations are presented in Tables II and III in italics.

not be repeated here. The findings in [11] are that MEMS circuits generate insignificant amount of intermodulation products, especially if the difference Δf is higher than the mechanical resonant frequency of the bridge (90–100 kHz). Intermodulation measurements of the 8-element matching network were done at 8 and 17 GHz (Fig. 14), and the 8-GHz results are shown due to the higher dynamic range. The circuit was terminated with a 50 Ω load or with a short circuit. In the case of a short-circuit condition, the intermodulation signal was measured at the input port using a circulator. It is seen that the intermodulation products are truly negligible (IIP3 > +30 dBm), even

under short-circuit conditions (Fig. 15). The IIP3 increases with Δf^4 and, therefore, it is expected to be > +70 dBm for a Δf of 1 MHz. This is so low that it is simply unmeasurable in most experimental setups. These findings confirm the results of [11].

VI. POWER ANALYSIS AND MEASUREMENTS

The position of a MEMS switch changes if a DC or RF voltage is applied between the bridge electrodes, since the electrostatic force is dependent on V^2 , and

TABLE II. Case Study: 10 Ω Load

Switch Combination	S_{11} at Center Freq. (dB) and -10 -dB BW (GHz)	Loss at Center Freq. and Max. Loss in -10 -dB Band
6 GHz		
S5, S6, S7	-20.6 (3.1)	0.09 (0.10)
S5, S6, S7, S8	-28.6 (3.6)	0.09 (0.11)
S4, S6, S7, S8	-17.4 (3.2)	0.09 (0.10)
12 GHz		
S7, S8	-17.1 (6.4)	0.12 (0.16)
S3, S6, S7	-44.5 (2.1)	0.21 (0.23)
S1, S4, S7, S8	-23.9 (2.5)	0.19 (0.20)
18 GHz		
S2, S4, S7	-20.0 (1.3)	0.44 (0.47)
S2, S3, S4, S7	-16.5 (1.6)	0.47 (0.49)
S2, S4, S6, S7, S8	-17.6 (1.5)	0.41 (0.42)
24 GHz		
S6, S7	-19.9 (2.0)	0.52 (0.54)
S1, S2, S4	-17.9 (2.3)	0.52 (0.56)
S3, S4, S6, S7, S8	-25.1 (3.5)	0.58 (0.65)

TABLE III. Case Study: 20 Ω Load

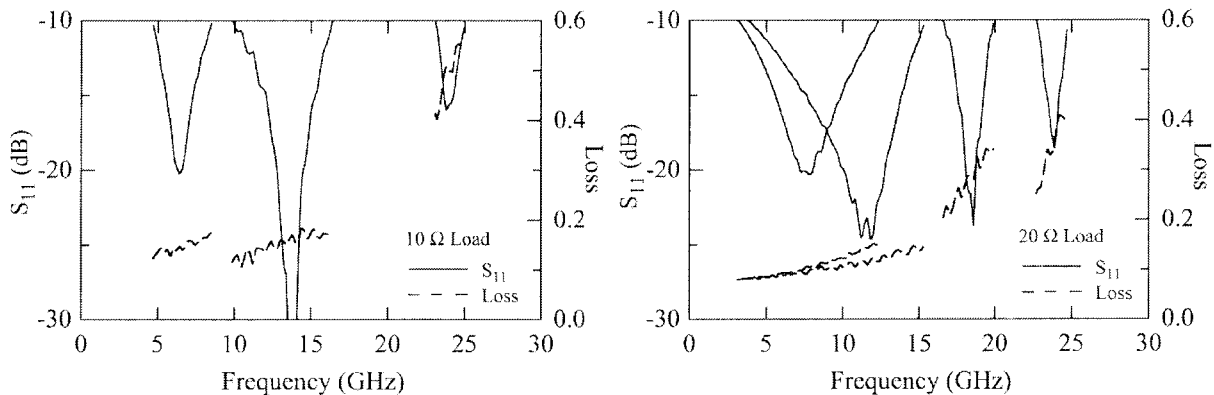
Switch Combination	S_{11} at Center Freq. (dB) and -10 -dB BW (GHz)	Loss at Center Freq. and Max. Loss in -10 -dB Band
6 GHz		
S6	-20.0 (9.1)	0.06 (0.13)
S5	-23.6 (5.8)	0.06 (0.08)
S5, S7	-21.8 (8.8)	0.07 (0.12)
12 GHz		
S7	-20.1 (11)	0.10 (0.12)
S6, S8	-22.3 (11.7)	0.12 (0.16)
S2, S4, S5, S7	-25.8 (3.5)	0.18 (0.19)
18 GHz		
S4, S6, S8	-18.14 (4.7)	0.29 (0.34)
S5, S6, S7	-20.5 (3.0)	0.27 (0.30)
S3, S4, S5, S7, S8	-20.5 (3.5)	0.34 (0.36)
24 GHz		
S2, S3	-17 (2.4)	0.36 (0.42)
S6, S7	-17.5 (2.6)	0.40 (0.42)
S4, S5, S7, S8	-17.7 (2.6)	0.52 (0.61)

TABLE IV. Simulated Loss of the Reconfigurable 8-Element Circuit with a 10Ω Load and Different T-Line Loss and Switched Capacitor Resistance

α at 10/20 GHz (dB/cm)	$R_{MEMS} + R_{MAM}$ (Ω)	Loss, 6 GHz S5, S6, S7, S8	Loss, 12 GHz S7, S8	Loss, 18 GHz S2, S4, S6, S7, S8	Loss, 24 GHz S6, S7
0.35/0.52	0.6	0.09 (0.11)	0.12 (0.16)	0.41 (0.42)	0.52 (0.54)
0.35/0.52	0.3	0.08 (0.10)	0.10 (0.12)	0.32 (0.32)	0.41 (0.42)
0.18/0.27	0.6	0.07 (0.09)	0.10 (0.13)	0.37 (0.38)	0.48 (0.50)
0.18/0.27	0.3	0.06 (0.08)	0.08 (0.10)	0.27 (0.27)	0.35 (0.36)

TABLE V. Case Study: 10Ω Source and 10Ω or 200Ω Load

Frequency	Switch Combination	S_{11} at Center Freq. (dB) and -10-dB BW (GHz)	Loss at Center Freq. and Max. Loss in -10-dB Band	Switch Combination	S_{11} at Center Freq. (dB) and -10-dB BW (GHz)	Loss at Center Freq. and Max. Loss in -10-dB Band	
		10Ω to 10Ω				200Ω to 10Ω	
6 GHz	S2, S3, S4, S5, S6, S7	-18.3, (1.3)	0.13 (0.14)	All up	-18.7, (1.2)	0.09 (0.10)	
12 GHz	S1, S2, S7, S8	-48.3, (5.0)	0.17 (0.23)	S4, S8	-31.8, (0.6)	0.42 (0.42)	
18 GHz	S1, S2	-22.6, (1.3)	0.42 (0.43)	S1, S2, S3, S4, S5	-28.1, (1.7)	0.37 (0.37)	
24 GHz	S1, S2, S7, S8	-19.1, (2.5)	0.45 (0.47)	S1, S2	-28.3, (1.8)	0.37 (0.38)	

**Figure 11.** Input-reflection coefficient and loss obtained from the measured S -parameters of the 8-element matching network with 10Ω and 20Ω loads. The 10Ω load is matched to 50Ω at 6 GHz using (S5, S6, S7, S8), at 12 GHz (S7, S8), and at 24 GHz (S6, S7). The 20Ω load is matched to 50Ω at 6 GHz using (S6), at 12 GHz (S7), at 18 GHz (S5, S6, S7) and at 24 GHz (S2, S3).

this translates to V_{rms}^2 for RF voltages [10]. If V_{rms} is equal or greater than the pull-down voltage of the MEMS switch (V_p is typically 15–30 V), the switch will self-actuate to the down-state position. Another problem related to high RF power is the hold-down voltage in the down-state position (V_h is typically 5–8 V). This is important for switched capacitors, since the RF voltage across the MEMS switch can be quite high. The reason for this is that the switch is not connected to a ground, and therefore can sustain a high-RF voltage even in the down-state position.

Also, the RF current in the switch in the down-state position can be quite high, and this is especially true in standing-wave circuits. Still, switched-capacitor circuits result in more favorable down-state conditions than standard RF MEMS switches because the impedance to ground at any switch location is quite high ($X = -j33\Omega$ at 12 GHz), compared to a capacitive short-circuit (around $X = -j5\Omega$ at 12 GHz).

The rms voltages and currents (V_{RF-rms} , I_{RF-rms}) for the N -switched capacitors under different combination can be easily obtained using a circuit simulator

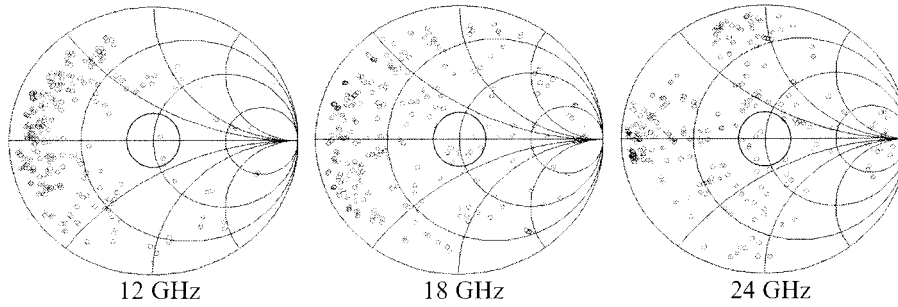


Figure 12. Simulated impedances seen by the 50Ω source connected to the 8-element matching network and a 200Ω load.

and the circuit model of Table 1 (Agilent ADS). The 8-element circuit was terminated with 10Ω or 20Ω loads and matched with the same switch combinations as in Tables 2 and 3 (Fig. 16). An input-RF power of $+27\text{-dBm}$ (500 mW) was used, but the voltages and current for different powers can be easily obtained, since they are proportional to \sqrt{P} . From Figure 16, it is easy to locate which switches are in the down-state position from the large simulated current in the switched-capacitors. The simulated voltage is divided between the MEMS switch ($C_{U\text{-MEMS}} = 91\text{ fF}$, $C_{D\text{-MEMS}} = 750\text{ fF}$) and the MAM capacitor ($C_{MAM} = 850\text{ fF}$), and therefore, the voltage across the MEMS switch is $0.9 V_{RF\text{-rms}}$ and $0.47 V_{RF\text{-rms}}$ for the up-state

and down-state positions, respectively. The current in the MEMS switch is the same as the MAM capacitors (there are two MAM capacitors connected to each MEMS switch). The simulations indicate that the rms voltages across the MEMS switches for the 10Ω and 20Ω loads are below 6 V for the up-state and down-state positions at $6\text{--}18\text{ GHz}$, and therefore, there is no failure due to self-actuation or hold-down. The rms current values are also reasonable (up to 300 mA at $6\text{--}18\text{ GHz}$) which can be easily sustained by $0.8\text{ }\mu\text{m}$ -thick gold bridges [12]. However, at 24 GHz , the down-state voltage and current in S6 and S7 are quite high, and will likely result in a hold-down condition (MEMS switch voltage $> 6\text{ V}$) or a current-failure

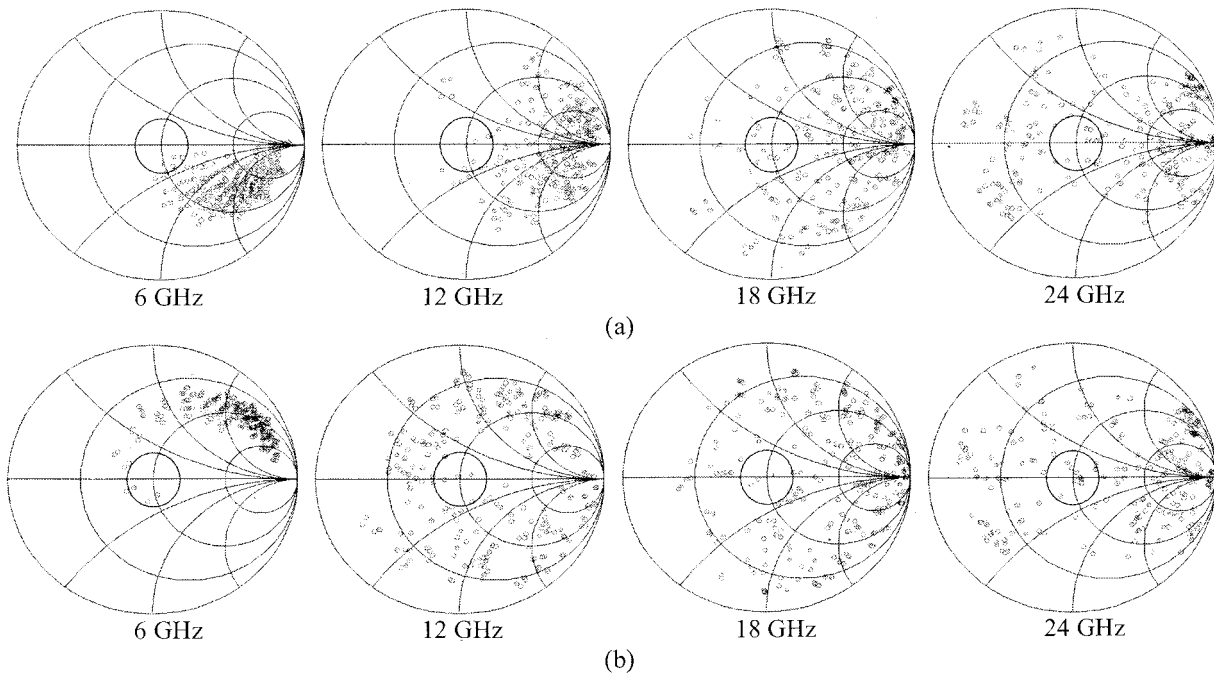


Figure 13. Simulated impedances seen by a PA ($Z_{PA} = 10\Omega$) connected to the 8-element matching network and antenna with impedance (Z_A) of (a) 10Ω or (b) 200Ω . The Smith chart is a 10Ω chart in both cases above.

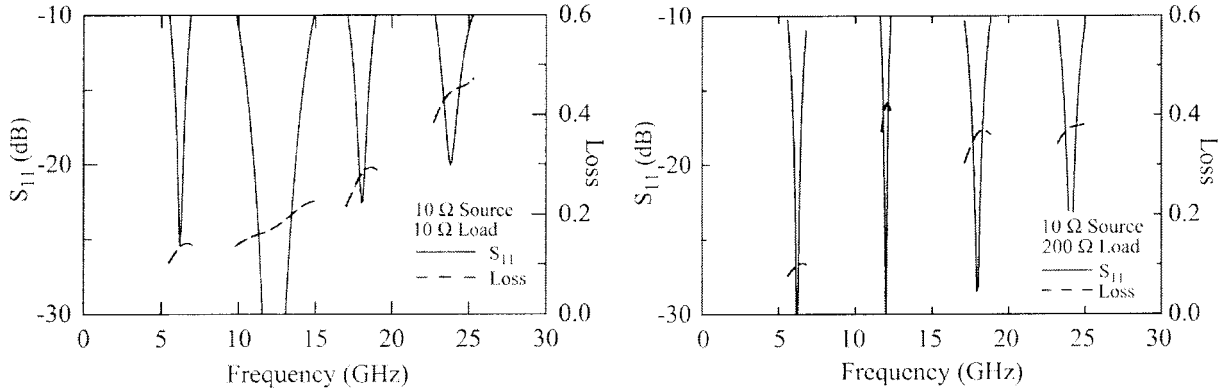


Figure 14. Simulated input-reflection coefficient and loss of a PA ($Z_{PA} = 10\Omega$) connected to the 8-element matching network and an antenna impedance (Z_A) of 10Ω or 200Ω .

condition ($I_{RF-rms} = 400$ mA). The high current level can be explained by the value of the loading reactance at 24 GHz ($X = -j16\Omega$), which is approaching a short circuit, and the high-voltage levels are due to the electrically long low-impedance resonator, which is

synthesized even with the use of two switches in the down-state position. Again, for -20 -GHz operation, it is best to use a smaller loading capacitance and a shorter separation s .

Power measurements using on-wafer 50Ω set-ups are not easily achievable, and if a lumped 10Ω load is placed at port 2, then S_{21} cannot be measured, and most important, the absorbed power in the lumped load cannot be measured unless a precise calorimetric technique is used. Another method is to use a fixed impedance transforming network and a 50Ω load (that is, power meter) at port 2, with the goal of presenting an effective load of 10Ω to the reconfigurable network; however, this is quite narrowband, and due to the electrical delay in the CPW probe launchers, one cannot be sure that the impedance seen at port 2 is actually as designed. We have therefore decided to load the reconfigurable network with a standard 50Ω load (that is, power meter and a CPW probe) and purposely mismatch the input port by selecting different switch combinations. The important value in Figure 17 is the achieved VSWR or S_{11} under the different switch combinations, since this gives a good idea of the standing waves in the circuit.

The reconfigurable circuit was measured at 8 and 17.6 GHz due to the availability of TWT amplifiers at these frequencies. The measured maximum on-wafer power was $+29$ dBm at 8 GHz and $+30.5$ dBm at 17.6 GHz (Fig. 17). At 8 GHz, it is evident that the 8-element circuit can handle up to $+29$ dBm of RF power (loading reactance is $X = -j50\Omega$) for different VSWR conditions. In this case, the minimum input impedance which can be achieved is about 13Ω (see Figs. 5 and 7), which corresponds to $S_{11} = -4.6$ dB. At 17.6 GHz, the circuit starts to self-actuate or hold-down at an RF power of $+28$ – 29 dBm when S4 and S7 were actuated ($S_{11} = -2.6$ dB). The case of (S2,

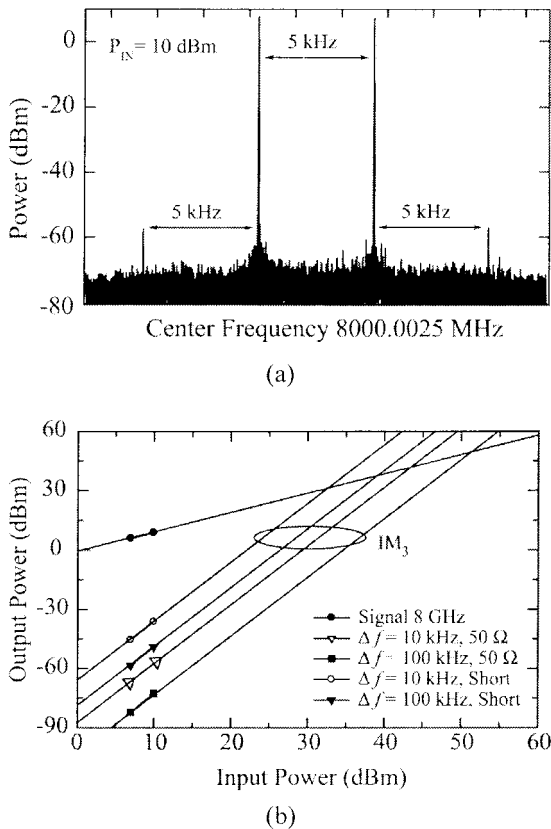


Figure 15. (a) Representative measured spectrum of the output (50Ω) or input (short circuit) signals at 8 GHz in the intermodulation setup; (b) measured third-order intermodulation as a function of Δf and the input power level.

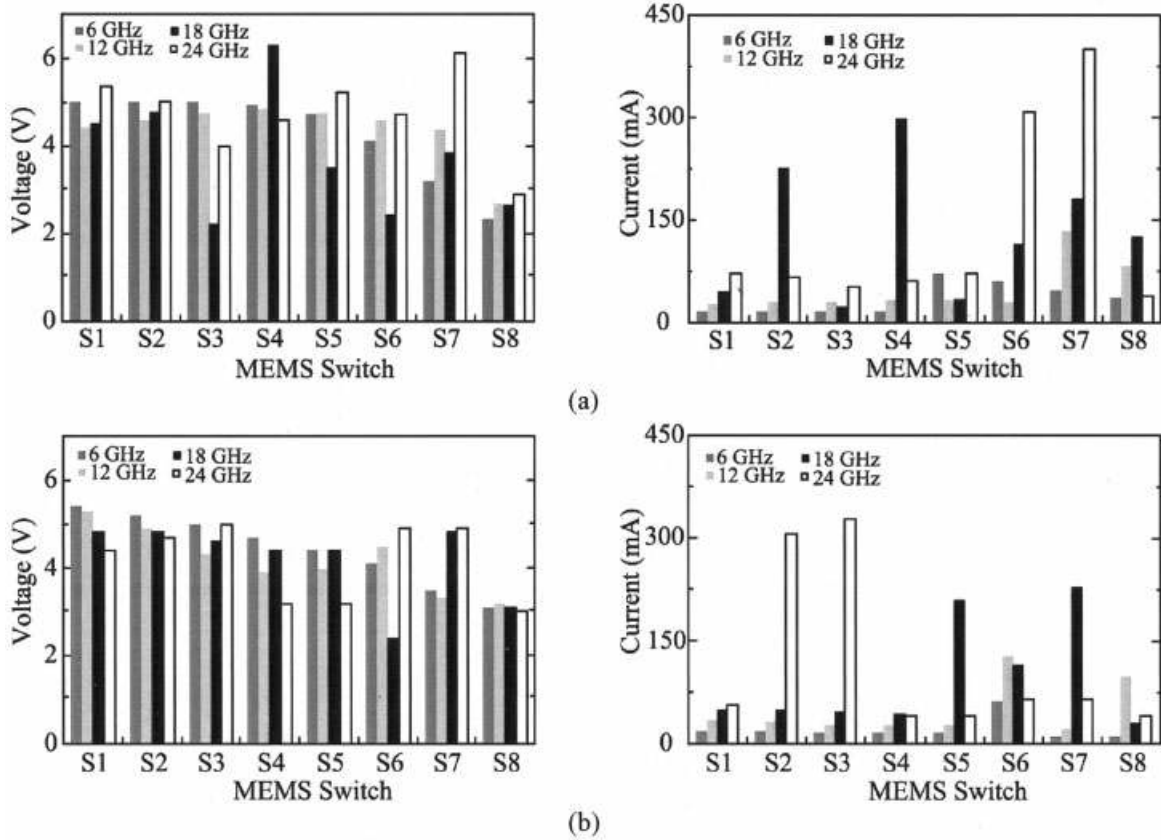


Figure 16. Simulated rms voltages and currents for the 8-element matching network with +27-dBm input power at 6, 8, 12, and 24 GHz. The component was terminated with a load of (a) 10Ω or (b) 20Ω, and matched to 50Ω (see Tables II and III, italic entries).

S7) with a VSWR of 13.7 is particularly interesting since one can see an RF self-actuation of several switches at 28.5 dBm thereby changing the reconfigurable circuit impedance (Fig. 18). It is quite hard to determine exactly which switches are actuated as S3

and S4 are starting to be pulled down, the impedance of the network changes and different voltage distribution occurs on the 8-element network. A coupled-microwave-circuit mechanical analysis is needed to find the solution in a dynamic fashion and is not done

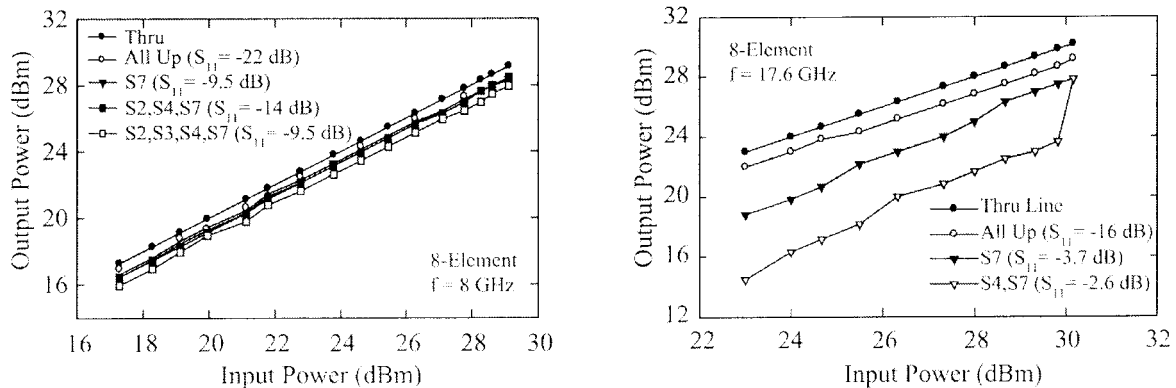


Figure 17. The measured power handling of the 8-element MEMS matching networks at 8 and 17.6 GHz under different VSWR (S_{11}) conditions.

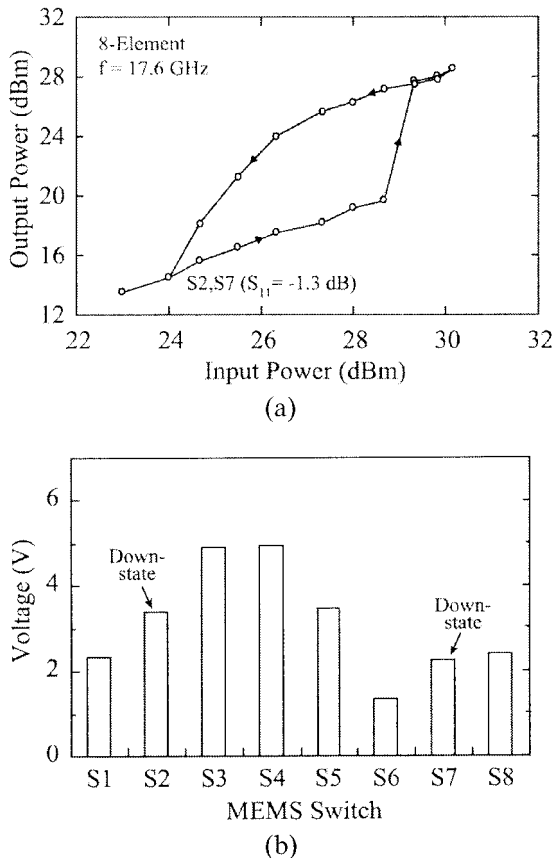


Figure 18. An RF self-actuation and hold-down case at 17.6 GHz: (a) measured output power and (b) simulations of the rms circuit voltages when (S2, S7) are actuated at $P_{IN} = 28.5$ dBm.

in this work. An RF hold-down is present in this new circuit until the power is reduced to 24 dBm, where these particular switches are released to the up-state position and the circuit returns to its initial condition.

We feel confident that the 8-element reconfigurable network with $g_0 = 1.1 \mu\text{m}$, $V_p = 15\text{--}20$ V, and $V_h = 6\text{--}8$ V, can handle >1 W of RF power at 8 GHz, and around 600 mW at 18 GHz under different VSWR conditions. If higher-power handling is required, then the pull-down and hold-down voltages must be increased, and this can be done by increasing g_0 or the spring constant of the bridge. Another solution at 18 GHz is to reduce the loading capacitance to $C_D = 200$ fF (see Fig. 7). This will result in less standing waves and higher power handling, and can still match a 10Ω impedance at 18 GHz. However, if a wideband 6–18-GHz matching network is needed, then one has to accept the fact that the network will handle more RF power at the low end of the band than at the high end.

VII. EXTENSION TO 4-ELEMENT NETWORKS

A 4-element tuning network with 16 possible states was also developed for power-amplifier applications. The element values are exactly as Table 1 and the dimensions of the 4-element network are $1.9 \text{ mm} \times 1.3 \text{ mm}$ (see Fig. 3). The main advantage of this network is a reduced loss at the expense of less impedance coverage.

The measured impedance values with a 50Ω load agree very well with simulations over a wide frequency range (Fig. 19). The effect of the MEMS bridge capacitance $C_{U\text{-MEMS}}$, loading capacitance C_D , and separation s are the same as the 8-element network and will not be repeated here. A detailed loss calculation based on the circuit model and the measured S -parameters shows a loss of 0.25–0.5 dB up to 12 GHz and 1.0–1.5 dB at 18 GHz for 10Ω and 20Ω loads (Fig. 20). The impedance match at these frequencies is around -15 to -20 dB, since the network results in only 16 possible combinations. Still, this is excellent for most power amplifiers operating at 4–18 GHz.

Power measurements were also done on the 4-element network (Fig. 21). At 8 and 17.6 GHz, the network can handle at least +29 and +31 dBm of input power under a large range of S_{11} and VSWR values. Notice that the TWT amplifier was unleveled and this explains the deviations in linearity even at low-RF input powers.

VIII. CONCLUSION

This article has presented a novel reconfigurable matching network based on 4- and 8-element loaded-line networks for 4–18 GHz power amplifier applications. The 8-element network presents 256 impedance combinations, including a 50Ω impedance, and is also applicable for noise-parameter and load-pull measurement systems over a 3:1 frequency band. The 4-element network results in about 66% loss in the 8-element design, but with a smaller impedance coverage (which can be a limitation if the both the PA and antenna impedances varies). Still, it may be an excellent design for PA applications driving a constant load. The designs are quite robust (especially the 8-element network), and can be easily scaled to 18–50 GHz by reducing the loading capacitance and the switched-capacitor separation. It is possible that a 6-element network with 64 impedance points may ultimately be the best compromise between imped-

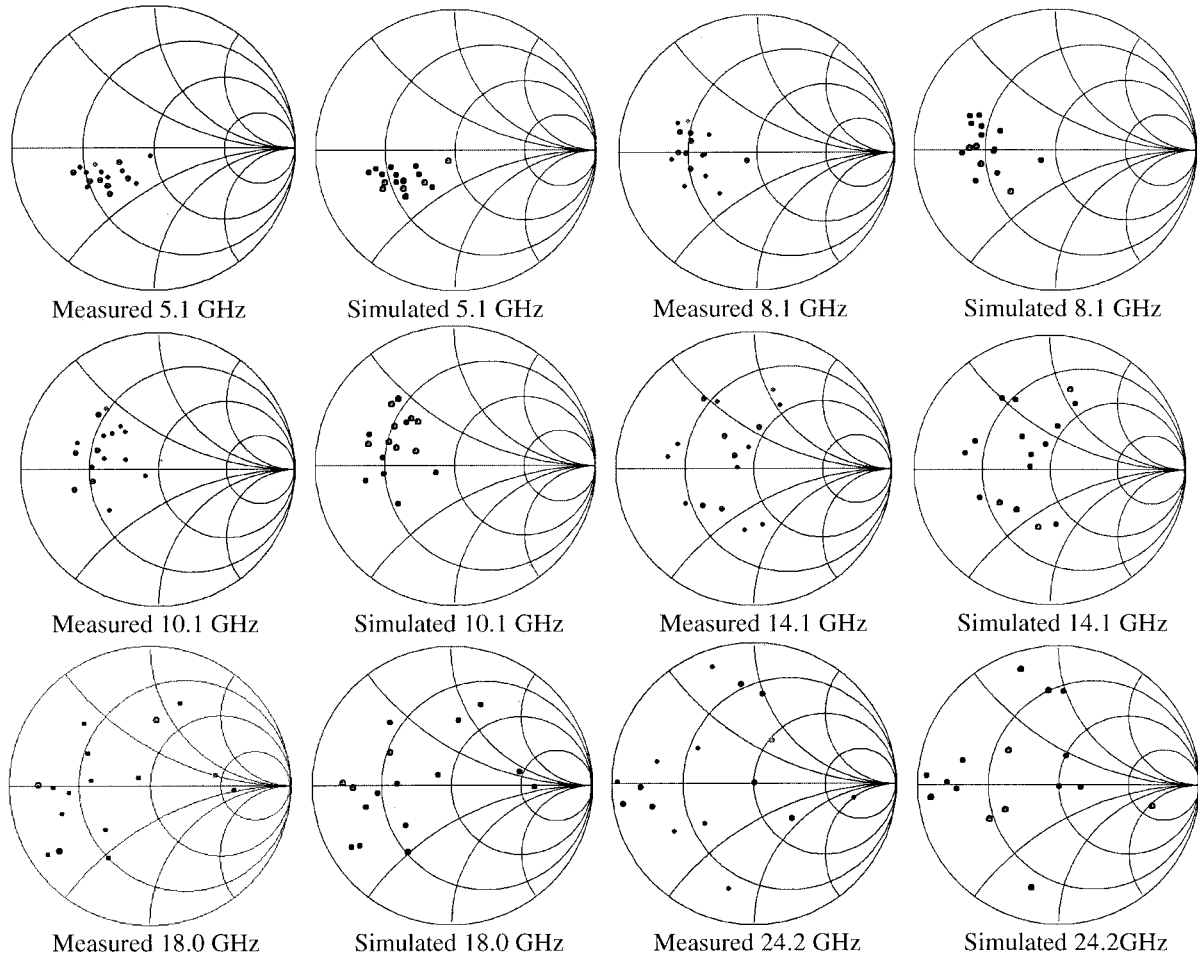


Figure 19. Measured (16) and simulated (16) impedances of the 4-element reconfigurable MEMS matching network. The single-cell values are given in Table I.

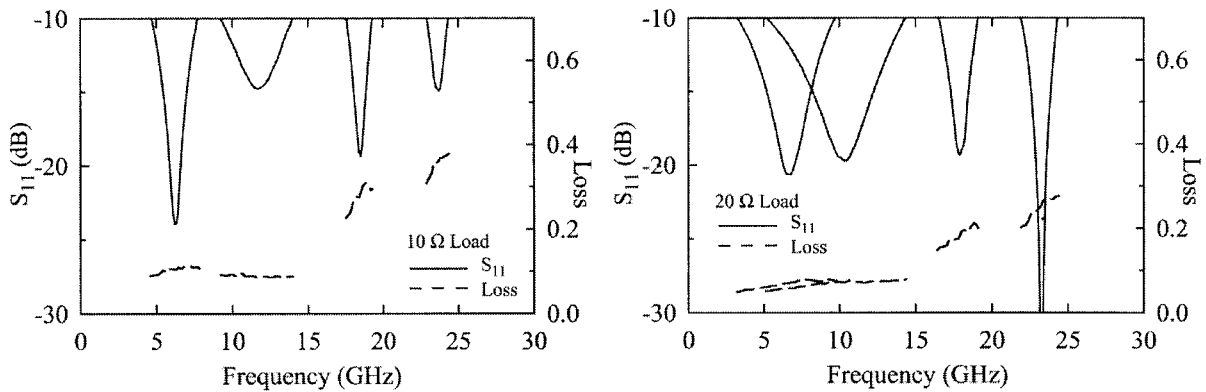


Figure 20. Input-reflection coefficient and loss obtained from measured S -parameters of the 4-element matching network with 10Ω and 20Ω loads. The 10Ω load is matched to 50Ω at 6 GHz using (S1, S2, S3), at 12 GHz (S3), at 18 GHz (S1, S2, S3), and at 24 GHz (S2, S3). The 20Ω load is matched to 50Ω at 6 GHz using (S1, S3), at 12 GHz (S3), at 18 GHz (S1, S2, S3), and at 24 GHz (S2, S3).

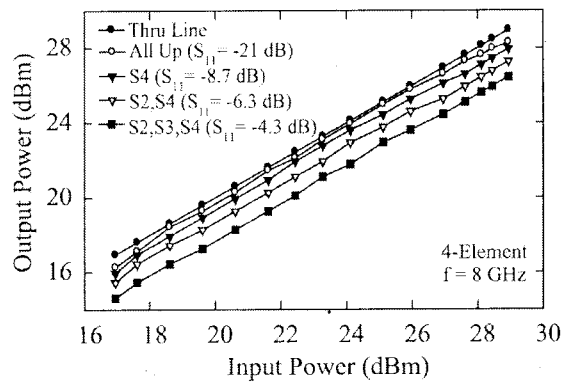
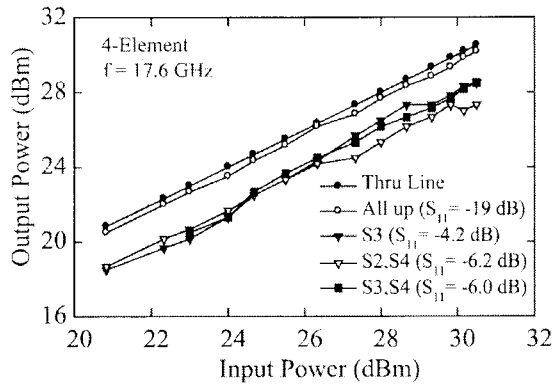


Figure 21. Power handling of the 4-element MEMS matching networks at 8 and 17.6 GHz under different VSWR (S_{11}) conditions.

ance flexibility and network loss in PA to antenna applications.

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BIOGRAPHIES

Tauno Vähä-Heikkilä was born in Finland in 1977. He received his B.S. and M.S. degrees in Physics from the University of Turku in 2000 and 2001, respectively. From 2000 to 2001 he was a Trainee Research Scientist, and since 2001 has worked as a Research Scientist at the Millimeter Wave Laboratory of Finland–MilliLab, VTT TECHNICAL RESEARCH CENTER OF FINLAND. He was a Visiting Scholar at the Radiation Laboratory, the University of Michigan at Ann Arbor in 2002 and 2003. His research interests include development of reconfigurable RF MEMS circuits and millimeter wave on-wafer noise parameter and cryogenic measurements of active devices.

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