Purdue University
Purdue e-Pubs

# A Combinatorial Problem Concerning Processor Interconnection Networks 

Michael J. O'Donnell
Carl H. Smith
Report Number:
80-352

O'Donnell, Michael J. and Smith, Carl H., "A Combinatorial Problem Concerning Processor Interconnection Networks" (1980). Department of Computer Science Technical Reports. Paper 283.
https://docs.lib.purdue.edu/cstech/283

This document has been made available through Purdue e-Pubs, a service of the Purdue University Libraries. Please contact epubs@purdue.edu for additional information.

# A Combinatorial Problem Concerning Processor Interconnection Networks 

Michael J. O'Donnell ${ }^{7}$<br>Carl H. Smith ${ }^{2}$<br>Department of Computer Sciences<br>Purdue University<br>West Lafayette, Indiana 47907

CSD TR 352

In highly parallel machine architectures, an important issue is how to interconnect components so as to pass data between processors. A crossbar switch connecting all the processors is very flexible, allowing any interprocessor connection. Crossbars however have cost proportional to the square of the number of processors they connect. As a consequence of the advances in microcircuitry, systems with thousands of processors are now feasible. Several less costly, and less flexible, processor interconnection networks have been proposed [3]. Of interest are the tradeoffs between the cost and the flexibility of each of the various interconnection schemes. The flexibility of an interconnection systern is measured by counting the number of input to output permutations realizeide by the network. In this note we count precisely the number of possible connection permutations achieved by the last stage of the Augmented Data Manipulator (ADM) introduced in [4] as a modification of the data manipulator network of [1].

In the final stage of the ADM network, a linear sequence of $n$ components is

[^0]connected in such a way that adjacent positions may be interchanged. The contents of the first component may also be interchanged with the last. Every set of interchanges is possible, as long as a single component does not participate in two different interchanges (else the same datum would go to two places, and the result would not be a permutation). In addition, left and right circular shifts by one position are allowed. Helow we calculate precisely $P(n)$, the number of different permutations which an $A D M$ network of length $n \geqq 1$ may achieve in a single step.

With each permutation performed by an ADM network of length $n$ there is an associated bit string of length $n$. Within the bit string, the $i^{\text {th }}$ bit is one if and only if the $i^{\text {th }}$ and the $i+1^{\text {st }}$ inputs are interchanged by the associated permutation (see Figure 1).


Figure 1. A permutation with $n=8$ and associated bit string

Thus, each setting of the network, except the two circular shifts, is associated with a string of 0 's and 1 's with the restriction that adjacent bits may not both be 1. Let $A(n)=$ the number of diferent strings of $n$ bits in which no two adjacent bits are both 1, and the first and last bits are not both 1. Except for the trivial cases when $n=1$ or $2, P(n)=A(n)+2$.

## Proposition 1

$$
\begin{aligned}
& P(1)=1 \\
& P(2)=2 \\
& P(n)=A(n)+2 \text { for } n \geqq 3
\end{aligned}
$$

Proof:
$P(1)=1$ is obvious. $P(2)=2$ since the only two possibilities are the identity permutation and the exchange. For $\pi \geqq 3$, every different setting of the ADM network of size $n$ produces a different permutation. Consider two different settings, $s_{1}$ which interchanges $i$ and $i+1, s_{2}$ which does not interchange $i$ and $i+1$. $s_{1}$ maps $i$ to $i+1, s_{2}$ maps $i$ to either $i$ or $i-1$. For $\pi \geqq 3$, $i, i+1, i-1$ are all different mod $\pi$, so the two permutations are different. By the discussion above, there is a one-to-one correspondence between network settings, excepting the two circular shifts, and the bit strings counted in the definition of $A(\pi)$.

The function $A(n)$ is easier to analyze using a similar function $B(n)$, which eliminates the restriction that the first and last bits in a string must not both be 1 , i.e. $B(n)=$ the number of different strings of n bits in which no two adjacent bits are both 1.

Proposition 2

$$
\begin{aligned}
& A(1)=1 \\
& A(2)=3 \\
& A(3)=4 \\
& A(n)=B(n-1)+B(n-3) \text { for } n \geqq 4
\end{aligned}
$$

## Proof:

The first three cases are verified by counting. For $\pi \geqq 4$, consider an arbitrary string of $n$ bits with no two adjacent 1 's, including end around adjacency. If the first bit is 0 , then the remaining $\pi-1$ bits may be set in any
fashion as long as no two adjacent bits are 1. There are $B(n-1)$ such strings. If the first bit is 1 , then the second and last bits must both be 0 , but the remaining $\pi-3$ may be set in any fashion as long as no two adjacent bits are 1. There are $B(n-3)$ such strings.

## Proposition 3

$B(n)$ is a shifted Fibonacci series.
$B(1)=2$
$B(2)=3$
$B(n)=B(n-1)+B(n-2)$ for $n \geqq 3$

## Proof:

For $n \geqq 3$, consider an arbitrary string of $n$ bits with no two adjacent 1's. If the first bit is 0 , then the remaining $n-1$ bits may be set in any fashion as long as no two adjacent bits are 1. There are $B(n-1)$ such strings. If the first bit is 1 , then the second bit must be 0 , but the remaining $n-2$ bits may be set in any fashion as long as no two adjacent bits are 1 . There are $B(n-2)$ such strings.

Using equation (15) in [2 §1.2.8] and the above Propositions, we have that for $\pi \geqq 6$

$$
P(n)=\left[\frac{\varphi^{n+1}}{\sqrt{5}}\right]+\left[\frac{\varphi^{n-1}}{\sqrt{5}}\right]+2
$$

where $\varphi$ is the golden ratio $1 / 2(1+\sqrt{5})$ and $[x]$ is $x$ rounded to the nearest integer. In [5] it is shown how to count the total number of permutations produced by an ADM in terms of $P(n)$.

We wish to acknowledge H. J. Siegel and G. Adams for bringing the above problem to our attention. Computer time was supplied by the Department of Computer Sciences at Purdue University.

## References

 -1. Feng, T., "Data manipulating functions in parallel processors and their implementations," IEEE Transactions on Computers C-23(3) pp. 309-318 (1974).
2. Knuth, D., The Art of Computer Programming: Furdamental Algorithms, Addison-Wesley , Reading, Mass. (1968).
3. SEGEL, H. J., 'Interconnection networks for SIMD machines." Computer 12(6) pp. 57-65 (1979).
4. Stegel, H. J. and Smith, S. D., "Study of multistage SIMD interconnection networks,' Proceedings of the Fifth Annual Symposium on Computer Architecture, pp. 223-229 (197B).
5. Smith, S. D., Siegei, H. J., Mcmdien, H. J., and Adams, G. B., 'Use of augmented data manipulator multistage networks for SIMD machines." Proceedings of the 1980 International Conference on Parallel Processing, (1980).

[^0]:    1. Supported by NSF grant MCS 7801812.
    2. Supported by NSF grant MCS 7903912.
