

### NIH Public Access

Author Manuscript

*IEEE Trans Ind Electron*. Author manuscript; available in PMC 2014 November 07

#### Published in final edited form as:

IEEE Trans Ind Electron. 2012 November 16; 60(11): 5292-5305. doi:10.1109/TIE.2012.2227914.

### A Figure-of-Merit for Designing High-Performance Inductive Power Transmission Links

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#### Abstract

Power transfer efficiency (PTE) and power delivered to the load (PDL) are two key inductive link design parameters that relate to the power source and driver specs, power loss, transmission range, robustness against misalignment, variations in loading, and interference with other devices. Designers need to strike a delicate balance between these two because designing the link to achieve high PTE will degrade the PDL and vice versa. We are proposing a new figure-of-merit (FoM), which can help designers to find out whether a two-, three-, or four-coil link is appropriate for their particular application and guide them through an iterative design procedure to reach optimal coil geometries based on how they weigh the PTE versus PDL for that application. Three design examples at three different power levels have been presented based on the proposed FoM for implantable microelectronic devices, handheld mobile devices, and electric vehicles. The new FoM suggests that the two-coil links are suitable when the coils are strongly coupled, and a large PDL is needed. Three-coil links are the best when the coils are loosely coupled, the coupling distance varies considerably, and large PDL is necessary. Finally, four-coil links are optimal when the PTE is paramount, the coils are loosely coupled, and their relative distance and alignment are stable. Measurement results support the accuracy of the theoretical design procedure and conclusions.

#### **Index Terms**

Charging electric vehicles; implantable microelectronic devices; inductive; mobile devices; power transfer efficiency (PTE); wireless power transmission

#### I. Introduction

Inductive power transmission has received considerable attention in recent years for use in a wide variety of applications that require contactless energy transfer over short distances. Depending on the application, the power level varies from microwatts in radio frequency identification tags [1], [2], to milliwatts in implantable microelectronic devices (IMDs), such

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as cochlear implants [3]–[5], watts in handheld mobile devices (HMDs) [6]–[10], and kilowatts in charging electric vehicles (EVs) [11], [12].

In a generic power transmission link, shown in Fig. 1(a), a highly efficient power amplifier (PA) on the transmitter side (Tx) drives the primary coil, which is mutually coupled to a secondary coil on the receiver side (Rx) to power up the load ( $R_L$ ). In the rest of this paper,  $R_L$  represents the ac equivalent of the power conversion circuitry and the dc load.

A key design requirement in all the above applications is to provide sufficient power delivered to the load (PDL) while maintaining high power transfer efficiency (PTE). High PTE is required to reduce 1) heat dissipation within the coils, 2) exposure to electromagnetic field, which can cause additional heat dissipation in the power transmission medium, 3) size of the main energy source (e.g., battery), and 4) interference with nearby electronics that is necessary to satisfy regulatory requirements [13]–[15]. At the same time, the link should deliver sufficient power to the load while considering practical limitations of the energy source and the PA. When  $R_L$  is constant, the PDL would be equivalent to the inductive link voltage gain all the way from the source to the load. Increasing the source voltage,  $V_s$  in Fig. 1(a), to increase the PDL can reduce the driver efficiency, require larger transistors in the PA, and make it more difficult and costly to meet the safety requirements [16].

Several methods have been proposed for designing wireless power transmission links. In [2], [17], and [18], geometries of the primary and secondary coils,  $L_2$  and  $L_3$  in Fig. 1(a), have been optimized to achieve the maximum voltage on the load, and consequently a high PDL. PDL is a key design merit when the space is limited and coils need to be miniaturized, such as in the IMD applications. Similarly, design and geometrical optimization of the two-coil inductive links based on the PTE have been widely studied [19]–[24]. The approach proposed in [25] is somewhat different by first designing  $L_3$  to maximize the PTE for a given  $R_L$ , and then optimizing  $L_2$  to achieve the desired voltage gain from source to the load, and consequently improve the PDL. However, the literature still lacks a clear figure-of-merit (FoM) that can incorporate both the PTE and the PDL, and guide designers to give proper weight to each of these important but contrasting parameters. A well-defined FoM can provide designers with much needed insight on how to optimize their inductive power transmission links based on the application requirements.

Recently, multi-coil power transmission links have been proposed and studied to further increase the PTE, particularly at large coupling distances,  $d_{23}$  [26]–[30].We have also presented the analysis, design, and optimization of the two-, three-, and four-coil inductive links based on the PTE and analyzed the PDL of these links [16].We have shown that there is a compromise in the four-coil links between simultaneously achieving high PTE and PDL at small and large distances, respectively. This can also be seen in [29] where the PTE has been purposely reduced at smaller  $d_{23}$  from 70% to 10% to achieve a high PDL at large  $d_{23}$ . However, our proposed three-coil link has achieved both high PTE and PDL [16].

In this paper, we propose a new FoM, which serves two purposes. First, it guides designers toward inductive power transmission links with the highest possible PTE and PDL. Second, it helps the designer to choose between the two-, three-, and four-coil link options for a

given application. In the following section, we have introduced the new FoM and the tradeoffs between achieving high PTE and PDL, simultaneously, in a two-coil inductive link. Section III presents the two-coil inductive link performance comparison when designed based on PTE, PDL, and FoM for different source and load scenarios. Section IV describes the design and optimization procedure for the three- and four-coil inductive links based on the FoM. Calculation and measurement results for the HMD charging and theoretical calculation for EV applications are included in Section V, followed by the discussion and concluding remarks in Sections VI and VII, respectively.

#### **II. New Fom for Inductive Power Transmission**

In Fig. 1(a), key parameters that affect the design of this link from the energy source to the load, aside from the coils are:  $V_s$ , power required by the load ( $P_L$ ), PA supply voltage ( $V_{DD}$ ),  $R_s$  (representing the PA loss), PA transistor breakdown voltage, and safety limits for the application [21]. In a class-E PA, zero-voltage-switching allows for high power efficiency with peak voltages across the primary coil and the PA transistor that are 1.07 and 3.56 times  $V_{DD}$ , respectively [31]. Therefore, when the application requires a large  $P_L$ ,  $R_s$  should be reduced to make sufficient power available from the source,  $P_{av}=V_s^2/8R_s$  at a reasonable  $V_s$  and  $V_{DD}$  [21], [29]. Unfortunately, to achieve a high PTE, the delivered power to  $L_2$  would be much smaller than  $P_{av}$  because the impedance matching on the Tx side reduces the PA efficiency to 50%. Utilizing large transistors to reduce  $R_s$  results in increased dynamic switching losses in the PA [25]. Therefore, the power transmission link should be designed in a way that it achieves high PTE and also provides sufficient PDL, while considering practical limitations of the PA circuit design.

It can be shown that the highest PTE and PDL for the inductive link, shown in Fig. 1(a), can be achieved when both LC tanks are tuned at the same resonance frequency,

 $f_0=1/2\pi \sqrt{L_2C_2}=1/2\pi \sqrt{L_3C_3}$  [25]. At resonance, the effect of the Rx on the Tx can be modeled by the reflected impedance

$$R_{\rm ref} = k_{23}^2 \omega_0 L_2 Q_{3L} \quad (1)$$

where  $k_{23}=M_{33}/\sqrt{L_2L_3}$ ,  $M_{23}$  is the mutual inductance between  $L_2$  and  $L_3$ , and  $Q_{3L} = Q_3Q_L/(Q_3 + Q_L)$ ,  $Q_3 = \omega_0L_3/R_3$ , and  $Q_L = R_L/\omega_0L_3$  for parallel load connection, which is often referred to as the load quality factor [1]. At resonance, the primary loop can be simplified to the circuit shown in Fig. 1(b).

To calculate the PTE at resonance, we should consider that the power provided by the PA  $(V_s)$  divides between  $R_s + R_2$  and  $R_{ref}$ , and that portion of the power delivered to  $R_{ref}$ , i.e., the power that is received by the secondary loop, divides between  $R_3$  and  $R_L$ , which are the only power-consuming components. These assumptions lead to

$$\eta_{2-\text{coil}} = \frac{R_{\text{ref}}}{R_s + R_2 + R_{\text{ref}}} \cdot \frac{Q_{3L}}{Q_L} = \frac{k_{23}^2 Q_2 Q_{3L}}{1 + k_{23}^2 Q_2 Q_{3L}} \cdot \frac{Q_{3L}}{Q_L} \quad (2)$$

where  $Q_2 = \omega_0 L_2/(R_s + R_2)$  [25]. The first and second terms in (2) correspond to the power division between  $R_s + R_2$  and  $R_{ref}$ , and  $R_3$  and  $R_L$ , respectively. Therefore, according to (2), in order to achieve the highest PTE,  $R_{ref}$  should be maximized.

PDL can be calculated by multiplying the power provided by  $V_s$ , i.e.,  $V_s^2/2(R_s+R_2+R_{ref})$ , with the PTE from (2)

$$P_{L,2-\text{coil}} = \frac{V_s^2 R_{\text{ref}}}{2(R_s + R_2 + R_{\text{ref}})^2} \cdot \frac{Q_{3L}}{Q_L} = \frac{V_s^2}{2(R_s + R_2)} \frac{k_{23}^2 Q_2 Q_{3L}}{(1 + k_{23}^2 Q_2 Q_{3L})^2} \cdot \frac{Q_{3L}}{Q_L}.$$
 (3)

It can be seen in (3) that PDL does not necessarily increase by maximizing  $R_{ref}$ . Taking the derivative of (3) with respect to  $R_{ref}$  indicates that the maximum PDL at a certain coupling distance,  $d_{23}$ , is achieved when  $R_{ref} = R_s + R_2$ . Under this impedance-matched condition, PTE  $\leq 50\%$ , because at least half of the power is dissipated in  $R_s + R_2$ .

To better understand and manage the necessary compromise between the PTE and PDL when designing an inductive power transfer link, we propose a new FoM

$$FoM = \frac{\eta^n \times P_L}{V_s^2} = \frac{\eta^{n+1}}{2(R_s + R_2 + R_{\rm ref})} \quad (4)$$

where *n* depends on the importance of the PTE versus PDL in a particular application. Interestingly, the FoM unit is in Siemens, which implies how conductive the wireless link is to electric power transfer. The FoM for a two-coil link can be derived from other circuit parameters in Fig. 1 by substituting (2) and (3) in (4)

$$FoM_{2-\text{coil}} = \frac{\left(k_{23}^2 Q_2 Q_{3L}\right)^{n+1}}{2\left(R_s + R_2\right)\left(1 + k_{23}^2 Q_2 Q_{3L}\right)^{n+2}} \cdot \left(\frac{Q_{3L}}{Q_L}\right)^{n+1}.$$
 (5)

According to (2), the PTE profile of a two-coil link is a monotonically decreasing function of the coils' coupling distance,  $d_{23}$  [16]. The PDL and FoM profiles, on the other hand, can be maximized at a particular  $d_{23}$  by calculating the derivatives of (3) and (5) with respect to  $k_{23}$ , respectively,

$$k_{23,\text{PDL}} = \frac{1}{(Q_2 Q_{3L})^{\frac{1}{2}}}, k_{23,\text{FoM}} = \left(\frac{n+1}{Q_2 Q_{3L}}\right)^{\frac{1}{2}}.$$
 (6)

By substituting  $k_{23,FoM}$  in (2) and (3), one can find the PTE and PDL of the two-coil link when the FoM is maximized at a particular  $d_{23}$ 

$$\eta_{2-\text{coil,FoM}} = \frac{n+1}{n+2} \cdot \frac{Q_{3L}}{Q_L} P_{L,2-\text{coil,FoM}} = \frac{V_s^2}{2(R_s + R_2)} \cdot \frac{n+1}{(n+2)^2} \cdot \frac{Q_{3L}}{Q_L}.$$
 (7)

These equations do not consider any additional constraints on the coils' geometries, which may result in further reductions in the PTE and/or PDL in order to meet those requirements. The proposed FoM in (4) reduces to the PTE or PDL if  $n \to \infty$  or n = 0, respectively.

$$\eta_{2-\text{coil,max}} = \frac{Q_{3L}}{Q_{L}}, P_{L,2-\text{coil,max}} = \frac{V_s^2}{8(R_s + R_2)} \cdot \frac{Q_{3L}}{Q_{L}}.$$
 (8)

Using (7) and (8), we can calculate the percentage of the PTE and PDL that the two-coil link should forfeit when it is designed based on the optimal FoM

$$\eta_{\text{Loss,2-coil}} = \frac{\eta_{2-\text{coil,max}} - \eta_{2-\text{coil,FoM}}}{\eta_{2-\text{coil,max}}} = \frac{1}{n+2} P_{L,\text{Loss,2-coil}} = \frac{P_{L,2-\text{coil,max}} - P_{L,2-\text{coil,FoM}}}{P_{L,2-\text{coil,max}}} = \frac{n^2}{(n+2)^2}.$$
 (9)

Fig. 2 shows the PTE and PDL losses versus *n*, based on (9). The key points to be learned from these curves are: 1) If the two-coil link is optimized for the PDL only, i.e., n = 0, it loses 50% of the PTE. 2) If the two-coil link is optimized for the PTE only, i.e.,  $n \to \infty$ , it loses 100% of the PDL. However, in practice, the application and coil fabrication process constraints limit the PTE and allow a small amount of power to be delivered to the load (PDL > 0). 3) When n = 2, the PTE and PDL losses are balanced, equal to 25% each.

In a particular application, it is often possible to determine whether the PTE, PDL, or both are important. Based on this determination and Fig. 2, the designer can choose a suitable n for the proposed FoM in (4), and proceed with the design of the two-coil link by maximizing it. We believe that n = 2can work well for the majority of applications, in which there is a need to achieve a large PDL with the highest possible PTE, while considering practical driver limitations. Even though in the rest of this paper, we consider n = 2, our discussions are applicable to any desired n in the FoM, defined in (4).

It should also be noted that for a given set of  $Q_2$ ,  $Q_3$ , and  $k_{23}$  values, there is an optimal load,  $R_{L,FoM} = \omega_0 L_3 Q_{L,FoM}$ , which maximizes the FoM of the two-coil link at a particular  $d_{23}$ . Calculating the derivative of (5) with respect to  $Q_L$  leads to

$$Q_{L,\text{FoM}} = \frac{2(n+1)Q_3}{k_{23}^2 Q_2 Q_3 + \sqrt{(k_{23}^2 Q_2 Q_3)^2 + 4(n+1)^2 (1+k_{23}^2 Q_2 Q_3)}}.$$
 (10)

Similarly, the optimal load values for PTE and PDL can be calculated from (10) by substituting *n* with  $\infty$  and 0, respectively [16]

$$Q_{L,\text{PTE}} = \frac{Q_3}{\left(1 + k_{23}^2 Q_2 Q_3\right)^{\frac{1}{2}}}, Q_{L,\text{PDL}} = \frac{Q_3}{1 + k_{23}^2 Q_2 Q_3}.$$
 (11)

### III. Comparison Between Two-Coil Inductive Links Designed Based on Fom, Pte, and PdI

In this section, we have compared the performance of three different sets of two-coil links, designed based on the PTE, PDL, and the new FoM for IMD applications. First, a design

procedure is explained, based on which the optimal coil geometries are calculated. This design procedure will also be used for designing multi-coil links in Section IV. The effects of  $d_{23}$ ,  $R_s$ , and  $R_L$ , which are often imposed by the application, are studied on the two-coil link design with respect to the FoM.

#### A. Two-Coil Inductive Link Design Procedure

All design examples are based on wire-wound coils (WWC) made of single filament solid wires. The relationship between the inductive link parameters, such as *k* and *Q*, and the WWC geometries can be found in the Appendix. The two-coil optimization flowchart in Fig. 3 is similar to the design procedure proposed in [23] with the following differences: 1) WWC geometries are optimized here instead of printed spiral coils (PSCs). In the WWC models presented in the Appendix, the number of turns,  $n_i$ , in WWCs substitutes the fill factor,  $\Phi$ , in PSCs while other geometrical parameters remain the same. 2) In Fig. 3, (2), (3), or (5) can be used to maximize the PTE, PDL, or FoM, respectively. Our focus in this paper will be on optimization based on the FoM, while [23] optimizes the PTE. 3) Another difference between Fig. 3 and [23] is that here we have added a weight limit for the coils. This will lead to one more 3-D surface for the weight of the WWC in each step.

Fig. 3 starts with the design constraints imposed by the application and coil fabrication technology. The former defines the maximum values for the outer diameter,  $D_{o3}$ , of the implantable coil,  $L_3$ , the maximum wire diameters for  $L_2$  and  $L_3$ ,  $w_{2,\text{max}}$ ,  $w_{3,\text{max}}$ , and their weights,  $W_{2,\text{max}}$ ,  $W_{3,\text{max}}$ , respectively. For instance, the size and weight of  $L_3$  depend on the IMD location in the body. The weight of a circular-shaped WWC with  $n_i$  turns and a wire spacing of *s* between the surface of the conductors can be found from (44) in the Appendix. The minimum value for *s* is twice the thickness of the wire insulation, which is used in the rest of this paper [23]. The nominal values for  $d_{23}$ ,  $R_L$ , and  $R_s$  are also required in step-1, all determined by the application or initial estimation. The nominal distance,  $d_{23}$ , is considered the average spacing between  $L_2$  and  $L_3$  during normal operation of the inductive link in every particular application.

In step-2, the initial values for  $L_2$  and  $L_3$  geometries, such as  $w_2$ ,  $n_2$ ,  $w_3$ , and  $n_3$  are chosen. A more detailed discussion about how to choose these initial values can be found in [23]. In step-3.1, key geometrical parameters of  $L_2$ , i.e.,  $D_{o2}$  and  $n_2$  are swept, using (5) and (44), respectively, leading to two 3-D surfaces for the FoM and  $W_2$ . The FoM surface is similar to the PTE surface in [23] when  $D_{o2}$  and  $\Phi_2$  were swept.  $D_{o2}$  and  $n_2$  values that result from the maximum FoM are first chosen. If such values lead to  $W_2 > W_{2,\text{max}}$ , i.e., the resulting optimal coil being too heavy, an imaginary horizontal plane should be drawn over the  $W_2$  surface at  $W_2 = W_{2,\text{max}}$  to find the best  $D_{o2}$  and  $n_2$  pair on the cross section that give the highest FoM.

Parameters associated with  $L_3$  geometry, such as  $w_3$  and  $n_3$ , are swept in step-3.2, using  $D_{o2}$  and  $n_2$  values from step-3.1. They generate two 3-D surfaces for FoM and  $W_3$  using (5) and (44) in the Appendix. Similar to step-3.1, the FoM surface is similar to the PTE surface in [23] when  $w_3$  and  $\Phi_3$  were swept. The  $w_3$  and  $n_3$  values that result in the highest FoM and  $W_3 < W_{3,max}$  are then chosen. In step-3.3,  $w_2$  and  $n_2$  are swept to maximize the FoM in (5) and still satisfy the  $W_2 < W_{2,max}$  condition in (44).

Steps 3.1–3.3 are repeated iteratively until  $w_2$  and  $n_2$  values change < 0.1% from one iteration to the next, satisfying the condition in step-3.4. This step determines the optimal two-coil link geometries that would achieve the maximum FoM. This can be further validated and fine-tuned using field solvers, such as HFSS (Ansoft, Pittsburgh, PA) or through measurements. It should be noted that in steps 3.2 and 3.3,  $w_2$  and  $w_3$  should be limited to  $w_{2,max}$  and  $w_{3,max}$ , respectively.

For the IMD design example, we considered a retinal implant with the following assumptions:  $L_2$  and  $L_3$  were considered circular-shaped WWCs, operating at 13.56 MHz. The link was designed to deliver 250 mW to an  $R_L = 100 \Omega$  from a nominal coupling distance of  $d_{23} = 10$  mm via a driver (PA) with output resistance of  $R_s = 0.5 \Omega$ . We also considered  $D_{o3} = 10$  mm,  $w_{3,max} = 0.51$  mm (AWG24) to reduce the implant thickness,  $W_{2,max} = 10$  g, and  $W_{3,max} = 0.5$  g to limit the weights of  $L_2$  and  $L_3$  with respect to the electronics [32], [33].

#### **B. PTE versus PDL Tradeoffs**

Table I summarizes the results of the design procedure in Fig. 3 for three sets of two-coil inductive links optimized based on the PTE, PDL, and FoM. It can be seen that the link optimized based on the FoM has not only achieved 1.47 times more PTE than the one optimized for PDL (71.3% versus 48.5%) but also provided 16 times larger PDL compared to the one optimized for PTE (180.4 mW versus 11.3 mW for  $V_s = 1$  V). To deliver 250 mW to the load with a class-E PA, driving voltages of  $V_s = 1.18$  V, 4.7 V, and 1.03 V are needed for FoM-, PTE-, and PDL-optimized links, creating peak voltages of 4.2 V, 16.7 V, and 3.7 V across the PA transistor, respectively [31].

These results clearly show the disadvantages of the links with low FoM. For instance, the PA for the PTE-optimized link cannot be implemented on-chip in most standard CMOS fabrication processes due to their lower transistor breakdown voltages. On the other hand, the PDL-optimized link, which relaxes the driver design, suffers from low PTE. It should be noted that the FoM-optimized link achieves its high ratings at the cost of only 24% drop in the PTE and PDL compared to the PTE- and PDL-optimized links. This was slightly less than the estimated 25% theoretical level in Fig. 2 due to our WWC fabrication constrains. Hence, unless an application requires very small amount of power, an inductive link that is designed based on the proposed FoM would be preferable by simultaneously providing high PTE and high PDL.

#### C. Coupling Distance Variations

In applications such as IMDs,  $d_{23}$  varies in a wide range, and the inductive link should provide high PTE and PDL for the entire range [34], [35]. Severe misalignments also have a similar effect as increasing  $d_{23}$  by reducing  $k_{23}$ . Figs. 4(a) and (b) show the PTE, PDL, and FoM of the links specified in Table I versus  $d_{23}$ , respectively. Two important points to be made in these curves are: 1) The PTE and PDL of the PDL-optimized link are significantly reduced at large coupling distances,  $d_{23} > 13$  mm, while the FoM-optimized link provides both large PTE and PDL, and consequently high FoM, over an extended range. 2) Within smaller coupling distances,  $d_{23} < 10$  mm, the FoM-optimized link has achieved high PTE

and PDL, while the PTE-optimized link suffers from very small PDL. Comparing the FoM of the three sets of links in Fig. 4(b) shows that the link that is optimized based on the proposed FoM maintains the highest performance for a widest range of coupling variations.

#### **D. Source Resistance Effects**

The driver output resistance,  $R_s$ , plays an important role in the optimization of the inductive link from the energy source to the load because  $R_s$  significantly affects the available power that the PA can deliver through the inductive link [16]. To study the effects of  $R_s$  on the twocoil inductive link design, we have optimized our IMD design example for  $R_s$  values from 0.1  $\Omega$  to 5  $\Omega$ . Fig. 5(a) shows that the FoM-optimized link has achieved high PTE and PDL particularly when  $R_s$  is small (i.e., when large PDL is needed). For instance, at  $R_s = 0.1 \Omega$ , the FoM-optimized link has achieved 53 times larger PDL than the PTE-optimized link and only 24.5% less PDL than the PDL-optimized link. On the other hand, the 71.6% PTE of the FoM-optimized link is almost half between the other two designs. As a result, the FoMoptimized link for the same amount of PDL. Moreover, Fig. 5(b) shows how the FoM values are higher for the FoM-optimized link versus  $R_s$ . For large  $R_s$ , which translate to low PDL, the PTE is considered more important, and both PDL and FoM are small for all three links. Therefore, the link designed based on PTE is optimal.

#### E. Series versus Parallel Connection of the Load Resistance

Inductive power transmission covers applications with a wide range of power requirements. A common question for designers is whether the secondary loop should be connected in parallel, similar to Fig. 1, or in series (as in Fig. 7). In [28], the loaded quality factor of the secondary loop,  $Q_{3L}$ , which only depends on the  $L_3$  geometry and  $R_L$ , has been used to differentiate between parallel and series load connections. In [36], however, it is shown that the series or parallel load connection also depends on  $k_{23}$  and whether the link should be optimized for the PTE or PDL. Overall, the indication between whether the secondary loop should be connected in series or in parallel is not very clear.

The proposed FoM can clearly determine the superior secondary loop topology, while considering both PTE and PDL, which are already linked to  $k_{23}$  and coils geometries. We optimized our IMD design example based on the FoM for different  $R_L$  and  $R_s$  values for parallel and series secondary configurations and depicted the results in Fig. 6(a) and (b), respectively. All equations in Section II are applicable for the series topology, except for  $Q_L$ , which should be defined as  $\omega_0 L_3/R_L$ . It can be seen that for  $R_L < 10 \Omega$ , the series connection results in higher FoM, almost regardless of  $R_s$ , while for larger  $R_L$ , the parallel connection is superior. Therefore, for the IMDs, in which  $R_L$  is often larger than 10  $\Omega$ , the parallel secondary configuration is preferred.

#### IV. Multi-Coil Inductive Power Transmission Links

Multi-coil inductive power transmission in the form of three-and four-coil links has been proposed based on the coupled mode theory to achieve high PTE particularly at large distances [26], [37]. We have also presented a comprehensive circuit-based analysis of

multi-coil links in [16] and [38]. However, the optimal choice between the two-, three-, and four-coil links for a particular application is yet another source of confusion for designers, which can be addressed by our proposed FoM.

In an *m*-coil link, the reflected load from the (i + 1)th coil to the *i*th coil can be found from [16],

$$R_{\text{ref}i,i+1} = k_{i,i+1}^2 \omega L_i Q_{(i+1)L}, i=1,2,\dots,m-1 \quad (12)$$

where  $k_{i,i+1}$  is the coupling coefficient between the *i*th and (i + 1)th coils.  $Q_{(i+1)L}$  is the loaded quality factor of the (i + 1)th coil, which can be found from

$$Q_{iL} = \frac{\omega L_i}{R_i + R_{\text{ref},i+1}} = \frac{Q_i}{1 + k_{i,i+1}^2 Q_i Q_{(i+1)L}}, i = 1, 2, \dots, m-1 \quad (13)$$

where  $Q_i = \omega L_i/R_i$  and  $R_i$  are the unloaded quality factor and parasitic series resistance of the *i*th coil ( $L_i$ ), respectively. It should be noted that for the last coil, which is connected to the load in series,  $Q_{mL} = \omega L_m/(R_m + R_L)$  and for the first coil, which is connected to the source in series,  $R_s$  should be added to  $R_1$ .

Assuming that the coupling between non-neighboring coils is negligible, the partial PTE from the *i*th coil to (i + 1)th coil can be written as

$$\eta_{i,i+1} = \frac{R_{\text{ref}i,i+1}}{R_i + R_{\text{ref}i,i+1}} = \frac{k_{i,i+1}^2 Q_i Q_{(i+1)L}}{1 + k_{i,i+1}^2 Q_i Q_{(i+1)L}}.$$
 (14)

Using (12)–(14), the overall PTE in such a multi-coil inductive link can be found from

$$\eta_{m-\text{coil}} = \prod_{i=1}^{m-1} \eta_{i,i+1} \cdot \frac{Q_{mL}}{Q_L}.$$
 (15)

PDL can be calculated by multiplying the power provided by the source, i.e.,  $V_s^2/2(R_s+R_1+R_{ref1.2})$ , by the PTE from (15)

$$P_{L,m-\text{coil}} = \frac{V_s^2}{2(R_s + R_1 + R_{\text{rcf1},2})} \eta_{m-\text{coil}} \quad (16)$$

#### A. Three-Coil Inductive Power Transmission Link

The three-coil inductive power transmission link, shown in Fig. 7, can be used to achieve high PTE and PDL by adjusting  $k_{34}$  to transform any arbitrary  $R_L$  into the optimal resistance required in the  $L_2 - L_3$  inductive link [16]. The PTE of this circuit can be calculated by reflecting the resistive components of each loop from the load back toward the primary coil loop, one stage at a time, using (12), and calculating the percentage of the power that is delivered from one stage to the next, using (14), until it reaches  $R_L$ . According to (15), this leads to [16]

$$\eta_{3-\text{coil}} = \frac{(k_{23}^2 Q_2 Q_3)(k_{34}^2 Q_3 Q_{4L})}{\left[(1+k_{23}^2 Q_2 Q_3 + k_{34}^2 Q_3 Q_{4L})(1+k_{34}^2 Q_3 Q_{4L})\right]} \cdot \frac{Q_{4L}}{Q_L} \quad (17)$$

where  $k_{24}$  has been ignored due to the large separation between  $L_2$  and  $L_4$ . One can also calculate the PDL of this circuit using (12)–(16)

$$P_{L,3-\text{coil}} = \frac{V_s^2}{2(R_2 + R_s)} \cdot \frac{(k_{23}^2 Q_2 Q_3)(k_{34}^2 Q_3 Q_{4L})}{(1 + k_{23}^2 Q_2 Q_3 + k_{34}^2 Q_3 Q_{4L})^2} \cdot \frac{Q_{4L}}{Q_L}.$$
 (18)

It should be noted that in (17) and (18), the driver output resistance should be considered in  $Q_2 = \omega L_2/(R_2 + R_s)$  [16].

The FoM for a three-coil inductive link can be found by substituting (17) and (18) in (4)

$$FoM_{3-\text{coil}} = \frac{1}{2(R_2 + R_s)} \times \frac{(k_{23}^2 Q_2 Q_3)^{n+1} (k_{34}^2 Q_3 Q_{4L})^{n+1}}{\left(1 + k_{23}^2 Q_2 Q_3 + k_{34}^2 Q_3 Q_{4L}\right)^{n+2} \left(1 + k_{34}^2 Q_3 Q_{4L}\right)^n} \cdot \left(\frac{Q_{4L}}{Q_L}\right)^{n+1}.$$
 (19)

It will be shown that, similar to two-coil links, the PTE and PDL losses in three-coil links follow Fig. 2 and (9), when optimized for the FoM in (4) and (19).

The PTE profile of the three-coil link according to (17) is a monotonically decreasing function of the coils' coupling distance,  $d_{23}$  [16]. However, the PDL and FoM profiles are maximized at a particular  $d_{23}$  that can be found by calculating the derivatives of (18) and (19) versus  $k_{23}$ , respectively

$$k_{23,\text{PDL}} = \left(\frac{1 + k_{34}^2 Q_3 Q_{4L}}{Q_2 Q_3}\right)^{\frac{1}{2}} k_{23,\text{FoM}} = \left[\frac{(n+1)(1 + k_{34}^2 Q_3 Q_{4L})}{Q_2 Q_3}\right]^{\frac{1}{2}}.$$
 (20)

By substituting  $k_{23,FoM}$  in (17) and (18), one can find the PTE and PDL of the three-coil link when it is designed to maximize the FoM at that particular  $d_{23}$ 

$$\eta_{3-\text{coil},\text{FoM}} = \frac{n+1}{n+2} \frac{k_{34}^2 Q_3 Q_{4L}}{1+k_{34}^2 Q_3 Q_{4L}} \cdot \frac{Q_{4L}}{Q_L} P_{L,3-\text{coil},\text{FoM}} = \frac{V_s^2}{2R_2} \frac{n+1}{(n+2)^2} \frac{k_{34}^2 Q_3 Q_{4L}}{1+k_{34}^2 Q_3 Q_{4L}} \cdot \frac{Q_{4L}}{Q_L} \cdot (21)$$

Additional constraints on the coils' geometries in order to satisfy the application or fabrication requirements may result in further reductions in the PTE and/or PDL. The maximum achievable PTE and PDL at a particular  $d_{23}$  can be calculated from (21) by substituting *n* with  $\infty$  and 0, respectively

$$\eta_{3-\text{coil,max}} = \frac{k_{34}^2 Q_3 Q_{4L}}{1+k_{34}^2 Q_3 Q_{4L}} \cdot \frac{Q_{4L}}{Q_L}, \ P_{L,3-\text{coil,max}} = \frac{V_s^2}{8(R_s+R_2)} \cdot \frac{k_{34}^2 Q_3 Q_{4L}}{1+k_{34}^2 Q_3 Q_{4L}} \cdot \frac{Q_{4L}}{Q_L}.$$
(22)

Using (21) and (22), we can calculate the losses in PTE and PDL when the link is designed based on the optimal FoM

$$\eta_{\text{Loss,3-coil}} = \frac{\eta_{3-\text{coil,max}} - \eta_{3-\text{coil,FoM}}}{\eta_{3-\text{coil,max}}} = \frac{1}{n+2} P_{L,\text{Loss,3-coil}} = \frac{P_{L,3-\text{coil,max}} - P_{L,3-\text{coil,FoM}}}{P_{L,3-\text{coil,max}}} = \frac{n^2}{(n+2)^2}.$$
 (23)

The results are exactly the same as those found in (9) for the two-coil links. It should be noted that in comparison to two-coil links, the three-coil inductive links have an additional degree of freedom owing to  $k_{34}$ , which allows them to transform any arbitrary  $R_L$  to the optimal values for FoM, PTE, and PDL in (10) and (11). Two-coil links fail to achieve the optimal  $Q_L$ s in (10) and (11), which become quite large when  $k_{23}$  drops as a result of large  $d_{23}$  or severe misalignments [16].

#### **B. Four-Coil Inductive Power Transmission Links**

Fig. 8 shows a four-coil inductive power transmission link, which is capable of achieving a high PTE by adjusting  $k_{12}$  when there is a large  $R_s$ , imposed by a weak driver. In four-coil inductive links, there is a tradeoff between the highest PTE that can be achieved at short distances while maintaining sufficient PDL at large distances [16]. The PTE of the four-coil link can be calculated using (12)–(15)

$$\eta_{4-\text{coil}} = \frac{(k_{12}^2 Q_1 Q_2)(k_{23}^2 Q_2 Q_3)(k_{34}^2 Q_3 Q_{4L})}{(B+k_{23}^2 Q_2 Q_3)(A+k_{23}^2 Q_2 Q_3)} \cdot \frac{Q_{4L}}{Q_{L}} \quad (24)$$

$$A = 1 + k_{34}^2 Q_3 Q_{4L} B = (1 + k_{12}^2 Q_1 Q_2) \cdot (1 + k_{34}^2 Q_3 Q_{4L})$$
(25)

where  $k_{13}$ ,  $k_{14}$ , and  $k_{24}$  are ignored in comparison to  $k_{12}$ ,  $k_{23}$ , and  $k_{34}$ . Using (14)–(16), PDL of the four-coil inductive links can be found from

$$P_{L,4-\text{coil}} = \frac{V_s^2}{2(R_1 + R_s)} \frac{(k_{12}^2 Q_1 Q_2)(k_{23}^2 Q_2 Q_3)(k_{34}^2 Q_3 Q_{4L})}{(B + k_{23}^2 Q_2 Q_3)^2} \cdot \frac{Q_{4L}}{Q_L}.$$
 (26)

It should be noted that in (24)–(26), the driver output resistance should be considered in  $Q_1 = \omega L_1/(R_1 + R_s)$ .

The four-coil FoM can be calculated by substituting (24) and (26) in (4)

$$FoM_{4-\text{coil}} = \frac{1}{2(R_1 + R_s)} \times \frac{\left[(k_{12}^2 Q_1 Q_2)(k_{23}^2 Q_2 Q_3)(k_{34}^2 Q_3 Q_{4L})\right]^{n+2}}{(B + k_{23}^2 Q_2 Q_3)^{n+2} (A + k_{23}^2 Q_2 Q_3)^n} \cdot \left(\frac{Q_{4L}}{Q_L}\right)^{n+2}.$$
 (27)

$$k_{23,\text{PTE}} = \left(\frac{\sqrt{AB}}{Q_2 Q_3}\right)^{\frac{1}{2}}, \\ k_{23,\text{PDL}} = \left(\frac{B}{Q_2 Q_3}\right)^{\frac{1}{2}}, \\ k_{23,\text{FoM}} = \left[\frac{1}{2Q_2 Q_3(n+1)}\left(B - A + \sqrt{A^2 + B^2 + 4AB(n^2 + 2n+1/2)}\right)\right]^{\frac{1}{2}}$$
(28)

The four-coil PTE, PDL, and FoM profiles are maximized at a particular  $d_{23}$  that can be found by calculating the derivatives of (24), (25), and (27) with respect to  $k_{23}$ , respectively, as shown by (28) at the bottom of the page.

The optimal  $k_{23,\text{FoM}}$  in (28) is too complicated to be used for calculating the PTE and PDL losses as a result of FoM-based optimization. Therefore, we apply some approximation. In four-coil links, we know that  $k_{12}$  should be large in order to achieve a high PTE between  $L_1$  and  $L_2(\eta_{12} \approx 1)$ , particularly at short coupling distances where  $L_2$  is strongly loaded by  $L_3$  [16]. Assuming  $k_{12}^2 Q_1 Q_2 \gg 1$ , we can conclude that in (25),  $B \gg A$ . Therefore,  $k_{23,\text{FoM}}$  in (28) can be further simplified to

$$k_{23,\text{FoM}} = \left(\frac{B}{Q_2 Q_3(n+1)}\right)^{\frac{1}{2}}$$
. (29)

By substituting  $k_{23,FoM}$  in (24) and (26), and assuming  $B \gg A$ , one can find the PTE and PDL of the four-coil link when the FoM is maximized at a particular  $d_{23}$ 

$$\eta_{4-\text{coil},\text{FoM}} \approx \frac{(k_{12}^2 Q_1 Q_2)(k_{34}^2 Q_3 Q_{4L})}{[B+B/(n+1)]} \cdot \frac{Q_{4L}}{Q_L} P_{L,4-\text{coil},\text{FoM}} \approx \frac{V_s^2}{2(R_1+R_s)} \frac{(k_{12}^2 Q_1 Q_2) \left\lfloor \frac{B}{(n+1)} \right\rfloor (k_{34}^2 Q_3 Q_{4L})}{[B+B/(n+1)]^2} \cdot \frac{Q_{4L}}{Q_L}.$$
(30)

The maximum achievable PTE and PDL at a particular  $d_{23}$  are calculated by substituting *n* with  $\infty$  and 0, respectively

$$\eta_{4-\text{coil,max}} \approx \frac{(k_{12}^2 Q_1 Q_2)(k_{34}^2 Q_3 Q_{4L})}{B} \cdot \frac{Q_{4L}}{Q_L}, \ P_{L,4-\text{coil,max}} \approx \frac{V_s^2}{8(R_s+R_1)} \cdot \frac{(k_{12}^2 Q_1 Q_2)(k_{34}^2 Q_3 Q_{4L})}{B} \cdot \frac{Q_{4L}}{Q_L}.$$
(31)

Using (30) and (31), similar to the two- and three-coil links, we can calculate the losses in PTE and PDL when the four-coil link is designed based on the optimal FoM

$$\eta_{\text{Loss,4-coil}} = \frac{\eta_{4-\text{coil,max}} - \eta_{4-\text{coil,FoM}}}{\eta_{4-\text{coil,max}}} \approx \frac{1}{n+2}, \ P_{L,\text{Loss,4-coil}} = \frac{P_{L,4-\text{coil,max}} - P_{L,4-\text{coil,FoM}}}{P_{L,4-\text{coil,max}}} \approx \frac{n^2}{(n+2)^2}.$$
(32)

Interestingly, the results are consistent with those found in (9) and (23). Therefore, designers can use the PTE and PDL loss curves in Fig. 2 to find the best *n* for a given application.

#### C. Design Procedure for Multi-Coil Power Transmission Links Based on the Proposed FoM

In Section III, we presented an iterative optimization procedure for two-coil inductive links based on the new FoM, which has been extended for multi-coil links. The relationship between the inductive link parameters, such as k and Q, and the WWC geometries can be found in the Appendix.

In step-1 of Fig. 9 flowchart, the design constraints imposed by the application and coil fabrication technology are applied. Using (44) in the Appendix, the weight of the coils on the Tx side,  $W_t$ , is defined as the weight of  $L_2$  or the sum of the weights of  $L_1$  and  $L_2$  for three- and four-coil links, respectively. The weight of the Rx coils,  $W_r$ , also equals the sum of the weights of  $L_3$  and  $L_4$  for both three- and four-coil links. The nominal values for  $D_{o3}$ ,  $d_{12}$ ,  $d_{23}$ ,  $d_{34}$ ,  $R_L$ , and  $R_s$  are also required in this step, which are defined by the application  $(d_{12}$  is needed only for the four-coil link).  $D_{o3}$  is often chosen to be the maximum allowed

by the application, particularly in IMDs. The designer should also limit the coils' wire diameters,  $w_{1,\text{max}} \sim w_{4,\text{max}}$ , to account for the overall thickness of the inductors in the given application. The minimum wire spacing between the surface of the conductors,  $s_1 \sim s_4$ , is considered twice the thickness of the wire insulation in all WWCs [23].

In step-2, the initial values for  $L_1 \sim L_4$  geometries namely  $w_1 \sim w_4$ ,  $n_1 \sim n_4$ ,  $D_{o1}$ ,  $D_{o2}$ , and  $D_{o4}$  are chosen. In step-3,  $L_2 - L_3$  link is optimized using the design procedure shown in Fig. 3, with (19) and (27) used instead of (5) for three- and four-coil links, respectively. This step provides the interim optimal values for  $w_2$ ,  $w_3$ ,  $n_2$ ,  $n_3$ , and  $D_{o2}$  based on the initial values for  $L_1$  and  $L_4$  geometries.

 $L_4$  geometry is optimized in step-4 by sweeping  $n_4$  and  $D_{o4}$ , followed by sweeping  $n_4$  and  $w_4$  using (19) and (27) for three-and four-coil links, respectively. In each step, two 3-D surfaces for FoM and  $W_r$  are obtained while  $n_4$  and  $D_{o4}$  or  $n_4$  and  $w_4$  values that result in the maximum FoM and  $W_r < W_{r,max}$  are chosen. Steps 4.1 and 4.2 are repeated iteratively until  $D_{o4}$  and  $w_4$  change less than 0.1% and satisfy the condition in step-4.3.

In step-5, key parameters in the geometry of  $L_2$ , i.e.,  $n_2$  and  $D_{o2}$ , are swept to maximize the FoM in (19) and (27) for three- and four-coil links, respectively, while maintaining  $W_t < W_{t,max}$ . Step-5 also verifies whether the  $L_4$  geometry resulted from step-4 is optimal. This is because  $L_4$  was optimized in step-4 with respect to  $L_3$  to achieve the desired  $k_{34}$  while  $L_2 - L_3$  link had been optimized in step-3 with non-optimized  $L_1$  and  $L_4$  geometries. Steps-3 and 4 are repeated iteratively until  $n_2$  and  $D_{o2}$  change less than 0.1%, and satisfy the condition in step-4.3. This step concludes the three-coil link design for maximum FoM, which can be further validated using field solvers, such as HFSS (Ansoft, Pittsburgh, PA).

The rest of steps in Fig. 9 are specific to four-coil links to optimize the geometry of  $L_1$  for the highest FoM based on (27). Step-6 is similar to step-4, which determines the optimal geometry of  $L_1$ . However, step-7 verifies if the  $L_2$  geometry is still optimal for the new  $L_1$ geometry from step-6. Therefore,  $D_{o1}$  and  $D_{o2}$  are swept in step-7.1 to maximize FoM in (27) while keeping  $W_t < W_{t,max}$ . The new  $D_{o1}$  along with  $n_1$  and  $w_1$  from step-6.2 are used to start a new iterative optimization loop from step-3, as long as  $D_{o1}$  and  $D_{o2}$  change more than 0.1% in step-7.2. The four-coil link is designed for maximum FoM if the condition in step-7.2 is satisfied.

#### V. Inductive Power Transmission Links for Industrial Applications

Recharging HMDs and EVs need high PDLs in the order of watts to kilowatts. In this section, we have provided design examples for industrial applications to differentiate between the two-, three-, and four-coil links based on our proposed FoM.

#### A. Wireless Charging of HMDs

Wireless chargers for transferring energy over several tens of cm to charge HMDs or operate small home appliances are now commercially available [39]. Here, we have designed three sets of inductive links for HMD charging application with two, three, and four WWCs. These links were designed based on our proposed FoM optimization procedure in Fig. 9, and

the results are summarized in Table II. These links operate at 13.56 MHz and deliver 1.6 W to an  $R_L = 5 \Omega$  from a nominal coupling distance of  $d_{23} = 10$  cm using a PA with  $R_s = 0.5 \Omega$ . The maximum wire diameters of  $L_3$  and  $L_4$ ,  $w_{3,max}$  and  $w_{4,max}$ , were considered to be 0.64 mm (AWG22), while  $L_1$  and  $L_2$  maximum wire diameters,  $w_{1,max}$  and  $w_{2,max}$ , were limited to 1.6 mm (AWG14) to reduce the Tx and Rx size and thickness. Furthermore, in order to shrink the size of the Rx module,  $D_{o3}$  was limited to 4 cm, and  $L_4$  in the three- and four-coil links was placed in the middle of  $L_3$ . Similarly,  $L_1$  in the four-coil link was placed in the module of  $L_2$ . It should be noted that according to Section III-E, the secondary loop for the two-coil link was connected in series because of the small  $R_L$  value.

Fig. 10(a) shows the setup, proposed in [16], to accurately measure the PTE and PDL of a multi-coil inductive link. The FoMs are then calculated from the measured PTE and PDL values using (4) with n = 2. In this method, resonance capacitors,  $R_s$ , and  $R_L$  in Figs. 1(a), 7, and 8 for two-, three-, and four-coil links, respectively, are all connected to the coils, which are collectively considered a two-port system along with the multi-coil inductive link. The network analyzer is then used to measure the S-parameters and consequently the Z-parameters [40]. The PTE and PDL are found from the two-port equations

$$\text{PTE} = \frac{|Z_{41}|^2}{R_L |Z_{11}| \cos(\angle Z_{11})}, \text{ PDL} = \frac{|V_4|^2}{2R_L} = \frac{|V_s|^2}{2R_L |Z_{11}|^2} |Z_{41}|^2 \quad (33)$$

where  $Z_{11} = V_1/I_1$  and  $Z_{41} = V_4/I_1$  are measured when  $I_4 = 0$ , with the acceptable assumption that the network analyzer loading of the inductive link is negligible compared to  $R_L$ .

Fig. 10(b) shows the experimental setup for measuring the PTE and PDL in a four-coil inductive link. These coils were fabricated based on the values listed in Table II and held in parallel and perfectly aligned using Plexiglas supports.  $L_1$  and  $L_4$  were placed in the middle of  $L_2$  and  $L_3$  in a co-planar and co-centric fashion to achieve  $k_{12} = 0.03$  and  $k_{34} = 0.092$ , respectively, in the four-coil link. In the three-coil link,  $k_{34} = 0.088$  when  $L_4$  is placed in the middle of  $L_3$ .

Fig. 11(a) and (b) show the differences between calculated and measured values of the PTE and FoM, respectively, for the two-, three-, and four-coil links. It can be seen in Fig. 11(a) that the four-coil link PTE (42.8%) is higher than the two- and three-coil link PTEs (28.4% and 36.1%, respectively) for  $d_{23} \ge 9.5$  cm. According to Fig. 11(b), the four-coil link has also achieved higher FoM (20.9 m $\Omega^{-1}$ ) compared to its two- and three-coil counterparts (6.4 m $\Omega^{-1}$  and 12 m $\Omega^{-1}$ , respectively) for  $d_{23} \ge 7.5$  cm. However, this superior FoM at  $d_{23} = 10$  cm, which is the nominal distance for this design, comes at the cost of much lower PTE, and consequently the FoM, at shorter coupling distances, as shown in Fig. 11(a) and (b). This is because the PDL of the four-coil link has been deliberately increased at  $d_{23} = 10$  cm by reducing  $k_{12}$  (by reducing  $D_{o1}$ ) to maximize the FoM. The PTE and FoM drop at short distances confirm our conclusion in [16] that there is an inherent tradeoff between achieving high PDL at larger distances with a high PTE at smaller distances in four-coil links.

However, the three-coil link has achieved both high PTE and FoM at both large and small distances. Based on the PDL values in Table II for  $V_s = 1$  V, to deliver 1.6 W to the load

with a class-E PA, the two-, three-, and four-coil links need  $V_s = 4.5$  V, 4.2 V, and 3.7 V and peak voltages across the PA transistor of 16 V, 15 V, and 13.2 V, respectively. These voltage levels are reasonable thanks to the use of FoM. However, they could be much larger if PTE had been optimized instead.

Small discrepancies between the calculated and measured PTEs are due to the measurement non-idealities and limited Q of the tuning capacitors, which have been considered ideal in our models. The capacitors' measured Q was < 5000 at 13.56 MHz, which can reduce the unloaded  $Q_3$  in three-coil and  $Q_2$  and  $Q_3$  in four-coil links by 10%. The FoM curves show more error because the PTE and PDL errors accumulate in the  $FoM = \eta^2 \times P_L(V_s = 1 \text{ V})$ .

#### B. Wireless Charging of EVs

We have designed two-, three-, and four-coil inductive links, using WWCs, for the EV charging application based on the proposed FoM guideline. Table III summarizes the results of the optimization procedure in Fig. 9. These links operate at 145 kHz and deliver 3.3 kW to an  $R_L = 20 \Omega$  from a nominal coupling distance of  $d_{23} = 20$  cm using a PA driver with 0.5  $\Omega$  output resistance. The thicknesses of the Tx and Rx circular-shaped WWCs, i.e.,  $w_{1,max}$ ,  $w_{2,max}$ ,  $w_{3,max}$ , and  $w_{4,max}$ , were limited to 5 cm to reduce the size of the charger inductors.  $L_3$  diameter,  $D_{o3}$ , was also limited to 50 cm to easily fit under different vehicles. Furthermore,  $L_4$  in the three- and four-coil links and  $L_1$  in the four-coil link were placed in the middle of  $L_3$  and  $L_2$ , respectively, leading to  $d_{34} = d_{12} = 0$  mm. The secondary loop for the two-coil link was connected in series to achieve higher FoM, based on our calculations.

Fig. 12 shows the comparison between the calculated PTE and FoM values in two-, three-, and four-coil inductive links. It can be seen that at  $d_{23} = 20$  cm, the three-coil link has achieved larger FoM (81.3 m $\Omega^{-1}$ ) compared to its four- and two-coil counterparts (72.6 m $\Omega^{-1}$  and 65.3 m $\Omega^{-1}$ , respectively). According to Fig. 12, the four-coil link not only achieves lower PTE and FoM than the three-coil link at  $d_{23} = 20$  cm but also shows inferior performance at smaller  $d_{23}$ . The two-coil link performance is comparable to three-coil and even better in terms of PTE at lower  $d_{23}$  because  $L_2$  and  $L_3$  are strongly coupled ( $k_{23} = 0.2$ ) and achieving  $Q_{LFOM}$  in (10) for the two-coil link is feasible.

In a class-C PA, optimal design allows for high-power efficiency with peak voltages across the primary coil and the PA transistor that are close to  $V_{DD}$  and  $2V_{DD}$ , respectively [31]. Based on the PDL values in Table III for  $V_s = 1$  V, in order to deliver 3.3 kW to the load with a class-C PA, the two-, three-, and four-coil links will need  $V_s = 137$  V, 140 V, and 142 V, resulting in 274 V, 280 V, and 284 V peak voltages across the PA transistor, respectively. It should be noted that these voltage levels would have been much larger, and therefore impractical, if the links had been optimized based on the PTE.

Metallic objects around the inductive link, particularly in EV applications, can affect the inductive link performance. The geometries and relative positioning of such metallic objects can be entered in field solvers, such as HFSS, to account for phenomena such as the eddy current. Furthermore, ferrite cores can be used to increase the mutual coupling between the coils and potentially improve the FoM. However, modeling, design, and optimization of the ferrite cores are out of the scope of this paper [44].

#### VI. Discussion

There are a number of parameters that can guide designers toward the optimal inductive power transmission link for their specific application. The amount of PDL ( $P_L$ ), driver output resistance ( $R_s$ ), source voltage ( $V_s$ ), nominal coupling distance ( $d_{23}$ ), and the range of coupling distance variations or coil misalignments are among important parameters in addition to the application-based limitations on the coils geometries. The optimization procedure can be made much simpler if it can be shaped around a single measure that would best fit the application. PTE, which has been dominantly used in the past, is a good measure when  $P_L$  is small or  $R_s$  is large. However, delivering sufficient PDL becomes challenging when there are stringent limitations on the PA driver. Hence, for the majority of applications, in which achieving high PTE and PDL are both important, our proposed FoM would be a better choice.

Another challenge for designers is how to choose between different topologies, i.e., two-, three-, and four-coil links, for their given application. As demonstrated, three examples across a wide range of power levels, most often the proposed FoM can clearly show the topology that fits the best. If two different topologies achieve similar or close FoMs, one can change the parameter *n* in (4) to give a heavier weight to PTE (higher) or PDL (lower). Our recommendation is going for a higher PTE because the necessary PDL can be achieved by increasing  $V_s$ , as long as the PA transistors can handle the higher voltage levels. According to earlier design examples, the two-, three-, and four-coil links are suitable for these conditions: 1) Two-coil links are chosen when the coils are strongly coupled, and a large PDL is needed, i.e.,  $R_s$  is small. This is because the optimal  $Q_L$ s for FoM, PTE, and PDL in (10) and (11) are easier to achieve with a large  $k_{23}$ . Another justification for using a two-coil link is extreme size constraints. 2) Three-coil links are chosen when the coils are loosely coupled, the coupling distance varies in a wide range, and a large PDL is needed. 3) Finally, four-coil links are optimal when the PTE is paramount, the load power consumption is very small, the coils are loosely coupled, and their relative distance and alignment are stable.

In high-power applications,  $R_s$  mainly represents the large signal resistance of the driver, which would change with the voltage swing around the driver transistor, temperature, and frequency of operation. Therefore, the largest possible  $R_s$  was chosen for the inductive link design to achieve the highest FoM in the worst case condition.  $R_s$  variations change the PDL according to (3), (18), and (26) for two-, three-, and four-coil inductive links, respectively, and if necessary, can be compensated by a closed-loop power transmission mechanism [34].

The additional degree of freedom provided by  $k_{34}$  in three-coil links and  $k_{34}$  and  $k_{12}$  in fourcoil links allows designers to increase the loaded quality factor of  $L_3(Q_{3L})$  and  $L_2(Q_{2L})$  in four-coil links). This leads to higher sensitivity to carrier frequency variations or detuning in multi-coil links, resulting in considerable degradation in both PTE and PDL. There is little concern about detuning of  $L_4$  and  $L_1$  because these coils have very small loaded quality factors and therefore are much less sensitive to frequency shifts.

#### VII. Conclusion

A new FoM has been proposed to design high-performance inductive power transmission links. We have demonstrated the tradeoffs between maximizing the PTE and PDL, simultaneously, to help designers choose the best measure for a particular application. It is even possible to tune the FoM to give more weight to PTE over PDL or vice versa. We have proposed a design procedure based on the new FoM for two-coil links and extended it to multi-coil arrangements for designing state-of-the-art inductive power transmission links. Three design examples, ranging from milliwatt to kilowatt power levels, were presented to show the usage and efficacy of the proposed FoM, which can also differentiate between two-, three-, and four-coil inductive links for a given application. The FoM guides designers to choose two-coil links for strongly coupled coils used in applications that need large PDL, three-coil links for loosely coupled coils where the coupling distance varies considerably, and four-coil links when small PDL is required at high PTE and the coils are loosely coupled but have a stable coupling distance and alignment.

#### Acknowledgments

The authors would like to thank Uei-Ming Jow for his help with the modeling and measurements.

This work was supported in part by the National Institutes of Health under Grant NIBIB-1R21EB009437-01A1 and Grant NINDS-1R01NS062031-01A1, and in part by National Science Foundation Award ECCS-824199.

#### Appendix

WWCs are often modeled as distributed RLC networks with a self-inductance in series with a resistance, both of which are in parallel with the coil parasitic capacitance [41]. An analytical expression for the self-inductance of a one turn circular conductive loop can be found from [41]

$$L_i = 0.5\mu_0\mu_r D_o \ln\left(\frac{D_o}{w}\right) \quad (34)$$

where  $\mu_0$  and  $\mu_r$  are the permeability of space and of the conductor, respectively.  $D_o$  is the diameter of the loop, and w is the diameter of the wire. For mutual inductance, M, a WWC can be considered a set of concentric single-turn loops with various diameters, all connected in series. Using Maxwell equations,  $M_{ij}$  between a pair of parallel single-turn circular loops at radii  $r_i = D_i/2$  and  $r_j = D_j/2$  can be found from [23]

$$M_{ij} = \frac{2\mu}{\alpha} \sqrt{r_i \cdot r_j} \left[ \left( 1 - \frac{\alpha^2}{2} \right) K(\alpha) - E(\alpha) \right]$$
(35)

where

$$\alpha{=}2\sqrt{\frac{r_i\cdot r_j}{(r_i{+}r_j)^2{+}d_{ij}^2}} \quad (36)$$

In this equation,  $d_{ij}$  is the coupling distance between the two coils and  $i \neq j$ .  $K(\alpha)$  and  $E(\alpha)$  are the complete elliptic integrals of the first and second kind, respectively [23]. Therefore, the mutual inductance between two coils with  $n_i$  and  $n_j$  number of turns can be found from

$$M = \sum_{k=1}^{n_i} \sum_{l=1}^{n_j} M_{k,l}.$$
 (37)

Hence, the self-inductance of a WWC can be found from

$$L_{\text{self}} = \sum_{i=1}^{n_i} L_i + \sum_{k=1}^{n_i} \sum_{j=1}^{n_i} M_{k,j} \text{ for } k \neq j. \quad (38)$$

To model the series parasitic resistance of the WWC, its dc resistance should be calculated from

$$R_{DC} = \frac{\rho_c}{\pi(\frac{w}{2})^2} \cdot \pi [n_i D_o - (n_i - 1)(w + s)] \quad (39)$$

where  $\rho_c$  is the resistivity of the conductive material, *s* is the wire spacing between the surface of the conductors, and  $n_i$  is *i*th coil total number of turns. As the operating frequency increases, the skin effect increases the series resistance, which can be modeled as [43]

$$R_{_S} \cong R_{_{DC}} \cdot \frac{w^2}{4\delta \cdot (w-\delta)} \quad (40)$$

where

$$\delta = \sqrt{\frac{\rho_c}{\pi \cdot \mu \cdot f}} \quad (41)$$

When the wire spacing, s, is smaller than the wire diameter, w, the ac resistance of the coil in (40) increases due to the proximity effect at higher frequencies [45].

The parasitic capacitance between two turns of a WWC can be found from [41]

$$C_{\text{turn}} = \varepsilon_0 \varepsilon_r \int_0^{\frac{w_r}{2}} \frac{\pi \cdot D_o \cdot w/2}{\varsigma + (\frac{w}{2})\varepsilon_r (1 - \cos\theta) + 0.5\varepsilon_r \cdot s'} d\theta \quad (42)$$

where  $\varepsilon_r$  is the relative permittivity of the insulation material,  $\varsigma$  is the thickness of the insulation layer,  $\theta_e$  is the effective angle between turn *i* and turn *j*, i.e., 90°, and *s'* is the wire spacing between the surface of the insulation layers. Hence, the sum of the turn-to-turn parasitic capacitance from (42) leads to the total parasitic capacitance,  $C_P$ .

Finally, considering  $R_S$  in series with  $L_{self}$  and  $C_P$  in parallel with both, the quality factor of a WWC at operating frequency,  $\omega_0$ , can be found from [23]

$$Q = \frac{\omega_0 L_{\text{sclf}} - \omega_0 \left( R_s^2 + \omega_0^2 L_{self}^2 \right) C_P}{R_s} \approx \frac{\omega_0 L_{\text{sclf}}}{R_s} \quad (43)$$

which is valid for low frequency and small  $C_P$ .

The weight of a circular-shaped WWC with  $n_i$  turns, wire spacing of *s* between the surface of the conductors, and wire diameter of *w* can be written as

$$W = \frac{\rho \pi^2 w^2}{4} [n_i D_o - (n_i - 1)(w + s)] \quad (44)$$

where  $\rho$  is the density of the conducting material, which is 8.96 g/cm<sup>3</sup> for copper. In (44), we have ignored the weight of the insulating material.

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#### Fig. 1.

(a) Lumped circuit model for a generic two-coil inductive power transmission link with the PA loss modeled as  $R_s$ . (b) Equivalent circuit at resonance showing the reflected load from the secondary loop onto the primary loop.



#### Fig. 2.

PTE and PDL losses in percentage with respect to their maximum possible values versus n when the two-coil link is designed to maximize the proposed FoM in (4) as opposed to the PTE or PDL. It can be seen that n = 2 results in similar PTE and PDL losses, equal to 25%. Similar curves can be derived for the three- and four-coil links.

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Fig. 3.

Iterative two-coil inductive link optimization flowchart.

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#### Fig. 4.

Comparison between three sets of two-coil links optimized for the PTE, PDL, and FoM versus coupling distance,  $d_{23}$ .  $R_s = 0.5 \Omega$  and  $R_L = 100 \Omega$ . (a) The calculated values of the PTE and PDL for  $V_s = 1$  V (b) The FoM values defined in (4) with n = 2. The inductive links' specifications for the nominal distance of  $d_{23} = 10$  mm are summarized in Table I.



#### Fig. 5.

Comparison between three sets of two-coil links optimized for PTE, PDL, and FoM versus the driver output resistance,  $R_s$ .  $R_L = 100 \Omega$ . (a) The calculated values of the PTE and PDL for  $V_s = 1$ V (b) The FoM values defined in (4) for n = 2. The inductive links' specifications for  $R_s = 0.5\Omega$  are summarized in Table I.



#### Fig. 6.

The FoM comparison for parallel and series load connection versus  $R_L$  and  $R_s$ : (a) Parallel load connection. (b) Series load connection. The inductive links specifications are summarized in Table I.

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Fig. 7.

Lumped circuit model of a three-coil inductive power transmission link with the PA loss modeled as  $R_s$ .



#### Fig. 8.

Lumped circuit model of the four-coil inductive power transmission link with the PA loss modeled as  $R_s$ .





Iterative multi-coil links optimization flowchart based on the proposed FoM.





#### Fig. 10.

(a) The PTE and PDL measurement setup using network analyzer with all the coils tuned at the carrier frequency, and both  $R_s$  and  $R_L$  connected in the setup, (b) Four-coil inductive link used to measure the PTE and PDL. Coil specifications are listed in Table II.



#### Fig. 11.

Comparison between measured and calculated values of the (a) PTE, and (b) our proposed FoM versus coupling distance,  $d_{23}$  for two-, three-, and four-coil inductive links specified in Table II for HMD applications ( $V_s = 1$  V).



#### Fig. 12.

Comparison between calculated values of PTE and FoM versus  $d_{23}$  for two-, three-, and four-coil inductive links for wireless EVs charging applications specified in Table III ( $V_s = 1$  V).

## **TABLE I**

Two-Coil Inductive Link Specifications Results From the PTE-, PDL-, and FoM-Based Optimizations

			Optim	ized Para	meter
	Parameters	Symbols	PTE	PDL	FoM
	Inductance (µH)	$L_2$	2.37	0.095	0.135
	Outer diameter (mm)	$D_{o2}$	36	13	23.2
	Wire diameter (mm)	2м	1.15	1.2	1.75
<b>F</b> 2	Num. of turns	<sup>2</sup> u	10	3	2
	Wire spacing (µm)	52		100	
	Quality facter*	$\mathcal{Q}_2$	177.5	15.9	22.5
	Inductance (µH)	$L_3$		0.024	
	Outer diameter (mm)	$D_{o3}$		10	
	Wire diameter (mm)	2м		0.51	
r3	Num. of turns	εu		1	
	Wire spacing (µm)	£S		100	
	Quality facter	$\mathcal{O}_3$		1248	
$L_{2}-L_{3}$	coupling distance (mm)	$d_{23}$		10	
Max. v	vire diam. of $L_2/L_3$ (mm)	W2/3,max	- / 0	.51 (AWO	324)
Ma	x weight of $L_2/L_3$ (g)	$W_{2/3,max}$		10 / 0.5	
Nomi	nal source resistance $(\Omega)$	$R_S$		0.5	
	Nominal load $(\Omega)$	$R_L$		100	
Powei	carrier frequancy (MHz)	$f_0$		13.56	
Powe	r transfer efficiency (%)	և	93.4	48.4	71.3
Power	delivered to load ** (mW)	$P_L$	11.3	236.7	180.4
Ë	gure-of-Merit $(m\Omega^{-1})$	FoM	9.8	55.6	91.8
$^*$ $Q2$ also	includes $R_S = 0.5 \Omega$ .				

\*\* Delivered power when  $V_S = 1$  V.

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## TABLE II

Multi-Coil Inductive Link Specifications for the FoM-Based Optimization for HMD Charging Application

	Parameters	Symbols	2-coil	3-coil	4-coil
	Inductance (µH)	$L_{I}$	ı		0.21
	Outer diameter (cm)	$D_{ol}$	-	-	3
	Wire diameter (mm)	$w_I$	-	-	1.6
17	Num. of turns	$^{I}u$	-	-	2
	Wire spacing (µm)	$S_I$	-	-	100
	Quality factor*	$\mathcal{Q}_{I}$	I	I	34.6
	Inductance (µH)	$L_2$	1.4	1.3	0.52
	Outer diameter (cm)	$D_{o2}$	12.5	11.7	15
	Wire diameter (mm)	$w_2$		1.6	
<b>L</b> 2	Num. of turns	$n_2$	2	2	1
	Wire spacing (µm)	$S_2$		100	
	Quality factor*	$\mathcal{Q}_2$	116.8	117.5	427
	Inductance (µH)	$L_{3}$	4.6	0.	13
	Outer diameter (cm)	$D_{o3}$		4	
1.5	Wire diameter (mm)	$W_{\mathcal{B}}$		0.64	
ĵ	Num. of turns	$n_3$	6		
	Wire spacing (µm)	$s_{3}$		100	
	Quality factor	$\mathcal{Q}_3$	222.8	55	3.3
	Inductance (nH)	$L_{4}$		40.8	42.4
	Outer diameter (cm)	$D_{o4}$		1.6	1.65
1.1	Wire diameter (mm)	$W_{4}$	-	0.0	54
4	Num. of turns	$n_4$	ı		
	Wire spacing (µm)	$S_{4}$		1(	0
	Quality factor	$\mathcal{Q}_4$	-	1092	1068

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Parameters	Symbols	2-coil	3-coil	4-coil
$L_2$ and $L_3$ coupling distance (cm)	$d_{23}$		10	
Max. wire diam. of Tx coils (mm)	W1/2,max	1.	6(AWG1	4)
Max. wire diam. of Rx coils (mm)	W3/4,max	0.6	64(AWG2	(2)
Nominal source resistance $(\Omega)$	$R_S$		0.5	
Nominal load $(\Omega)$	$R_L$		5	
Power carrier frequency (MHz)	$f_0$		13.56	
Power transfer efficiency (%)	և	28.4	36.1	42.8
Power delivery to load <sup>**</sup> (mW)	PDT	79	92	14.4
Figure-of-Merit $(m\Omega^{-1})$	$F_{OM}$	6.4	12	20.9

 $^*$  *QI* in 4-coil and *Q2* in 2- and 3-coil links also include  $R_S = 0.5 \Omega$ .

\*\* Delivered power when  $V_S = 1 \text{ V}$ 

Grayed rows indicate the design constraints.

# TABLE III

Multi-Coil Inductive Link Specifications for the FoM-Based Optimization for EV Charging Application

	Parameters	Symbols	2-coil	3-coil	4-coil
	Inductance (µH)	$L_{I}$	-	-	1.66
	Outer diameter (cm)	$D_{ol}$		-	14
	Wire diameter (mm)	IM		-	L'L
1-1	Num. of turns	lu	-	-	3
	Wire spacing (µm)	$I_S$	-	-	100
	Quality factor*	$\mathcal{Q}_{l}$	I	-	2.9
	Inductance (μH)	$L_2$	11.8	5.4	1.11
	Outer diameter (cm)	$D_{o2}$	75	62	34.3
	Wire diameter (mm)	$w_2$	45	50	50
<b>L</b> 2	Num. of turns	zu	4	3	2
	Wire spacing (µm)	$S_2$		100	
	Quality factor*	$\mathcal{Q}_2$	18.1	9.3	209.2
	Inductance (µH)	$L_{3}$	62.6	5	4.9
	Outer diameter (cm)	$D_{o3}$		50	
1.	Wire diameter (mm)	$w_{3}$	10.3	35	45
5	Num. of turns	$n_3$	11	3	4
	Wire spacing (µm)	$s_3$		100	
	Quality factor	$\mathcal{Q}_3$	39	138.9	116
	Inductance (µH)	$L_4$	-	3.97	6.3
	Outer diameter (cm)	$D_{o4}$	-	45	34
1.1	Wire diameter (mm)	$W_{4}$		12.6	17
4	Num. of turns	$n_4$	-	2	4
	Wire spacing (µm)	$S_4$		1(	00
	Quality factor	$\mathcal{Q}_4$	-	94.1	101

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 $Q_I$  in 4-coil and  $Q_2$  in 2- and 3-coil links also include  $R_S = 0.5 \Omega$ .

\*\* Delivered power when  $V_S = 1 \text{ V}$ 

Grayed rows indicate the design constraints.

IEEE Trans Ind Electron. Author manuscript; available in PMC 2014 November 07.

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