A STATE-SPACE MODELING OF NON-IDEAL DC-DC CONVERTERS

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ABSTRACT

A modeling based on the practical switches with finite turn-on, turn-off, delay, storage and reverse recovery times is proposed. The switching effects on the state equation, system stability, dc gain and efficiency are shown for the boost, buck and buck-boost converters. The difference of switching time variation by current on system stability is given as an extension of the storage time modulation effect. And the effect of average duty cycle variation on system stability is newly introduced. DC gains of the boost and buck-boost converters are shown to be much deteriorated by switching loss. It is also shown that no additional resistance is generated by switching in contradiction to previous work. Besides, it is found that buck converter is superior to others from the viewpoints of the system stability and dc gain, and the efficiencies of all the converters are maximized when the dc gain is close to unity. Previous related papers are improved and unified using a new state-space modeling. Analysis results are very simple in spite of the complex switching waveforms. This new modeling is believed to be very useful for the high frequency or high power applications where switching effects become dominant.

I. INTRODUCTION

The modeling of networks which contain switches has drawn much attention because of the unusual properties of switches in comparison with other circuit elements. This is the reason why there are so many modelings in the power electronics which are not found in other fields.

The difficulties in the modeling of switched networks are mainly due to the non-linear and time-varying nature of switches. Furthermore the control mechanism of switching systems is far different from ordinary systems. The difference is shown in Fig. 1. By switching, the topologies of networks are changed and the system matrices A, B, C and D are, accordingly, changed. So to control the output of switching systems, the system matrices should be varied by switches instead of the source as done in ordinary systems. This is why switching systems are non-linear and time-varying.

Most modelings in power electronics are mainly intended to convert this non-linear time-varying problem to an easier form [1-5]. However they are inadequate for some delicate problems since they are based on ideal switches. So a few papers based on non-ideal switches are proposed [6-9]. Focusing on the switching effects for the related previous works, the storage time modulation effect on small signal dynamics is found in [6]. And switching time effect on efficiency assuming ideal filters is investigated in detail in [7]. A substitution of switching effect to an equivalent resistance is done in [8]. Switching effect on dc gain degeneration is discussed in [9].

(a) a switching system
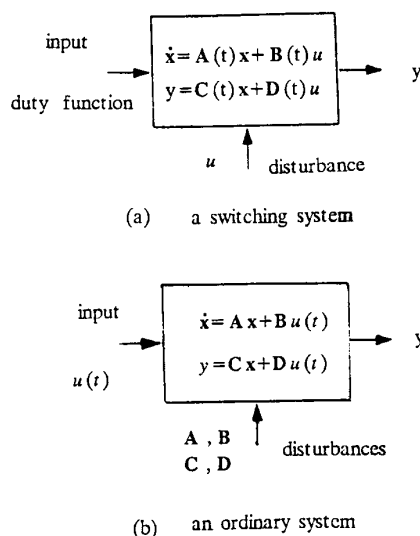

(b) an ordinary system

Fig. 1 Comparison of systems.

However they explain only parts of switching effects with somewhat rough or complex expressions.

This paper is intended to unify and extend the previous works so that this may be a good summary for switching effects on power systems. A new state space modeling is proposed to obtain both exact and simple results, which are believed to be the most important features of system modelings. Conventional state space average modeling is based on two or three sets of state equations which are valid for each mode. So this is inadequate for the representation of the interval between successive modes which appears in the practical switching case. Here a time-varying state equation valid not only for each mode but also for the interval between modes is set up using switching functions with little effort. Then more extended and compact state-space averaging and perturbations are taken for the boost converter example. The state-space analyses for the system stability, dc gain and efficiency for three converters are done and the results are summarized in a table.

II. MODELING PROCEDURE
(BOOST CONVERTER CASE)
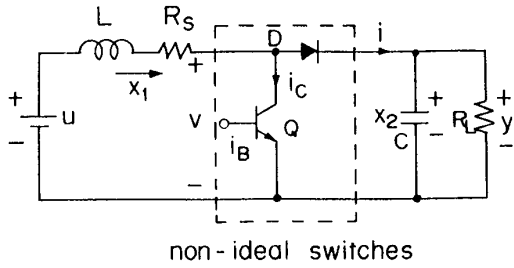
non-ideal switches

Fig. 2 Boost converter.

The modeling procedure is shown for the boost converter case. It is assumed that all the passive elements are LTI and that the switches have only switching loss. The conduction loss and parasitic resistance loss will be counted in the Part II of this paper in later. The converter is assumed to operate in the continuous conduction mode. Fig. 2 shows the circuit to be modeled and Fig. 3 shows the switching waveforms, respectively.

A. Exact State-Space Model

To derive a state equation, states x_1 and x_2 are allocated to the current of inductor L and the voltage of capacitor C, respectively. Then the derivatives of the states as functions of states, source and circuit parameters are found to be

$$L\dot{x}_1 = u - R_s x_1 - v \quad , \quad C\dot{x}_2 = i - \frac{x_2}{R_L} \quad . \quad (1)$$

Note that this state equation is valid also for the non-ideal switch case where the switching waveform can be arbitrary. What remains is v and i to be expressed as functions of already known variables. From the switching waveforms it can be seen that v and i are the switched portions of x_2 and x_1, respectively.

$$v = s_1(t) \ x_2 \quad , \quad i = s_2(t) \ x_1 \quad (2)$$

where $s_1(t)$, $s_2(t)$ are defined as the switching functions determined by

$$s_1(t) = \frac{v}{x_2} \quad , \quad s_2(t) = \frac{i}{x_1} \quad . \quad (3)$$

The switching functions are shown in Fig. 4. Provided that the switching times are known then the switching functions are fully determined.

By applying (2) to (1) and rearranging it, the state equation and the output equation of the boost converter are obtained.

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} -\dfrac{R_s}{L} & -\dfrac{s_1(t)}{L} \\ \dfrac{s_2(t)}{C} & -\dfrac{1}{CR_L} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} \dfrac{1}{L} \\ 0 \end{bmatrix} u \quad y = [0 \ 1] \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} (4)$$

(4) is of the form

$$\dot{x} = A(t)x + Bu \quad , \quad y = Cx \quad . \quad (5)$$
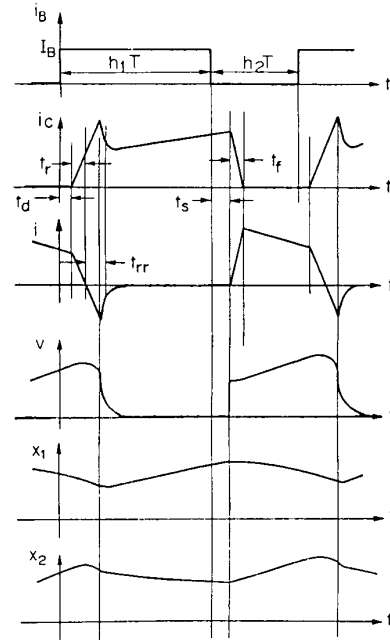

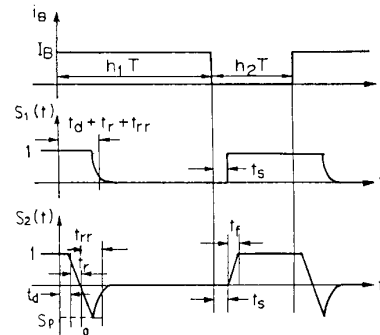Fig. 3 Switching waveforms of boost converter.

Fig. 4 Switching functions $s_1(t)$ and $s_2(t)$

Note that the exact model of the converter is of time-varying form, which is the general property of switching systems. This new model is exact enough to be used for simulations.

B. State - Space Averaging and Perturbation

To eliminate the time-varying terms in the matrices $A(t), B(t)$ and $C(t)$, a generalized state-space averaging is taken for them as

$$\dot{x} = \overline{A} x + \overline{B} u \quad , \quad y = \overline{C} x \quad (6)$$

where

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$$\overline{A} = \frac{1}{T} \int_0^T A(t) \, dt \ , \quad \overline{B} = \frac{1}{T} \int_0^T B(t) \, dt \ , \quad \overline{C} = \frac{1}{T} \int_0^T C(t) \, dt \quad .(7)$$

In the boost converter case the matrices are evaluated as

$$\overline{A} = \begin{bmatrix} -\dfrac{R_s}{L} & -\dfrac{s_1}{L} \\ \dfrac{s_2}{C} & -\dfrac{1}{CR_L} \end{bmatrix}, \quad \overline{B} = \begin{bmatrix} \dfrac{1}{L} \\ 0 \end{bmatrix}, \quad \overline{C} = [\, 0 \ \ 1\,] \quad . \ (8)$$

The s_1 and s_2 are the averages of $s_1(t)$ and $s_2(t)$, respectively. They are evaluated from Fig. 4 as

$$s_1 = \frac{1}{T} \int_0^T s_1(t) \, dt = 1 - h + t_1 f \quad \text{for} \ H_{min} \Big\langle \, h \, \Big\langle \, H_{max} \quad (9a)$$

$$s_2 = \frac{1}{T} \int_0^T s_2(t) \, dt = 1 - h + t_2 f \quad \text{for} \ H_{min} \Big\langle \, h \, \Big\langle \, H_{max} \quad (9b)$$

where

Q; t_d: delay time, t_r: rise time, t_f: fall time, t_s: storage time,
D; t_{rr}: reverse recovery time, s_p: reverse peak, h: duty factor,

$$h = h_1 = 1 - h_2 \ , \ t_1 = t_d + t_r + t_{rr} - t_s \ , \ t_2 = t_d + \frac{t_r}{2} - \tau - t_s - \frac{t_f}{2} \ ,$$

$$f = \frac{1}{T}, \, H_{min} = (t_d + t_r + t_{rr}) / T \, , H_{max} = 1 - (t_s + t_f - t_d) / T.(10)$$

H_{min}, H_{max} are determined by the turn-on, off overlap conditions of the switches. And τ is introduced assuming that the diode reverse recovery charge Q_{rr} is proportional to its forward current as

$$Q_{rr} = -\int_0^{t_{rr}} i_D(t) \, dt = -\int_0^{t_{rr}} x_1(t) s_1(t) \, dt \cong -x_1(t) \, \frac{s_p t_{rr}}{2}$$

$$= \tau x_1(t) \quad \text{or} \quad \tau = -\frac{s_p t_{rr}}{2} \quad . \quad (11)$$

The inductor current $x_1(t)$ has been assumed to be constant and the τ is found to be the life time of the minority carrier.

Assuming that the switching times are non-linear function of the inductor current and the C matrix is constant, then (6) becomes

$$\dot{x} = A(h,x) x + B(h,x) u \quad , \quad y = C_0 x \quad . \quad (12)$$

To eliminate the non-linearity in (12), matrices are expanded in power series neglecting higher order terms as

$$A(h,x) \cong A(H_0, X_1) + \frac{\partial A(h,x)}{\partial h} \Big|_{h = H_0} (h - H_0) +$$

$$\frac{\partial A(h,x)}{\partial x_1} \Big|_{x_1 = X_1} (x_1 - X_1) = A_0 + A_1 \cdot (h - H_0) + A_2 \cdot (x_1 - X_1) \tag{13a}$$

and

$$B(h,x) = B_0 + B_1 \cdot (h - H_0) + B_2 \cdot (x_1 - X_1) \quad . \quad (13b)$$

Applying (13) to (12) and taking the following perturbations

$$h = H_0 + \hat{h} \ , \ x = X_0 + \hat{x} \ , \ y = Y_0 + \hat{y} \tag{14}$$

the resultant equations become

$$(\dot{X}_0 + \dot{\hat{x}}) = (A_0 + A_1 \hat{h} + A_2 \hat{x}_1)(X_0 + \hat{x}) + (B_0 + B_1 \hat{h} + B_2 \hat{x}_1) U_0$$

$$(Y_0 + \hat{y}) = C_0 (X_0 + \hat{x}) \quad . \tag{15}$$

Ignoring bilinear terms, (15) is separated into the dc and ac parts as

$$\dot{X}_0 = A_0 X_0 + B_0 U_0 \quad , \quad Y_0 = C_0 X_0 \tag{16}$$

and

$$\dot{\hat{x}} = (A_0 + A_2 X_0 W + B_2 U_0 W)\hat{x} + (A_1 X_0 + B_1 U_0)\hat{h}$$

$$\hat{y} = C_0 \hat{x} \tag{17}$$

where W is defined as

$$W = [\, 1 \ \ 0\,] \quad . \tag{18}$$

From (16), operating point Y_0 is determined by setting $\dot{X}_0$ to zero as follows:

$$A_0 X_0 + B_0 U_0 = 0 \quad , \quad Y_0 = -C_0 A_0^{-1} B_0 U_0 \quad . \tag{19}$$

From (17), the transfer function $G(s)$ is determined as

$$G(s) = \frac{\hat{Y}(s)}{\hat{H}(s)} = C_0 (sI - A_0 - A_2 X_0 W - B_2 U_0 W)^{-1}$$

$$\cdot (A_1 X_0 + B_1 U_0). \tag{20}$$

System stability can be evaluated by examining the characteristic equation

$$| sI - A_0 - A_2 X_0 W - B_2 U_0 W | = 0 \quad . \tag{21}$$

In the boost converter case, the matrices, $A_0, A_1, \ldots, C_0$ are found to be

$$A_0 = \begin{bmatrix} -\dfrac{R_s}{L} & -\dfrac{1 - H_0 + t_1 f}{L} \\ \dfrac{1 - H_0 + t_2 f}{C} & -\dfrac{1}{CR_L} \end{bmatrix}, \quad A_1 = \begin{bmatrix} 0 & \dfrac{1}{L} \\ -\dfrac{1}{C} & 0 \end{bmatrix},$$

$$A_2 = \begin{bmatrix} 0 & -\dfrac{f}{L}\dfrac{dt_1}{dx_1} \\ \dfrac{f}{C}\dfrac{dt_2}{dx_1} & 0 \end{bmatrix}$$

$$B_0 = \begin{bmatrix} \dfrac{1}{L} \\ 0 \end{bmatrix}, \quad B_1 = B_2 = 0 \quad , \quad C_0 = [\, 0 \ \ 1\,] \ .(22)$$

C. Pole Frequency Deviation Effect

By applying (22) to (21) poles are found to be

$$s^2 + \left(\frac{R_s^*}{L} + \frac{1}{CR_L}\right)s + \frac{(1 - H_0 + t_1 f)(1 - H_0 + t_2^* f) + R_s^* / R_L}{L C}$$

$$= s^2 + 2\zeta \omega_n s + \omega_n^2 = 0 \tag{23}$$

where

$$R_s^* = R_s + f\frac{dt_1}{dx_1} X_2 \ , \quad t_2^* = t_2 + \frac{dt_2}{dx_1} X_1 \quad . \tag{24}$$
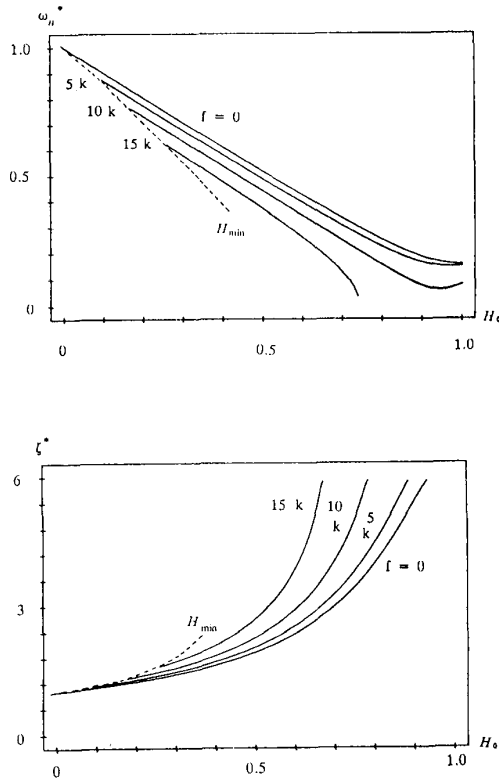
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Fig. 5 Pole frequency deviation of boost converter.

It is observed that R_s and t_2 are replaced by R_s^* and t_2^* by switching time modulation. This means that the poles can be calculated as if there is no switching time modulation by substituting R_s^* and t_2^* . Note that the switching time modulation effect which is undesirable in practice becomes dominant as X_1, X_2 are increased. So it is recommanded that the operating point be low to avoid the undesirable effect. It had been discussed in [6] that R_s is effectively varied by storage time modulation. That result is in part true, however, it needs to be extended as described here to give more exact solution. Still there remains another switching effect to be explained, the duty cycle variation effect. Duty cycle is moved from H_0 to $H_0 - t_1 f$ or $H_0 - t_2^* f$ as shown in (23). It is apparent that this effect will be dominant at high switching frequency.

The normalized natural frequency and normalized damping factor which are the ratios of the practical ones and the ideal ones with zero duty factor are

$$\omega_n^* = \frac{\omega_n}{\omega_n |_{t_1, t_2, H_0 = 0}} = \sqrt{\frac{R_s^*/R_L + (1-H_0+t_1 f)(1-H_0+t_2^* f)}{R_s^*/R_L + 1}}$$

$$(25a)$$

$$\zeta^* = \frac{\zeta}{\zeta |_{t_1, t_2, H_0 = 0}} = \sqrt{\frac{R_s^*/R_L + 1}{R_s^*/R_L + (1-H_0+t_1 f)(1-H_0+t_2^* f)}} .$$

$$(25b)$$

ω_n^* and ζ^* are plotted in Fig. 5 for a typical power switching transistor whose current level is hundred amperes. In fact the switching times are varied by current and temperature, however, they are fixed for illustration purpose. Typical values of them and resistors selected, which are to be used throughout this paper, are as follows:

$$t_d = 8.0 \ \mu s, \ t_r = 0.6 \ \mu s, \ t_{rr} = 8.5 \ \mu s, \ t_s = 5.0 \ \mu s,$$

$$t_f = 1.0 \ \mu s, \ \tau = 11.0 \ \mu s, \ t_1 = t_d + t_r + t_{rr} - t_s = 12.1 \ \mu s,$$

$$t_2 = t_d + \frac{t_r}{2} - \tau - t_s - \frac{t_f}{2} = -8.2 \ \mu s, \ \frac{dt_1}{dx_1}X_2 = 20 \ \mu s \ ohms,$$

$$\frac{dt_2}{dx_1}X_1 = -15 \ \mu s, \ R_s = 0.50 \ ohms, \ R_L = 20 \ ohms. \quad (26)$$

It can be seen that system poles are non-linear to the duty factor and are considerably deviated from the ideal switching case as the switching frequency is increased. And the open loop system stability for a typical switching case is improved by switching.

The switching effect on system stability can be summarized as two effects; one is the switching time modulation effect, which results in the variation of R_s, t_2 and the other is the average duty factor modification effect, which results in the variation of average duty factor.

D. DC Gain Deviation Effect

To check the switching effect on dc gain, (19) is evaluated using (22). The dc gain G_v is then

$$G_v = \frac{Y_0}{U_0} = -C_0 A_0^{-1} B_0 = \frac{(1-H_0+t_2 f)}{\frac{R_s}{R_L} + (1-H_0+t_1 f)(1-H_0+t_2 f)}$$

$$(27)$$

The dc gain is shown in Fig. 6 as a function of duty factor for several switching frequencies. The switching times and resistances used here are the same as (26). Note that the practical boost converter has the maximum dc gain point which occurs at the point, H_0^* given by

$$\frac{\partial G_v}{\partial H_0}\bigg|_{H_0 = H_0^*} \quad \text{or} \quad H_0^* = 1 - \sqrt{\frac{R_s}{R_L}} + t_2 f \quad . (28)$$
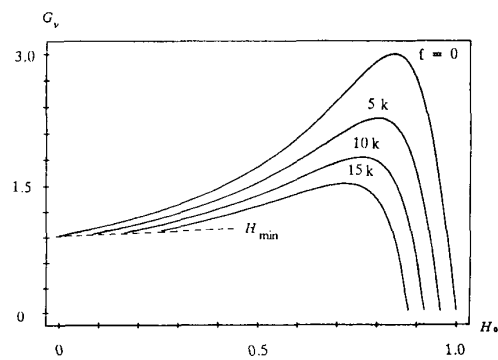

Fig. 6 DC gain degeneration of boost converter.

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Then the maximum dc gain becomes

$$G_v(H_0^*) = \cfrac{1}{2\sqrt{\cfrac{R_s}{R_L}} + (t_1 - t_2)f} \quad . \qquad (29)$$

It is obvious from (28) and (29) that the dc gain is deteriorated both by the conduction loss due to the inductor resistance R_s and by the switching loss due to the difference between voltage and current switching functions, $s_1(t) - s_2(t)$. However their effects are different. Hence it is not moderate to replace the switching effect with an equivalent resistance as done in [8] though this substitution gives somewhat similar result.

E. Efficiency Degeneration Effect

Efficiency η of the boost converter can be evaluated by comparing the input and output powers assuming low ripple.

$$\eta = \frac{P_o}{P_i} = \frac{Y_0^2 / R_L}{U_0 X_1} \qquad (30)$$

Evaluating X_0 from (19), Y_0 and X_1 are found to be

$$Y_0 = \cfrac{1 - H_0 + t_2 f}{\cfrac{R_s}{R_L} + (1 - H_0 + t_1 f)(1 - H_0 + t_2 f)} U_0 \qquad (31a)$$

$$X_1 = \cfrac{1}{R_s + R_L(1 - H_0 + t_1 f)(1 - H_0 + t_2 f)} U_0 \quad . \qquad (31b)$$

Then,

$$\eta = \cfrac{(1 - H_0 + t_2 f)^2}{R_s / R_L + (1 - H_0 + t_1 f)(1 - H_0 + t_2 f)} \quad . \qquad (32)$$

To concentrate our attention to the switching effect only the inductor resistance R_s is set to zero. Then

$$\eta \big|_{R_s = 0} = \frac{1 - H_0 + t_2 f}{1 - H_0 + t_1 f} \quad . \qquad (33)$$

Efficiency degeneration by switching effect is depicted in Fig. 7. Remark on the fact that the efficiency is maximized when the dc gain is close to unity, which is the common properties of the dc converters. This result is similar to the result obtained in [7].
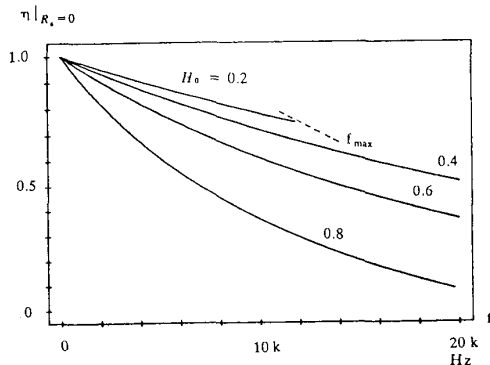

Fig. 7 Efficiency degeneration of boost converter.

III. BUCK CONVERTER MODELING

The buck converter as shown in Fig. 8 is modeled. The approach is very analogous to the boost converter case. Therefore detailed explanation is avoided here.

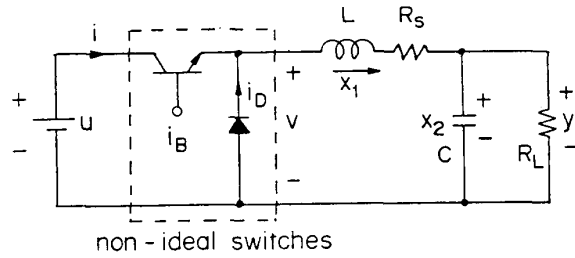### A. Exact State - Space Model

non - ideal switches

Fig. 8 Buck converter.

The state equation and the output equation are

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} -\dfrac{R_s}{L} & -\dfrac{1}{L} \\ \dfrac{1}{C} & -\dfrac{1}{CR_L} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} \dfrac{s_3(t)}{L} \\ 0 \end{bmatrix} u \quad y = [\,0\ \ 1\,] \begin{bmatrix} x_1 \\ x_2 \end{bmatrix}$$

$$(34)$$

or

$$\dot{x} = A x + B(t) u \quad , \quad y = C x \quad . \qquad (35)$$

The $s_3(t)$ is found to be

$$s_3(t) = 1 - s_1(t) \quad . \qquad (36)$$

Note that the $B(t)$ of the buck converter is time-varying instead of the $A(t)$ of the boost converter case.

B. State Space Averaging and Perturbation

The averaged matrices are

$$\overline{A} = A = \begin{bmatrix} -\dfrac{R_s}{L} & -\dfrac{1}{L} \\ \dfrac{1}{C} & -\dfrac{1}{CR_L} \end{bmatrix} , \ \overline{B} = \begin{bmatrix} \dfrac{s_3}{L} \\ 0 \end{bmatrix} , \ \overline{C} = C = [\,0\ \ 1\,]$$

$$(37)$$

and t_1, f are the same as (10). Then the perturbed matrices are

$$A_0 = A = \begin{bmatrix} -\dfrac{R_s}{L} & -\dfrac{1}{L} \\ \dfrac{1}{C} & -\dfrac{1}{CR_L} \end{bmatrix} , \ A_1 = A_2 = 0 \ , \ B_0 = \begin{bmatrix} \dfrac{H_0 - t_1 f}{L} \\ 0 \end{bmatrix}$$

$$B_1 = \begin{bmatrix} \dfrac{1}{L} \\ 0 \end{bmatrix} , \ B_2 = \begin{bmatrix} \dfrac{d t_1}{d x_1} \dfrac{f}{L} \\ 0 \end{bmatrix} , \ C_0 = C = [\,0\ \ 1\,] \quad (38)$$

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C. Switching Effects on Pole Frequency, DC Gain, and
Efficiency Degeneration

Characteristic equation is found to be

$$s^2 + \left(\frac{R_s^*}{L} + \frac{1}{CR_L}\right)s + \frac{1 + R_s^*/R_L}{LC} = 0 \qquad (39)$$

where

$$R_s^* = R_s + f\frac{dt_1}{dx_1}U_0 \quad . \qquad (40)$$

There is pole frequency deviation due to the switching time modulation, which results in a variation of R_s to R_s^*. However there is no duty factor variation effect on pole frequency. Therefore it can be said that the buck converter is relatively strong against the switching effect.

DC gain is found to be

$$G_v = \frac{Y_0}{U_0} = -C_0 A_0^{-1} B_0 = \frac{R_L}{R_L + R_s}(H_0 - t_1 f) \quad . \ (41)$$

The efficiency assuming low ripple is given by

$$\eta = \frac{P_o}{P_i} = \frac{Y_0 X_1}{U_0 I} = \frac{R_L}{R_s + R_L} \cdot \frac{H_0 - t_1 f}{H_0 - t_2 f} \quad . \qquad (42)$$

The efficiency considering the switching loss only becomes

$$\eta\Big|_{R_s=0} = \frac{H_0 - t_1 f}{H_0 - t_2 f} \quad . \qquad (43)$$

It can be seen that the efficiency becomes the maximum when the duty factor is unity. G_v, η are shown in Fig. 9 and Fig. 10 respectively.

IV. BUCK - BOOST CONVERTER

A. Exact State - Space Model

The state equation and output equation are found to be

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} \dfrac{R_s}{L} & -\dfrac{s_1(t)}{L} \\ \dfrac{s_2(t)}{C} & -\dfrac{1}{CR_L} \end{bmatrix}\begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} \dfrac{s_3(t)}{L} \\ 0 \end{bmatrix}u, \quad y = [\,0\ \ 1\,]\begin{bmatrix} x_1 \\ x_2 \end{bmatrix} (44)$$

or

$$\dot{x} = A(t)x + B(t)u \quad , \qquad y = Cx \quad . \qquad (45)$$

Note that $A(t)$ and $B(t)$ are exactly the same as those of the boost and the buck converters, respectively. Thus it is expected that the buck-boost converter is the general converter among the dc converters.

B. State - Space Averaging and Perturbation
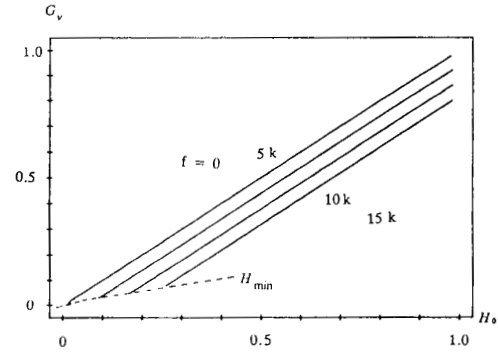
The averaged and perturbed matrices are
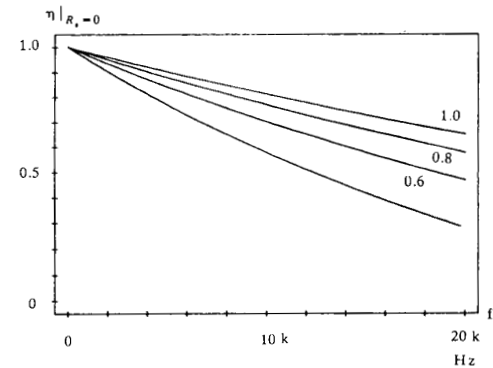

Fig. 9 DC gain deviation of buck converter.

Fig. 10 Efficiency degeneration of buck converter.

$$\overline{A} = \begin{bmatrix} \dfrac{R_s}{L} & -\dfrac{s_1}{L} \\ \dfrac{s_2}{C} & -\dfrac{1}{CR_L} \end{bmatrix}, \ \overline{B} = \begin{bmatrix} \dfrac{s_3}{L} \\ 0 \end{bmatrix}, \ \overline{C} = C = [\,0\ \ 1\,] \ (46)$$

and

$$A_0 = \begin{bmatrix} -\dfrac{R_s}{L} & -\dfrac{1 - H_0 + t_1 f}{L} \\ \dfrac{1 - H_0 + t_2 f}{C} & -\dfrac{1}{CR_L} \end{bmatrix}, \quad A_1 = \begin{bmatrix} 0 & \dfrac{1}{L} \\ -\dfrac{1}{C} & 0 \end{bmatrix},$$

$$A_2 = \begin{bmatrix} 0 & -\dfrac{f\,dt_1}{L\,dx_1} \\ \dfrac{f}{C}\dfrac{dt_2}{dx_1} & 0 \end{bmatrix} \quad B_0 = \begin{bmatrix} \dfrac{H_0 - t_1 f}{L} \\ 0 \end{bmatrix}, \quad B_1 = \begin{bmatrix} \dfrac{1}{L} \\ 0 \end{bmatrix}$$
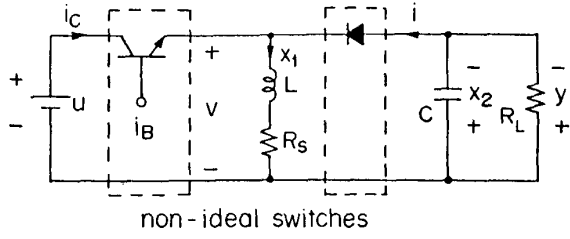
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non-ideal switches

Fig. 11 Buck-boost converter.

$$B_2 = \begin{bmatrix} -\dfrac{dt_1}{dx_1}\dfrac{f}{L} \\ 0 \end{bmatrix}, \quad C_0 = C = [0\ 1] \quad . \quad (47)$$

C. Switching Effects on Pole Frequency, DC Gain, and Efficiency Degeneration

The characteristic equation is found to be the same as that of the boost converter with a slight modification of the value R_s^*.

$$R_s^* = R_s + f\frac{dt_1}{dx_1}(U_0 + X_2) \quad (48)$$

DC gain is given by

$$G_v = \frac{Y_0}{U_0} = \frac{(1-H_0+t_2 f)(H_0-t_1 f)}{\dfrac{R_s}{R_L}+(1-H_0+t_1 f)(1-H_0+t_2 f)} \quad . \quad (49)$$

The tendency is much similar to the boost converter case.
The efficiency assuming low ripple is given by

$$\eta = G_v \cdot \frac{I}{I_c} = \frac{(H_0-t_1 f)(1-H_0+t_2 f)^2}{[\dfrac{R_s}{R_L}+(1-H_0+t_1 f)(1-H_0+t_2 f)](H_0-t_2 f)} \quad .$$

$$(50)$$

The efficiency considering the switching loss only becomes

$$\eta\bigg|_{R_s=0} = \frac{H_0-t_1 f}{1-H_0+t_1 f} \cdot \frac{1-H_0+t_2 f}{H_0-t_2 f} \quad . \quad (51)$$

Note that (51) is just the product of the efficiencies of the buck and boost converters. So the efficiency of the buck-boost converter is the lowest one. And the maximum efficiency is obtained at H_0^* given by

$$\frac{\partial \eta}{\partial H_0}\bigg|_{H_0=H_0^*} = 0 \quad \text{or} \quad H_0^* = \frac{1+(t_1+t_2)f}{2} \quad . \quad (52)$$

Therefore the maximum efficiency is

$$\eta_{max} = \eta\bigg|_{H_0=H_0^*} = \left[\frac{1-(t_1-t_2)f}{1+(t_1-t_2)f}\right]^2 \quad . \quad (53)$$
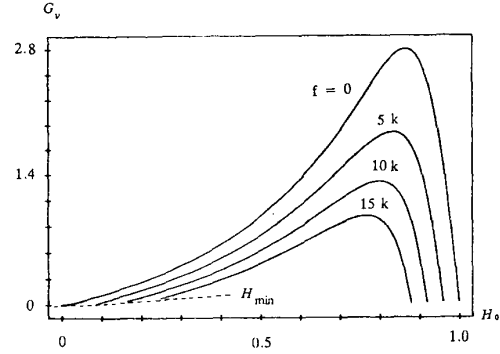

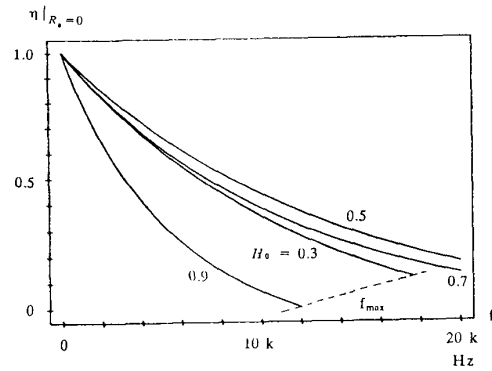Fig. 12 DC gain degeneration of buck-boost converter.

Fig. 13 Efficiency degeneration of buck-boost converter.

V. SUMMARY

Important results are tabulated using the normalized resistance r and the average switching functions s_1, s_2, s_3 and s_4 only. The results are very simple and regular in spite of the very complex switching waveforms and the exact description. In a whole, the properties of the buck or boost converters are the special cases of the general converter, buck-boost converter. The pole frequency, dc voltage gain, dc current gain and efficiency of the buck or boost converters are those of the buck-boost converter whose s_1, s_2 or s_3, s_4 are set to 1's, respectively.

Furthermore those of the buck-boost converter is just the products of those of buck converter and those of boost converter if the inductor resistance is neglected.

VI. CONCLUSION

The extension and generalization of the state-space average modeling to the non-ideal switching case and the unification and extension of the previous works have been performed. The switching effects on the system stability, dc gain and effi-

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Table. Summaries of switching effects

type	Buck	Boost	Buck-boost
items	F_1	F_2	F_3
state eq.	A,B(t)	A(t),B	A(t),B(t)
ω_n^*	1	$\sqrt{\dfrac{r^*+s_1 s_2^*}{r^*+1}}$	$\sqrt{\dfrac{r^*+s_1 s_2^*}{r^*+1}}$
ζ^*	1	$\sqrt{\dfrac{r^*+1}{r^*+s_1 s_2^*}}$	$\sqrt{\dfrac{r^*+1}{r^*+s_1 s_2^*}}$
G_v	$\dfrac{s_3}{r+1}$	$\dfrac{s_2}{r+s_1 s_2}$	$\dfrac{s_2 s_3}{r+s_1 s_2}$
$G_v|_{r=0}$	s_3	$\dfrac{1}{s_1}$	$\dfrac{s_3}{s_1}$
$G_i = \dfrac{I_o}{I_i}$	$\dfrac{1}{s_4}$	s_2	$\dfrac{s_2}{s_4}$
$\eta = G_v G_i$	$\dfrac{s_3}{(r+1)s_4}$	$\dfrac{s_2^2}{r+s_1 s_2}$	$\dfrac{s_2^2 s_3}{(r+s_1 s_2)s_4}$
$\eta|_{r=0}$	$\dfrac{s_3}{s_4}$	$\dfrac{s_2}{s_1}$	$\dfrac{s_2 s_3}{s_1 s_4}$

$$F_1 = F_3|_{s_1,s_2=1} \qquad F_2 = F_3|_{s_3,s_4=1}$$

$$F_3|_{r=0} = F_1|_{r=0} \cdot F_2|_{r=0}$$

$r = R_s/R_L$, $r^* = R_s^*/R_L$, $s_1 = 1-H_0+t_1 f$,
$s_2 = 1-H_0+t_2 f$, $s_2^* = 1-H_0+t_2^* f$,
$s_3 = H_0 - t_1 f$, $s_4 = H_0 - t_2 f$

ciency for the buck, boost and buck-boost converters are explored.

The stabilities of all converters are affected by the switching time modulation effect which results in the variations of R_s and t_2. And those of the boost and buck-boost converters are also affected by the average duty cycle variation effect due to finite switching times. In the sense of stability the buck converter is strong against switching effect, however the boost and buck-boost converters are weak in general and this becomes dominant when duty cycle approaches to unity.

The dc gains of the boost and buck-boost converters are deteriorated by the inductor resistance and the switching loss. And this effect becomes dominant also when duty cycle approaches to unity. So it is preferable to use the buck converter instead of other converters to avoid serious switching effects on dc gain. And it is needed especially to avoid unity duty cycle for the boost and buck-boost converters.

The efficiencies of all the converters are maximized when their dc gains are close to unity. It is recommanded to avoid the buck-boost converter for higher efficiency.

So it can be concluded that the optimal duty factor of the buck converter is near unity, that of the boost converter is near zero and that of the buck-boost converter is near half, respectively.

It is believed that the analysis results are very helpful for the high frequency or high power applications and for the summary of the switching effects on the system stability, dc gain and efficiency.

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