All-Optical 160 Gbit/s Retiming System Using Fiber Grating Based Pulse Shaping Technology

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Abstract— This paper demonstrates a retiming system operating at rates of 40 and 160 Gbit/s, which incorporates a superstructured fiber Bragg grating (SSFBG) as a pulse shaping element. The original data pulses are shaped into flattopped (rectangular) pulses to avoid conversion of their timing jitter into pulse amplitude noise at the output of a nonlinear fiber-based Kerr switch. Thus retiming is performed in a single step avoiding wavelength conversion. The benefits of using shaped rather than conventional pulse forms in terms of timing jitter reduction are confirmed by bit-error rate (BER) measurements.

Index Terms—All-optical regeneration, timing jitter, fiber Bragg grating, nonlinear switches, optical pulse shaping, ultrashort optical pulses.

I. INTRODUCTION

s data rates increase, and the associated pulse Adurations get shorter, it becomes critical to be able to manage the noise-related transmission impairments on the original data pulses, such as amplitude and timing jitter. At bit rates beyond 40 Gbit/s, it is considered both technically (due to the intrinsic benefits in speed) and economically (due to the generally lower power consumption requirements) beneficial to consider all-optical techniques for the mitigation of such impairments. Nevertheless, the issue of bit retiming is particularly challenging, since, apart from clock recovery, it also requires the availability of a switch with a uniform response over a time base extending across the duration of the bit slot. Whereas this might be conceptually straightforward in digital electronics, the analogue nature of nonlinear optical devices, whose response depends directly on the intensity of the control signal, introduces an extra architectural complexity.

One route towards achieving an extended switching window is by shaping the control optical pulse at the switch input into a rectangular waveform [1-3]. This provides then optimal resilience to timing jitter induced errors, and also reduces the absolute accuracy for temporal bit alignment. The shaping and manipulation of the optical pulses are conventionally achieved using liquid crystal spatial modulators [4-5] or arrayed-waveguide gratings (AWGs) [6]. In literature, picosecond and sub-picosecond long rectangular switching windows have been also demonstrated using uniform long periodic gratings operating in transmission [7-8] and propagation in a section of polarisation maintaining fibre [3]. In this last scheme, the pulse shaper is based on obtaining controllable pulse broadening in a Hi-Bi fibre due to polarisation mode walk-off.

In this paper, we demonstrate timing jitter reduction in an optical time division multiplexing (OTDM) system operating at data rates up to 160 Gbit/s by linearly reshaping RZ data pulses in a polarization insensitive SSFBG into longer rectangular (flat-top) pulses (~5ps) and then switching these pulses in a nonlinear Kerr switch with a synchronous optical clock signal [3, 9-10], while maintaining the original wavelength. SSFBG technology offers a ready route to the production of optical filters with very precisely controlled frequency and phase profiles of almost arbitrary complexity in a single continuous grating structure and which can therefore be used to manipulate the properties of incident optical signals. An SSFBG is defined as a standard fibre grating, i.e. a fiber with a rapidly varying refractive index modulation of uniform amplitude and pitch, onto which an additional, slowly varying amplitude/phase modulation has been imposed along its length [11]. In the low grating reflectivity limit, i.e. for which the light penetrates the full grating length, the optical frequency response of the grating is simply given by the Fourier transform of its spatial refractive index modulation profile, thereby allowing the spatial information of the grating superstructure to be directly mapped into the time domain [12].

SSFBGs have proven to be an extremely powerful tool for advanced photonic applications that require fine and accurate manipulation of the shape of short optical pulses, thanks to the high precision in the implementation of the superstructure phase and amplitude profiles offered by plane-by-plane

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grating fabrication techniques. Using this linear all-fiberized pulse shaping method, we have previously demonstrated the reshaping of short pulses of a few picoseconds duration into rectangular pulses of different pulse widths (10-20ps) [10, 12-14]. However, this extremely versatile technique is suitable for a far broader range of applications, ranging from optical code generation/recognition [15, 16], repetition rate multiplication [17], data format conversion [18], regeneration [19] and compression [20], to more exotic applications, such as Optical Fourier Transformation (OFT) [21].

This paper is organized as follows. In Section II, we present the principle of operation of the scheme and the pulse characterisation of the ~5ps rectangular pulses. Section III describes the experimental set-up we used, while the performance of the proposed scheme as a pure retimer is assessed in Section IV. In this Section, we show detailed timing jitter characterisations and BER measurements. These results were obtained at data rates of both 40 and 160 Gbit/s. Comparative measurements are also presented when shaping the data signal into Gaussian pulses with similar pulse durations as the rectangular pulses, highlighting the improvement obtained with the pulse shaping grating.

II. PRINCIPLE OF OPERATION AND PULSE CHARACTERISATION



Figure 1. Operating principle of the retiming technique without (a) and with (b) the use of a rectangular pulse shape.

The basic idea behind the retiming scheme is shown in Fig. 1. The Figure illustrates the effect of timing jitter on the performance of an optical switch (controlled by a short optical clock pulse) in the instance that: (a) no retiming; or (b) retiming in the form of pulse reshaping in the form of a rectangular waveform is applied to the signal. As can be appreciated in case (a), large timing jitter in the signal pulses results in significant pulse amplitude noise at the output of the switch when the signal is switched by a short conventional pulse shape. However, if the incoming data signal is shaped into a rectangular pulse prior to being injected into the switch (case (b) in Fig.1), the flat top of the new shape ensures that the switch is insensitive to coarse mistiming within the original



Figure 2. Refractive index modulation profile of the SSFBG (*a*) and corresponding simulated spectral response (*b*).



Figure 3. a) Cross-correlation measurement of the shaped 5ps rectangular pulse (solid line) and its comparison with a Gaussian pulse shape (dashed line) with a similar FWHM (~5ps). b) Spectral traces before and after the SSFBG.

signal. The maximum tolerable amount of timing jitter that can be suppressed is defined by the duration of the rectangular pulse and cannot be larger than the duration allocated to each data slot. The signal can then be switched by clean, short clock signal pulses that are temporally aligned to the nominal center of the rectangular data pulses to ensure that the output signal is properly retimed.

In our experiments, we generate ~5ps rectangular pulses using our SSFBG based pulse-shaping technology. <u>As we</u> <u>already discussed in the previous Section, this is a purely</u> <u>passive-filtering process; to be able to properly design the</u>

grating and, thus, achieve the desired pulse shape, the characteristics of the pulses which are to be fed to the grating input need to be known in advance. The SSFBG has been designed and fabricated with the correct phase and amplitude reflectivity profile to work optimally with a transform-limited ~1 ps Gaussian pulse at its input. We chose this particular pulse duration to ensure that we could accommodate an adequate number of spectral features of the SSFBG reflectivity within the available spectral bandwidth of the incoming signal, while avoiding to overcomplicate the SSFBG refractive index modulation profile. The corresponding simulated refractive index modulation profile and spectral response are shown in Fig. 2(a) and Fig. 2(b). The peak reflectivity of the grating is ~20%, corresponding to <u>a maximum modulation index of $\delta n \sim 10^{-3}$. We can</u> accommodate 9 spectral lobes within the available ~14nm spectral bandwidth, while the main body of the grating is written within ~2.5mm of fibre, emphasizing the high precision required during the fabrication process. Note that in this demonstration however, we used slightly broader pulses (~1.8ps), so that the shaped pulses have smoother edges and a narrower flat top than those targeted [12]. Fig. 3(a) shows the corresponding temporal profile of the pulse shaping grating, measured with a cross-correlator and using a ~600 fs sampling pulse. The flat top of the shaped pulse extends over ~ 2 ps. As a reference, we also include in the same figure a plot of a Gaussian pulse with a similar full width at half maximum (FWHM).

Fig. 3(b) compares the spectra both before and after the filtering, where the shaping function of the grating and the appearance of the *sinc*-like features corresponding to a rectangular waveform can be clearly observed. The SSFBG response is broader than the input spectrum, as previously explained, while the spectral asymmetry observed results from the slight spectral response asymmetry of the SSFBG.



Figure 4. Retiming set-up at 40- and 160-Gbit/s. Inset: cartoon showing the operating principle of the Kerr switch.

The experimental set-up is reported in Fig. 4. A tunable mode-locked laser (TMLL) operating at ~1557nm is used to generate ~1.8ps Gaussian pulses at a repetition rate of 10 GHz. The laser source has an inherent RMS timing jitter of τ_{rms} ~420 fs. These pulses are then amplitude modulated to

provide a 2^7 -1 pseudorandom bit sequence (PRBS) and subsequently multiplexed up to either 40 or 160 Gbit/s according to the specific experiment. The data signal is shaped via the SSFBG into ~5 ps rectangular pulses and fed into the Kerr switch. The nonlinear element in the switch is constituted by ~200m of highly nonlinear fiber (HNLF) with a nonlinear coefficient γ ~10.5W⁻¹km⁻¹, a dispersion slope and



Figure 5. Spectra after the Kerr switch at 40Gbit/s (a) and 160Gbit/s (b).

wavelength of 0.018ps/nm²/km and ~1554nm, respectively.

The clock source, synchronised to the data stream via the same RF synthesizer, is an Erbium-glass mode-locked laser (ERGO) with low inherent timing jitter (τ_{rms} ~210 fs), operating at 1544 nm and providing pulses with a FWHM of 1.3 ps when properly compressed in a short length of dispersion compensating fiber. These pulses are multiplexed up to either 40 or 160 GHz and amplified before being launched as the control signal onto the Kerr switch.

The data signal is aligned at 90° to the Kerr switch polariser so as to be attenuated in the Kerr switch when the control signal is absent. The clock pulses co-propagating in the Kerr switch are aligned at 45° relative to the shaped data pulses. The part of the square pulse that overlaps in time with the clock pulse has its polarisation rotated within the HNLF due to cross-phase modulation and can thus be transmitted through the polariser (see inset of Fig.4). Since the walk-off between the two signals inside the HNLF is negligible owing to the low fibre dispersion, the data pulses after the retiming scheme retain a similar pulse width (FWHM~1.2ps) and RMS timing jitter ($\tau_{rms} \sim 250$ fs) as the control signal.

The retimed signal is then characterized using the corresponding diagnostics and subsequently demultiplexed down to 10 Gbit/s. This was obtained using a non-linear optical loop mirror (NOLM), which was controlled by a 10GHz clean synchronized signal, with 50m HNLF (slope 0.018ps/nm²km, zero dispersion wavelength at 1554nm, γ ~10.5W⁻¹km⁻¹) as a demultiplexer before entering the BER tester (BERT).

Fig. 5(a) and Fig. 5(b) show the spectra at the output of the retiming scheme for operation at 40 and 160Gbit/s respectively, highlighting that the spectra lose their *sinc*-shape when properly switched and broaden with respect to the initial pulse spectrum (see Fig. 3(b)) while still centred on the

original wavelength. The two spectra clearly show the 40GHz and 160GHz tones, while the specific feature, which is common between the two traces, due to the similar peak power considered, is due to nonlinearity (mainly self-phase modulation and XPM) in the HNLF.

To study the benefit of the rectangular switching window compared to more conventional shapes, the SSFBG was then replaced by a conventional narrow Gaussian filter that shapes the incoming signal into Gaussian pulses of similar shape as the rectangular pulses, see Fig. 3(a).

IV. PULSE RETIMING: EXPERIMENTAL RESULTS

In order to evaluate the performance of the proposed system, we started by examining the retiming properties at 40Gbit/s, since this data rate allows us to measure the RMS timing jitter at the FWHM point on the trailing edge of the pulse on a Digital Communication Analyser (DCA) using an Agilent Precision Timebase Module. The results are reported in Table 1 and clearly show that the pulses at the output of the Kerr switch have their jitter drastically reduced from ~420 fs to ~250 fs (close to the clock pulse jitter of ~210fs), albeit with a slight increase in amplitude noise. The corresponding amplitude noise standard deviation values for the marks $(\sigma_{a,i})$, which are normalized to their mean values, are also reported in Table 1. The increase in the switched pulse amplitude noise may be linked to the non-uniformity of the rectangular pulse shape, to slight spectral distortions of the rectangular pulses caused by self-phase modulation experienced during propagation in the HNLF and to the inline amplifiers used. This indicates than ideally an amplitude regeneration stage should also be incorporated within the system.

In order to confirm the necessity for a retiming system we first compare the performance of the system when the retiming stage is applied either through the use of the pulse shaping SSFBG or through a conventional Gaussian shape, to that when no retiming is applied at all (Fig.6).

We started by characterising the switched data signal at 40Gbit/s when no pulse shaping was applied. An error floor at a BER $\sim 10^{-7}$ was encountered for the maximum receiver power of ~-20 dBm. In contrast, with the SSFBG in place, error-free operation was achieved for all 4 retimed channels and the corresponding opened eye diagrams are reported in Fig. 7(a). We then replaced the flat-top pulses with ~5 ps Gaussian pulses (see Fig. 3(a)) and repeated the same measurements. At 40Gbit/s, error free operation was achieved with only a very small power penalty compared to the flat top pulse shape and the corresponding eye diagrams are reported in Fig. 7(b). These results reflect the relatively low amount of RMS timing jitter associated with the data signal. The RMS timing jitter associated to the data signal and the two pulse shapes (flat-top and Gaussian) are sketched in Fig. 8(a). If the data pulses exhibited a larger amount of timing jitter then a severe BER degradation would be evident in the

measurements for Gaussian pulse shaped data signal.

	Original pulse	Clock	Retimed pulse
		pulse	
τ _{rms} [fs]	420	210	250
σ _{a.j.} [%]	3.6	2.4	7.1

Table 1: Timing and amplitude jitter values at different points of the system for operation at 40Gbit/s.



Figure 6. a) BER characteristics of all channels at 40 Gbit/s and 160 Gbit/s with and without any retiming scheme. b) Receiver sensitivity for all channels at 40 Gbit/s when the data pulses are rectangularly shaped (triangles down) and Gaussian pulses of a similar pulse width (stars). The same measurements at 160Gbit/s for shaping into rectangular pulses only (triangles up). c) BER at 40Gbit/s and 160Gbit/s without any retiming scheme at the maximum receiver power of ~-20dBm

We then increased the bit rate of the signal to 160 Gbit/s and performed similar assessments. Once again, without retiming, an error floor at BER $\sim 10^{-6}$ was obtained for the maximum receiver power, as shown in Fig. 6(a). When flat-topped pulses were used, error-free operation for all 16 retimed channels was achieved. The spread in sensitivity

among the retimed channels is due to uneven amplitudes in the multiplexed pulses in the data and clock arms, and to polarisation drifts in the Kerr switch (see last three channels in Fig. 6(b)). The corresponding switched eye diagrams are reported in Fig. 7(c).

We then used the \sim 5 ps Gaussian pulses and complete closure of the eyes after the Kerr switch was observed (as shown in Fig. 7(d)) which precluded BER measurements. At this data rate, the smooth edges of the Gaussian shape exceed the time slot available (>6.25ps), causing a severe cross-talk between the different channels. On the contrary, the sharper



Figure 7: Eye diagrams after the Kerr switch for rectangular (a, c) and Gaussian (b,d) shapes at 40Gbit/s and 160Gbit/s respectively.



Figure 8: Sketch of the rectangular and Gaussian pulse shapes and the corresponding timing jitter of the data signal at 40Gbit/s (a) and 160Gbit/s (b).



Figure 9: Measured BER at a constant received optical power (-36 dBm) at 10 Gbit/s for flat top (triangles) and Gaussian (circles) shapes.

edges of the rectangular shape have restricted the switching window to the available time slot (see sketch in Fig. 8(b)).

Finally, we measured the BER at a constant received power of ~ -36 dBm as a function of the time delay between the data and the control signal and the results are shown in Fig.9. This measurement was performed at a data rate of 10 Gbit/s to avoid the need for the demultiplexer. Error free performance was readily achieved over a window of ~1ps when the rectangular pulses are used. This is in contrast to the very narrow window provided by the 5ps Gaussian pulses, as also shown in Fig.9. The two shaded regions in the centre of Fig.9 indicate the conditions of error-free operation for the two different configurations, emphasizing the large improvement achieved when using rectangular pulse shaping. The same figure also shows the measured rectangular pulse shape with its associated level of timing jitter of τ_{rms} ~420 fs (shaded region), which justifies the limited extent of the effective retiming window. If the inherent timing jitter associated with the data pulse source could be eliminated, then we anticipate that error-free operation over the entire flat-top duration of the rectangular shape would have been possible.

V. CONCLUSION

In conclusion we have experimentally characterised a pulse retiming system based on a pulse shaping SSFBG technology. Thanks to the flexibility offered by plane-by-plane grating writing technology, we were able to generate ~5ps rectangular pulses and demonstrate a retiming scheme at repetition rates up to 160 Gbit/s. Using a flat top switching window, we clearly demonstrate a substantial performance enhancement, when compared with conventional (Gaussian) shapes, as confirmed through detailed BER measurements. Pulse shaping with SSFBGs has a range of attractive features including simple implementation, and robust, polarization insensitive operation.

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