This is a preprint of a paper intended for publication in a journal or proceedings. Since changes may be made before publication, this preprint is made available with the understanding that it will not be cited or reproduced without the permission of the author.

UCRL - 77548 PREPRINT CONY-760609--4



# LAWRENCE LIVERMORE LABORATORY

University of California/Livermore, California



AN ANALYTIC TECHNIQUE FOR ROUTER COMPARISON

David C. Wilson and Robert J. Smith, II

November 26, 1975

The report was present as a second of work property was present as a second of work property with the least second property with the least second property was a second property with the least second property was a second property with the least second property was a second property with the least second property was a second property with the least second property was a second property was a second property with the least second property was a second p

This paper was prepared for submission to the 13th Design Automation Conference, Palo Alto, CA June 27-29, 1976

DISTRIBUTE

## AN AMALYTIC TECHNIQUE FOR POUTER COMPARISON

David C. Wilson 2602 D Custer Forkway Richardson, Texas 75080

and

Robert J. Smith, II Flectronics Engineering Department Lawrence Liversone Laboratory Livermore, California 94550

INTRODUCTION

Obsign automation of electronic systems is generally separated into a number of distinct areas of effort. Breuer [1] has divided design automation into the areas of logic synthesis, gate simulation, partitioning, placement, routing, and fault detection and diagnosis. In the line separation may not be complete or entirely accurate, these functions generally must be perforued.

At some point in the design process the components have been chosen, the logical interconnections specified, and the components placed on a printed circuit board; the next step is the physical interconnection of electrically common elements. Numerous techniques have been proposed to solve the resulting interconnection or routing problem, with alrest endless minor variations on these techniques possible [3].

Lee's algorithm [2] is one of the few true algorithm spplitchle to the routing problem; it guarantees that an interconnection between two points will be found if a sotisfactory path exists. It is an exhaustive algorithm, as many true algorithms are, exploring all possible paths. Ippically it is qu'te slow in execution because all possible paths are exclored in parallel. A number of modifications to this basic technique have been proposed; most change the algorithm to a heuristic (it can no longer guarantee a solution will be found) in orde: to realize a gain in execution speed.

There are also many heuristics of very different kinds of solving the routing problem; nearly any possible technique for tracing lines is feasible, but results cannot be guaranteed. However, many heuristics run very rapidly in comparison to exhaustive algorithms and appear to do a "measonable" interconnection job.

One general classification scheme applicable to most routers is when her they are depth- ur broadth-first routers. That is, do they caplore a path to a great depth chaosing to explore another when failure is encountered, or do they explore multiple paths in parallel, stopping when the endpoint is reached by any path. Lee's algorithm is a breadth-first algorithm. Most houristics use

This work was performed under the auspices of the U.S. Energy Pasearch and Development Administration under contract runber W-7507-ENG-43.

depth-first approaches which attemnt to reduce execution time.

There has been some experiment tion with polyrouters. i.e., two or more rout—s operating in landum. Often a fast heuristic dep -first router is used to make as many connections followed by a slower, more expensive router attempting only those connect is that could not be made by the first state of the router.

Most of the nouters discussed in current literature are experientally evaluated rent some indication of their success, with performance operatly expressed as percentage completion figure for the number of wires routed on a board, compared to the number of wires attempted. There are few analytical tools, however, to indicate or predict router performance.

This paper introducs a model for printed circuit beards which can be used to predict the probability that a router will successfully make a connection. The model reflects certain characteristics of the circuit board that is being routed and the model incrementally changes as the board is routed. Routing procedures typically have certain parameters which influence selection of the set of paths that are explored, and determine the order of exoloration. These parameters obviously influence the probability that a particular connection can be made. These parameters may also be used to formulate a model of the behavior of a particular rounce to thing procedure.

The purpose of the board and router models proposed here is to allo. (comparative) predictions of router performance. Nore specifically, it is assumed that the features of a router which select the set of paths explored should impact the probability that a router will be successful, as well as router execution time. Relationships between execution time and performance can be analyzed using the concepts developed below.

This study also attempts to address several other questions: What factors cause a router to be good and how could a routers' performance be increased keeping the same general heuristic; which router in a polyrouter should be used for a particular connection or when should the change-over between rc'ers be made; and finally in what order should the connections be made to give the greatest expected number of completed connections?

.

Additionally none of these questions are answered fully in this paper. However, the "general analysis technique" way hold the patential for treating such issues. It shall also me ephastized that but parent and rayter behavior regists are statistically listed, and therefore no mutuarantee that a particular connection can be made between any two points; the approach taken will be to produce, for each connection, a probability that a satisfactory path will be tood. The statisfactory path will be tood, the country functions are performed Juring the computation of this probability.

#### THE BASIC MODELS

The model of a printed circuit board is based strictly on the 'density' of a board. Evereal authors [4,6] have noted that as a board becames more crowded (additional wires completed) it becomes harder to route wires, as would be expected. At a certain density it becomes nearly impossible to route an additional wire. This idea forms the basis of the problem board model.

The model assumes that each layer of a printed circuit board can be represented as a rectangular grid of small squares, each of which is either empty (can be used) or occupied (further use is illegal). The density of occupied straines (or just density) is defined by coustion (I) as the ratio of the number of a guide squares to the total number of squares all layers of a board. As density is just the probability of a square being used, equation (2) defines the probability F of a square being rew.

The initial modeling efforts reported here have been focused on three general classes of boards:

- MODEL 1. A board is filled with randomly distributed occupied squares. but no lines or groups of connected grid squares are included. This rodel might resemble a board containing randomly placed component pads.
- MODEL 2. The board has randwilly distributed continuous segments of occupied squeres, with all wires naving both expected width and expected length equal to constant values. This model screwhat resembles a partially routed bound, but is more regular in its layout.
- MOULD 3. The board has rung-12 distributed contiguous separats of occupied source, tiguous separats of occupied source, expected with and a random length chosen from some given distribution. This model attempts to zone accurately represent a partially routed printed circuit hourd.

Our modeling effort is based on the following argument. If the density of the board is calculated by equation (1), then the probability that a square is occupied is 0, and the probability

that a square is free i; F by equation ()). For a path of length n, the probability that a line can be noted exactly that ustance is it: probability that n free squares car be found followed by n occupied square, and is given by equation (3). The orotability that a line can be routed between two points, nowever, it not the probability that it can be routed exactly the distance between those points, but rather the probability it can be routed at least that distance, and is given by equation (4), which is simplified to equation (5).

(3) Probability of success for length n = P<sub>v</sub>(length=n) = fn \* D = (1-D)<sup>n</sup> \* D

(4) 
$$P_{\mathbf{x}}(1-0)^{L} = \sum_{k=0}^{\infty} (1-0)^{k} \cdot 0 = 1 + \sum_{k=0}^{n-1} (1-0)^{k}$$

$$P_{S}\{\lambda \stackrel{>}{>} n\} = 1 - D \cdot \{1-D\} \stackrel{\bullet}{>} D - \{1-D\}^{2} \stackrel{\bullet}{>} D$$

$$- \dots \dots - \{1-D\}^{n-1} \stackrel{\bullet}{>} D$$

$$\{1-D\} P_{S}\{\lambda \stackrel{>}{>} n\} = \{1-D\} - \{1-D\} \stackrel{\bullet}{>} D - \{1-D\}^{2}$$

$$\stackrel{\bullet}{>} D - \dots \dots - \{1-D\}^{n} \stackrel{\bullet}{>} D$$

$$P_s(z \ge n)(1-1+0) = (1-0)^n * 0$$
  
(5)  $P_s(z \ge n) = (1-0)^n = r^n$ 

In a similar manner the expected length for a routed wire can be calculated using the normal forumis for expected value calculations, equation (6).

(6) E(length) = 
$$\sum_{\text{Tength=0}}^{\infty} \text{length * } P_s(\text{length})$$

(7) 
$$E\{length\} = \sum_{n \in O} n \cdot (1-D)^n \cdot D$$

E(length)= 
$$(1-D) \cdot D + 2(1-D)^2 \cdot D + 3(1-D)^3$$
  
 $\cdot D + \dots$   
 $(1-D) \text{ E(length)} = (1-D)^2 \cdot D + 2(1-D^3) \cdot D$ 

{1-D) 
$$E(\text{length}) = (1-D)^2 \cdot D + 2(1-D^3) \cdot E + 3(1-D)^4 \cdot D + \dots$$

E(length) - (1-D) E(length) = (1-D) 
$$^{\circ}$$
 n + (1-D) $^{\circ}$   $^{\circ}$  D + (1-D) $^{3}$   $^{\circ}$  D + ..... D  $^{\circ}$  E(length) =  $\sum_{n=1}^{\infty}$  (1-O) $^{n}$   $^{\circ}$  D

(8) 
$$E(length) = \sum_{n=1}^{\infty} (1-D)^n = \frac{1}{1-(1-0)} = \frac{1}{0}$$

The expected length value of equation (8) is of a very simple form, just inversely proportional to the density of the hoard. Using rodel 1 a series of experiments were conducted to demonstrate that this was in fact an accurate model of the environment. For this experiment (and all others referenced here) the wire width was taken to be

one unit. Heres of greater width could be handled mathematically in a very similar manner. The following analysis illustrates the general technique, which uses the unit wire width assumption.

Figure 1 shows the results of routing experients, based on board which 1. A very high degree of agreement between reporting 8 and the price; mentally determined values was found. The closeness of the agreement is not surprising, because the mathematical model developed is esplicitly for the situation that was modeled: single, randomly placed used squares.

When the used squares are not single points (e.g., there are continuous occupied say ents of squares such as wires), then the probability of encountering an occupied square is queatly modified. When modeling a router which places where in our single direction on each layer of a printed circuit board, the probability of interference is related to the number of distinct segments of squares, rather than the number of occupied squares.

To develop equations for the effective density, and expected length for bourds with wire segments, the modeling technique shown in figure 2 is used. Figure 2 a Shows a line to be run, with one already-routed line segment in the path. The previously routed lines are collapped sints single points (shown in Figure 2b). A correction term is then applied to the expected length calculation to account for the finite length of the previously routed wires; this term is the average segment length divided by 2. Equations 9 and 10 reflect the results of these calculations.

(10) E(length) \* E(1) \* 
$$\frac{1}{p_e}$$
 - (average segment light)

Running parallel wires has reduced the effective density of a board; intuitively this approach has reduced the conjection. This suggests the importance of minimizing the number of distinct wire segments that must be used. Figures 1 and 4 summarize expertisents for board models 2 and 3. The average segment length of coustion 10 requries a priori knowledge of the segment lengths developed during routing. Obviously this value cannot be computed exactly before requiring or could be accumulated as routing proceeds if it is needed.

Figures 3 and 4 show agreement between the second model and the corresponding experiental values, although with increased error over the values in Figure 2. This increased error is the result of the variance in the average wire lengths, and could have been anticipated. Equations 9 and 10 could be applied to board notel 1 as well, since the number of osed segments is the number of occupied sypares in this case.

Using the expected density values, the probability of success on a path (equations 3 and 4) can be calculated. These probability values are modified because of the finite segment length for the kires that are present, and are given

again as equations (3a) and (4a):

(3a) 
$$P_e(1=n) = (1-D)^{n^e}$$
 (average segment lgth/2) D

Note that using equations 9 and 10 the expected number of successful pairs on a complete route may be calculated, as the number of paths tried times the per callity of success for each path. The vertices of quation (10) can also be calculated to give a confidence factor for routing. It is also of interest, though, to obtain equations for the probability of success P, for several routers and attempt comparisons dising this type of analysis.

#### MODELING SXAMPLES

A roadfreation of Loe's algorithm is the 'router-in-a-box' version [7]. As shown in Figure 5 the two points to be interconnected form the corners of a two-dirensional rectongle, and Lee's algorithm is applied within that rectongle. Intuitively this restriction appears justifiable, since the renaining three quadrants not used for routing generally lead easy from the destination point, and are therefore likely to be unproductive.

Figure 6 shows an enlargement of one area of figure 5, wit: a source at location (x,y) in the box, and the rour adjacent neighboring cells. The routing protecture is assumed to be up and to the left; the problem is to find the probability of routing from point (x,y) to point (),1). This is a function of the probabilities of success from two neighbor points only (because of the algorithm used), and is given by equation (1).

(11) 
$$P_{S}(x,y \text{ to } 1, t) = f * P_{S}(x-1,y) * F * P_{S}(x,y-1)$$
  
 $+ (1-F) * F * P_{S}(x-1,y)$   
 $+ (1-f) * F * P_{S}(x,y-1)$   
 $+ F * P_{S}(x-1,y) * F *$   
 $(1-P_{S}(x,y-1))$   
 $+ F * P_{S}(x,y-1) * F *$   
 $(1-P_{S}(x-1,y))$ 

Equation [11] is an iterative equation, but is easily tabulated becuase the boundary conditions are known. Figure 7 shows the boundary conditions for the two box sides, and the remainder of the values mry be tabulated by a straightforward application of [11] along either the rows or the columns.

For certain points in Figure 5 an interesting phenomena occurs: The probability of success may be uniform in a region of the board. That is, equation (11) yields

which can be spenlifted to

Marie Commission of Continued to the Con

(13) 
$$P_s(x,y \text{ to } 1,1) = x = \frac{2 + \frac{p}{r^2} - 1}{r^2}$$

This equation is graphed as Figure 8.

This is an unexpected and surprising result: The probability of completing a such between two points in the uniform region is inadependent of the path length between those point; and december only upon the board devity. This result wis suggested as possible by iconing [5], but is not obvious. Clearly, however, it is a very desirable characteristic for any router! Figure 8 also shows a relatively sharp cut-off point for routing at a board density of about 35; a figure which has been observed [4] in other work.

Path length independence is not valid over the entire box of Figure 5. The boundary conditions cause certain regions to have different (and generally much lower)  $P_{\rm v}$  values: these regions are shown as the shaded area of figure 9. The extent of this region is dependent only on the board density. The method used to calculate board ensity is the method used to calculate board entire board, using equation 11.

The path length independence formulas developed above do not nold for the original Lee algorithm. In this latter use the equation corresponding to equation (.3) is a cubic in  $P_{\rm g}$ , implying that there are (potentially) three distinct regions of stable probability. Although the original Lee algorithm gives a higher probability of Successful routing than the heuristic described above, the analysis is also much normal difficult. Equation (13) might well be used as a more directly obtainable lower bound on performance for the Lee algorithm.

Consider next a quite different routing heuristic used previously by the athors [6]. It can be described as a one-turn line probe router which attempts paths such as those shown in Figure 10a and 10b, with a parameter k designating hom many lines are attempted in each direction. The equation for the  $\rho_{\rm S}$  (interconnection of two points) is developed below, culminating in equation (14f)

(14s) Probability of failing to route at least one line, i units long:

$$\frac{k}{\pi} (1 - F^K - F^2)$$

(14b) Probability of success for previous (14a):

$$1 - \prod_{K=-k}^{k} (1 - F^{(K)} - F^2)$$

(14c) Probability of a successful interconnection over one cross of Figure 9:

$$(1 - \frac{k}{77}(1 - F^{[K]} - F^{\bar{\chi}})) \cdot (1 - \frac{k}{77}K^{c-k})$$

(14d) Probability of failure of (14c):

$$1 - (1 - \frac{k}{2r}(1 - F^{\frac{1}{2}k} | F^2)) \cdot (1 - \frac{k}{2r} | F^2 | F^2))$$

(14e) Probability of failure, on both crosses, of

$$(1 - (1 - \frac{k}{\pi}) (1 - F^{\{K\}}F^2) \cdot (1 - \frac{k}{\pi}) K^{n-k}$$

$$(1 - F^{\{K\}}F^n))^2$$

(14f) P for a route:

1 - {1 - (1 - 
$$\frac{k}{\pi}$$
 {1 -  $F^{[K]}F^2$ )} · ( ` -

$$\frac{k}{\pi} (1 - F^{|K|}F^{m})))^{2}$$

This equation is of limited use; however reasonable approximations can be made. First, assume that k < k and k < m, to yield (15a). Then, assume i = m (meaning the two points to be interconnected determine a square, to yield (15b), (15c), and (15g)

(15a) 
$$1 - (1 - (1 - \frac{k}{\pi} (1 - F^2)) \cdot (1 - \frac{k}{\pi} p_{n-k})$$

$$(1 - F^{m}))^{2}$$

(15b) 
$$1 - (1 - (1 - \frac{k}{\pi}(1 - F^{\ell}))) \cdot (1 - \frac{k}{\pi})^{p-k}$$

(15c) 
$$1 - (1 - (1 - \frac{k}{\pi})^2)^2$$

(15d) 1 - (1 - (1 - 
$$f^2$$
)<sup>2K+1</sup>)<sup>2</sup>)<sup>2</sup>

Equation (14f) has the property that is maximized if the twp points being interconnected are located at the corners of a square, rather than at the corners of an elongated rectangle, and the value of the equation drops off rather sharply if the two points form a very elongated rectangle. Equation (15) assumes (optimistically) that the point pairs alknys determine a square, rather than a rectangle, and thus (15) represents a maximum potential performance figure for the router. The length of the path being routed does clearly effect the P, value for this router.

because of the 1 term in the equation. Table 1 shows the P<sub>g</sub> value for a range of effective board densities. Experience (6) with this couter has indicated that it functions reaconably well until the effective density reaches between 4 and 5., which is verified by Isole i.

Table 1

ŧτ

fective Density :	P <sub>s</sub> ~	
2 1	100	
41	98	
28	23	
81	29	
101	12	

If the primary rectangle shape is an elongated rectangle, equation (15) is not valid. Since (15) represents a maximum performance figure, however, it should influence the objective function used by any placement method: obviously minimizing the distance between modules does not necessarily maximize, are routability of a board is quation (14) is not easily reducted. It is possible to assume an 'average' rectangle, but this is not mathematically justfiable. Additional work needs to be done to determine how the shape of the rectangle formed by the two points to be interconnected effects routability.

The experimental results described in the previous sections used statistical smalation models rather than production resules and production circuit boards. Nevification of the techniques suggested in this span requires that production conformants be used. We are projectly, in the reformants be used. We are projectly, in the reformants be used. We are projectly, in the reformants be used there are in the results and the models presented here, using the results all turns cause layer changes giving parallel lives on each layer), and a many-turn line probe router are presently nearing completion for production use in the Design Automation System at Lawrence Livernore Laboratory. A series of experiments using production boards and constrained versions of the routing programs is being planned and conducted. Using hell understood routers and known module piace-ent and interconnection requirements, the experiments are intended to verify or contradict the models presented

Experimental results will be reported in a later version of this paper.

### ORDERING

diven a set of point-to-point interconnections, the ordering problem attempts to ordering the problem attempts to ordering the expected number of completed where. Previous work [4] indicates no clear significance for any particular orderings: although in oach case a particular ordering may be best post priori, no ordering scheme seems clearly superior for the general case.

Our model gives one solution to this problem, based on the  $P_g$  figure.  $P_g$  is a function of D, the path length, and the router. This function can be determined, as illustrated above. To properly order the wires for routing it is necessary to maximize the sum of the  $P_g$  values for each wire, to obtain the kapected number of wires that will be completed (equation (16)).

(16) maximize 
$$\sum_{m=1}^{\infty} wires P_{g}$$
 (interconnection m)

For example, the Lee heuristic above has  $P_s$  given by (13), where D increases proportionately given to the length of each routed wire. The longer the wire that is completed the more the board density is increased for the unrouted wires. To maximize (16) it is clear that short wires should be routed first.

For a more general case, such as the second router heuristic discussed, the situation is far more complex. Dincreases with long wires fester than with stort ones, but P<sub>j</sub> decreases both with increasing length of the path and with increasing D. The conclusion about ordering here is very dependent upon the router heuristic, the P<sub>j</sub> equation, and the distribution of wires that must be completed.

Assuming the function for F shown in Figure 11, the desired ordering again is shortest wire first to maximize (16). Note that this is wire-length distribution dependent in general, by the argument in the preceding paragraph. It should also be noted that differences in the expected values for different orderings are sometimes very small in a numerical sense, which may be the reason that previous experimental results were inconclusive: this figure is also subject to statistical variance related to the from-to length distribution. Ae have yet to find a practical router where shortest wire first does not maximize the expected completion value provided by the mechanism presented above.

# MODEL LIMITATIONS

be acturate. The model assumes when a model will be acturate. The model assumes when a model will distributed over the took area, while the whys usually are clustered towards the center of a normal beard. Further, the model implicitly assumes an infinite board, but there are noticable edge effects on finite mards. These edge effects may be compensated for at some expense in model complexity. Ferhaps the strongest limitation of the model is that it is derived from our board model? I will be out of the model is that it is derived from our board model? I are the content of the model is that the strongest limitation of the model is that the board model 3 is much more realistic. The corrections that must be made to obtain model in an absolute sense, but are closer to 'educated' guesses that could be experimentally verified.

### CONCLUSIONS

A model has been presented for printed circuit board routing. The model is statistically based, and is predictive in that statistical sense. The model is supported by limited experimental evidence and verification, and conforms to several interesting and diverse observable features of the routing

From our calculations several interesting Ideas have been developed; some have yet to be verified or disproven. For both the Lee "router-in-a-box" heuristic and the line probe router the best Ps values are found when the counts to be interconnected are located on the corners of a schare, and not necessarily when they are claser together or located on the corners of a rectangle. This suggests that doing placement of components should attempt to minimize an objective function which considers primary rectangle shape factors as well as minimum distance.

For the line probe router the parameter k (number of wires to run in parallel) could be chosen, using equation (15d), to achieve a certain Ps value (related to confidence required) prior to routing. This would give the router a degree of adpative behavior depending upon its environment, and could potentially be quite effective in reducing computation time.

The model allows comparison of expected router performance as a function of the density of a performance as a function of the density of a printed circuit board and the routing heuristic, and thus allows comparison of routers and their cost/performance tradeoffs. The robel also gives some insight into the ordering problem, and supports the intuitively reasonable influence of ordering on performance of a particular router.

### REFERENCES

- REFERENCES
  (1) Brewer, M. A., "Design Automation of Digital
  Systems," V. 1, Prentice-Hall, 15:2
  [Lee, C. Y., "An Algorithm for Path Connection
  and its Applications," IEE framework.
  Electronic Computers, EC-10. 3, pp. 346-365
  [3] Milson, D. C. and Smith, R. J., "A Bibliography on Computer Aided Routing and Placement,"
  SIGDA NewSietter, V. 4, \*1, January 74,
  no. 16-45.
- SIGDA NewSetter, V. 4, =1, January 74, pp. 16-42.

  [4] Abel, L. C. Vin the Ordering of Connections for Automatic Wire Royling, IEEE Trans. on Computers, Nov. 1972, pp. 1727-1723.

  [5] Leonard, P. G., Private communication, 1974.

  [6] Wilson, O. C. and Smith, R. J., An Experi-
- mental Comparison of Force Directed Placement Techniques," Design Automation Workshop, 1974.
- [7]

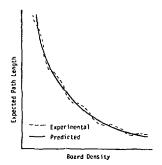


Figure 1. Model 1 Experimental Verification (10G0 paths per density).



Za. Actual Situation



Figure 2. Model 2 Development



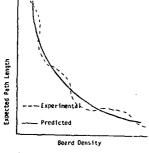
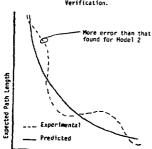


Figure 3. Board Model 2 Esperimental Verification.



Board Density
Figure 4. Board Model 3 Experimental
Verification.

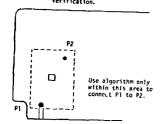


Figure 5. Constrained Lee Algorithm.

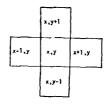


figure 6. The Adjacent Square Relationships.

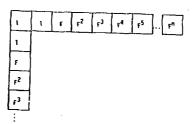


Figure 7. Boundary Conditions Imposed on Constrained Lee Router.

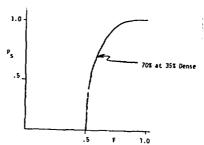


Figure 8. Probability of a Successful Route as a Function of F.

Figure 9. Edge Effects and Path Independence.

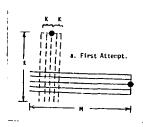
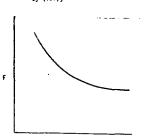




Figure 10. A Simple One-Bend Routing Algorithm.



11a. Connections Rooted Shortest First [Maximizes (16) if Router is Hodeled by (15f)]



11b. Longest First Routing. [Minimizes (16) if Pouter is Modeled by (15f)]

Figure 11. Distribution of Interconnection Outcomes.