An Enhanced Widely Linear CDMA Receiver With OQPSK Modulation

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Abstract—This paper studies an enhanced widely linear (WL) receiver for direct-sequence code-division multiple-access (DS-CDMA) systems that employ aperiodic spreading sequences with offset quadrature phase-shift keying (OQPSK) modulation. The modulation scheme generates improper complex multiple-access interference (MAI) and necessitates the use of WL receivers. Focus is on bandlimited pulse shapes and the inherent cyclostationarity (CS) of the uncoded received signal. The enhanced WL receiver replaces the conventional chip-matched filter with new filters that exploit the CS of the received signal through frequency shifting. The proposed WL receiver is shown to outperform the classical strictly linear (SL) receiver when the interfering users are (quasi-)synchronous with respect to the user of interest. High-powered interfering users, that may exist to support high data rates, increase the performance improvement delivered by the WL receiver. Moreover, it is shown that MAI can become proper, either identically or asymptotically, when users are asynchronous and equally powered. This is despite the fact that individual interfering signals are improper. Numerical results demonstrate that the WL receiver can outperform the SL receiver by 1-3 dB under the examined scenarios with current CDMA standards settings. In asynchronous or quasi-synchronous transmission modes, performance gain of the WL receiver degrades unless the number of high-powered active users remains small. An example for implementation of the WL receiver is proposed and compared with that of the SL receiver when minimum-shift keying modulation, a special case of OQPSK, is used. The implementation is based on a fractionally spaced equalizer whose taps are updated by an adaptive algorithm. It is shown that the proposed structure is capable of delivering the maximum signal-to-noise ratio predicted by theory.

Index Terms—Bandlimited pulses, code-division multiple-access (CDMA), cyclostationarity (CS), fractionally spaced equalizer (FSE), improper noise, linear/conjugate-linear (L/CL) filtering, minimum-shift keying (MSK), offset quadrature phase-shift keying (OQPSK), widely linear (WL) receiver.

I. INTRODUCTION

O PTIMUM filtering of complex envelope signals requires replacing the classical strictly linear (SL) minimum mean-square error (MMSE) receiver with the linear/conju-

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gate-linear (L/CL) structure which processes both the received signal and its complex conjugate [1]. An alternative terminology for L/CL filtering is widely linear (WL) filtering, which was initiated in [2] and has prevailed over the last decade as the more popular. WL receivers find their application when the complementary autocorrelation (or pseudocovariance) functions of the complex envelope signals are nonzero, i.e., the signals are improper [3]. Improperness may originate from either the modulation scheme or coding method. A few examples of improper signal constellations are binary phase-shift keying (BPSK), offset quadrature phase-shift keying (OQPSK), minimum-shift keying (MSK), *M*-ary amplitude-shift keying (ASK), and simplex signals in general. The space–time block coding (STBC) scheme of [4] is also a coding example that results in improper signals.

This paper studies an enhanced WL receiver for OQPSKmodulated direct-sequence code-division multiple-access (DS-CDMA) systems that employ aperiodic pseudonoise (PN) sequences and bandlimited pulses. OQPSK (or staggered QPSK) modulation delays the quadrature signal component with respect to the in-phase component by a time offset equal to a fraction of a symbol period. Such delay limits the phase jumps to $\pm \pi/2$ compared with $\pm \pi$ and $\pm \pi/2$ in QPSK [5]. Current long-code CDMA standards (IS-95 and cdma2000) have adopted OQSPK modulation [6], [7].

Previous work on WL receivers for CDMA systems includes, but is not limited to, [8]–[16]. The importance of processing the complementary autocorrelation function in CDMA systems with improper multiple-access interference (MAI) was first demonstrated in [8] with BPSK modulation. In [9], the concept was further extended by deriving suboptimum and blind adaptive implementations. Performance analysis of the proposed receiver in [9] is presented in [10]. Blind adaptive WL receivers are also discussed in [11]–[13], where the authors arrive at implementations with different degrees of complexity. In [14], a low-complexity training-based adaptive WL receiver is discussed. WL equalization in the presence of the STBC scheme of [4] is the subject of [15]. Finally, in [16], the benefits of WL processing in frequency-selective fading channels are explored.

Attention to bandlimited pulses and cyclostationarity (CS) of the uncoded received signal are conspicuously absent in existing works on WL receivers for CDMA systems (including the aforementioned), and is the subject of this paper. Prior art has either explicitly assumed time-limited chip pulses (e.g., [8]–[12]) or considered discrete outputs of the chip-matched filter (CMF) with perfect carrier synchronization for all users

(e.g., [13]–[16]). In this paper, focus is on the pre-CMF received signal, which is known to be a wide-sense cyclostationary (WSCS) random process [17]. By applying the frequency-shifting concept of [18] for bandlimited WSCS signals, new filters are designed that outperform the conventional CMF, known to maximize signal-to-noise ratio (SNR) only for Gaussian wide-sense stationary (WSS) signals. It will be shown that performance improvement of the WL receiver is directly related to the excess bandwidth of the chip pulse and chip delays of the interfering users.

Since the focus and approach of this work fundamentally differ from those in prior papers (e.g., [2] and [9]–[14]), the distinguishing features and their consequences on the receiver structure are clarified as the receiver formulation proceeds in Section III.

The contributions of this paper are as follows.

- The frequency response of the WL receiver for bandlimited pulses is derived and compared with that of the SL receiver. The extra components originating due to improper MAI are identified and analyzed. Expressions for SNR of both receivers are also presented and contrasted. A link between the concepts of (im)properness and CS is established. Moreover, a practical approach to the implementation of the WL receiver is also discussed. The implementation is based on an adaptive fractionally spaced equalizer (FSE) and is shown to deliver the theoretical maximum SNR. To disseminate the basic ideas and avoid notational complexity, this work proceeds in the additive white Gaussian noise (AWGN) channel. The design can be extended to frequency-selective channels, as was done in [19].
- Cases when interference becomes proper are identified. It is shown that for the ideal Nyquist chip pulse, interference turns to be a proper WSS random process and, consequently, the WL receiver reduces to the SL receiver. More interestingly, it is shown that if chip delays of the interfering users satisfy certain conditions, the pseudocovariance of MAI becomes identically zero, even though the individual interfering signals are improper. Users meeting these conditions are referred to as symmetric users. A large number of asynchronous users is shown to asymptotically form a symmetric set and make the MAI proper.
- Scenarios where the WL receiver delivers significant performance improvement over the SL receiver are discussed by contrasting the above cases. The WL receiver is shown to outperform the SL receiver when the interfering users are (quasi-)synchronous and high-powered with respect to the user of interest. This may arise in future generation systems that support high-data-rate services. Numerical results are presented to quantify the performance improvements.

The material presented in this paper applies to both OQPSK and MSK modulation schemes, as they only differ in the pulse shape. Numerical results, however, are more tailored toward the more spectrally efficient OQPSK with the square-root raisedcosine (Sqrt-RC) pulse. The paper is organized as follows. Section II describes the system model. The design of the WL receiver is discussed in Section III. Section IV analyzes the performance of the WL receiver and compares it with that of the SL receiver. Section V presents an example for implementation of the receiver. Numerical results are presented in Section VI, with concluding remarks in Section VII.

II. SYSTEM MODEL

Long-code DS-CDMA systems with OQPSK modulation is under review. This section describes the modeling of the received signal. The desired user is arbitrarily indexed with zero.

The received signal can be expressed as

$$r(t) = \sum_{k=0}^{K} s_k(t) + \eta(t)$$
 (1)

using the complex baseband representation of passband signals. The second term $\eta(t)$ is a complex, circularly symmetric, zero-mean AWGN process with a two-sided noise power spectral density (PSD) of \mathcal{N}_0 . The first term represents the sum of the received signals from each user, where $s_k(t)$ is the signal of user k. The baseband representation of $s_k(t)$ can be expressed as

$$s_{k}(t) = \sqrt{\frac{P_{k}}{2}} e^{j\theta_{k}} \sum_{n=-\infty}^{\infty} b_{n}^{(\mathbf{I},k)} a_{n}^{(\mathbf{I},k)} \left(t - nT_{b}^{(k)} - \tau_{k}\right) - j b_{n}^{(\mathbf{Q},k)} a_{n}^{(\mathbf{Q},k)} \left(t - nT_{b}^{(k)} - \tau_{k} - T\right)$$
(2)

where the in-phase (I) and quadrature-phase (Q) signal components are appropriately notated. The symbols P_k , θ_k , and τ_k represent, respectively, the signal power, phase offset, and arrival delay of user k. Code acquisition, carrier-phase and bit symbol timing synchronization for the desired user are assumed such that $\tau_0 = 0$ and $\theta_0 = 0$. The time offset corresponding to OQSPK modulation is constant for all users, and represented by $\mathcal{T} \in [0, T_c]$ where $1/T_c$ is the system chip rate.

The spreading waveforms $a_n^{(I,k)}(t)$ and $a_n^{(Q,k)}(t)$, used to spread the information symbol $b_n^{(k)} = b_n^{(I,k)} + jb_n^{(Q,k)}$, are

$$a_n^{(\mathcal{B},k)}(t) = \sum_{i=0}^{N_k - 1} a_{i+nN_k}^{(\mathcal{B},k)} q(t - iT_c)$$
(3)

where $a_i^{(\mathcal{B},k)} \in \{\pm 1\}$ represents the *i*th chip of the PN sequence corresponding to the \mathcal{B} branch of user k. The PN sequences are assumed to be equally likely independent and identically distributed (i.i.d.) random sequences with independence between the chips of different users. Mutual independence is also assumed among the bits and PN sequences of the I and Q branches. The spreading sequences associated with $b_n^{(\mathcal{B},k)}$ are defined as $a_n^{(\mathcal{B},k)} = [a_{nN_k}^{(\mathcal{B},k)}, a_{nN_k+1}^{(\mathcal{B},k)}, \ldots, a_{nN_k+N_k-1}^{(\mathcal{B},k)}]$. The ratio of the bit period of user k to the chip period, $N_k = T_b^{(k)}/T_c$, is referred to as the spreading factor of that user. The chip pulse q(t) is assumed to be real and bandlimited, either accurately or approximately, with an excess bandwidth of α . For instance, the Sqrt-RC pulse with α excess bandwidth is strictly limited to $|f| \leq (1 + \alpha)/(2T_c)$. The half-cosine pulse, on the other hand, can be truncated in the frequency domain to approximately represent a bandlimited pulse, where its excess bandwidth α depends on the desired level of accuracy. The processing gain (or bandwidth-expansion factor) of user k is $N_k(1 + \alpha)$.

The above setting covers most radio configurations (RC) defined in IS-95 and cdma2000 standards [6], [7]. For instance, binary information bits $(b_n^{(I,k)} = b_n^{(Q,k)})$ with OQPSK spreading $(\mathcal{T} = 0.5T_c)$ results in reverse fundamental and supplemental channel structure of RC1 and RC2. Alternatively, quaternary information bits $(b_n^{(I,k)} \neq b_n^{(Q,k)})$ with QPSK spreading $(\mathcal{T} = 0)$ results in a reverse traffic channel structure of RC3 and RC4. Furthermore, the OQPSK signals become MSK signals with $\mathcal{T} = T_c$ and a half-cosine pulse as the chip pulse [20].

The system model also supports multirate CDMA since, in general, $N_k \neq N_{k'}$ for $k \neq k'$, leading to multiple bit rates for different users. Moreover, if the baseband signal defined in (2) experiences a delay of τ_k , it can alternatively be expressed as

$$s_{k}(t) = \sqrt{\frac{P_{k}}{2}} e^{j\theta_{k}} \sum_{n=-\infty}^{\infty} d_{n}^{(\mathbf{I},k)} q \left(t - T_{k} - nT_{c}\right) -jd_{n}^{(\mathbf{Q},k)} q \left(t - T_{k} - nT_{c} - T\right)$$
(4)

where

$$d_n^{(\mathcal{B},k)} = b_{\lfloor (n-J_k)/N_k \rfloor}^{(\mathcal{B},k)} a_{n-J_k}^{(\mathcal{B},k)}$$

 $J_k = \lfloor \tau_k/T_c \rfloor$, and $T_k = \text{mod}(\tau_k, T_c)$ represent, respectively, the direct-sequence spread bits, the bit delay as an integer multiple of T_c , and the bit delay *modulo* the chip period such that $T_k \in [0, T_c)$ and $\tau_k = J_k T_c + T_k$. The floor function $\lfloor x \rfloor$ returns the integer portion of a real number x. The term T_k is referred to as the chip delay of the kth user. With such reformulation, from the perspective of user 0, $d_n^{(\mathcal{B},k)} \in \{\pm 1\}$ can be effectively modeled as equally likely i.i.d. random sequences. Thus, the effects of the interferer bits and integer bit delays disappear under the random spreading assumption.

III. WIDELY LINEAR RECEIVER

This section treats the design of the WL receiver. The design approach employs the same tools used in [19] and [21]. The distinguishing feature, however, is the presence of improper complex noise, which results in two distinct integral equations to be solved. The integral equations are first presented, followed by their solutions and a discussion on the structure of the WL receiver.

A. Integral Equations

For optimum filtering of complex envelope signals, the conventional SL receiver structure must be replaced by the L/CL structure of Fig. 1 [22]. Both the received signal and its conjugate are linearly processed by two time-variant filters $\phi_n(t)$ and $\psi_n(t)$, where the index *n* denotes the filter responses associated with the detection of $b_n^{(0)}$. The conjugate linear branch in Fig. 1 is absent in the classical linear receiver, and bears significance

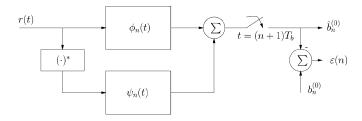


Fig. 1. L/CL filtering structure for user 0.

for improper signals. Let $N = N_0$ and $T_b = T_b^{(0)}$. The estimation error is $\varepsilon(n) = b_n^{(0)} - \hat{b}_n^{(0)}$, where $\hat{b}_n^{(0)}$ can be expressed as

$$\hat{b}_n^{(0)} = \int_{-\infty}^{\infty} r(u)\phi_n(T_b - u) + r^*(u)\psi_n(T_b - u)du.$$
 (5)

The two filters, $\phi_n(t)$ and $\psi_n(t)$, are designed to minimize the mean-square error $\mathcal{J}_n = \mathrm{E}[|\varepsilon(n)|^2]$, where $\mathrm{E}[\cdot]$ represents the expectation operation. They are determined using the two orthogonality conditions for complex envelopes [22]: $\mathrm{E}[\varepsilon(n)r(t)] = 0$ and $\mathrm{E}[\varepsilon(n)r^*(t)] = 0$. Substituting $\varepsilon(n)$ and $\hat{b}_n^{(0)}$ into the orthogonality conditions yields the following two Fredholm integral equations of the first kind:

$$\int_{-\infty}^{\infty} R_{rr}(t,u)\phi_n(T_b-u) + R_{rr^*}(t,u)\psi_n(T_b-u)du$$

= E[b_n^{(0)}r(t)] (6a)
$$\int_{-\infty}^{\infty} R_{rr^*}^*(t,u)\phi_n(T_b-u) + R_{rr}^*(t,u)\psi_n(T_b-u)du$$

= E[b_n^{(0)}r^*(t)] (6b)

for $-\infty < t < \infty$, where $R_{xx^*}(t, u) = E[x(t)x^*(u)]$ and $R_{xx}(t, u) = E[x(t)x(u)]$ denote, respectively, the autocorrelation function and complementary autocorrelation function of a complex random process x(t) [5, p. 312]. The expectation operations are conditioned on the knowledge of PN sequences of the desired user plus the signal powers, arrival delays, and phase offsets of all users.

The right-hand sides (RHS) of (6) are forcing functions of the integral equations, i.e., the received signal associated with $b_n^{(0)}$. By moving the desired signal components of r(t) to the RHS, (6) can alternatively be expressed as

$$\int_{-\infty}^{\infty} R_{nn}(t,u)\phi_n(T_b-u) + R_{nn^*}(t,u)\psi_n(T_b-u)du$$

= $\alpha_n a_n^{(I,0)}(t-nT_b) + \beta_n a_n^{(Q,0)}(t-nT_b-T)$ (7a)
$$\int_{-\infty}^{\infty} R_{nn^*}^*(t,u)\phi_n(T_b-u) + R_{nn}^*(t,u)\psi_n(T_b-u)du$$

= $\alpha_n a_n^{(I,0)}(t-nT_b) - \beta_n a_n^{(Q,0)}(t-nT_b-T)$ (7b)

where $R_{nn^*}(t, u)$ and $R_{nn}(t, u)$ are now the autocorrelation and complementary autocorrelation functions of noise only, defined as

$$n(t) = \sum_{k=1}^{K} s_k(t) + \eta(t).$$
 (8)

The scalars α_n and β_n absorb the desired signal components from the left-hand sides and are defined as

$$\alpha_n = \sqrt{\frac{P_0}{2}} - \frac{P_0}{2} \int_{-\infty}^{\infty} a_n^{(\mathbf{I},0)} (u - nT_b) \phi_n(T_b - u) + a_n^{(\mathbf{I},0)} (u - nT_b) \psi_n(T_b - u) du$$
(9a)

$$\beta_n = \sqrt{\frac{P_0}{2} + \frac{P_0}{2}} \int_{-\infty}^{\infty} a_n^{(Q,0)} (u - nT_b - \mathcal{T}) \phi_n (T_b - u) - a_n^{(Q,0)} (u - nT_b - \mathcal{T}) \psi_n (T_b - u) du.$$
(9b)

It is shown in Appendix I that α_n and β_n are directly related to SNR, and $\alpha_n = \beta_n$. Defining $\phi_n(t)$ as $\phi_n(t) = \alpha_n g_n(t)$ and $\psi_n(t)$ as $\psi_n(t) = \alpha_n h_n(t)$, the equations of (7) can be further simplified to

$$\int_{-\infty}^{\infty} R_{nn}(t,u)g_n(T_b-u) + R_{nn^*}(t,u)h_n(T_b-u)du$$

= $a_n^{(I,0)}(t-nT_b) + a_n^{(Q,0)}(t-nT_b-T)$ (10a)
$$\int_{-\infty}^{\infty} R_{nn^*}^*(t,u)g_n(T_b-u) + R_{nn}^*(t,u)h_n(T_b-u)du$$

= $a_n^{(I,0)}(t-nT_b) - a_n^{(Q,0)}(t-nT_b-T)$ (10b)

where (11a)–(11b) appear at the bottom of the page.

It can be observed that, in general, the complementary autocorrelation function of (11a) is nonzero for $\mathcal{T} \neq 0$. Therefore, the OQPSK modulation introduces improper complex noise into the system. However, phase locking on all the interfering users is necessary [1], otherwise, it is easy to show that $R_{nn}(t, u) = 0$ for i.i.d. phase offsets with uniform distribution $\theta_k \in U[0,2\pi)$. Henceforth, it is assumed that the receiver locks on phase offsets θ_k and compensates for them. As the knowledge of phase offsets becomes available, compensating for them turns out to be straightforward. Phase estimation can be achieved with the aid of pilot signals that are already incorporated in CDMA standards [6], [7]. After phase estimation and compensation, $R_{nn}(t, u)$ can be rewritten with $\theta_k = 0$ for all k. It is also noted that the noise considered in (11) consists of MAI and AWGN only. Intersymbol interference (ISI) is neglected. This is justified, as chip pulses considered either satisfy the Nyquist-I criterion (e.g., the Sqrt-RC pulse) or are time-limited to $|t| \leq 2T_c$ (e.g., the half-cosine pulse) in the ISI-free AWGN channel. Next, the solutions to the filters $q_n(t)$ and $h_n(t)$ are presented.

B. Solutions

It is straightforward to show that the noise defined by (8) and (11) has zero mean and is periodic in the second moment. Hence, it is a WSCS random process with a period of $T_{\rm CS} = T_c$ [17], [23]. As in [19] and [21], the harmonic series representation (HSR) technique [22] is used to solve for $g_n(t)$ and $h_n(t)$. The HSR technique accomplishes a decomposition of a WSCS process into a countable set of jointly WSS and bandlimited processes. Hence, a scalar WSCS process can be treated as a WSS vector process. In such case, the integral equations of (10) reduce to convolution integrals, and can alternatively be expressed in the frequency domain by the following matrix equations:

$$\$ \mathbb{G}_n + \mathbb{R} \mathbb{H}_n = \mathbb{Q}_n^+ \tag{12a}$$

$$\mathbb{R}^* \mathbb{G}_n + \mathbb{S}^* \mathbb{H}_n = \mathbb{Q}_n^- \tag{12b}$$

where \mathbb{G}_n and \mathbb{H}_n are vectors containing harmonics of the $g_n(t)$ and $h_n(t)$, respectively. Also, \mathbb{Q}_n^+ and \mathbb{Q}_n^- are vectors containing the forcing functions of (10a) and (10b). The matrices corresponding to autocorrelation and complementary autocorrelation functions are represented by \mathbb{R} and \mathbb{S} . The detailed mathematical description of all the above elements can be found in Appendix II.

The matrix equations of (12) appear to be similar to those in [2]. However, their formations are fundamentally different. The matrices involved in [2] (and [9]–[16]) are determined by the observation vector which constitutes samples of the filtered received signal in the time domain. In contrast, the vectors and matrices in (12) are determined by the HSR of the filters and received signal spectrum. Whereas the dimensions of \mathbb{R} and \mathbb{S} correspond to the number of observation samples in [2] and [9]–[16], here they are determined by α , the excess bandwidth of the pulse shape in (12).

Prior to presenting the solutions to \mathbb{G}_n and \mathbb{H}_n , some interesting properties of \mathbb{R} and \mathbb{S} are reviewed. From Appendix II, it can be shown that \mathbb{R} is Hermitian ($\mathbb{R}^H = \mathbb{R}$) and nonsingular. However, \mathbb{S} is always singular, and $\mathbb{S}' = \mathbb{S}$. Also, the arrays on the longest diagonal of \mathbb{S} perpendicular to its main diagonal are zero.

Accounting for the singularity of , the solutions to the unknown filters are

$$\mathbb{G}_{n} = \left[\mathcal{A}_{n}^{(\mathrm{I},0)} \left(e^{j2\pi fT_{c}} \right) + \mathcal{A}_{n}^{(\mathrm{Q},0)} \left(e^{j2\pi fT_{c}} \right) \right] e^{-j2\pi fT_{b}} \mathbb{G}_{1} \\
+ \left[\mathcal{A}_{n}^{(\mathrm{I},0)} \left(e^{j2\pi fT_{c}} \right) - \mathcal{A}_{n}^{(\mathrm{Q},0)} \left(e^{j2\pi fT_{c}} \right) \right] e^{-j2\pi fT_{b}} \mathbb{G}_{2} \tag{13}$$

where \mathbb{G}_1 and \mathbb{G}_2 are time-invariant filters defined as

$$\mathbb{G}_1 = \left(\mathbb{S}^* \mathbb{R}^{-1} \mathbb{S} - \mathbb{R}^*\right)^{-1} \mathbb{S}^* \mathbb{R}^{-1} \mathbb{Q} \cup$$
(14a)

$$\mathbb{G}_2 = \left(\mathbb{R}^* - \mathbb{S}^* \mathbb{R}^{-1} \mathbb{S}\right)^{-1} \mathbb{Q} \mathbb{U}.$$
 (14b)

Similarly

$$R_{nn}(t,u) = \sum_{k=1}^{K} \sum_{n=-\infty}^{\infty} \frac{P_k}{2} e^{j2\theta_k} [q(t-nT_c-\tau_k)q(u-nT_c-\tau_k) - q(t-nT_c-\tau_k-\mathcal{T})q(u-nT_c-\tau_k-\mathcal{T})] \quad (11a)$$

$$R_{nn^*}(t,u) = \sum_{k=1}^{K} \sum_{n=-\infty}^{\infty} \frac{P_k}{2} [q(t-nT_c-\tau_k)q(u-nT_c-\tau_k) + q(t-nT_c-\tau_k-\mathcal{T})q(u-nT_c-\tau_k-\mathcal{T})] + \mathcal{N}_0 \quad (11b)$$

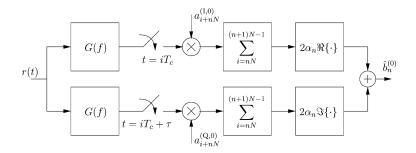


Fig. 2. WL receiver structure for user 0.

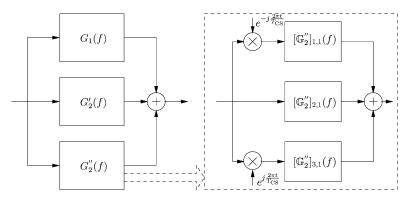


Fig. 3. Broken-down structure of G(f) with the dashed block zoomed in on $\mathbb{G}''(f)$ to illustrate the frequency shifters and filters.

where $\mathbb{H}1$ and \mathbb{H}_2 are

$$\mathbb{H}_1 = \mathbb{G}_2^* \qquad \mathbb{H}_2 = \mathbb{G}_1^*. \tag{16}$$

As expected from [22], the solutions to \mathbb{G}_n and \mathbb{H}_n are comprised of two parts: time-variant components consisting of the Z-transforms of the PN sequences responsible for despreading the desired signal, and time-invariant components \mathbb{G}_1 and \mathbb{G}_2 which replace the CMF. The exponential component $e^{-j2\pi fT_b}$ in (14) and (16) simply make the filters causal. It is noted that the Z-transforms and the exponential component are scalars and invariant to frequency shifts introduced by HSR. It is also worth mentioning that the solutions of \mathbb{G}_1 and \mathbb{G}_2 involve the inverse of Schur complement of \mathbb{R} with respect to the augmented covariance matrix, as defined in [24].¹

C. Receiver Structure

The WL receiver accounts for the complementary autocorrelation functions which were overlooked in the design of the SL receiver. It can be easily verified that setting \$ = 0 results in $\mathbb{G}_1 = 0$ and $\mathbb{G}_2 = \mathbb{R}^{*-1}\mathbb{Q}U$, which is identical to the solution of the SL receiver formulated in [21]. The extra components resulting from $\$ \neq 0$ in the WL receiver structure can also be illustrated via the following reformulation of \mathbb{G}_2 :

$$G_{2} = [\mathbb{R}^{*}(\mathbb{I} - \mathbb{A})]^{-1} \mathbb{Q} \mathbb{U}$$

= $(\mathbb{I} - \mathbb{A})^{-1} \mathbb{R}^{*-1} \mathbb{Q} \mathbb{U}$
= $\underbrace{\mathbb{R}^{*-1} \mathbb{Q} \mathbb{U}}_{G_{2}^{''}} + \underbrace{\mathbb{A} \mathbb{R}^{*-1} \mathbb{Q} \mathbb{U} + \mathbb{A}^{2} \mathbb{R}^{*-1} \mathbb{Q} \mathbb{U} + \cdots}_{G_{2}^{'}}$ (17)

¹The Schur complement of \mathbb{R} with respect to the augmented covariance matrix of $\begin{pmatrix} \mathbb{R} & S \\ S^* & \mathbb{R}^* \end{pmatrix}$ is $\mathbb{R}^* - S^* \mathbb{R}^{-1} S$.

where $\mathbb{A} = \mathbb{R}^{*-1} \mathbb{S}^* \mathbb{R}^{-1} \mathbb{S}$ and the last line of (17) is obtained from the matrix series expansion

$$(\mathbb{I} - \mathbb{A})^{-1} = \mathbb{I} + \mathbb{A} + \mathbb{A}^2 + \cdots$$
(18)

for an arbitrary square matrix \mathbb{A} . In (17), the vector \mathbb{G}_2'' is the solution to the SL receiver, and the vector \mathbb{G}_2' arises due to improper complex noise.

The structure of the WL receiver can be shown to simplify to that in Fig. 2 after accounting for equations in (16). The new filter, G(f), whose broken-down structure is shown in Fig. 3, replaces the CMF and is expressed as $G(f) = G_1(f) + G'_2(f) +$ $G''_2(f)$ where $G_1(f), G'_2(f)$, and $G''_2(f)$ are constructed from a parallel bank of frequency shifters, each followed by their corresponding filters formulated in $\mathbb{G}_1, \mathbb{G}_2'$, and \mathbb{G}_2'' , respectively. This is illustrated for \mathbb{G}_2'' in the dashed block of Fig. 3 [18]. The top and bottom branches in Fig. 2 estimate $b_n^{(1,0)}$ and $b_n^{(Q,0)}$, respectively, with $\Re\{\cdot\}$ and $\Im\{\cdot\}$ representing the real and imaginary operators.

IV. PERFORMANCE ANALYSIS

This section analyzes the WL receiver. Special cases when the WL receiver reduces to the SL receiver, either identically (\$ = 0) or asymptotically ($\$ \to 0$), will be investigated. Also, scenarios when the WL receiver delivers maximum performance improvement over the SL receiver will be discussed. The SNR expressions of both receivers will be presented and compared in the end.

A. \$ = 0

Of interest is the study of cases when \$ = 0. To identify such cases, the expression for \$ is rewritten as $\$ = \sum_{k=1}^{K} \$_k$,

where S_k is the complementary autocorrelation function corresponding to user k, defined similar to (40), except for replacing \mathbb{P}^{S} with \mathbb{P}^{S_k} , as defined in (19), shown at the bottom of the page.

In the above, $T_{\rm CS} = T_c$. This matrix representation is valid for $\alpha < 200\%$, but can easily be extended to larger values of excess bandwidth.

Four cases can occur that yield proper noise: 1) $\mathcal{T} = 0$; 2) K = 0; 3) $\alpha = 0$; and 4) symmetric interferers. The first case yields balanced QPSK where the WL receiver simplifies to the SL receiver. In the second and third cases, the noise becomes a WSS random process with the new filters reducing to the CMF. Moreover, all matrices and vectors become scalars for $\alpha = 0$. The last case requires a closer look at \mathbb{P}^{S_k} , and is now investigated.

Symmetric interferers are hereby defined as a set of interfering users whose collective sum of nonzero complementary autocorrelation functions ($\forall_k \ S_k \neq 0$) is zero. A system has symmetric interferers, if for every distinct interferer² with power P_k and chip delay T_k , there exists two other interferers with equal power and chip delays of $mod(T_k + T_{CS}/2, T_{CS})$ and $mod(T_k + T_{CS}/4, T_{CS})$. More precisely

$$\forall \{P_k, T_k\}, \exists \left\{ P_{k'} = P_k, \ T_{k'} = \text{mod}\left(T_k + \frac{T_{\text{CS}}}{2}, T_{\text{CS}}\right) \right\}$$
(20a)
$$\forall \{P_k, T_k\}, \exists \left\{ P_{k''} = P_k, \ T_{k''} = \text{mod}\left(T_k + \frac{T_{\text{CS}}}{4}, T_{\text{CS}}\right) \right\}.$$
(20b)

If these two conditions are met, it can be shown that \$ = 0 by examining the arrays of (19). For instance, with $T_{\rm CS} = T_c$ and K = 4 equally powered users with chip delays $T = [0.15, 0.40, 0.65, 0.90]T_c$ create a set of symmetric interferers. If $T = 0.5T_c$, as with the case in CDMA standards [6], [7], the main diagonal of $\$_k$ is also zero and, consequently, only (20a) needs to be satisfied to make the interferers symmetric.

 $B. \ \mathbb{S} \to 0$

In addition to the previous four cases where S turned out to be identically zero, another scenario can occur that makes $S \to 0$. The nonzero arrays of \mathbb{P}^{S_k} in (19) are all comprised of an $e^{-jp\pi T_k/T_c}$ term (p = 2, 4) whose expectation is zero

²As evident from the system model, the average impact of two users with identical chip delays is the same in long-code CDMA systems. Two users are therefore referred to as distinct users if their chip delays differ.

for $T_k \in U[0, T_c)$. As the number of equally powered asynchronous interfering users with i.i.d. chip delays of such distribution grows in the system, the sum $\sum_{k=1}^{K} \mathbb{P}^{\mathbb{S}_k}$ asymptotically approaches the statistical average of $\mathbb{P}^{\mathbb{S}_k}$

$$\lim_{K \to \infty} \sum_{k=1}^{K} \mathbb{P}^{\mathbb{S}_k} = \mathbb{E}[\mathbb{P}^{\mathbb{S}_k}] = 0.$$
 (21)

This can also be intuitively explained. As the number of active users in the system with uniform power distribution increases, the probability of forming a symmetric or near-symmetric set of interferers grows, and the effect of \$ gradually diminishes. Simulations demonstrate that K = 30 is large enough to make $\$ \rightarrow 0$.

C. $\$ \gg 0$

Naturally, scenarios where the WL receiver outperforms the SL receiver can be identified by counterarguing the cases discussed so far. The symmetry that results in \$ = 0 or $\$ \to 0$ is violated when interfering users demonstrate one or both of the conditions of nonuniform power distribution and (quasi-)synchronism. Nonuniform power distribution may occur in nextgeneration CDMA systems which support various data rates with different qualities of service (QoS). Synchronism between users takes place on the forward link, where the base station transmits the signals of all users simultaneously. In the reverse link, however, only quasi-synchronism can be achieved with the aid of a timing reference. Quasi-synchronous CDMA signals can be modeled with chip delays chosen from a probability distribution function $f_T(\cdot)$, which is nonzero in $[0, \Delta_m]$ for $0 < \infty$ $\Delta_m < T_c$ [25]. For instance, in wideband CDMA (WCDMA) systems, all users are synchronized within $\Delta_m = T_c/8$ in the reverse link to reduce intracell interference [26]. It will be shown in the next section that even quasi-synchronous signals yield remarkable gains in favor of the WL receiver, as the tightened range of allowed chip delays prevents forming symmetric interferers.

D. Signal-to-Noise Ratio

The expressions for SNR of the WL and SL receivers are now presented. Following Appendix I and the steps in [8], the complex baseband SNR expression for the WL receiver in the I phase after the real operator can be written as in (22), shown at the bottom of the page, where

$$\xi(t,u) = \int_{-\infty}^{\infty} \left[R_{nn}(t,u)g_n(T_b-u) + R_{nn^*}(t,u)h_n(T_b-u) + R_{nn^*}^*(t,u)g_n(T_b-u) + R_{nn}^*(t,u)h_n(T_b-u) \right] du.$$
(23)

$$\mathbb{P}^{\mathbb{S}_{k}} = \frac{P_{k}}{2T_{c}} \begin{bmatrix} e^{j4\pi T_{k}/T_{c}} \left(1 - e^{j4\pi T/T_{c}}\right) & e^{j2\pi T_{k}/T_{c}} \left(1 - e^{j2\pi T/T_{c}}\right) & 0\\ e^{j2\pi T_{k}/T_{c}} \left(1 - e^{j2\pi T/T_{c}}\right) & 0 & e^{-j2\pi T_{k}/T_{c}} \left(1 - e^{-j2\pi T/T_{c}}\right)\\ 0 & e^{-j2\pi T_{k}/T_{c}} \left(1 - e^{-j2\pi T/T_{c}}\right) & e^{-j4\pi T_{k}/T_{c}} \left(1 - e^{-j4\pi T/T_{c}}\right) \end{bmatrix}$$
(19)

$$SNR_{n}^{WL,I} = \frac{\frac{P_{0}}{2} \left\{ \int_{-\infty}^{\infty} \Re \left[a_{n}^{(I,0)}(u - nT_{b})g_{n}(T_{b} - u) + a_{n}^{(I,0)}(u - nT_{b})h_{n}(T_{b} - u) \right] du \right\}^{2}}{\Re \left\{ \frac{1}{2} \int_{-\infty}^{\infty} g_{n}(T_{b} - u)\xi(t, u) + h_{n}(T_{b} - u)\xi(t, u) du \right\}}$$
(22)

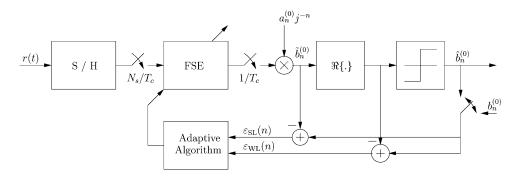


Fig. 4. Adaptive receiver architecture with an updating period of T_c .

If the integral equations in (10) hold, then (22) can be further simplified to

$$SNR_{n}^{WL,I} = \frac{P_{0}}{2} \int_{-\infty}^{\infty} \Re \Big[a_{n}^{(I,0)} (u - nT_{b}) g_{n}(T_{b} - u) \\ + a_{n}^{(I,0)} (u - nT_{b}) h_{n}(T_{b} - u) \Big] du.$$
(24)

As shown in Appendix I, $\text{SNR}_n^{\text{WL}} = 2\text{SNR}_n^{\text{WL},\text{I}}$. An alternative, and yet computationally efficient, form can be expressed in the frequency domain by using the inverse Fourier representation of signals and their HSR as

$$\operatorname{SNR}^{\operatorname{WL}} = 2NP_0 \Re \left\{ \int_{-1/2T_c}^{1/2T_c} \mathbb{U} \mathbb{Q} \left(\mathbb{G}_1 + \mathbb{G}_2 \right) df \right\}$$
(25)

where the effect of PN spreading sequences are averaged out. Similarly, the SNR expression for the SL receiver is

$$\operatorname{SNR}^{\operatorname{SL}} = 2NP_0 \Re \left\{ \int_{-1/2T_c}^{1/2T_c} \mathbb{U} \, \mathbb{Q}(\mathbb{G}_{2|\mathbb{S}=0}) df \right\}$$
(26)

where the SL receiver is evaluated by setting \$ = 0 in \mathbb{G}_2 . The relative performance improvement of the WL receiver over the SL receiver, the parameter of interest in this paper, can therefore be formulated as

$$\Delta \text{SNR} = \frac{\Re \left\{ \int_{-1/2T_c}^{1/2T_c} \bigcup \mathbb{Q} \left(\mathbb{G}_1 + \mathbb{G}_2 \right) df \right\}}{\Re \left\{ \int_{-1/2T_c}^{1/2T_c} \bigcup \mathbb{Q} \left(\mathbb{G}_{2|\mathbb{S}=0} \right) df \right\}}.$$
 (27)

Numerical results on performance comparison of the two receivers are based on evaluating Δ SNR for different scenarios.

It is reiterated that the design, performance analysis, and formulation of SNR expressions presented here apply to any pulse shape, as long as its frequency response has even symmetry. Hence, the preceding discussions cover both OQPSK and MSK modulation schemes.

V. RECEIVER IMPLEMENTATION

In the previous two sections, the SNR-maximizing WL receiver was accurately and explicitly derived and analyzed. To avoid any form of approximation or compromise in performance, the continuous-time impulse response of the filter was not restricted to a finite time support. To address this impractical assumption, an example of receiver implementation via adaptive algorithms is discussed here. The proposed architecture operates in discrete time, and is based on a finite impulse response (FIR) FSE.

A. Architecture

Fig. 4 depicts the proposed structure of the adaptive receiver for user 0. The FSE, whose structure can be found in [19], is the basic block of the structure. In [27], it is shown that exploiting the CS of the received signal through frequency shifting, as illustrated in the dashed block of Fig. 3, can also be realized by FSEs.

For simplicity, the adaptive receiver is based on binary MSK, which is a simplified form of the OQPSK model as described in Section II. With binary information bits and PN sequences, the notions of I and Q are dropped in this section.

The received signal is processed by a "Sample and Hold" (S/H) block, which is assumed to be ideal with impulse response $h_{\rm S/H}(t) = \delta(t)$. The S/H output is oversampled at the rate of $T_s = T_c/N_s$, where $N_s > 1$. The samples are next input to the FSE and stacked in a $(M + 1) \times 1$ vector of $\boldsymbol{x}(n)$ defined as

$$\boldsymbol{x}(n) = [r(nT_c - T_D), r(nT_c - T_D + T_s), \dots, r(nT_c - T_D + MT_s)]^{\prime}$$

where T_D is a delay to make the receiver causal and center $d_n^{(0)} = b_{\lfloor n/N \rfloor}^{(0)} a_n^{(0)}$ in the FSE. The FSE has $M + 1$ complex tap
weights spaced T_s apart. The coefficients form the $(M + 1) \times 1$
vector $\boldsymbol{w}(n)$, defined as $\boldsymbol{w}(n) = [w_0(n), w_1(n), \dots, w_M(n)]^{\prime}$.
The FSE output is sampled at the chip rate and despread with the
locally generated PN sequence of the desired user to obtain the
chip estimate. The parameter j^{-n} in the despreader keeps the
signal constellation of the desired user on the real axis, as the
MSK constellation is known to jump between real and imagi-
nary axes every T_c seconds [20]. The despreader output is there-
fore expressed as

$$\tilde{b}_{n}^{(0)} = a_{n}^{(0)} j^{-n} \left[\boldsymbol{w}^{H}(n) \boldsymbol{x}(n) \right].$$
(28)

The FSE tap weights are updated by an adaptive algorithm, where, depending on the receiver type (SL or WL), the error signal used to correct the tap weights differs. Next, the simple least-mean square (LMS) algorithm for the two receivers is discussed.

B. Adaptive Algorithm

The iterative equation of the LMS algorithm [28] can be expressed as

$$\boldsymbol{w}(n+1) = \boldsymbol{w}(n) + \mu \left(j^{-n} a_n^{(0)} \right) \varepsilon^*(n) \boldsymbol{x}(n)$$
(29)

where the step size μ is a small positive constant. The parameter $j^{-n}a_n^{(0)}$ rids the content of the FSE input from the PN sequence of the desired user at the time of updating, and adjusts the phase

of the correction term in (29). For the implementation of the SL receiver, the error term $\varepsilon(n)$ takes the conventional form of

$$\varepsilon_{\rm SL}(n) = b_n^{(0)} - \tilde{b}_n^{(0)} \tag{30}$$

when the adaptive algorithm is under training and a replica of $b_n^{(0)}$ is available at the receiver side. When the SNR is high enough to make the outputs of the decision device reliable, the algorithm switches to the decision-directed (DD) mode and the error term in (30) becomes $\varepsilon_{\text{SL,DD}}(n) = \hat{b}_n^{(0)} - \tilde{b}_n^{(0)}$. For the implementation of the WL receiver, the error term is modified to

$$\varepsilon_{\rm WL}(n) = b_n^{(0)} - \Re\left\{\tilde{b}_n^{(0)}\right\} \tag{31}$$

in the training mode, and $\varepsilon_{\text{WL,DD}}(n) = \hat{b}_n^{(0)} - \Re{\{\tilde{b}_n^{(0)}\}}$ in the DD mode. The derivation of (31) can be found in [14]. It can also be intuitively argued that when the signal constellation lies on the real axis, the two filters in Fig. 1 become complex conjugates, i.e., $\psi_n(t) = \phi_n^*(t)$. Hence, the collective effect of the two filters can also be realized by only one filter with a real operator afterwards. Therefore, the output of the WL receiver must be taken after the real operator, as shown in Fig. 4.

Receiver complexity is determined by the number of FSE taps M + 1, the adaptive algorithm, and the updating rate. Simulation results for several scenarios in the AWGN channel show that typically M + 1 = 10 taps or less is sufficient for the FSE to deliver the maximum SNR. The choice of the adaptive algorithm depends on the application and affordable computational complexity. If, for instance, faster convergence rates are desirable, the recursive least-squares (RLS) algorithm can be employed. The updating rate is an adjustable design parameter. In Fig. 4, the FSE tap weights are updated every chip period T_c . To decrease the burden of computational complexity, the updating rate can be lowered to a bit period T_b , as in [19], at the expense of slower convergence.

It is emphasized that the proposed adaptive receiver is only one of several options to implement the WL receiver. Another implementation method is through actual frequency shifters, each followed by a filter, as illustrated in Fig. 3. However, good estimates of the parameters of the interfering users (such as chip delays T_k and signal powers P_k) are required to construct the filters. The advantage of the proposed adaptive receiver is that it needs no *a priori* knowledge of such parameters, and can automatically estimate them. As another example, an alternative frequency-domain approach to the implementation is discussed in [29].

VI. NUMERICAL RESULTS

Numerical results presented in this section achieve two goals. In the first set of results, the feasibility of the proposed adaptive receiver is demonstrated. It is shown that the maximum theoretical SNR can be achieved by the adaptive receiver. Next, the performance improvement achieved by the WL receiver over the SL receiver is quantified in various conditions. The results also confirm the previous discussions on performance analysis.

Fig. 5 presents a sample of training curves of the LMS algorithm for the SL and WL receivers in two cases where the interfering users are (a) symmetric, and (b) asymmetric. The modula-

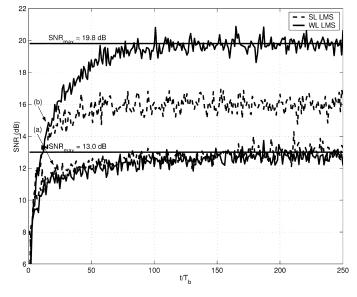


Fig. 5. Training curves for a system with three equally powered users. Solid and dashed curves represent WL-LMS and SL-LMS, respectively. Horizontal lines indicate the theoretical SNR_{max}. Chip delays of the interfering users are (a) $T = [0.2 \ 1.2]T_c$ and (b) $T = [0.6 \ 0.8]T_c$.

tion scheme is binary MSK, as described in Section V. It is noted that for MSK signals with half-cosine chip pulse, the period of CS is $T_{CS} = 2T_c$. There are K + 1 = 3 asynchronous equally powered users in the system with $E_b/N_0 = 20$ dB. Other parameters are set as follows: $\mu = 10^{-3}$, $N_s = 2$, N = 16, M = 15, and $T_D = 5T_c$. Each curve is averaged over 100 Monte Carlo runs with the FSE tap weights initialized as $\boldsymbol{w}(0) =$ $\mathbf{0}_{(M+1) \times 1}$. In (a), the chip delays of the interfering users are $T = [0.2 \ 1.2]T_c$, which according to (20) forms a symmetric set. It can be seen that both algorithms perform identically, and reach the $SNR_{max} = 13.0 \text{ dB}$. This is so, since MAI becomes proper and the WL receiver reduces to the SL receiver. In (b), the chip delays of the interfering users are $T = [0.6 \ 0.8]T_c$, which makes MAI improper. Hence, the WL receiver significantly outperforms the SL receiver. The steady-state SNR of the WL algorithm is 19.8 dB, whereas the SL algorithm stabilizes at 16.0 dB.

Fig. 6 plots bit-error rate (BER) versus bit-energy-to-thermalnoise ratio E_b/N_0 under various network loads, with K_H and K_L representing the number of high- and low-powered users, respectively. The power of interferers in the K_H set can be either 3 or 6 dB above the user of interest, whereas interferers in the K_L set are of equal power with respect to the desired user. The power and delay profile of the active users in the network can be summarized in the **P** and **T** vectors,³ where $[\mathbf{P}]_{k,1} = P_k$ and $[\mathbf{T}]_{k,1} = T_k$. For cases (a) and (b), the first three and six elements of P and T are used, respectively. Also, N = 32. Solid BER plots are simply obtained by invoking the standard Gaussian approximation (SGA) on the SNR. The points marked by "×" represent simulated BER when the receiver is implemented adaptively, as described in Fig. 5. It is observed that the adaptive implementation can achieve the predicted performance fairly accurately under various network loads. Moreover, com-

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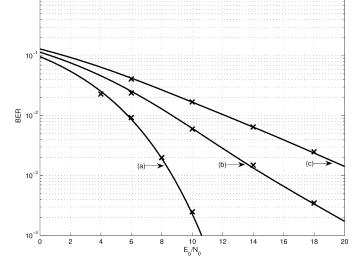


Fig. 6. BER versus E_b/N_0 for N = 32 under diverse network loads. (a) $K_H = 2$ and $K_L = 1$. (b) $K_H = 4$ and $K_L = 2$. (c) $K_H = 6$ and $K_L = 4$. Solid lines represent the theoretical BER by invoking the SGA. Points marked by "×" represent the simulation results when the receiver is implemented adaptively with $N_s = 2$.

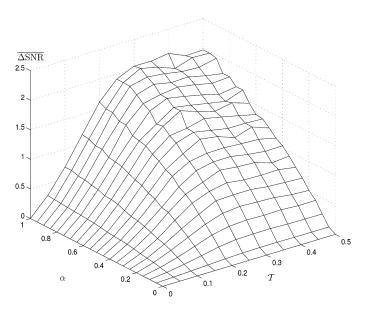


Fig. 7. $\overline{\Delta \text{SNR}}$ versus α and \mathcal{T} for a system with K = 3 equally powered asynchronous users at $E_b/\mathcal{N}_0 = 20$ dB.

pared with plots (b) and (c), plot (a) shows a waterfall effect in BER, confirming the near-far resistance of long-code MMSE receivers, where K_H users can be effectively tuned out as long as $K_H \leq 1 + \alpha$ [17], [30]. The remaining numerical results rely on evaluating (27) for performance comparison of the two receivers under the more spectrally efficient OQPSK modulation with Sqrt-RC pulse.

Fig. 7 plots $\overline{\Delta \text{SNR}}$, the average of ΔSNR , as a function of \mathcal{T} and α when $0 \leq \mathcal{T} \leq 0.5T_c$ and $0 \leq \alpha \leq 1$. There are K + 1 = 4 asynchronous equally powered users in the system with $E_b/\mathcal{N}_0 = 20$ dB. The average is calculated over more than 10^3 profiles of chip delays generated according to the uniform

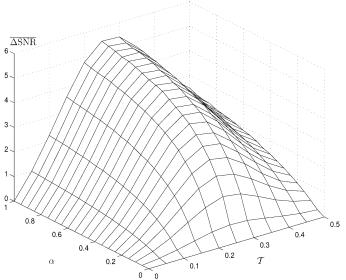


Fig. 8. $\overline{\Delta \text{SNR}}$ versus α and \mathcal{T} for a system with K = 30 equally powered quasi-synchronous users, with $E_b/\mathcal{N}_0 = 20$ dB and $T_k \in U[0, 0.5T_c)$.

distribution of $T_k \in U[0, T_c)$ to obtain tight confidence intervals. Upper and lower bounds of the 99% confidence interval differ by only 0.1 dB. This is also true for the remaining figures of this section. The α axis can be interpreted as a measure of CS of the noise (MAI), whereas the T axis conceives the significance of improperness. Fig. 7 shows that as the noise approaches a WSS process ($\alpha \rightarrow 0$) or becomes proper ($\mathcal{T} \rightarrow 0$), the difference between the performance of the WL and SL receivers disappear. For $\mathcal{T} \neq 0$ and $\alpha \neq 0$, however, the WL receiver exploits the complementary autocorrelation function, which is overlooked in the SL receiver. As $\alpha \rightarrow 1$, the performance improvement grows. For instance, $\overline{\Delta SNR} = 0.6 \text{ dB}$ for $(\alpha, \mathcal{T}) = (0.22, 0.5T_c)$ and grows to slightly less than 2 dB for $(\alpha, \mathcal{T}) = (1, 0.5T_c)$. Increasing the number of asynchronous equally powered users significantly decreases the performance improvement achieved by the WL receiver. It was observed that for K = 30, $\overline{\Delta SNR}$ is just 0.2 dB for $(\alpha, T) = (0.22, 0.5T_c)$, and approximately 0.5 dB for $(\alpha, T) = (1, 0.5T_c)$. Such decrease in Δ SNR is due to the fact that $\$ \rightarrow 0$ with increasing K, as previously discussed.

Fig. 8 examines the effect of quasi-synchronous users on the performance improvement of the WL receiver. Here, the number of interfering users is K = 30. However, users are quasi-synchronous, with chip delays chosen from the uniform distribution of $T_k \in U[0, 0.5T_c)$. Significant gains in the performance of the WL receiver can be observed even as K = 30. The maximum value of $\overline{\Delta}$ SNR for each α appears around $\mathcal{T} = 0.25T_c$, with the peak of $\overline{\Delta}$ SNR = 5.5 dB in $(\alpha, \mathcal{T}) = (1, 0.25T_c)$. Also for $(\alpha, \mathcal{T}) = (0.22, 0.5T_c), \overline{\Delta}$ SNR is slightly less than 1.0 dB, compared with 0.2 dB in the asynchronous case.

The last set of numerical results investigates the presence of high-powered users in the system. In Fig. 9, the parameter setting is as that in Fig. 7, except that $P_1 = 2P_0$, $P_2 = 4P_0$, and $P_3 = 8P_0$. Compared with Fig. 7, the performance gain is now noticeably higher. For instance, at $(\alpha, T) = (0.22, 0.5T_c)$, $\overline{\Delta}$ SNR grows to 1.7 dB from 0.6 dB in Fig. 7. The numerical results verify that (quasi-)synchronism and nonuniform power

The desired signal in the I component is

$$S_n^{\text{WL,I}} = \sqrt{\frac{P_0}{2}} b_n^{(I,0)} \int_{-\infty}^{\infty} a_n^{(I,0)} (u - nT_b) \phi_n(T_b - u) + a_n^{(I,0)} (u - nT_b) \psi_n(T_b - u) du. \quad (32)$$

To obtain the variance of noise in (8), passband representation of signals and filters in terms of their baseband forms is used. For instance, the passband form of the noise signal can be expressed as $N(t) = (1/2)[n(t)e^{j\omega_c t} + n^*(t)e^{-j\omega_c t}]$, where ω_c is the carrier frequency [23]. Following the same steps as in [8] and after simplifications, the noise variance can be expressed as

$$(\sigma_n^{\rm WL})^2 = \int_{-\infty}^{\infty} a_n^{(I,0)} (u - nT_b) \phi_n(T_b - u) + a_n^{(I,0)} (u - nT_b) \psi_n(T_b - u) du.$$
(33)

The signal components in the I and Q phases are i.i.d., and hence, it can be shown that $S_n^{\text{WL},\text{I}} = S_n^{\text{WL},\text{Q}} = 0.5S_n^{\text{WL}}$, where S_n^{WL} is the overall desired signal and $(\sigma_n^{\text{WL},\text{I}})^2 = (\sigma_n^{\text{WL},\text{Q}})^2 = 0.5(\sigma_n^{\text{WL}})^2$. Hence, it is straightforward to show that $\text{SNR}_n^{\text{WL},\text{I}} = \text{SNR}_n^{\text{WL},\text{Q}} = 0.5\text{SNR}_n^{\text{WL}}$. From (32) and (33), α_n can be expressed as $\alpha_n = \sqrt{P_0/2} - \text{SNR}_n^{\text{WL},\text{I}}$. Similarly, β_n can be written as $\beta_n = \sqrt{P_0/2} - \text{SNR}_n^{\text{WL},\text{Q}}$, and consequently, $\alpha_n = \beta_n$.

APPENDIX II ELEMENTS OF THE MATRIX EQUATIONS

Using the HSR technique, the frequency response of $g_n(t)$, $G_n(f)$, can be written as

$$G_n(f) = \sum_{m=-M_H}^{M_H} G_{n,m} \left(f - \frac{m}{T_c} \right)$$
(34)

where $M_H = \lceil \alpha/2 \rceil$, $G_{n,m}(f) = V(f)G_n(f + m/T_c)$, and with V(f) = 1 if $|f| \leq 1/(2T_c)$ and V(f) = 0, otherwise. The vector \mathbb{G}_n is an $L_H \times 1$ column vector where $L_H = 2M_H + 1$. Each of its arrays consists of a portion of $G_n(f)$ frequency response shifted to the $|f| \leq 1/(2T_c)$ band and filtered by an ideal low-pass filter. More specifically, $[\mathbb{G}_n]_{i,1} = G_{n,i+1-L_H}(f)$. The $L_H \times 1$ column vector of \mathbb{H}_n is constructed from $H_n(f)$, the frequency response of $h_n(t)$, in a similar fashion.

The vectors on the RHS of the matrix equations are also $L_H \times 1$ vectors defined as

$$\mathbb{Q}_{n}^{+} = \left[\mathcal{A}_{n}^{(\mathrm{I},0)} \left(e^{j2\pi fT_{c}} \right) + \mathcal{A}_{n}^{(\mathrm{Q},0)} \left(e^{j2\pi fT_{c}} \right) \right] e^{-j2\pi fT_{b}} \mathbb{Q} \cup \tag{35a}$$

$$\mathbb{Q}_{n}^{-} = \left[\mathcal{A}_{n}^{(\mathrm{I},0)} \left(e^{j2\pi fT_{c}} \right) - \mathcal{A}_{n}^{(\mathrm{Q},0)} \left(e^{j2\pi fT_{c}} \right) \right] e^{-j2\pi fT_{b}} \mathbb{Q} \cup \tag{35b}$$

where

$$\mathcal{A}_{n}^{(\mathrm{I},0)}(z) = \sum_{i=nN}^{(n+1)N-1} a_{i+nN}^{(\mathrm{I},0)} z^{-i}$$
(36a)

$$\mathcal{A}_{n}^{(\mathbf{Q},0)}(z) = \sum_{i=nN}^{(n+1)N-1} a_{i+nN}^{(\mathbf{I},0)} z^{-(i+\mathcal{T}/T_{c})}$$
(36b)

are the Z-transforms of the desired user's spreading sequences $a_n^{(I,k)}$ and $a_n^{(Q,k)}$, with the effect of time offset in the Q phase properly introduced in (36b). The matrix Q is an $L_H \times L_H$

Fig. 9. $\overline{\Delta \text{SNR}}$ versus α and \mathcal{T} for a system with K = 3 asynchronous users, with $P_1 = 2P_0, P_2 = 4P_0$, and $P_3 = 8P_0$.

distribution are system features that drive the performance gain of the WL receiver over the SL receiver significantly higher.

VII. CONCLUSION

This paper studied an enhanced WL receiver for CDMA systems when an OOPSK (or MSK) modulation scheme is employed. OQPSK modulation introduces improper complex noise, which necessitates the use of WL receivers instead of classical SL receivers. The design of WL receivers accounts for the generally nonzero complementary autocorrelation function that is overlooked in SL receivers. The distinguishing feature of this paper is its close look at the CS of bandlimited CDMA signals. The proposed receiver exploits the CS of the received signal through the excess bandwidth of the chip pulse by frequency-shifting its spectrum. The frequency response of the WL receiver was derived. Cases where the WL receiver reduces to the SL receiver were examined. An example of WL receiver implementation by adaptive algorithms was also proposed and verified to deliver the SNR performance predicted by theory. Moreover, scenarios where the WL receiver outperforms the SL receiver were identified. It was shown that (quasi-)synchronous systems with high-powered users, that can occur in future generation systems, yield remarkable gains in SNR performance. However, in asynchronous and quasi-synchronous transmission modes, performance gain of the WL receiver degrades unless the number of high-powered active users remains small. It has been shown in the literature that even the SL receiver has such a limitation. Further research is required to find how WL processing can be effectively applied to fully loaded systems.

APPENDIX I PROOF OF $\alpha_n = \beta_n$

To prove the equality of the two scalars, it will be shown that α_n and β_n are directly related to SNR in the I and Q components, respectively.

diagonal matrix containing partial bands of Q(f), the frequency response of q(t), where the nonzero arrays are defined as $[\mathbb{Q}]_{i,i} = Q_{i+1-L_H}(f)$. The $L_H \times 1$ column vector \mathbb{U} is defined as $[U]_{i,1} = 1$.

The matrix \mathbb{R} representing the cross spectral density (CSD) of the noise is

$$\mathbb{R} = \mathcal{N}_0 \,\mathbb{I} + \frac{1}{T_c} \,\mathbb{Q} \,\mathbb{P}^{\mathbb{R}} \,\mathbb{Q} \tag{37}$$

where I is the $L_H \times L_H$ identity matrix. All the information concerning the chip delays and signal powers of interfering users is contained in the power matrix

$$\mathbb{P}^{\mathbb{R}} = \sum_{k=1}^{K} \boldsymbol{P}_{k,T_{k}} \boldsymbol{P}_{k,T_{k}}^{H} + \boldsymbol{P}_{k,T_{k}+\mathcal{T}} \boldsymbol{P}_{k,T_{k}+\mathcal{T}}^{H}$$
(38)

where H is the Hermitian transpose, and the $L_H \times 1$ column vector P_{k,t_0} is

$$[\mathbf{P}_{k,t_0}]_{i,1} = \sqrt{\frac{P_k}{2}} \exp\left(\frac{-j2\pi(i+1-L_H)t_0}{T_c}\right).$$
 (39)

The matrix \$ representing the HSR of the complementary autocorrelation function is formulated as

$$S = \frac{1}{T_c} \mathbb{Q} \mathbb{P}^S \mathbb{Q}$$
⁽⁴⁰⁾

where

$$\mathbb{P}^{\mathbb{S}} = \sum_{k=1}^{K} \mathbf{P}_{k,T_{k}} \mathbf{P}'_{k,T_{k}} - \mathbf{P}_{k,T_{k}+\mathcal{T}} \mathbf{P}'_{k,T_{k}+\mathcal{T}}$$
(41)

with ' denoting the transpose operator.

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REFERENCES

- [1] W. M. Brown and R. B. Crane, "Conjugate linear filtering," IEEE Trans. *Inf. Theory*, vol. IT-15, no. 7, pp. 462–465, Jul. 1969. [2] B. Picinbono and P. Chevalier, "Widely linear estimation with complex
- data," IEEE Trans. Signal Process., vol. 43, no. 8, pp. 2030-2033, Aug. 1995
- [3] F. D. Neeser and J. L. Massey, "Proper complex random processes with applications to information theory," IEEE Trans. Inf. Theory, vol. 39, no. 7, pp. 1293–1302, Jul. 1993.
- [4] S. M. Alamouti, "A simple transmit diversity technique for wire-less communications," *IEEE Trans. Commun.*, vol. 16, no. 10, pp. 1451-1458, Oct. 1998.
- [5] E. A. Lee and D. G. Messerschmitt, Digital Communication, 2nd ed. Norwell, MA: Kluwer, 1994.
- Mobile Station-Base Station Compatibility Standard for Dual-Mode Wideband Spread Spectrum Cellular Systems,, TIA/EIA/IS-95, 1993.
- Physical Layer Standard for cdma2000 Spread Spectrum Systems (2002, [7] May). [Online]. Available: http://www.3gpp2.org. Tech. Steering Group C (TSG-C)
- Y. C. Yoon and H. Leib, "Maximizing SNR in improper complex noise [8] and applications to CDMA," IEEE Commun. Lett., vol. 1, no. 1, pp. 5-8, Jan. 1997
- [9] S. Buzzi, M. Lops, and A. M. Tulino, "A new family of MMSE multiuser receivers for interference suppression in DS/CDMA systems employing BPSK modulation," IEEE Trans. Commun., vol. 49, no. 1, pp. 154-167, Jan. 2001.

- [10] S. Buzzi and M. Lops, "Performance analysis for the improved linear multiuser detectors in BPSK-modulated DS-CDMA systems," IEEE Trans. Commun., vol. 51, no. 1, pp. 37-42, Jan. 2003.
- [11] G. Gelli, L. Paura, and R. P. Ragozini, "Blind widely linear multiuser detection," *IEEE Commun. Lett.*, vol. 4, no. 6, pp. 187–189, Jun. 2000. [12] S. Buzzi, M. Lops, and A. M. Tulino, "A generalized minimum-mean-
- output-energy strategy for CDMA systems with improper MAI," *IEEE Trans. Inf. Theory*, vol. 48, no. 3, pp. 761–767, Mar. 2002.
 R. Schober, W. H. Gerstacker, and L. Lampe, "A blind widely linear
- minimum-output-energy algorithm," in Proc. IEEE Wireless Commun. Netw. Conf., vol. 1, 2003, pp. 612–617. —, "A widely linear LMS algorithm for MAI suppression in
- [14] DS-CDMA," in Proc. IEEE Int. Conf. Commun., vol. 4, 2003, pp. 2520–2525.
- [15] W. H. Gerstacker, F. Obernosterer, R. Schober, A. Lehmann, A. Lampe, and P. Gunreben, "Widely linear equalization for space-time block-coded transmission over fading ISI channels," in Proc. IEEE Veh. Technol. Conf., vol. 1, Sep. 2002, pp. 238-242.
- [16] W. H. Gerstacker, R. Schober, and A. Lampe, "Receivers with widely Inear processing for frequency-selective channels," *IEEE Trans. Commun.*, vol. 51, no. 9, pp. 1512–1523, Sep. 2003.
 T. F. Wong, T. M. Lok, and J. S. Lehnert, "Asynchronous multiple-ac-
- cess interference suppression and chip waveform selection with aperiodic random sequences," IEEE Trans. Commun., vol. 47, no. 1, pp. 103-114, Jan. 1999.
- [18] W. A. Gardner, "Cyclic Wiener filtering: Theory and method," IEEE Trans. Commun., vol. 41, no. 1, pp. 151–163, Jan. 1993.
 [19] A. Mirbagheri and Y. C. Yoon, "A linear MMSE receiver for multipath
- asynchronous random-CDMA with chip pulse shaping," IEEE Trans. Weh. Technol., vol. 51, no. 5, pp. 1072–1086, Sep. 2002.
 [20] S. Pasupathy, "Minimum shift keying: A spectrally efficient modula-
- tion," *IEEE Commun. Mag.*, vol. 17, no. 4, pp. 14–22, Jul. 1979. [21] Y. C. Yoon and H. Leib, "Chip-delay locked matched filter for
- DS-CDMA systems using long sequence spreading," *IEEE Trans. Commun.*, vol. 49, no. 8, pp. 1468–1478, Aug. 2001.
 [22] L. E. Franks, "Polyperiodic linear filtering," in *Cyclostationarity in*
- Communications and Signal Processing, W. A. Gardner, Ed. New York: IEEE Press, 1994, pp. 240–266.
- [23] J. G. Proakis, Digital Communications, 3rd ed. New York: McGraw-Hill, 1995.
- [24] P. J. Schreier and L. L. Scharf, "Second-order analysis of improper complex random vectors and processes," IEEE Trans. Signal Process., vol. 51, no. 3, pp. 714-725, Mar. 2003.
- [25] V. M. DaŠilva and E. S. Sousa, "Multicarrier orthogonal CDMA signals for quasi-synchronous communication systems," IEEE J. Sel. Areas Commun., vol. 12, no. 5, pp. 842-852, Jun. 1994.
- [26] T. K. Woo, "Orthogonal code design for quasisynchronous CDMA," IEE *Electron. Lett.*, vol. 36, no. 19, pp. 1632–1633, Sep. 2000. [27] W. A. Gardner and W. A. Brown, "Frequency-shift filtering theory for
- adaptive co-channel interference removal," in Proc. Asilomar Conf. Signals, Syst., Comput., Pacific Grove, CA, Oct. 1989, pp. 562–567. [28] S. Haykin, Adaptive Filter Theory, 3rd ed. Englewood Cliffs, NJ: Pren-
- tice-Hall, 1996.
- [29] E. R. Ferrara, "Frequency-domain implementations of periodically time-varying filters," IEEE Trans. Acoust., Speech, Signal Process., vol. ASSP-33, no. 4, pp. 883–892, Aug. 1985.
- [30] Y. C. Yoon, "SNR maximizing linear filters with interference suppression capabilities for DS-CDMA," Ph.D. dissertation, McGill Univ., Montreal, OC, Canada, 1998.



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