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Author Correction: Planar and van der Waals heterostructures for vertical tunnelling single electron transistors

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Correction to *Nature Communications*; <https://doi.org/10.1038/s41467-018-08227-1>, published online 16 January 2019

The original version of this Article contained an error in the spelling of the author Matthew Holwill, which was incorrectly given as Mathew Holwill. This has now been corrected in both the PDF and HTML versions of the Article.

Published online: 25 February 2019



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