International Journal of Bifurcation and Chaos, Vol. 18, No. 1 (2008) 121–140 © World Scientific Publishing Company

COMPLEX INTERMITTENCY IN SWITCHING CONVERTERS

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Received January 9, 2007; Revised January 29, 2007

Intermittent instability is commonly observed in switching power supplies during the design and development phase. It manifests as symmetrical period-doubling bifurcation in the time domain with long intermittent periods. Such intermittent operation is considered undesirable in practice and is usually avoided by appropriate adjustments of circuit parameters. This paper explores the mechanism and conditions for the emergence of intermittency in a common voltage-mode controlled buck converter. It is found that interference at frequencies near the switching frequency or its rational multiples will induce intermittent operation. The strengths and frequencies of the interfering signals determine the type and period of intermittency. The problem is analyzed by transforming the conventional parameter-bifurcation analysis to a time-bifurcation analysis. Analytical results are verified by simulations and experimental measurements.

Keywords: Switching power converter; intermittency; bifurcation; chaos; nonlinear system.

1. Introduction

Intermittent operation, sometimes referred to as "breathing" in the physics literature [Hu *et al.*, 2000; Qu *et al.*, 1995; Yang *et al.*, 1996], is a phenomenon which is frequently observed in periodically driven nonlinear systems. A specific form of intermittent operation exhibits period-doubling bifurcation in two symmetrical directions over the time domain. In this type of intermittent operation, the system intermittently bifurcates from the initial regular (or subharmonic) operation to the higher subharmonic operation or chaos, and then returns to the initial operation through the same bifurcation sequence in the reverse manner. To distinguish it from the usual bifurcation in the parameter space, we refer to this intermittent bifurcation in the time domain as *time-bifurcation*, as it manifests as a change of qualitative behavior of the system as time elapses [Zhou *et al.*, 2003] rather than as parameter changes. Such intermittency may arise in periodically driven nonlinear systems, where the frequency of a coupled signal is not consistent with the system's driving frequency. Switching power converters are periodically driven nonlinear systems, to which abundant sources of periodic interference are coupled via unintended paths (e.g. conducted or radiated paths) [Ferreira *et al.*, 1997; William, 1994]. When the interference frequency differs from the switching frequency, and if the interference is strong enough, intermittent operation occurs [Tse *et al.*, 2003]. Intermittent chaotic operation in a current-mode controlled boost converter has been studied recently [Wong *et al.*, 2004]. In practice, intermittency is an undesirable operating state that should be avoided because frequent deviations from the intended working regime may increase device stresses and hence jeopardize reliability. As the emergence of intermittency in power supplies is a complex issue of practical importance, a through understanding of the mechanism and the conditions under which intermittency occurs is vital to the reliable design of power supplies.

This paper studies the simple voltage-mode controlled buck converter using a simple model that takes into account the coupling of intruding interference signal with the converter. With this model, the condition for the emergence of intermittency is studied in detail. Simulation and experimental results verify the analytical findings. In particular, we will identify the parameters that are crucial to the emergence of intermittency. We will show that when the frequency of the intruding interference is close to the switching frequency or its rational multiples, intermittency may emerge in a few different forms (or patterns). The traditional bifurcation analysis deals with bifurcation in a parameter space, and hence is not directly relevant to the present study. By "transferring" the analytical procedure from a parameter domain to the time domain, we consider the variation of the characteristic multipliers as time elapses and establish a few important findings regarding the appearance of intermittent bifurcation in the voltage-controlled buck converter. Computer simulation and experimental measurements verify the analytical results. This paper focuses on a voltagemode controlled buck converter, but the same modeling approach and analysis can be extended to other switching power converters as well as other periodically driven nonlinear systems.

2. Voltage-Mode Controlled Buck Converter Coupled with Intruding Interference

The buck converter consists of an inductor, a switch, a diode, a storage capacitor and a resistor load, which are connected as shown in Fig. 1(a). When switch G turns on, the inductor current ramps up almost linearly, and when switch Gturns off, the inductor current ramps down and de-energizes through the diode to the load. In the voltage-mode control scheme, the output voltage error with respect to the reference voltage is amplified to give a control voltage $V_{\rm con}$:

$$V_{\rm con} = A(v_o - V_{\rm ref}) \tag{1}$$

which is then compared with a ramp signal $V_{\rm ramp}$, defined as

$$V_{\text{ramp}} = V_L + (V_U - V_L) \left(\frac{t}{T} \mod 1\right), \qquad (2)$$

where all symbols are explained in Fig. 1. The comparator output, u, gives the pulse-width-modulated signal necessary for driving the switch. Typically, the switch G is turned on when $V_{\rm con} \leq V_{\rm ramp}$, and turned off when $V_{\rm con} > V_{\rm ramp}$, as illustrated in Fig. 1(b). The state equations can be written as

$$\dot{x} = A_{\rm on}x + B_{\rm on}E \quad \text{for switch } G \text{ on} \dot{x} = A_{\rm off}x + B_{\rm off}E \quad \text{for switch } G \text{ off}$$
(3)

where x denotes the state variables, i.e. $x = [v_o \ i_L]^T$, the A's and B's are the system matrices given by

$$A_{\rm on} = A_{\rm off} = \begin{bmatrix} -\frac{1}{RC} & \frac{1}{C} \\ -\frac{1}{L} & 0 \end{bmatrix}, \qquad (4)$$

$$B_{\rm on} = \begin{bmatrix} 0\\ \frac{1}{L} \end{bmatrix}, \quad B_{\rm off} = \begin{bmatrix} 0\\ 0 \end{bmatrix}. \tag{5}$$

When it operates under a common voltage-mode control, the buck converter has been shown to exhibit complex behavior [Banerjee & Verghese, 2000; di Bernardo & Vasca, 2000; Fossas & Olivar, 1996; Hamill et al., 1992; Tse & di Bernardo, 2002; Tse, 1994; Wong *et al.*, 2004]. Figure 1(b) gives the key waveforms under this control. In practice, the intruding interference can take the form of coupling via conducted or radiated paths. Sometimes, the intruders can live on the same circuit board or be present at a very close proximity. Suppose the overall effect of the intruding signal is lumped to one spurious source v_s . We can model this coupling as an additive process which superposes the spurious signal v_s directly on the control signal $V_{\rm con}$, as shown in Fig. 1(a). The resulting perturbed control voltage is then given by

$$V_{\rm con}^* = V_{\rm con} + v_s \tag{6}$$

When the circuit parameters are chosen as follows: $L = 20 \text{ mH}, C = 47 \mu\text{F}, R = 22 \Omega$,



Fig. 1. Voltage-mode controlled buck converter coupled with spurious interference. (a) Schematic diagram; (b) key operation waveforms; (c) bifurcation diagram with E as the bifurcation parameter.

 $1/T = f_o = 2500 \text{ Hz}, V_{\text{ref}} = 11.3 \text{ V}, A = 8.4, V_L = 3.8 \text{ V}, V_U = 8.2 \text{ V}, and without the interference (<math>v_s = 0$), the buck converter will experience a typical period-doubling bifurcation cascade with input voltage E varying from 12 V to 33 V as shown in Fig. 1(c) [Fossas & Olivar, 1996; Zhou *et al.*, 2003]. The first bifurcation occurs when $E \approx 24.6 \text{ V}$, and the buck converter eventually enters a chaotic region when $E \approx 32.3 \text{ V}$.

3. A Glimpse at the Phenomenon

With the same set of circuit parameters shown above, the unperturbed buck converter operates in a regular period-1 orbit (steady state) when E is fixed at 22 V [Fig. 2(a)], which corresponds to $V_{\rm con} \approx 6.2$ V. In general, if we consider the interference v_s being a periodic signal, e.g. sinusoidal signal with amplitude \hat{v}_s , the perturbed control voltage can be written as

$$V_{\rm con}^* = V_{\rm con} + \hat{v}_s \sin(2\pi f_s t)$$

= $V_{\rm con} \left[1 + \alpha_v \sin(2\pi f_s t)\right]$ (7)

where f_s is the frequency of the interference, and α_v is the strength of the interference which is defined as the ratio of the \hat{v}_s to $V_{\rm con}$, i.e. $\alpha_v = \hat{v}_s/V_{\rm con}$. Here, we assume that in the steady state, the ripple of $V_{\rm con}$ is negligible. Coupling with this intruding interference signal, the converter will operate in a range of



Fig. 2. (a) Period-1 operation; Poincaré sections of responses induced by different levels of interference with (b) $\alpha_f = \sqrt{3}/2$, $\alpha_v = 0.015$; (c) $\alpha_f = 1/2$ or 1/3, $\alpha_v = 0.021$; (d) $\alpha_f = 1$, $\alpha_v = 0.0046$; (e) $\alpha_f = 2$ or 3, $\alpha_v = 0.0046$.

possible regimes, such as periodic operation, quasiperiodic operation, etc., according to the frequency ratio α_f ($\alpha_f = f_s/f_o$). We can summarize them in two cases.

Case 1. Frequency ratio α_f is an irrational number

In this case, the buck converter exhibits quasiperiodic behavior, because there are two incommensurate frequencies, i.e. switching frequency f_o and interference frequency f_s in the system. The quasi-periodicity is characterized by a torus on the Poincaré section. Figure 2(b) shows an example with $\alpha_f = \sqrt{3}/2$ and $\alpha_v = 0.015$.

Case 2. Frequency ratio α_f is a rational number

If α_f is a rational number, we have

$$\alpha_f = \frac{f_s}{f_o} = \frac{N_{\text{num}}}{N_{\text{den}}} \tag{8}$$

where N_{num} and N_{den} are positive integers. In this case, the buck converter operates periodically with period number equalling N_{den} , i.e. have an operation of period- N_{den} subharmonics. We can further summarize them in two subcases.

- (a) $\alpha_f = 1/N_{\text{den}}$: In this subcase, the buck converter operates in a period- N_{den} subharmonic orbit. Figures 2(c) and 2(d) show three examples with $N_{\text{den}} = 3$, 2, 1, which correspond to period-3, period-2 and period-1 operation, respectively, and are characterized by 3, 2 and 1 intersections on the Poincaré section.
- (b) $\alpha_f = N_{\text{num}}$: In this subcase, the buck converter operates in a period-1 orbit definitely. Figures 2(d) and 2(e) show three examples with $N_{\text{num}} = 1, 2, 3$, all corresponding to period-1 operation, and being characterized by one intersection on the Poincaré section.

4. Intermittency: A Detailed Look at the Time-Bifurcation Diagrams

Switching power converters are susceptible to abundant sources of periodic interference via unintended paths (e.g. conducted or radiated paths) [Ferreira *et al.*, 1997; William, 1994]. As the intruding signal is coupled unintentionally, its frequency f_s is usually not equal to the switching frequency f_o , or its rational multiples exactly. Therefore, it is unlikely to observe the kind of operation given in Fig. 2. But it is possible that the interference frequency f_s approaches the switching frequency f_o , or its rational multiples. In this situation, we have $f_s = nf_o + \hat{f}$, i.e. $\alpha_f = n + \hat{f}/f_o$, where \hat{f} is a small number compared to f_o and n is a rational number. This kind of interference will induce intermittency in a few different forms (such as intermittent subharmonics, intermittent chaos). The type and period of intermittency are determined by the interference frequency f_s and strength α_v .

When the interference frequency is close to the switching frequency, or its rational multiples, the perturbed control voltage (7) can be rewritten as

$$V_{\rm con}^* = V_{\rm con} [1 + \alpha_v \sin(2\pi f_s t)] = V_{\rm con} [1 + \alpha_v \sin 2\pi (n f_o + \hat{f}) t].$$
(9)

Assuming $\hat{f} = 1$, and *n* being 1/2, 1 and 2, respectively, we can obtain the time-bifurcation diagrams of intermittency, as shown in Figs. 3 to 5. From these figures, we have the following observations.

- (1) When the strength of the interference is very weak (i.e. small α_v), the converter can still maintain its expected steady-state period- N_{den} operation, though the average operating point may fluctuate. The effect of the interference is not significant at this stage, and no intermittency shows up. Figures 3(a), 4(a) and 5(a) show the corresponding time-bifurcation diagrams.
- (2) As the interference signal strength increases, the converter experiences higher subharmonic operation intermittently on top of the period- $N_{\rm den}$ operation. For a relatively low interference signal strength, period- $2N_{\rm den}$ subharmonics are observed intermittently with period- $N_{\rm den}$. Figures 3(b), 4(b) and 5(b) give the corresponding time-bifurcation diagrams.
- (3) Further increase in interference signal strength causes period- $2^2 N_{\text{den}}$ subharmonics to occur intermittently with period- $2N_{\text{den}}$ subharmonics and the period- N_{den} operation, as shown in Figs. 3(c), 4(c) and 5(c).
- (4) For a sufficiently high intruding signal strength, the converter starts to experience chaotic operation intermittently with period- $2^i N_{den}$ subharmonics and the period- N_{den} operation, as shown in Figs. 3(d), 4(d) and 5(d).
- (5) The intermittent period T_{int} is represented as

$$T_{\rm int} = \frac{1}{N_{\rm den}\hat{f}} \tag{10}$$

where $\hat{f} = |f_s - nf_o|$. Thus, if the interference signal frequency is very close to the switching



Fig. 3. Sampled inductor current waveforms (time-bifurcation diagrams) for $f_s = nf_o + \hat{f} = \frac{1}{2}f_o + 1 = 1251$ Hz. (a) $\alpha_v = 0.021$; (b) $\alpha_v = 0.066$; (c) $\alpha_v = 0.069$; (d) $\alpha_v = 0.075$.



Fig. 4. Sampled inductor current waveforms (time-bifurcation diagram) for $f_s = nf_o + \hat{f} = f_o + 1 = 2501$ Hz. (a) $\alpha_v = 0.0046$; (b) $\alpha_v = 0.046$; (c) $\alpha_v = 0.054$; (d) $\alpha_v = 0.069$.



Fig. 4. (Continued)



Fig. 5. Sampled inductor current waveforms (time-bifurcation diagrams) for $f_s = nf_o + \hat{f} = 2f_o + 1 = 5001$ Hz. (a) $\alpha_v = 0.0046$; (b) $\alpha_v = 0.012$; (c) $\alpha_v = 0.041$; (d) $\alpha_v = 0.053$.

frequency of the converter, or its rational multiples, the intermittency is long. In the examples here, $T_{\rm int} = 1$ s with $\hat{f} = 1$ Hz for the case of $N_{\rm den} = 1$, which are shown in Figs. 4 and 5; and $T_{\rm int} = 1/2$ s with $\hat{f} = 1$ Hz for the case of $N_{\rm den} = 2$, which are shown in Fig. 3.

5. Experimental Observations

We have built a circuit prototype to verify the simulation results. The circuit parameters are same as those used in the simulations. Without any interference, the converter will operate in the original period-1 orbit as shown in Fig. 6 for E = 22 V. Sinusoidal intruding interference signals have been used in the experiment. For consistency, here we provide three typical cases which correspond to the simulations reported in the last section, i.e. n equals 1/2, 1 and 2. These cases will also be analyzed in Secs. 6 and 7. By varying the interference signal strength, we observe intermittent subharmonics and chaos, which are consistent with the simulations. Figures 7 to 9 show a few time-bifurcation diagrams obtained experimentally, in which upper traces are output voltage v_o and lower traces are inductor current i_L .

- (1) Figure 7 shows the case when the interference frequency nears $f_o/2$. As the interference strength α_v increases, we observe fluctuating period-2 operation (Fig. 7(a), v_o : 200 mV/div, i_L : 100 mA/div), intermittent subharmonic operation (Fig. 7(b), v_o : 200 mV/div, i_L : 100 mA/div) and chaos (Fig. 7(c), v_o : 500 mV/div, i_L : 250 mA/div).
- (2) Figure 8 represents the case when the interference frequency is close to f_o . As the interference



Fig. 6. Measured phase portrait of period-1 operation with E = 22 V, x-axis: output voltage v_o , 40 mV/div; y-axis: inductor current i_L , 50 mA/div.



(a)

1 200%/ 2 20%/ F 0.00s 100%/ Step £ 3 1.00%



(b)

Fig. 7. Measured time-bifurcation diagrams with sinusoidal interference of different strengths α_v and n = 1/2. (a) $\alpha_v = 0.021$; (b) $\alpha_v = 0.067$; (c) $\alpha_v = 0.093$.

strength increases, we observe intermittent subharmonics (Fig. 8(a), v_o : 90 mV/div, i_L : 100 mA/div and Fig. 8(b), v_o : 150 mV/div, i_L : 100 mA/div) and chaos (Fig. 8(c), v_o : 250 mV/div, i_L : 200 mA/div). Here, the fluctuating period-1 operation cannot be observed,

-12.0m

(a)

100%/

100

F 4 11.5V

Stop

£ 4 11.5V





(b)



Print to disk file: PRINT

1 200%/

2 200



(c) Fig. 8. Measured time-bifurcation diagrams with sinusoidal

interference of different strengths α_v and n = 1. (a) $\alpha_v =$

0.015; (b) $\alpha_v = 0.061$; (c) $\alpha_v = 0.064$.

Fig. 9. Measured time-bifurcation diagrams with sinusoidal interference of different strengths α_v and n = 2. (a) $\alpha_v = 0.012$; (b) $\alpha_v = 0.052$; (c) $\alpha_v = 0.061$.

(c)

because the minimum output of the signal generator used here will still induce the intermittent subharmonics.

(3) Figure 9 shows the case when the interference frequency is close to $2f_o$. Similar to the case in Fig. 8, as the interference strength increases, we

observe intermittent subharmonics (Figs. 9(a) and 9(b), v_o : 200 mV/div, i_L : 100 mA/div) and chaos (Fig. 9(c), v_o : 200 mV/div, i_L : 100 mA/div). The fluctuating period-1 operation is absent for the same reason as stated previously.

6. Mapping from Time-Bifurcation to Parameter-Bifurcation

As the traditional bifurcation analysis mainly deals with parameter-bifurcations, it is not directly applicable to the analysis of intermittent bifurcation in the present study. Before we analyze the observed intermittency, we need to apply a transformation, which will transfer the change of time to the change of another variable. As we will see, the new variable is a conceptual "phase shift", which is used to represent the equivalent drift in the interfering frequency from the switching frequency. Basically we express the perturbed control voltage, originally given in (9), as

$$V_{\rm con}^* = V_{\rm con} \{ 1 + \alpha_v \sin[2\pi (nf_o + \hat{f})t] \}$$
$$= V_{\rm con} [1 + \alpha_v \sin(2\pi nf_o t + \theta)]$$
(11)

where $\theta = 2\pi \hat{f}t$. After this transformation, a sinusoidal term with the phase shift θ , and at the same frequency as the switching frequency f_o or its rational multiples, is obtained. Now we can map the change of the time over interval $[0, 1/\hat{f}]$ onto the change of phase shift θ over interval $[0, 2\pi]$.

Using the above perturbed control voltage, a parameter-bifurcation diagram can be constructed by selecting θ as the bifurcation parameter, which



Fig. 10. Parameter-bifurcation diagram with θ as bifurcation parameter for $nf_o = 1250$ Hz. (a) $\alpha_v = 0.021$; (b) $\alpha_v = 0.066$; (c) $\alpha_v = 0.069$; (d) $\alpha_v = 0.075$.



Fig. 11. Parameter-bifurcation diagrams with θ as bifurcation parameter for $nf_o = 2500$ Hz. (a) $\alpha_v = 0.0046$; (b) $\alpha_v = 0.046$; (c) $\alpha_v = 0.054$; (d) $\alpha_v = 0.069$.

is equivalent to the scenario of intermittency over $N_{\rm den}$ intermittent period. Using the same set of interference parameters as those used in Figs. 3 to 5, we obtain the corresponding parameter-bifurcation diagrams as shown in Figs. 10 to 12. When we compare the time-bifurcation diagrams and parameter-bifurcation diagrams, we find that they look very similar and the results are consistent.

7. Analysis of Intermittent Bifurcation

In this section, we try to analyze the stability of the converter. After we apply the transformation, we are able to carry out the standard parameterbifurcation analysis, and the results can be used to explain the corresponding time-bifurcation of intermittency. Essentially the parameter-bifurcation over the interval of θ is exactly the same as the time-bifurcation over an intermittent period shown in Figs. 3 to 5. In this section, we will first derive the iterative discrete-time map, and then analyze the stability of the converter by computing the Jacobian [Alligood *et al.*, 1996] and examining the loci of the characteristic multipliers [di Bernardo & Vasca, 2000; Hamill *et al.*, 1992; Zhou *et al.*, 2003].

By inspecting the time-bifurcation diagram (or the corresponding parameter-bifurcation diagram),



Fig. 12. Parameter-bifurcation diagrams with θ as bifurcation parameter for $nf_o = 5000$ Hz. (a) $\alpha_v = 0.0046$; (b) $\alpha_v = 0.012$; (c) $\alpha_v = 0.041$; (d) $\alpha_v = 0.053$.

it is found that the intermittency starts each intermittent period with the expected steady-state operation given by the one shown in Sec. 3. In our previous examples, there are two different cases, i.e. with the expected steady-state operation under the particular choice of parameters being a period-1 orbit for n = 1, 2, and being a period-2 subharmonic operation for n = 1/2. Therefore, we will carry out the analysis for these two cases separately.

7.1. $\alpha_f = n + \hat{f}/f_o$, where n = 1, 2

In the case of n being 1 or 2, with the expected steady-state operation of the buck converter being a period-1 orbit, the converter will first lose stability and bifurcate from regular period-1 to period-2 subharmonic operation as θ increases. (Note that varying θ is equivalent to varying t, from (11), for a given period $1/\hat{f}$.) A discrete-time iterative map $x_{n+1} = f(x_n)$ should be constructed for the analysis of stability. From the steady-state waveforms of the converter shown in Fig. 13, we can divide the operation in one period $(t_n - t_{n+1})$ into two phases according to the switch state of switch G, as illustrated below:

(1) Phase 1, $t_n - t_m$: The state of the converter is x_n in the beginning of this phase. In this phase, G is switched off with $V_{\rm con} > V_{\rm ramp}$. The state of the converter at the end of this phase, x_m , can be expressed as

$$x_m = N_{\text{off}}(\overline{d}_n)x_n + M_{\text{off}}(\overline{d}_n)E.$$
(12)



Fig. 13. Typical waveforms with n = 1, 2. Note that G is switched off with $V_{\rm con} > V_{\rm ramp}$; and is switched on with $V_{\rm con} < V_{\rm ramp}$.

(2) Phase 2, $t_m - t_{n+1}$: *G* is switched on with $V_{\text{con}} < V_{\text{ramp}}$ after t_m . At the end of this phase, the state of the converter will be x_{n+1} , which can be expressed as

$$x_{n+1} = N_{\text{on}}(1 - \overline{d}_n)x_m + M_{\text{on}}(1 - \overline{d}_n)E$$
 (13)

Combining (12) and (13), an iterative map over one switching period can be written as

$$\begin{aligned} x_{n+1} &= f(x_n, \overline{d}_n) \\ &= N_{\text{on}}(1 - \overline{d}_n) N_{\text{off}}(\overline{d}_n) x_n \\ &+ [N_{\text{on}}(1 - \overline{d}_n) M_{\text{off}}(\overline{d}_n) \\ &+ M_{\text{on}}(1 - \overline{d}_n)]E \end{aligned}$$
(14)

where d_n is the duty cycle,

$$\bar{d}_n = 1 - d_n,$$

$$N_{\rm on}(\bar{d}) = e^{A_{\rm on}\bar{d}T},$$

$$M_{\rm on}(\bar{d}) = A_{\rm on}^{-1}[N_{\rm on}(\bar{d}) - \mathbf{1}]B_{\rm on}$$

$$N_{\rm off}(\bar{d}) = e^{A_{\rm off}\bar{d}T},$$

$$M_{\rm off}(\bar{d}) = A_{\rm off}^{-1}[N_{\rm off}(\bar{d}) - \mathbf{1}]B_{\rm off}.$$

To complete the discrete-time model, we need to derive the defining function for the duty cycle. Essentially, we wish to find the connection between the switching instant t_m , or more precisely \overline{d}_n , and the state variables. By inspecting Fig. 13, we may define a switching function $S(\cdot)$ as

$$S(\overline{d}_n) = V_{\rm con}^*(\overline{d}_n T) - V_{\rm ramp}(\overline{d}_n T), \qquad (15)$$

from which G is switched on when $S(\overline{d}_n) < 0$, and off otherwise. Also, $S(\overline{d}_n) = 0$ defines the switching

instants. Taking into account the perturbed control voltage and expanding (15) gives:

$$S(\overline{d}_n) = \begin{bmatrix} A & 0 \end{bmatrix} \begin{bmatrix} v_o(\overline{d}_n T) - V_{\text{ref}} \\ i_L(\overline{d}_n T) \end{bmatrix} + v_s(\overline{d}_n T)$$
$$-V_L - (V_U - V_L)\overline{d}_n T$$
$$= \begin{bmatrix} A & 0 \end{bmatrix} \begin{bmatrix} N_{\text{off}}(\overline{d}_n)x_n + M_{\text{off}}(\overline{d}_n)E \end{bmatrix} - AV_{\text{ref}}$$
$$+ \alpha_v V_{\text{con}} \sin(2\pi n \overline{d}_n + \theta) - V_L$$
$$- (V_U - V_L)\overline{d}_n T$$
(16)

The discrete-time iterative map derived above can be used to study the stability of the system. Specifically, by computing the characteristic multipliers about the equilibrium point, we can analyze the way the system loses stability as a certain parameter is varied.

First, we denote the Jacobian of the discretetime iterative map by Γ , which can be evaluated by exploiting the implicit function derivation theorem to (14) and (16), i.e.

$$\Gamma(x_n, \overline{d}_n) = \frac{\partial x_{n+1}}{\partial x_n} = \frac{\partial f}{\partial x_n} - \frac{\partial f}{\partial \overline{d}_n} \left(\frac{\partial S}{\partial \overline{d}_n}\right)^{-1} \frac{\partial S}{\partial x_n}$$
(17)

where

$$\begin{aligned} \frac{\partial f}{\partial \overline{d}_n} &= [-A_{\rm on} \, TN_{\rm on}(1-\overline{d}_n)N_{\rm off}(\overline{d}_n) \\ &+ N_{\rm on}(1-\overline{d}_n)A_{\rm off} \, TN_{\rm off}(\overline{d}_n)]x_n \\ &+ [-A_{\rm on} \, TN_{\rm on}(1-\overline{d}_n)M_{\rm off}(\overline{d}_n) \\ &+ N_{\rm on}(1-\overline{d}_n)N_{\rm off}(\overline{d}_n)B_{\rm off}T \\ &- N_{\rm on}(1-\overline{d}_n)B_{\rm on}T]E \end{aligned}$$
$$\begin{aligned} \frac{\partial f}{\partial x_n} &= N_{\rm on}(1-\overline{d}_n)N_{\rm off}(\overline{d}_n) \\ \frac{\partial S}{\partial \overline{d}_n} &= [A \quad 0]N_{\rm off}(\overline{d}_n)(A_{\rm off}x_n + B_{\rm off}E)T \\ &+ 2\pi V_{\rm con}\alpha_v n\cos(2\pi n\overline{d}_n + \theta) - (V_U - V_L)T \end{aligned}$$
$$\begin{aligned} \frac{\partial S}{\partial x_n} &= [A \quad 0]N_{\rm off}(\overline{d}_n) \end{aligned}$$

Then the characteristic multipliers, λ , can be found by solving

$$\det[\lambda \mathbf{1} - \Gamma(x_n, \overline{d}_n)]_{x_n = X_Q, \overline{d}_n = \overline{d}_Q} = 0$$
(18)

where x_Q and \overline{d}_Q are the equilibrium values, and **1** is the identity matrix.



Fig. 14. Loci of the characteristic multipliers for $nf_o = 2500$ Hz. (a) $\alpha_v = 0.0046$; (b) $\alpha_v = 0.046$; (c) $\alpha_v = 0.054$; (d) $\alpha_v = 0.069$.

We now examine the movement of the characteristic multipliers as the phase shift θ varies. The loci of the characteristic multipliers are shown graphically in Figs. 14 and 15. It is noted that we use the same strength and frequency of interference as in Sec. 5. Thus, the system's stability near the equilibrium point can be studied. The movements of the loci of the characteristic multipliers agree perfectly well with the parameter-bifurcation diagrams shown in Figs. 11 and 12. We can summarize the results as follows.

(1) With very weak interference ($\alpha_v = 0.0046$ for $nf_o = 2500$ Hz and 5000 Hz), the loci of the characteristic multipliers are found as in

Figs. 14(a) and 15(a). From these figures, we observe that as the phase shift θ increases from 0, two characteristic multipliers move apart along a circle of radius less than 1 (equals 0.824 here) for the case of $nf_o = 2500$ Hz (for the case of $nf_o = 5000$ Hz, two characteristic multipliers move apart along the circle of radius less than 1 in opposite directions). When θ increases further, they move back along the original path. For all values of θ , they stay within the unit circle, it implies that the converter maintains its expected operation. This result is consistent with the parameter-bifurcation diagrams in Figs. 11(a) and 12(a) and the time-bifurcation diagrams in Figs. 4(a) and 5(a).



Fig. 15. Loci of the characteristic multipliers for $nf_o = 5000$ Hz. (a) $\alpha_v = 0.0046$; (b) $\alpha_v = 0.012$; (c) $\alpha_v = 0.041$; (d) $\alpha_v = 0.053$.

(2) As the strength of the interference increases $(\alpha_v = 0.046 \text{ for } nf_o = 2500 \text{ Hz}, \text{ and } \alpha_v = 0.012$ for $nf_o = 5000 \,\mathrm{Hz}$), bifurcation is observed as θ varies, as shown in Figs. 11(b) and 12(b), which corresponds to the intermittent subharmonics in Figs. 4(b) and 5(b). This scenario can be explained in terms of the movement of the loci of the characteristic multipliers, as illustrated in Figs. 14(b) and 15(b). For the case of $nf_o = 2500 \,\mathrm{Hz}$, one characteristic multiplier crosses the unit circle along the real axis from outside when θ increases up to 1.6. Thus, the converter becomes stable as θ increases up to about 1.6. It remains in this stable operation state till $\theta = 5.25$. At this point, one characteristic multiplier leaves the unit circle and the converter undergoes a period-doubling. While for the case of $nf_o = 5000$ Hz, one characteristic multiplier crosses the unit circle along the real axis from inside when θ increases up to 2.65. Thus, the converter experiences a perioddoubling bifurcation and becomes unstable as θ increases up to about 2.65. It remains in this unstable operation state till $\theta = 4.79$. At this point, one characteristic multiplier enters the unit circle and the converter returns to its intended regular operation.

(3) For higher values of α_v (for both $nf_o = 2500 \text{ Hz}$ and 5000 Hz), the converter period-doubles up to period-4 subharmonics and chaotic regime. The loci of the characteristic multipliers in Figs. 14(c), 15(c), 14(d) and 15(d) also locate the onset of the period-doubling bifurcation clearly. The movements of the characteristic multipliers are very similar to those in the previous case ($\alpha_v = 0.046$ for $nf_o = 2500$ Hz, and $\alpha_v = 0.012$ for $nf_o = 5000$ Hz), and they are not repeated here.

7.2. $\alpha_f = n + \hat{f}/f_o$, where n = 1/2

In the case of n being 1/2, with the expected steady-state operation of the converter being a period-2 subharmonic orbit, the converter will first lose stability and bifurcate from a regular period-2 operation to a period-4 operation. A discrete-time iterative map in the form of $x_{n+2} = f(x_n)$ should be constructed for the analysis of stability. From the steady-state waveforms of the converter shown in Fig. 16, we can divide the operation over two periods $(t_n - t_{n+2})$ into four phases according to the switch state of switch G, as illustrated below:

(1) Phase 1, $t_n - t_{m1}$: The state of the converter is x_n at the beginning of this phase. In this phase, G is switched off with $V_{\rm con} > V_{\rm ramp}$. The state of the converter at the end of this phase, x_{m1} , can be expressed as

$$x_{m1} = N_{\text{off}}(\bar{d}_{1n})x_n + M_{\text{off}}(\bar{d}_{1n})E$$
 (19)

(2) Phase 2, $t_{m1} - t_{n+1}$: *G* is switched on with $V_{\rm con} < V_{\rm ramp}$ after t_{m1} . At the end of this phase, the state of the converter will be x_{n+1} , which can be expressed as

$$x_{n+1} = N_{\rm on}(1 - \bar{d}_{1n})x_{m1} + M_{\rm on}(1 - \bar{d}_{1n})E$$
(20)



Fig. 16. Typical waveforms with n = 1/2. Note that G is switched off with $V_{\rm con} > V_{\rm ramp}$; and is switched on with $V_{\rm con} < V_{\rm ramp}$.

(3) Phase 3, $t_n + 1 - t_{m2}$: *G* is switched off with $V_{\rm con} > V_{\rm ramp}$. The state of the converter at the end of this phase, x_{m2} , can be expressed as

$$x_{m2} = N_{\text{off}}(\bar{d}_{2n})x_{n+1} + M_{\text{off}}(\bar{d}_{2n})E \qquad (21)$$

(4) Phase 4, $t_{m2} - t_{n+2}$: *G* is switched on with $V_{\rm con} < V_{\rm ramp}$ after t_{m2} . At the end of this phase, the state of the converter will be x_{n+2} , which can be expressed as

$$x_{n+2} = N_{\rm on}(1 - \bar{d}_{2n})x_{m2} + M_{\rm on}(1 - \bar{d}_{2n})E$$
(22)

Following similar procedures, we can write an iterative map over two switching periods, using (19) to (22), as

$$\begin{aligned} x_{n+2} &= f(x_n, \bar{d}_{2n}, \bar{d}_{1n}) \\ &= N_{\rm on}(1 - \bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})N_{\rm on}(1 - \bar{d}_{1n}) \\ &\times N_{\rm off}(\bar{d}_{1n})x_n \\ &+ N_{\rm on}(1 - \bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})N_{\rm off}(1 - \bar{d}_{1n}) \\ &\times M_{\rm off}(\bar{d}_{1n})E \\ &+ N_{\rm on}(1 - \bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})M_{\rm on}(1 - \bar{d}_{1n})E \\ &+ N_{\rm on}(1 - \bar{d}_{2n})M_{\rm off}(\bar{d}_{2n})E \\ &+ M_{\rm on}(1 - \bar{d}_{2n})E \end{aligned}$$
(23)

where N's and M's are defined as before.

Similarly, by inspecting Fig. 16, two switching functions defining the duty cycles can be written as

$$S_{1}(x_{n}, \overline{d}_{1n}) = [A \quad 0][N_{\text{off}}(\overline{d}_{1n})x_{n} + M_{\text{off}}(\overline{d}_{1n})E] - AV_{\text{ref}} + \alpha_{v}V_{\text{con}}\sin(\pi\overline{d}_{1n} + \theta) - V_{L} - (V_{U} - V_{L})\overline{d}_{1n}T$$
(24)

 $S_2(x_n, \overline{d}_{1n}, \overline{d}_{2n}) = \begin{bmatrix} A & 0 \end{bmatrix} \begin{bmatrix} N_{\text{eff}} \\ n \end{bmatrix}$

$$= [A \quad 0] [N_{\text{off}}(\bar{d}_{2n}) N_{\text{on}}(1 - \bar{d}_{1n}) N_{\text{off}}(\bar{d}_{1n}) x_n + N_{\text{off}}(\bar{d}_{2n}) N_{\text{on}}(1 - \bar{d}_{1n}) M_{\text{off}}(\bar{d}_{1n}) E + N_{\text{off}}(\bar{d}_{2n}) M_{\text{on}}(1 - \bar{d}_{1n}) E + M_{\text{off}}(\bar{d}_{2n}) E] - AV_{\text{ref}} - \alpha_v V_{\text{con}} \sin(\pi \bar{d}_{2n} + \theta) - V_L - (V_U - V_L) \bar{d}_{2n} T$$
(25)

We can analyze the way the system loses stability of period-2 subharmonic operation by computing the characteristic multipliers of the discrete-time iterative map derived above near the equilibrium point. First, we denote the Jacobian of the discrete-time iterative map by Γ , which can be evaluated by applying the implicit function

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derivation theorem to (23) through (25), i.e.

$$\Gamma = \frac{\partial f}{\partial x_n} - \frac{\partial f}{\partial \overline{d}_{1n}} \left(\frac{\partial S_1}{\partial \overline{d}_{1n}}\right)^{-1} \frac{\partial S_1}{\partial x_n} - \frac{\partial f}{\partial \overline{d}_{2n}} \left(\frac{\partial S_2}{\partial \overline{d}_{2n}}\right)^{-1} \left[\frac{\partial S_2}{\partial x_n} - \frac{\partial S_2}{\partial \overline{d}_{1n}} \left(\frac{\partial S_1}{\partial \overline{d}_{1n}}\right)^{-1} \frac{\partial S_1}{\partial x_n}\right]$$
(26)

where

$$\begin{split} \frac{\partial f}{\partial x_n} &= N_{\rm on}(1-\bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})N_{\rm out}(1-\bar{d}_{1n})N_{\rm off}(\bar{d}_{1n}) \\ \frac{\partial f}{\partial \bar{d}_{1n}} &= N_{\rm on}(1-\bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})\frac{\partial N_{\rm on}(1-\bar{d}_{1n})}{\partial \bar{d}_{1n}}N_{\rm off}(\bar{d}_{1n})x_n \\ &+ N_{\rm on}(1-\bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})\frac{\partial N_{\rm on}(1-\bar{d}_{1n})}{\partial \bar{d}_{1n}}M_{\rm off}(\bar{d}_{1n})E + N_{\rm on}(1-\bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{1n}) \\ &\times \frac{\partial M_{\rm off}(\bar{d}_{2n})}{\partial \bar{d}_{1n}}E + N_{\rm on}(1-\bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})\frac{\partial M_{\rm on}(1-\bar{d}_{1n})}{\partial \bar{d}_{1n}}E \\ \frac{\partial f}{\partial \bar{d}_{2n}} &= \frac{\partial N_{\rm on}(1-\bar{d}_{2n})}{\partial \bar{d}_{2n}}N_{\rm off}(\bar{d}_{2n})N_{\rm off}(\bar{d}_{2n})\partial N_{\rm off}(\bar{d}_{2n})\frac{\partial M_{\rm on}(1-\bar{d}_{1n})}{\partial \bar{d}_{2n}}E \\ \frac{\partial f}{\partial \bar{d}_{2n}} &= \frac{\partial N_{\rm on}(1-\bar{d}_{2n})}{\partial \bar{d}_{2n}}N_{\rm off}(\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{1n})N_{\rm off}(\bar{d}_{1n})x_n \\ &+ N_{\rm on}(1-\bar{d}_{2n})\frac{\partial N_{\rm off}(\bar{d}_{2n})}{\partial \bar{d}_{2n}}N_{\rm on}(1-\bar{d}_{1n})N_{\rm off}(\bar{d}_{1n})x_n \\ &+ \frac{\partial N_{\rm on}(1-\bar{d}_{2n})}{\partial \bar{d}_{2n}}N_{\rm off}(\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{1n})M_{\rm off}(\bar{d}_{1n})E \\ &+ \frac{\partial N_{\rm on}(1-\bar{d}_{2n})}{\partial \bar{d}_{2n}}N_{\rm off}(\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{1n})M_{\rm off}(\bar{d}_{1n})E \\ &+ \frac{\partial N_{\rm on}(1-\bar{d}_{2n})}{\partial \bar{d}_{2n}}N_{\rm off}(\bar{d}_{2n})M_{\rm on}(1-\bar{d}_{1n})M_{\rm off}(\bar{d}_{2n})E \\ &+ \frac{\partial N_{\rm on}(1-\bar{d}_{2n})}{\partial \bar{d}_{2n}}N_{\rm off}(\bar{d}_{2n})M_{\rm on}(1-\bar{d}_{1n})E \\ &+ \frac{\partial N_{\rm on}(1-\bar{d}_{2n})}{\partial \bar{d}_{2n}}M_{\rm off}(\bar{d}_{2n})E \\ &= \left[A \quad 0\right] \left[\frac{\partial N_{\rm off}(\bar{d}_{2n})}{\partial \bar{d}_{2n}}X_{\rm on}(1-\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{2n})N_{\rm on}(1-\bar{d}_{2n})\frac{\partial N_{\rm off}(\bar{d}_{2n})}{\partial \bar{d}_{2n}}X_{\rm on} \\ &+ \frac{\partial N_{\rm off}(\bar{d}_{2n})\frac{\partial N_{\rm off}(\bar{d}_{2n})}{\partial \bar{d}_{1n}}M_{\rm off}(\bar{d}_{2n})N_{\rm on}$$

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$$\frac{\partial S_2}{\partial \bar{d}_{2n}} = \begin{bmatrix} A & 0 \end{bmatrix} \begin{bmatrix} \frac{\partial N_{\text{off}}(\bar{d}_{2n})}{\partial \bar{d}_{2n}} N_{\text{on}}(1-\bar{d}_{1n}) N_{\text{off}}(\bar{d}_{1n}) x_n + \frac{\partial N_{\text{off}}(\bar{d}_{2n})}{\partial \bar{d}_{2n}} N_{\text{on}}(1-\bar{d}_{1n}) M_{\text{off}}(\bar{d}_{1n}) E \\ + \frac{\partial N_{\text{off}}(\bar{d}_{2n})}{\partial \bar{d}_{2n}} M_{\text{on}}(1-\bar{d}_{1n}) E + \frac{\partial M_{\text{off}}(\bar{d}_{2n})}{\partial \bar{d}_{2n}} E \end{bmatrix} - \alpha_v V_{\text{con}} \pi \cos(\pi \bar{d}_{2n} + \theta) - (V_U - V_L) T$$

Then, the characteristic multipliers, λ , can be found by solving

$$\det\left[\lambda \mathbf{1} - \Gamma(x_n, \bar{d}_{1n}, \bar{d}_{2n})\right]_{x_n = X_Q, \bar{d}_{1n} = \bar{d}_{1,Q}, \bar{d}_{2n} = \bar{d}_{2,Q}} = 0$$
(27)

where x_Q , $\overline{d}_{1,Q}$ and $\overline{d}_{2,Q}$ are the equilibrium values, and **1** is the identity matrix. We should note that the intermittent period is 1/2 with $T_{\text{int}} = 1/(N_{\text{den}}\hat{f})$ as indicated in Sec. 4. Therefore, the parameter-bifurcation diagram of θ over interval

 $[0, \pi]$ is exactly equivalent to that of θ over interval $[\pi, 2\pi]$. Also, we only need to compute the characteristic multipliers with θ over interval $[0, \pi]$, and the characteristic multipliers corresponding to θ over interval $[\pi, 2\pi]$ are the same.



Fig. 17. Loci of the characteristic multipliers for $nf_o = 1250$ Hz. (a) $\alpha_v = 0.021$; (b) $\alpha_v = 0.066$; (c) $\alpha_v = 0.069$; (d) $\alpha_v = 0.075$.

We now examine the movement of the characteristic multipliers as the phase shift θ varies for different values of the strength and the frequency of interference. The loci of the characteristic multipliers are shown graphically in Fig. 17. Thus, the stability of the expected steady-state operation (period-2 subharmonics) can be studied. The movements of the loci of characteristic multipliers are in perfect agreement with the parameter-bifurcation diagrams shown in Fig. 10. We can summarize the results as follows.

- (1) For very weak interference ($\alpha_v = 0.021$), the loci of the characteristic multipliers are shown in Fig. 17(a). We observe that as phase shift θ increases from 0, the two characteristic multipliers move along a circle of radius less than 1 (equals 0.68 here). When θ increases further, they move back along the original path. For all values of θ , the corresponding characteristic multipliers are located within the unit circle, it implies that the buck converter maintains its expected steady-state operation. This result is consistent with the parameter-bifurcation diagram in Fig. 10(a) and time-bifurcation diagram in Fig. 3(a).
- (2) As the strength of the interference increases $(\alpha_v = 0.066)$, bifurcation occurs as θ is varied as shown in Fig. 10(b), which corresponds to the intermittent subharmonics of the same parameters in Fig. 3(b). This scenario can be explained in terms of the movement of the loci of the characteristic multipliers, as illustrated in Fig. 17(b). From the figure, one characteristic multiplier crosses the unit circle along the real axis from outside when θ increases up to 0.89. Thus, the converter is stabilized to the expected steady-state period-2 operation as θ increases up to about 0.89. It remains in this stable operation state till $\theta = 2.59$. At this point, one characteristic multiplier leaves the unit circle and the converter period-doubles.
- (3) For higher values of α_v , the converter perioddoubles up to period-8 subharmonics or chaotic regime. The loci of the characteristic multipliers in Figs. 17(c) and 17(d) also locate the onset of the period-doubling bifurcation clearly. The movements of the characteristic multipliers are very similar to the previous case ($\alpha_v = 0.066$), and they are not repeated here.

8. Conclusion

Switching power converters are nonlinear systems which have been shown to exhibit a range of complex behavior. In this paper we attempt to explore a commonly observed but rarely explained nonlinear phenomenon in power supplies. Sources of periodic interference are easily coupled to the converters via unintended paths. We find that when the interference frequency approaches the switching frequency or its rational multiples, intermittency occurs with the intended period- $N_{\rm den}$ operation being interrupted. We also show that the signal strength and frequency of the interference signal determine the type and period of intermittency. Furthermore, a mapping that transforms the timebifurcation to parameter-bifurcation is introduced. By applying the transformation, the intermittency can be analyzed indirectly by using the traditional numerical analysis procedures. We derive the iterative discrete-time map and examine the movement of the corresponding loci of the characteristic multipliers to explain the observed intermittency. The same line of analysis can be used to study the intermittency in other types of converters, as well as other periodically driven nonlinear systems.

Acknowledgments

This work was supported by National Natural Science Foundation of China (No. 60672023), ARC Discovery Projects Grant (No. DP0559109), and Hong Kong Polytechnic University Research Grant (No. 1BB-ZA).

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