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Construction and test of a $1 \times 1 \text{ m}^2$ Micromegas chamber for sampling hadron calorimetry at future lepton colliders

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Abstract

Sampling calorimeters can be finely segmented and used to detect showers with high spatial resolution. This imaging power can be exploited at future linear collider experiments where the measurement of jet energy by a Particle Flow method requires optimal use of tracking and calorimeter information. Gaseous detectors can achieve high granularity and a hadron sampling calorimeter using Micromegas chambers as active elements is considered in this paper. Compared to traditional detectors using wires or resistive plates, Micromegas is free of space charge effects and could therefore show superior calorimetric performance. To test this concept, a prototype of $1 \times 1 \text{ m}^2$ equipped with 9216 readout pads of $1 \times 1 \text{ cm}^2$ has been built. Its technical and basic operational characteristics are reported.

Keywords: Large area Micromegas, Micro Pattern Gaseous Detectors, Digital hadron calorimetry, Future linear colliders

1. Introduction

1.1. Particle Flow calorimetry

The detailed study of electroweak symmetry breaking and of the properties of the Higgs boson within and beyond the Standard Model (SM) are some of the physics goals motivating the construction of a linear electron positron collider (ILC or CLIC [1, 2]). This physics case is now enhanced with the discovery at LHC of a Higgs-like new particle [3, 4]. Many interesting physics channels at a linear collider will be reconstructed in multi-jet final states, often accompanied by charged leptons and missing transverse energy associated with neutrinos or possibly the lightest super-symmetric particles. The reconstruction of the invariant masses of two or more jets will be important for event reconstruction and event identification. The dijet mass resolution should be good enough to identify Z and W bosons in their hadronic final states with an accuracy comparable to their natural decay width. This requires an excellent jet energy resolution of 3–4% over an energy range extending up to 1.5 TeV for a 3 TeV collider.

Two techniques are studied by the DREAM [5] and CALICE [6] collaborations to meet this goal. The first one, called Dual Readout, is a compensation technique that uses cherenkov and scintillation light produced in hadron showers to correct for fluctuations of the electromagnetic fraction which otherwise dominate the jet energy resolution [7]. The Particle Flow technique relies on highly segmented calorimeters and a precise tracker to separate the jet's charged and neutral components [8].

The use of the tracking information reduces the dependence on hadronic calorimetry and results in the required excellent jet energy and di-jet mass resolution [9].

1.2. Semi-digital hadron calorimetry

Two hadron calorimeters using steel or tungsten absorbers are developed by the the CALICE collaboration. The first is instrumented with $3 \times 3 \text{ cm}^2$ scintillating tiles read out by SiPM and 12 bit ADCs [10]. The second uses gaseous detectors with higher segmentation ($1 \times 1 \text{ cm}^2$) and simpler readout (1 bit or 2 bit [11, 12]). The first favours single hadron energy resolution (higher sampling fraction, analogue readout) while the second targets a high shower separation capability (smaller cells) probably at the expense of resolution (digital readout).

A digital hadron calorimeter (1 bit, DHCAL) is expected to have two regimes of operation. A low energy linear regime where the response to the electromagnetic and hadronic shower parts, taken separately, is constant. In this regime, Landau fluctuations are suppressed resulting in improved resolution with respect to a perfect analogue readout. A higher energy saturated regime where the energy information is lost due to undercounting and the resolution degrades with increasing hadron energy [13, 14]. The energy frontier between the two regimes depends mainly on the cell size and absorber material. In an SiD-like HCAL geometry [15] ($1 \times 1 \text{ cm}^2$ pads, steel absorbers), Monte Carlo studies indicate a frontier between 20–30 GeV.

The electromagnetic part of hadron showers results in dense energy deposits and is responsible for the saturation of a DHCAL. A way to account for these deposits in the energy reconstruction is to use additional readout thresholds (2 bit, semi-digital HCAL or SDHCAL). With the right threshold settings

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58 and energy reconstruction algorithm, it should be possible to
 59 improve the energy resolution beyond the saturated regime.

60 1.3. The Micromegas detector and calorimeter project

61 Micromegas is a Micro Pattern Gas Detector (MPGD) that
 62 uses a thin mesh to separate the gas volume into two regions
 63 [16]. A low field region where primary electrons are released
 64 from the atoms and a high field region where they are drifted
 65 to and multiply by avalanche. Thanks to a fast collection of the
 66 avalanche ions, Micromegas is free of space charge effects up
 67 to very high particle rates and therefore well suited for track-
 68 ing in high rate environments. This property also makes this
 69 detector very appealing for calorimetry because the sum of the
 70 anode signals is proportional to the energy deposited in the drift
 71 region. This is an improvement with respect to wire chamber
 72 based gaseous calorimeters which suffered from intrinsic signal
 73 saturation from the ion space charge around the wires [17]. In
 74 addition, ageing effects in Micromegas are minimal because it
 75 operates in simple gas mixtures (*e.g.* Ar/CO₂) and at relatively
 76 low electric fields (~ 40 kV/cm with a gap of $128 \mu\text{m}$).

77 The Micromegas calorimeter project was initiated in 2006.
 78 The first step of the project was the characterisation of small
 79 prototypes equipped with off-detector electronics. Based on the
 80 successful results [18], the project moved on to the next phase
 81 by integration of the electronics on the detector printed circuit
 82 board (PCB) and by scaling up of the detector dimensions.

83 2. Description of the $1 \times 1 \text{ m}^2$ Micromegas prototype

84 2.1. Active sensor units

85 An Active Sensor Unit (ASU) is a $32.4 \times 48.4 \text{ cm}^2$ PCB
 86 (8 layers, 1.2 mm thin) segmented into 1536 anode pads of
 87 $1 \times 1 \text{ cm}^2$ arranged in 32 rows and 48 columns. It is equipped
 88 with a Micromegas mesh and 24 front-end chips. The mesh is
 89 laminated on the PCB pad plane according to the Bulk process
 90 [19] and held by small equally spaced pillars and 2 mm wide
 91 strips on the four ASU edges. Packaged chips are soldered to
 92 the opposite PCB side, together with gas discharge protection
 93 diodes, polarisation resistors, high voltage decoupling capaci-
 94 tors and flat connectors.

95 The ASU chips are read out with 2 Detector Interface boards
 96 (DIF, inter-DIF) which also distribute voltage to the front-end
 97 electronics and to the Micromegas mesh. ASU and inter-DIF
 98 are connected with flat cables to minimise the detector thick-
 99 ness and to allow for some mechanical flexibility between the 2
 100 boards. Thanks to flat connectors on both sides of the ASU, sev-
 101 eral ASUs can be read out in a row (Figure 1). This is essential
 102 for constructing large chambers as several ASUs can be chained
 103 and read out with only one pair of DIF/inter-DIF boards.

104 2.2. Front-end electronics

105 The ILC beam will be pulsed and composed of 1 ms long
 106 bunch trains separated by 199 ms. During a train, bunches col-
 107 lide every 340 ns and detector signals are digitised and associ-
 108 ated to the time of a bunch. Between trains, all information is
 109 read out from the memory to the back-end electronics and the

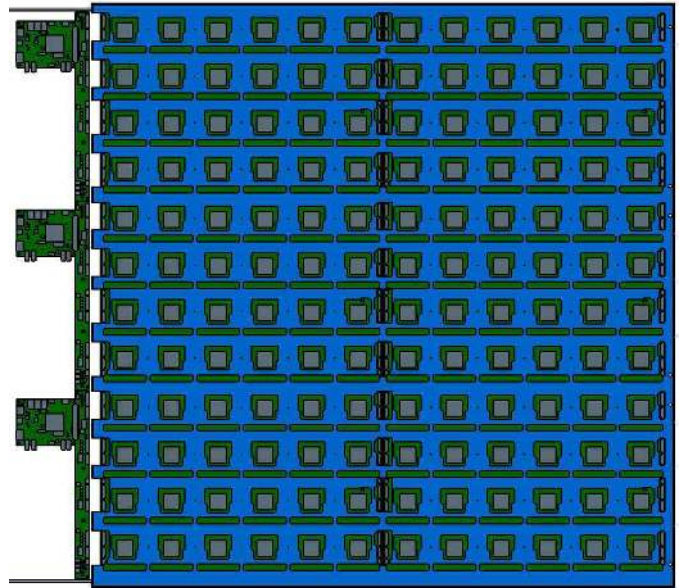


Figure 1: Chip side of the $1 \times 1 \text{ m}^2$ prototype showing the readout boards on the left side (three pairs of DIF and inter-DIF), six ASUs glued to a rigid mask (in blue) and the gas inlet and outlet at the top and bottom left corners.

front-end circuits are turned off to reduce the heat dissipation inside the calorimeters. Key electronics features are thus self-triggering with memory, time-stamping and power-pulsing.

A front-end chip optimised for the detection of Micromegas signals has been developed [20]. It is called MICROROC (Micromegas Readout Chip) and belongs to a generation of chips optimised for calorimetry at a future linear collider [21]. The MICROROC is a 64 channel chip, with three readout thresholds and a power-pulsing capability to reduce its consumption from a nominal value of 3.7 mW at 3.5 V per channel to $20 \mu\text{W}$ (assuming a duty cycle of 0.5 %). Each channel input is protected against gas discharges by a diode network followed by a charge preamplifier and two shapers of low and high gain and tunable peaking time (75–200 ns). The shaper outputs are connected to three discriminators. When a signal crosses the low threshold, the content of the 64 channel matrix is written to memory with a clock time (so-called event). A total of 127 events can be recorded before filling completely the chip memory. The latter is read out either when it is full (self-trigger mode) or upon the arrival at the chip of an external trigger signal (trigger mode).

The high gain shaper is connected to the low and medium threshold discriminators and has a dynamic range of 200 fC. The low gain shaper has a linear response up to 500 fC and is connected to the high threshold discriminator. The 3 thresholds are set by 10 bit DACs common to the 64 channels. Each channel features a 4 bit DAC that can be used to shift the pedestal voltage with respect to the common thresholds and minimise their dispersions. A detailed characterisation of the detector can be performed with a calibration test input and a multiplexed shaper output (analogue readout). The calibration of the electronics is presented in section 3.1 and the analogue readout of the shaper signals is illustrated in section 4.7.

2.3. Mechanical design

The mechanical constraints to build an ILC hadron calorimeter are stringent. The calorimeter will be located inside the solenoid magnet which limits its total depth to preserve cost; the envisaged space between absorbers is 8 mm. The front-end chips will be integrated in the active layers to minimise dead zones between modules; only readout boards are foreseen at the module edges. Another challenge is the fabrication of large area active layers (up to $1 \times 3 \text{ m}^2$ in the SiD design).

Modular and scalable to larger area, the $1 \times 1 \text{ m}^2$ Micromegas prototype consists of six ASUs glued on a rigid mask and placed in a single gas volume (Figure 1 and 2). Small spacers are inserted in the 1 mm gap between ASUs and support the cathode cover, defining precisely a drift gap of 3 mm (Figure 3). Plastic frames are closing the chamber sides, leaving openings for two gas pipes and flat cables for electronics connections. After assembly, the chamber is equipped with readout boards (three pairs of DIF/inter-DIF) and a patch panel for voltage distribution.



Figure 2: The $1 \times 1 \text{ m}^2$ prototype during assembly showing the mesh side of the six ASUs, the readout boards and the cathode cover.

The total chamber thickness is 9.2 mm which includes 2.7 mm for the cathode cover, 3 mm of drift gap and 3.5 mm for PCB, ASICs and mask. With this mechanical design, the fraction of non-instrumented area is 1.5 % of the total area defined by the six ASUs. Dead zones are mainly caused by the 1 mm gap between ASUs and the 2 mm wide inactive photore-sist strips that support the mesh on the four ASU sides.

3. Tests prior to the assembly of the $1 \times 1 \text{ m}^2$ prototype

The electronics and Bulk mesh characteristics of all ASUs are measured to verify the ASU quality before they are sealed in the prototype. In addition, the response to X-rays of one ASU has been determined at various voltages and thresholds to define the operating point of the prototype. These measurements are reported in the next sections.

3.1. Electronics calibration

3.1.1. Method

The calibration enables to set the three thresholds by measuring the electronic gain (DAC/fC) of the two shapers. It consists of injecting voltage pulses to the test capacitor of each channel and changing the relevant threshold every 100 pulses. For a given test charge, the channel response (0/1) versus threshold is measured and latter differentiated. The gain and noise of the shapers are deduced from the mean μ and root mean squared (RMS) σ of the resulting distribution. The calibration was performed with a single chip test board after a production of 343 MICROROCs. According to the rejection criteria defined in [20], a yield of 96.5 % was reached. After soldering the chips to the PCBs and lamination of the Bulk mesh, another calibration was performed on the six available ASUs giving compatible results. These results are presented in the following sections.

3.1.2. Shaper gains and noise

The gains of 9216 channels are distributed around a mean value of 7.0 DAC/fC (high gain) and 1.6 DAC/fC (low gain). The channel to channel variation in both cases is $\sim 2\%$ RMS (Figure 4). This is smaller than the signal variations resulting from mechanical imperfections of the Micromegas amplification gap ($\sim 10\%$). These imperfections depends on the tension of the mesh during the lamination of the Bulk process and eventually dominate the response uniformity of this detector [18].

The low threshold discriminator triggers the writing to memory of the 64 channel content. It is connected to the output of the high gain shaper and therefore only the noise of this shaper is relevant for our purposes. Calculating the noise as σ divided by the gain, an average noise of 0.25 fC is found with variations of 0.03 fC RMS over all channels (Figure 5). This is quite small compared to a typical minimum ionising particle (MIP) signal of 1–20 fC and close to what was measured before soldering the chips to the PCBs (in the latter case, a capacitor equal to the detector pad to ground capacitance was connected to the chip inputs). In conclusion, neither the design of the PCB nor the mesh lamination increase the noise level at the channel inputs.

3.1.3. Thresholds and pedestals

The three discriminator DACs of a MICROROC are common to the 64 channels. The lowest possible threshold is therefore determined by the channel with the highest pedestal, for instance 5σ above this pedestal, channels with lower pedestals experiencing larger thresholds. To minimise the threshold spread, each channel is equipped with a pedestal DAC. The latter controls the pedestal voltage and can be used to correct the individual thresholds by a few fC. A method to align the pedestals is to set the pedestal DACs to obtain a uniform noise rate over all the channels. With this method, the spread of thresholds is reduced by a factor of 2–3 (Figure 6). The improvement of the detector response is significant when operating at a moderate gas gain (≤ 1000).

In a semi-digital calorimeter, the values of the medium and high thresholds should be optimised for best energy resolution over the relevant energy range. During the test beam period reported in section 4, several settings were tried.

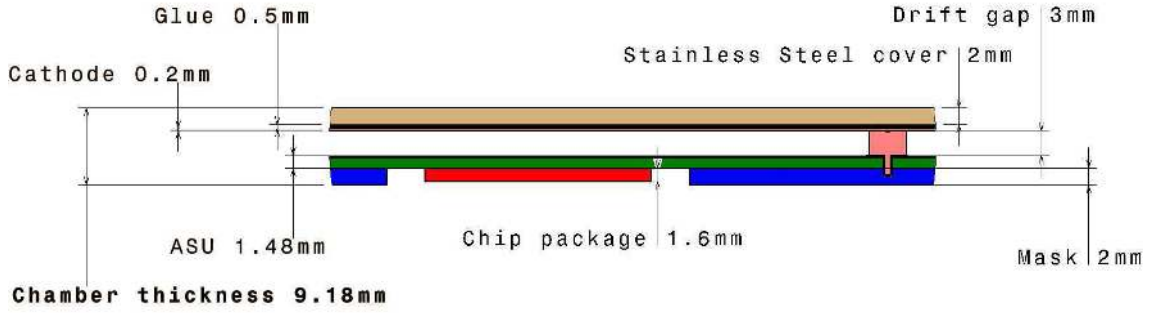


Figure 3: Cross-section of the $1 \times 1 \text{ m}^2$ prototype close to a junction between two ASUs. The spacer on the right side defines the 3 mm drift gap.

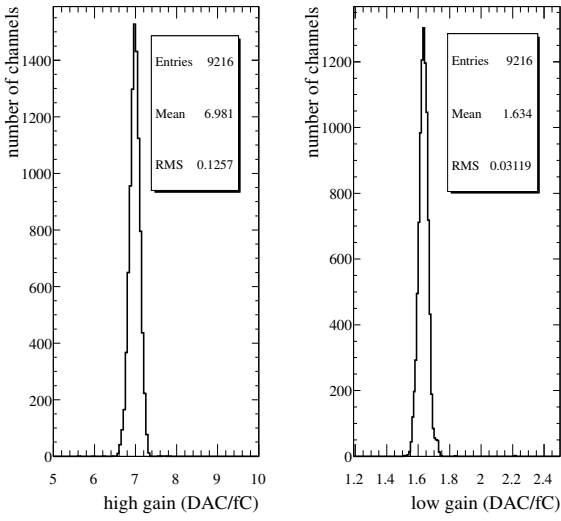


Figure 4: Shaper gains measured on all channels of the $1 \times 1 \text{ m}^2$ prototype.

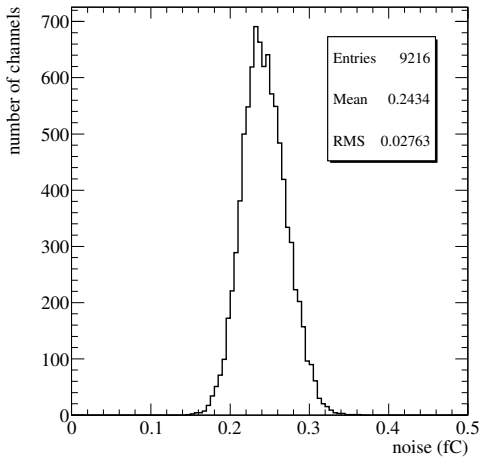


Figure 5: Noise at the output of the high gain shaper for all channels of the $1 \times 1 \text{ m}^2$ prototype.

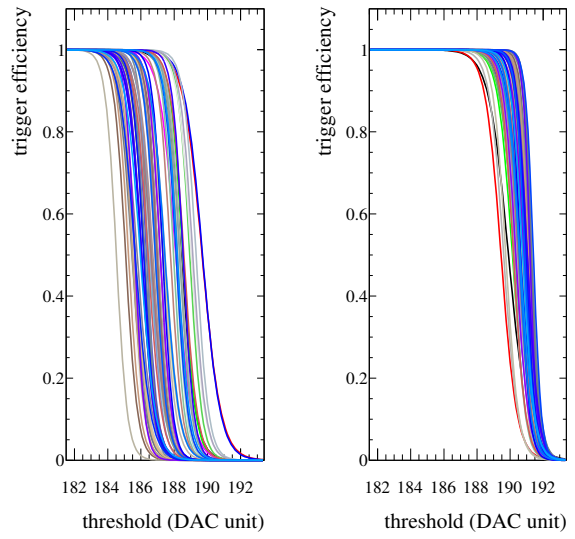


Figure 6: Channel pedestals of one chip measured during a threshold scan before (left) and after (right) alignment of the noise rate.

3.2. X-ray tests

Counting experiments are performed with an ^{55}Fe 5.9 keV X-ray source to characterise all ASUs before they are sealed in the $1 \times 1 \text{ m}^2$ prototype. A dedicated gaseous chamber with 14 mm drift gap and perforated drift cover has been constructed to measure the response of any of the 1536 ASU channels to true Micromegas signals.

In a non-flammable mixture of $\text{Ar}/\text{CF}_4/i\text{C}_4\text{H}_{10}$ 95/3/2, ^{55}Fe quanta can convert in the gas mainly by a photoelectric effect on an argon atom. This interaction results on average in 115 or 230 primary electrons depending on the atom relaxation process: fluorescence (escape peak) or Auger cascade (photopeak) [22]. After drifting, almost all primary electrons are multiplied in the amplification gap [18]. If above threshold, pad signals are recorded as hits in the chip memory. An external trigger signal is used to read out the memory every 1 s. The counting rate was measured for various sets of experimental parameters (thresholds, mesh voltage and source position). Each run lasted 60 s and the drift field was set to the local maximum of the drift velocity. Results are presented and discussed below.

250 3.2.1. Threshold scans

251 The gas gain is deduced from measurements of the counting
 252 rate R versus threshold t at various mesh voltages. Low thresh-
 253 old scans were performed at voltages between 300 and 350 V.
 254 With the field settings used, the expected average spread of a
 255 point-like cloud of electrons (from a photoelectric conversion)
 256 at the mesh is $\sim 230 \mu\text{m}$ in the direction transverse to the field
 257 and $\sim 2 \text{ ns}$ in time [23]. With the source collimated to the centre
 258 of a pad, most primary electrons are collected on one pad. For
 259 simplicity all other pads were electronically disabled. The results
 260 are shown in Figure 7. Each $R(t)$ trend is well described by
 261 the sum of two sigmoid functions accounting for the photopeak
 262 (peak 1) and the escape peak (peak2):

$$263 \quad R(t) = \frac{R_1}{1 + \exp\left(\frac{t-t_1}{\Delta t_1}\right)} + \frac{R_2}{1 + \exp\left(\frac{t-t_2}{\Delta t_2}\right)} \quad (1)$$

264 where the parameters (R_1, R_2) are the rates at zero threshold,
 265 (t_1, t_2) the inflexion thresholds at the peak maxima and ($\Delta t_1,$
 266 Δt_2) are proportional to the peak widths. In order to reduce the
 267 number of parameters fitted to the data points, the following
 268 approximations between the two peaks are used:

$$269 \quad \frac{R_1}{R_2} = \frac{1-f}{f} = \frac{85}{15} \quad (2)$$

270 where f is the fluorescence yield of an excited argon atom [24].
 271 Noting E_1 and E_2 the mean energy of the photopeak and escape
 272 peak:

$$273 \quad \frac{t_1}{t_2} = \frac{E_1}{E_2} = 2 \quad (3)$$

$$274 \quad \frac{\Delta t_1}{\Delta t_2} = \sqrt{\frac{E_1}{E_2}} = \sqrt{2} \quad (4)$$

275 and Equation 1 becomes:

$$276 \quad R(t) = R_1 \left[\frac{0.85}{1 + \exp\left(\frac{t-t_1}{\Delta t_1}\right)} + \frac{0.15}{1 + \exp\left(\frac{t-0.5t_1}{\Delta t_1/\sqrt{2}}\right)} \right] \quad (5)$$

277 After the fit, all hit rates converge to roughly 8 Hz at zero thresh-
 278 old. The measured charge at the various inflexion points (t_1)
 279 is used to calculate the gas gain, assuming an average of 230
 280 primary electrons for photopeak events. The gain exhibits the
 281 usual exponential dependence on the mesh voltage (Figure 8)
 282 with a slope of 0.036/V typical of argon-based gas mixtures
 283 [25]. At 350 V, a scan of the high threshold was performed too.
 284 The resulting $R(t)$ trend is shown in Figure 9 together with the
 285 low threshold trend. The two threshold scans give gas gain val-
 286 ues of 323 and 300 respectively. The agreement is reasonable
 287 and the 4 % difference can probably be explained by systematic
 288 errors during the calibration.

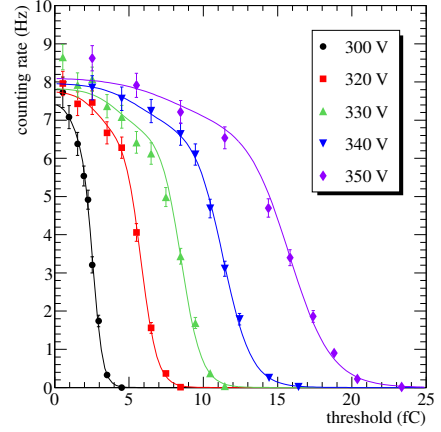


Figure 7: ^{55}Fe quanta counting rate of one channel versus low threshold at various mesh voltages.

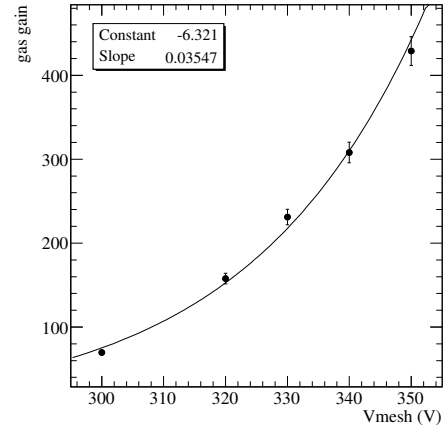


Figure 8: Gas gain versus mesh voltage deduced from the threshold scans. Data points are described by the formula $G = \exp(A + BV_{\text{mesh}})$ with $A \sim -6.32$ and $B \sim 0.036/\text{V}$.

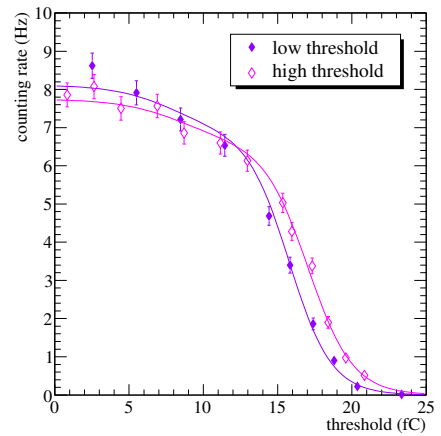


Figure 9: ^{55}Fe quanta counting rate of one channel versus low and high thresholds at a mesh voltage of 350 V.

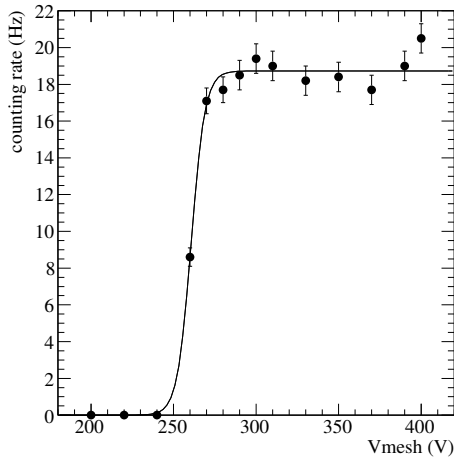


Figure 10: ^{55}Fe quanta counting rate of one channel versus mesh voltage.

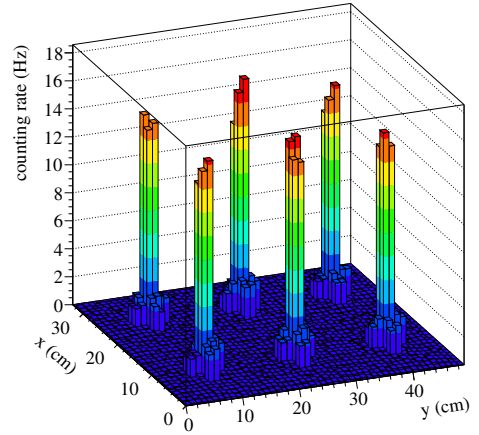


Figure 11: Two-dimensional hit distribution obtained with an ^{55}Fe source successively placed at six positions over the ASU test chamber.

3.2.2. Mesh voltage scan

The smallest detectable charge is deduced from a measurement of the counting rate versus gas gain. In this study, the source is collimated to the centre of one pad while the other pads are disabled. The low threshold of the tested pad is set by iteratively decreasing the chip discriminator DAC until the count rate becomes dominated by noise. The final DAC value is set one unit above this steep transition and this configuration is defined as the configuration of lowest workable threshold. The counting rate is then measured at various mesh voltages (200–400 V) in this threshold configuration. As shown in Figure 10, it increases with voltage as the charge spectrum shifts above threshold. The trend can be described by an sigmoid function with an inflexion point at 260 V. At this voltage, the rate is by definition half of its maximum value which implies that the threshold is equal to the average pad charge. The smallest detectable charge is then:

$$Q = q_e N G = 1.6 \cdot 10^{-4} \cdot 212 \cdot 20 \approx 0.7 \text{ fC} \quad (6)$$

where N is averaged over the ^{55}Fe spectrum (*i.e.* $0.85 \cdot 230 + 0.15 \cdot 115$). Previous measurements showed that a MIP efficiency larger than 95 % is achieved when the most probable value of the charge is roughly three times the threshold [18]. Assuming a most probable number of primary electrons from MIPs of 14 [18], this condition should be met at a moderate gas gain around 1000.

3.2.3. Position scan

The uniformity of the gas gain and of the low threshold can be verified by measuring the X-ray counting rate on various positions for all six ASUs. For a given ASU, the position scan is performed on six different positions. At each position the source is collimated onto a region of 2×2 pads centred between four chips (Figure 11). In this way, all 24 ASU chips are involved in the counting experiment. For this study, all channels are enabled and their thresholds are equalised according to the procedure explained in section 3.1.3. The mesh voltage is set

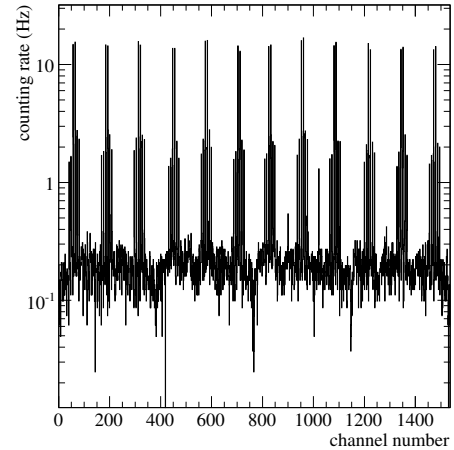


Figure 12: Channel occupancy obtained with an ^{55}Fe source successively placed at six positions over the ASU test chamber.

to 320 V at which an average ^{55}Fe signal of $\sim 5 \text{ fC}$ is expected. Given the collimation of the source and the transverse electron diffusion in the gas, the count rate is now calculated over 8×8 pad regions.

Position scans have been performed for six ASUs before assembly in the $1 \times 1 \text{ m}^2$ prototype. As illustrated in Figure 12, the response of the channels to the source quanta seems uniform. A flat noise-free background from cosmic particles can be seen when plotting the channel occupancy in a logarithmic scale. The results are summarised in Table 1. For each ASU, small variations of the counting rate are observed (the statistical error is 0.4 Hz). ASU to ASU variations of the mean rate are a few Hz. They could be caused by change of atmospheric conditions from one test to the other.

ASU number	1	2	3	4	5	6
Mean rate (Hz)	86.2	85.2	87.0	79.3	84.2	84.3
RMS (Hz)	2.0	1.7	1.1	1.6	2.2	2.5

Table 1: ^{55}Fe quanta counting rates and their variations measured on six ASUs (six measurements per ASU). The statistical error on the mean is 0.4 Hz.

3.2.4. Conclusion of the ASU tests

The manufacturing technique and the calibration procedure allow to achieve very low detection threshold, negligible noise and good response uniformity in a reproducible way. After careful characterisation of six ASUs, the first $1 \times 1 \text{ m}^2$ Micromegas prototype with MICROROC readout was constructed in May 2011 and subsequently tested in beam in July 2011. The results of the test beam are presented in the next section.

4. Functional tests of the prototype in particle beams

The goal of the test beam is to validate the mechanical design of the $1 \times 1 \text{ m}^2$ prototype, to measure its response to MIPs and to test its main functionalities. The test set-up consists of the $1 \times 1 \text{ m}^2$ prototype and a telescope of small Micromegas chambers and three scintillating paddles of $6 \times 16 \text{ cm}^2$ read out by photomultiplier tubes (PMT) (a detailed description of the telescope is given in [18]). This set-up was installed at the CERN SPS facility in the H4 beam line and exposed to $150 \text{ GeV}/c$ muons and pions. All chambers were flushed with a gas mixture of $\text{Ar}/\text{CF}_4/i\text{C}_4\text{H}_{10}$ 95/3/2. The Micromegas pad planes are vertical and perpendicular to the beam axis. During the pion runs, a 20 cm long block of iron ($10 \times 10 \text{ cm}^2$ cross-section) was placed between the telescope and the prototype to study its behaviour in hadron showers. The trigger is generated by the time coincidence of the three PMT signals. It is delayed by $1.5 \mu\text{s}$ before reaching the detectors in order to accommodate for the peaking time of the prototype and telescope electronics (200 ns and $1.2 \mu\text{s}$ respectively) and also to check the behaviour of the prototype after the passage of the beam particles. To account for the dead time of the telescope and prototype, a gate signal enters the coincidence such that any PMT signal arriving during the readout of the detectors is vetoed. The dead time of the whole set-up is dominated by the telescope and is $\sim 10 \text{ ms}$.

4.1. Externally triggered operation

The contributions from beam muons, cosmic particles and electronic noise to the $1 \times 1 \text{ m}^2$ prototype data are studied in trigger mode. To this end, a low intensity muon beam of 250 Hz collimated to roughly the size of the scintillators ($\sim 100 \text{ cm}^2$) is used. The mesh voltage is set to 370 V at which a MIP efficiency larger than 95 % is reached (cf. section 4.4). The thresholds are equalised according to the procedure previously described, resulting in a number of disabled channels of 10.

In Figure 13, the counting rate of roughly one fifth of the prototype channels is shown. Broad peaks correspond to beam muons and reach a maximum counting rate of $\sim 3.8 \text{ Hz/channel}$

in the centre of the beam. They are separated by an almost flat background of $\sim 0.3 \text{ Hz}$ that can be attributed to cosmic particles. A few noisy channels are spotted as isolated peaks over the background, they are however very few and their counting rate is small ($< 2.0 \text{ Hz}$).

By applying a cut on the time of the trigger ($\pm 500 \text{ ns}$), cosmics and noise hits are suppressed. This cut removes some hits from beam particles as well. These are interpreted as particles traversing the prototype during a readout. Although vetoed by the trigger, they can still be recorded by the prototype because its dead time is shorter than the one of the telescope and because its electronics is self-triggered.

4.2. Self-trigger operation

Thanks to the excellent noise conditions reported in the previous section, the $1 \times 1 \text{ m}^2$ prototype can be operated without an external trigger. In this self-trigger mode, no telescope nor trigger electronics are used: the prototype is read out when a memory full signal sent by a MICROROC is received at a DIF board (in trigger mode, a memory full signal resets all chip memories and does not introduce dead time). The beam and voltage settings of the previous test (with external trigger) are used.

A simple way to verify that the prototype is efficient in this mode is to compare the average time between readouts in spill to its expected value. The latter is calculated as the ratio of the memory depth (127 events) to the highest chip counting rate ($\sim 130 \text{ Hz}$) and is roughly 1 s. This is in agreement with measurements as illustrated in Figure 14 (top). Another evidence for an efficient operation in self-trigger mode is shown in Figure 14 (bottom) where the channel counting rates in trigger and self-trigger modes are compared and found similar. Successful operation in self-trigger mode is possible because of the negligible noise rate and discharge rate. Such rates are achieved thanks to a precise electronic calibration and a reliable mesh manufacturing process.

4.3. Response of the six Micromegas meshes

The $1 \times 1 \text{ m}^2$ prototype was moved across the beam to measure the MIP efficiency and hit multiplicity of the six ASUs. A muon beam of similar intensity as in the previous studies was directed at the centre of each ASU. Roughly 10^5 events were recorded for each position at a mesh voltage of 390 V. Efficiency and hit multiplicity are deduced from the distribution of the number of hits per triggering muons. This distribution is built by finding a track in the telescope, extrapolating its intersection with the prototype and counting the number of hits in time with the trigger inside a search region centred around the pad containing the extrapolated track position. Events are selected by applying the following cuts:

1. Telescope cut

Single aligned hits in the three chambers to select tracks with minimum angle w.r.t. the beam axis and to extrapolate the track position at the prototype in the most precise way. This cut reduces the statistics by roughly one third.

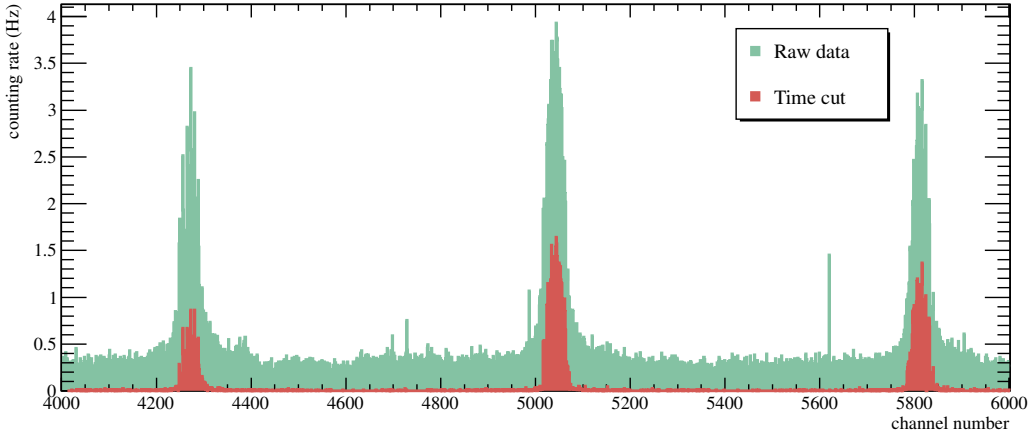


Figure 13: Counting rate of one fifth of the $1 \times 1 \text{ m}^2$ prototype channels measured with all hits (Raw data) and for hits in time with the trigger (Time cut). The three broad peaks are interpreted as beam muons, the background between these peaks as cosmic particles and isolated spikes (e.g. channel 5650) as noisy channels.

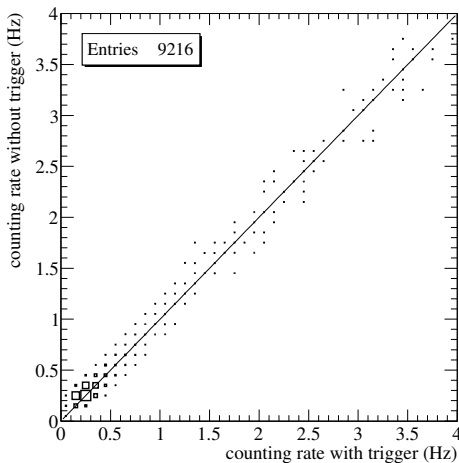
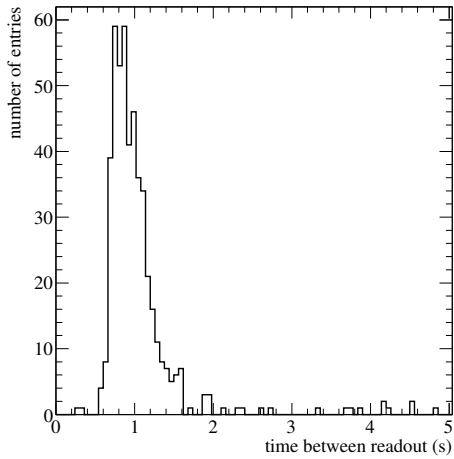


Figure 14: Time between readout in self-trigger mode (top). Channel counting rate during spills (bottom): self-trigger versus trigger mode.

2. Prototype cut

No hits in time with the trigger outside the search region to reduce the impact of multiple scattering on the measured efficiency. The radial distribution of hits (in time) w.r.t. the extrapolated pad falls rapidly and exhibits a long tail from muons scattered in the last telescope chamber. From the distribution shape, a search region of 7×7 pads is chosen. This cut reduces further the statistics by 5%.

About 30×10^3 events pass the selection for each ASU. They are used to build the distribution of the number of hits above the 3 thresholds. The efficiency ϵ of a given threshold is calculated as the probability to have at least one hit in the search region:

$$\epsilon = 1 - N_0 / N_t \quad (7)$$

and the hit multiplicity m as the average number of hits in the search region provided there is at least one hit in the search region:

$$m = \sum_{i=1}^{49} i \frac{N_i}{N_t - N_0} \quad (8)$$

where N_i is the number of events with “i” hits and N_t the total number of selected events. Efficiency and hit multiplicity were calculated for the six ASU and for the three thresholds. High efficiency and low hit multiplicity were achieved for the low threshold, with little spread from ASU to ASU (Table 2). Medium and high thresholds were set to 2 and 10 MIP respectively and show smaller values. Because these two thresholds are set within the signal distribution, their response is more sensitive to the detector non-uniformity than the one of the low threshold and indeed, more spread is observed. These variations could be due to small differences of the amplification gap size from one ASU to the other. They are, however, not too large and can be attenuated by adjusting the mesh voltage or the corresponding chip thresholds. In section 4.7, a way to calculate these corrections using the direct readout of shaper signals is presented.

ASU	1	2	3	4	5	6
ϵ_0 (%)	97.74	97.47	98.74	98.23	98.25	96.61
m_0	1.064	1.072	1.079	1.080	1.075	1.079
ϵ_1 (%)	34.83	36.67	46.38	40.95	38.65	46.00
m_1	1.033	1.033	1.035	1.035	1.037	1.033
ϵ_2 (%)	3.68	3.68	4.61	3.97	4.04	4.61
m_2	1.050	1.057	1.059	1.075	1.052	1.046

Table 2: MIP efficiency ϵ and hit multiplicity m of the six ASU for the three thresholds. The statistical error on ϵ is below 0.10% and below 0.008 for m .

4.4. Effect of the peaking time

The MICROROC chip was designed for various MPGD geometries, for instance with a Bulk mesh of different gap size or with a Gas Electron Multiplier structure [26]. For this purpose, the peaking time of the preamplifier can be set to 75, 115, 150 or 200 ns (the latter being the default value of the $1 \times 1 \text{ m}^2$ prototype). In the gas mixture used, the signal from the multiplication of a single primary electron in a $128 \mu\text{m}$ gap consists of a fast electron peak ($\sim 1 \text{ ns}$) and a longer ion tail ($\sim 100\text{--}200 \text{ ns}$). For a traversing MIP, the signal is the sum of, on average, 30 primary electrons arriving at the mesh in about 30 ns [24]. Therefore, a strong dependence of the efficiency on the peaking time is expected.

This dependence was measured by performing a voltage scan for the four values of the peaking time in the muon beam. The beam was directed to the centre of one ASU and the efficiency calculated as in the previous section. The 150 ns and 200 ns trends shown in Figure 15 are similar, meaning that the Micromegas MIP signal is completed in 150 ns or less. The loss of efficiency from 150 ns to 115 ns peaking time indicates, however, that the signal lasts longer than 115 ns which is compatible with expectations. At shorter peaking times, an efficiency larger than 95% can be maintained by increasing the gas gain. This is illustrated in Table 3 where the voltages for 95% efficiency are listed: the loss of signal when changing the peaking time from 200 ns to 75 ns is compensated by a 20 V increase of mesh voltage. These voltages are calculated using the empirical parametrisation:

$$\epsilon(V) = \frac{\epsilon_{\max}}{1 + \exp\left(\frac{V_{50} - V}{\Delta V}\right)} \quad (9)$$

where ϵ_{\max} is the efficiency at infinite voltage, V_{50} is the voltage for 50% efficiency and ΔV describes the rise of the $\epsilon(V)$ trend. All adjusted ϵ_{\max} parameters are compatible and yield an average of $99.3 \pm 0.3\%$. The fact that the ϵ_{\max} is not equal to one could be explained by the dead areas from the mesh supporting pillars. The voltage V_{50} decreases at longer peaking time as a result of the increased available signal and becomes constant between 115–150 ns. At decreasing peaking times below 115 ns, the efficiency rise with voltage is steeper which is accounted for by smaller ΔV values.

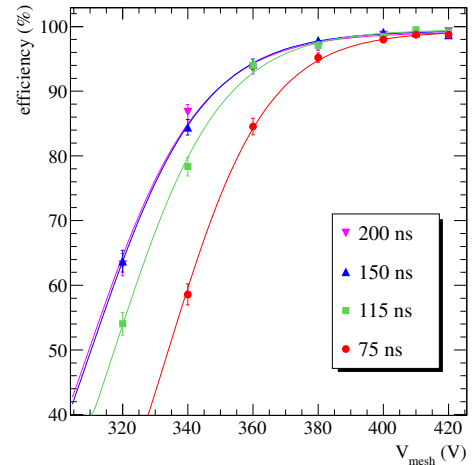


Figure 15: MIP efficiency versus mesh voltage for various settings of the MICROROC peaking time.

t_p (ns)	75	115	150	200
ϵ_{\max} (%)	99.3 ± 0.3	99.6 ± 0.3	99.4 ± 0.3	99.1 ± 0.3
V_{50} (V)	333.9 ± 0.7	317.4 ± 0.8	310.1 ± 0.9	309.4 ± 0.8
ΔV (V)	15.2 ± 0.5	16.3 ± 0.7	17.0 ± 0.8	17.1 ± 0.7
V_{95} (V)	380.9	366.7	362.6	363.0

Table 3: Parameters describing the voltage dependence of the efficiency for various settings of the MICROROC peaking time. The voltage V_{95} necessary to reach an efficiency of 95% is indicated in the last line.

4.5. Impact of dead zones between ASUs

Non-instrumented areas inside the prototype amount to 1.5% of the total area occupied by the six ASUs ($96.9 \times 97.4 \text{ cm}^2$). Another contribution to the prototype inefficiency may come from possible non-uniformity of the electric field at the ASU edges. This hypothesis was tested by placing a block of iron in a pion beam (collimated to a $3 \times 3 \text{ cm}^2$ region) and measuring downstream of the block secondary particles produced in hadron showers. In this way a large fraction of the prototype is exposed and possible discontinuities in the measured hit profile can be looked for. For this measurement, the mesh voltages were set to 375 V.

Given the block size ($10 \times 10 \text{ cm}^2$ transverse size and 20 cm length along the beam), roughly half of the pions interact inside the block. The distribution of the number of hits in the prototype thus shows a peak at one hit from penetrating pions and a long tail up to 300 hits from showering pions. Horizontal and vertical profiles of showers are constructed from events with a hit multiplicity larger than three. They are shown in Figure 16 where a small drop of efficiency for pads at the ASU edges is observed.

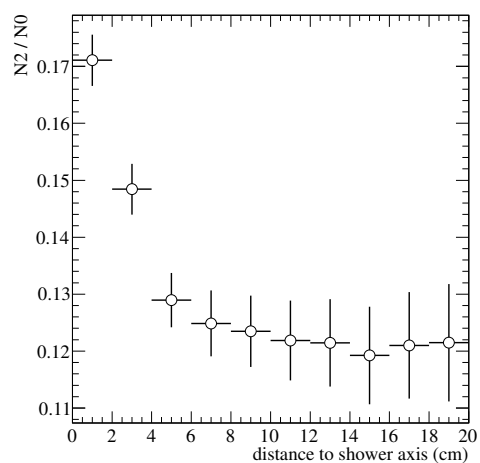
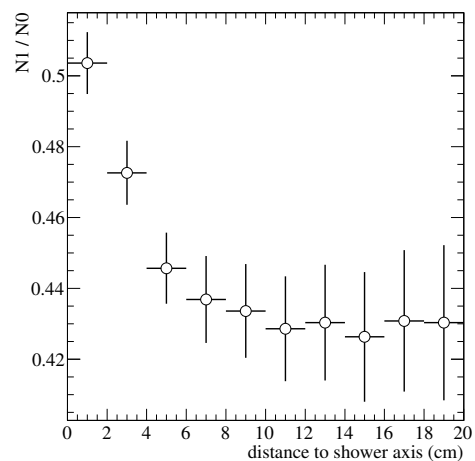
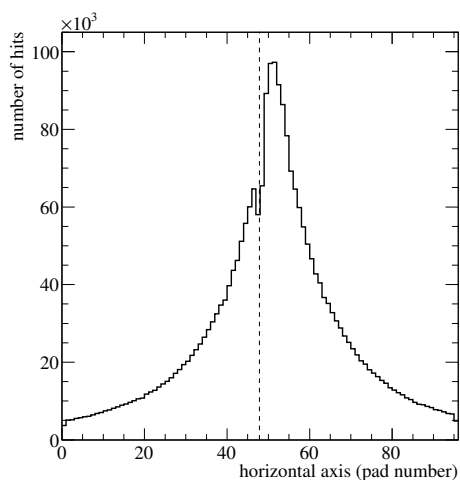
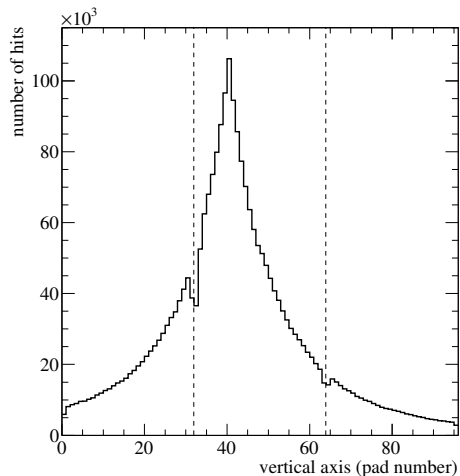


Figure 16: Vertical and horizontal profiles of 150 GeV/c pion showers ($\sim 5 \times 10^4$ events). The dashed lines indicate the junctions between ASUs.

Figure 17: Medium and high threshold efficiency in 150 GeV/c pion showers versus the distance to the shower axis.

By extrapolation of the inner pad occupancy to the ASU edges, the number of hits there is 20 % lower than expected. The number of pads at the ASU edges is 936 which implies that 10 % of the pads experience a 20 % efficiency loss. The overall resulting inefficiency of 2 % could probably be reduced with different voltage settings (*e.g.* higher amplification or drift field) or a different mechanical design (*e.g.* larger ASUs).

4.6. Measurement of pion showers with three thresholds

On average, hadron showers consist of a dense electromagnetic core from neutral meson decays surrounded by a halo of particles. Saturation in a DHCAL will be caused mainly by the electromagnetic part and offline compensation techniques (using for instance the detector granularity) will be necessary to improve the energy resolution. In a SDHCAL, the charge information from the three thresholds can also be exploited to identify the electromagnetic part. This identification capability can be illustrated by measuring the threshold efficiencies for various energy deposits. Because the energy density decreases with the distance to the shower axis, the efficiencies were measured as a

function of position using the set-up described in the previous section. The mesh voltage was 370 V and the thresholds were set to roughly (0.5, 2, 5) MIP.

The MIP efficiency of the low threshold is high ($\geq 95\%$) and therefore the efficiency of the other thresholds is approximated to N_1/N_0 and N_2/N_0 where the indices 0, 1 and 2 stand for low, medium and high thresholds. These ratios are plotted versus distance to the shower axis in Figure 17. Both trends indicate that the electromagnetic core is contained in a circle of 10 cm radius. This is larger than the 99 % containment radius in iron ($3.5 R_M \sim 6$ cm where the Molière radius R_M is equal to 1.7 cm [27]) because of the 1 m thick air gap between the rear surface of the iron block and the $1 \times 1 \text{ m}^2$ prototype.

Compared to the halo of the shower, the core has a higher energy density which explains the probability variation with distance: N_1/N_0 increases from 0.43 to 0.51 and N_2/N_0 from 0.12 to 0.17. The threshold information can thus help to identify the electromagnetic part of hadron showers and probably improve the calorimeter performance with dedicated software compensation methods.

4.7. Analogue readout of the shaper

A correction of the mesh voltage or of the readout threshold may be necessary to improve the response uniformity of the prototype, in particular for the medium and high thresholds (cf. section 4.3). The most straight-forward way to calculate the correction is to measure the signal distribution. For this reason, dedicated lines were implemented on the PCBs to measure the output voltage of the low gain shaper. This analogue readout uses a trigger signal that first arrives at the DIFs. After a programmable delay matching the peaking time of the MICRO-ROC, the DIFs forward the signal to the chips. The voltages of the shaper outputs of all channels are then multiplexed and sent to the DIFs where they are digitised with a 12 bit resolution.

The analogue readout was tested in the muon beam. The Landau distribution as measured on roughly 100 pads and corrected for channel to channel pedestal variations is shown in Figure 18 (top). By applying cuts on the passed thresholds, the signal distribution is cropped from zero to the threshold value. The latter is thus measured in unit of charge (Figure 18) or in unit of the MIP value which is a natural energy unit in a calorimeter. Threshold and MIP values are determined using the following parametrisation of the charge spectrum:

$$f(q) = s(q, Q_{\text{thr}}, \Delta Q_{\text{thr}}) \cdot l(q, C, Q_{\text{mpv}}, \Delta Q_{\text{mpv}}) \quad (10)$$

where $s(q)$ is a sigmoid function of inflexion point Q_{thr} , width ΔQ_{thr} and with a maximum value of one accounting for the channel to channel threshold dispersion. The function $l(q)$ is the Landau function of most probable value Q_{mpv} , width ΔQ_{mpv} and normalisation factor C . When adjusting the parameters to the data of Figure 18, it is found that (for this particular run) low, medium and high thresholds are respectively equal to 0.5, 0.9 and 2.3 times the MIP value of 5.2 fC. This shows that the threshold values can be monitored and adjusted during operation using dedicated calibration runs.

4.8. Power-pulsing of the MICROROC chips

The MICROROC chip can be turned on and off rapidly according to an external timing signal (e.g. the accelerator clock). When the chip is turned on, an external programmable delay is applied before any detector signal can be recorded to the memory. This delay accounts for the stabilisation of the various voltages and currents inside the chip and should be as short as possible to reduce the power consumption. If the delay is too short, the detector occupancy is dominated by noise until stabilisation. This is illustrated in Figure 19 (left) where the number of hits in the $1 \times 1 \text{ m}^2$ prototype is plotted versus time for a short run in self-trigger mode. During the run, the delay was set to $50 \mu\text{s}$ and a power-pulsing on/off timing of $3/4.5 \text{ s}$ was used. This timing, although different from the one of the ILC bunches ($1/199 \text{ ms}$), is well suited to determine the right delay: at $100 \mu\text{s}$, the high peaks every 7.5 s disappear because stabilisation has been achieved (Figure 19 (right)). With this short stabilisation time, a duty cycle of the front-end electronics at ILC of at most 0.55% can be achieved, compared to 0.5% if no stabilisation is needed. The corresponding increase of power consumption would thus be small, below 10% .

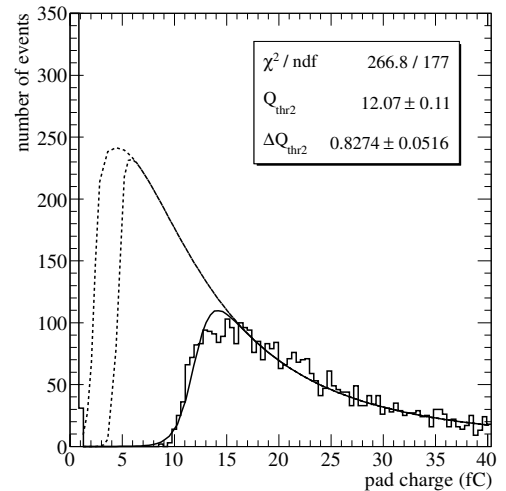
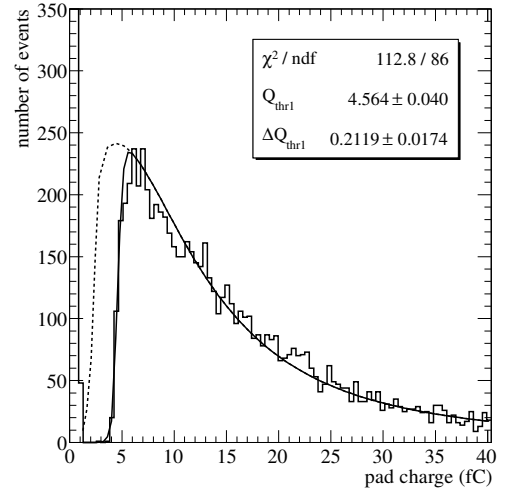
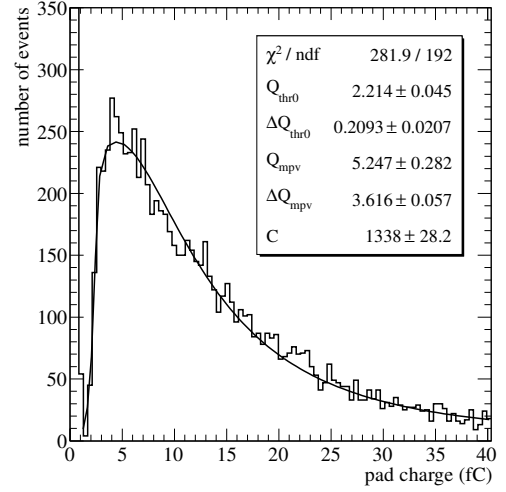


Figure 18: Landau distribution measured with muons for low (top), medium (centre) and high (bottom) threshold hits.

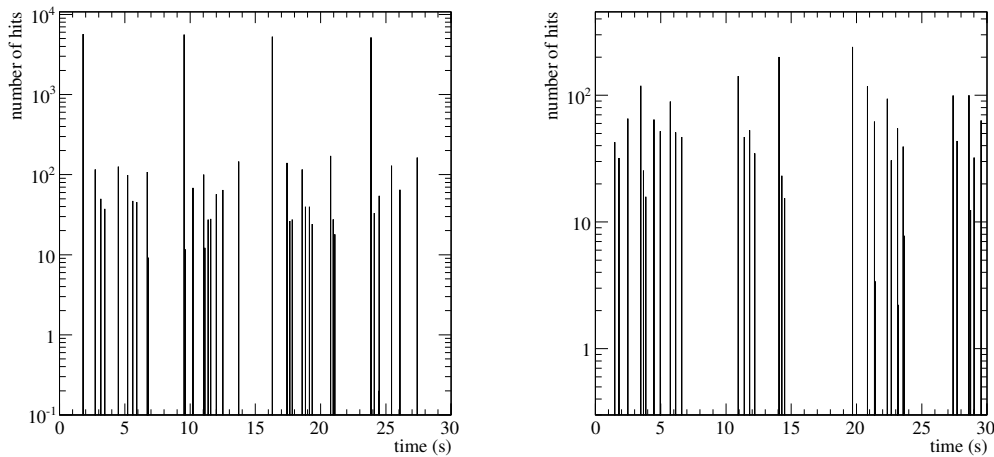


Figure 19: Number of hits versus time when power-pulsing the MICROROC chips of the $1 \times 1 \text{ m}^2$ prototype: with $50 \mu\text{s}$ (left) and $100 \mu\text{s}$ (right) delay between the power on signal and the start of the self-trigger acquisition.

5. Conclusion

A Micromegas prototype of $1 \times 1 \text{ m}^2$ consisting of six independent Micromegas boards with integrated 2 bit front-end electronics has been constructed. This modular design introduces little dead zones ($\sim 1.5\%$) and allows to achieve an overall thickness of 9.5 mm and a constant drift gap of 3 mm. Thanks to adequate discharge protections and low noise front-end circuits, more than 99.98% of the 9216 prototype channels are operational. Most importantly, the six Micromegas boards exhibit comparable performance to X-rays, muons and pion showers and all provide the necessary gas gain for an efficiency of 96% or larger.

Compared to a pure digital gaseous calorimetry, an approach with three thresholds will rely strongly on the proportionality of the sampling detector and on its cell to cell signal uniformity. This kind of Micromegas is free of saturation effects and its amplification gap is precisely defined by the mesh supporting pillars over the anode plane. Small variations of this gap have probably been observed from mesh to mesh. But the necessary corrections to the mesh voltages or the chip thresholds can be calculated by means of the direct readout of detector signals. Combined with other features such as power-pulsing and self-triggering, the constructed Micromegas prototype is therefore an excellent candidate for Particle Flow calorimetry at a future linear collider.

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