

# Correctness of Source-Level Safety Policies

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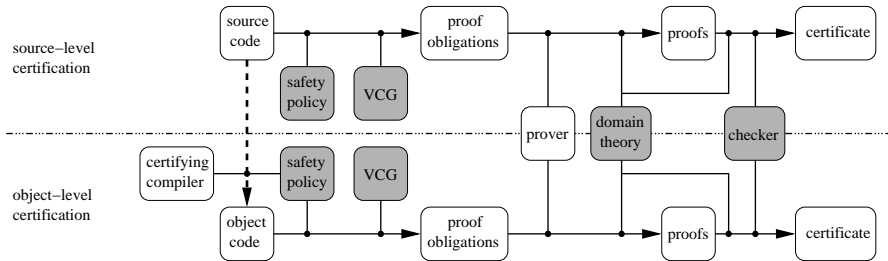
**Abstract.** Program certification techniques formally show that programs satisfy certain safety policies. They rely on the correctness of the safety policy which has to be established externally. In this paper we investigate an approach to show the correctness of safety policies which are formulated as a set of Hoare-style inference rules on the source code level. We develop a framework which is generic with respect to safety policies and which allows us to establish that proving the safety of a program statically guarantees dynamic safety, i.e., that the program never violates the safety property during its execution. We demonstrate our framework by proving safety policies for memory access safety and memory read/write limitations to be sound and complete. Finally, we formulate a set of generic safety inference rules which serve as the blueprint for the implementation of a verification condition generator which can be parameterized with different safety policies, and identify conditions on appropriate safety policies.

**Keywords.** Program verification, Hoare logic, program safety, code certification, proof-carrying code

## 1 Introduction

Program certification techniques like proof-carrying code (PCC) [12] use formal reasoning techniques to show that programs satisfy certain *safety policies* as for example memory safety (i.e., that they do not access out-of-bounds memory), rather than full functional correctness.

In effect, these techniques shift the trust burden from the original program to the certification system: instead of having to trust an arbitrary program to be safe, users have to trust the certifier to be correct. However, this still requires a lot of trust since a certifier is itself a complex system involving many different components and steps. In the original PCC approach [12], a compiler first translates an untrusted source program into an annotated machine program, to which a verification condition generator (VCG) then applies a safety policy, formulated as a set of Hoare rules. This produces a set of proof obligations, which are processed by a theorem prover; the resulting proofs are finally scrutinized by a proof checker (cf. Figure 1). Fortunately, not all of these components have to be trusted—here, trust is required only in the safety policy, the VCG, and the proof checker but not in the much larger prover or the compiler itself.



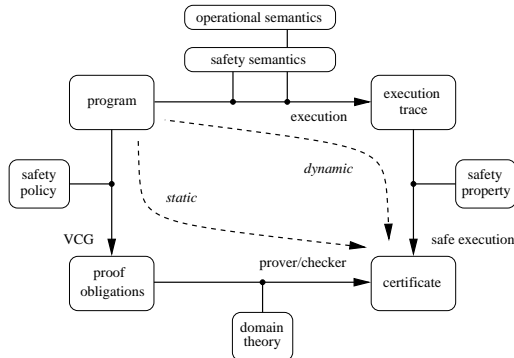
**Fig. 1.** System architecture for source-level certification and object-level certification; trusted components are shaded.

However, the fact that the safety policy still has to be trusted turns out to be the Achilles heel of the approach, both for theoretical and practical reasons. On the theoretical side, if the rules are unsound or do not exactly formalize the intuitive notion of safety, “all bets are off” [10], i.e., even a safety proof does not guarantee that the program is actually safe. On the practical side, since a safety policy can consist of a collection of fairly complex Hoare rules, it is as liable to error as any other component of the certifier. Moreover, the VCG and the proof checker can be reused essentially unchanged for different safety policies and can thus be hardened over time, but the Hoare rules change with each new safety policy. Recent work has thus concentrated on ways to guarantee the correctness of safety policies, using approaches like type-preserving compilation [10], foundational PCC [2, 6], and reduction to core safety policies [13].

However, all these approaches work on the object code level, and cannot directly be extended to safety policies which are formulated on the source code level. Here, we investigate an approach to showing the correctness of source-level safety policies. Our goal is to develop a generic framework (cf. Figure 2) which lets us establish that proving the safety of a source program using a safety policy formulated as a set of Hoare rules guarantees safe execution, i.e., the program never violates the safety property.<sup>1</sup> In other words, we want to establish that the static and dynamic notions of safety coincide. We thus explicitly distinguish between the static safety *policies* (which are logical characterizations) and the dynamic safety *properties* (which are operational characterizations). We also explicitly separate the formalization of the safety properties from the operational semantics: a program can be unsafe even if its execution does not raise an exception. We restrict ourselves to the analysis of individual programs only, and do not consider system-level safety aspects. However, our techniques could be used with system-level analysis techniques, e.g., to decide whether a system is still safe even if an exception can occur in one of the system components.

Our interest in source-level policies has a number of reasons. (i) Programmers make errors on the source code level, so showing safety on the source code level seems not only to be more natural, it also makes it easier to pinpoint

<sup>1</sup> Provided that the compiler preserves the property, of course. See below for a more detailed discussion of this.



**Fig. 2.** Establishing correctness of source-level certification—general framework.

the errors. *(ii)* Some safety policies can be formulated more naturally (e.g., initialization-before-use) or only (e.g., loop variable restrictions) on the source code level.<sup>2</sup> In particular, high-level domain-specific policies such as *frame safety* [9] are inherently source level. *(iii)* Source-level certification is complementary to object-level approaches like PCC. In fact, to ensure that compilation does not compromise the demonstrated safety policy, source-level certification should be followed by object-level certification. However, explicit source-level certification provides a separation of concerns as different safety policies can be applied at different levels of abstraction. *(iv)* Formal software certification processes (e.g., DO-178B) usually also cover source code level, so certification support has to work on that level. *(v)* Finally, we are interested in the combination of certification and program synthesis [19], using certification (in a roundabout way) to increase confidence in our synthesis system, which generates source code and not object code.

The main contributions of this paper are as follows. First, we develop a general notion of safety property: we distinguish *stateless* properties, where safe execution can be defined in terms of the original operational semantics (i.e., execution traces), and *stateful* properties, where for book-keeping purposes the operational semantics must be augmented with a separate safety semantics. Second, we develop a *semantic* definition of safety, which lets us reason about the soundness and completeness of our safety policies. Third, we give a generic method of extending Hoare rules to incorporate an arbitrary safety property. In particular, our framework can serve as the basis for the implementation of a generic VCG.

In Section 2, we develop the basic theory of stateless safety properties, and then extend this in Section 3 to some examples of stateful safety. In Section 4 we present a general account of safety. Finally, Sections 5 and 6 discuss related work and draw some conclusions. Throughout this paper we assume a working familiarity with Hoare-style program correctness proofs (see [11], for example).

<sup>2</sup> This is related to the use of certification to enforce syntactic restrictions and coding standards.

## 2 Stateless Safety Properties

We introduce our framework using the deliberately simplistic language  $\mathcal{L}_0$  of *while*-programs as shown in Figure 3; it uses the unspecified sets  $Var$  and  $Const$  of variables and literal constants.

$Cmd ::= skip$	$Expr ::= Const$
$Var := Expr$	$Var$
<b>if</b> $Expr$ <b>then</b> $Cmd$	$Expr * Expr$
<b>while</b> $Expr$ <b>do</b> $Cmd$	$Expr / Expr$
$Cmd ; Cmd$	$Expr + Expr$
	$Expr - Expr$
	$Expr = Expr$

**Fig. 3.** Syntax of *while*-language  $\mathcal{L}_0$

Our initial safety property is *operator safety*, i.e., expression operators such as division are only applied to arguments within their respective domains. For  $\mathcal{L}_0$ , this boils down to the question of whether divisors are non-zero. However, even for this simple case we cannot naively define the safety of commands in terms of the safety of their subexpressions. Consider, for example, the commands

`if false then x:=x+1/0      while true do skip; x:=x+1/0`

which contain unsafe subexpressions but which we would nevertheless want to regard as safe (w.r.t. operator safety) because the division-by-zero exception will never be raised. Consider also the sequence `x:=y; w:=1/x` where safety of the subexpressions is not sufficient either because this does not incorporate the information that the division `1/x` is performed when `x` is bound to the value of `y`. Hence, we need an analysis of safety which takes into account the (operational) semantics of the programs.

### 2.1 Formulation of Safety Properties

A *safety property* is an operational characterization of the fact that “a program does not go wrong.” We formalize safety properties as *judgements* of the form  $\eta \models c \text{ safe}$ , i.e., the command  $c \in Cmd$  is safe under the environment  $\eta \in Env$ . As usual, we use environments  $\eta : Var \rightarrow Val_{\perp}$  to record value bindings for the variables. Note that we use the bottom element  $\perp$  only as an operational concept to denote and propagate the result of an undefined computation, but not to denote (un-) safety. In particular, a command can still be safe under an environment which contains a binding  $x \mapsto \perp$ ; for example, `y:=x+1` is obviously still safe w.r.t. operator safety (i.e., division-by-zero free) simply because it does not contain any occurrence of the division operator. Conversely, unsafety does not necessarily manifest itself in a binding  $x \mapsto \perp$ .

$$\begin{aligned}
& \eta \vDash c \text{ safe}_{\text{op}} \\
& \eta \vDash x \text{ safe}_{\text{op}} \\
& \eta \vDash e_1 \text{ op } e_2 \text{ safe}_{\text{op}} \text{ iff } \eta \vDash e_1 \text{ safe}_{\text{op}} \text{ and } \eta \vDash e_2 \text{ safe}_{\text{op}} \text{ and } \text{op} \in \{*, +, -, =\} \\
& \eta \vDash e_1 / e_2 \text{ safe}_{\text{op}} \text{ iff } \eta \vDash e_1 \text{ safe}_{\text{op}} \text{ and } \eta \vDash e_2 \text{ safe}_{\text{op}} \text{ and } \llbracket e_2 \rrbracket_{\eta} \neq 0
\end{aligned}$$

**Fig. 4.** Operator safety for  $\mathcal{L}_0$ -expressions

We can then define the judgement  $\text{safe}_{\text{op}}$  which formalizes operator safety for  $\mathcal{L}_0$ -expressions in the expected way, as shown in Figure 4. We use the notation  $\llbracket e \rrbracket_{\eta}$  to denote evaluation of an expression  $e \in \text{Expr}$  in an environment  $\eta$ .

Extending operator safety to commands requires an operational semantics for the commands; here, we assume the standard single-step operational semantics  $\langle c, \eta \rangle \Rightarrow \langle c', \eta' \rangle$  for *while*-programs.<sup>34</sup> However, there are two different approaches to an extension. The first approach factors safety into two different judgements,  $\text{safestate}_{\text{op}}$  and  $\text{safe}_{\text{op}}$  (cf. Figure 5), where  $\eta \vDash c \text{ safestate}_{\text{op}}$  formalizes the intuition that the immediately next command is safe to execute (i.e., all of the expressions which it would evaluate immediately are safe) and the reduction relation restricts the application of  $\text{safestate}_{\text{op}}$  to reachable commands and environments only. Hence, we have  $\eta \vDash \text{while true do skip; } x:=1/0 \text{ safe}_{\text{op}}$ , as expected. This approach essentially mirrors the definition of what is called the safety policy in the syntactic FPCC-approach of Hamid et al. [6].

$$\begin{aligned}
& \eta \vDash \text{skip safestate}_{\text{op}} \\
& \eta \vDash x := e \text{ safestate}_{\text{op}} \quad \text{iff } \eta \vDash e \text{ safe}_{\text{op}} \\
& \eta \vDash \text{if } b \text{ then } c \text{ safestate}_{\text{op}} \quad \text{iff } \eta \vDash b \text{ safe}_{\text{op}} \\
& \eta \vDash \text{while } b \text{ do } c \text{ safestate}_{\text{op}} \quad \text{iff } \eta \vDash b \text{ safe}_{\text{op}} \\
& \eta \vDash c_1 ; c_2 \text{ safestate}_{\text{op}} \quad \text{iff } \eta \vDash c_1 \text{ safestate}_{\text{op}} \\
& \eta \vDash c \text{ safe}_{\text{op}} \text{ iff } \forall \langle c, \eta \rangle \Rightarrow^* \langle c', \eta' \rangle \cdot \eta' \vDash c' \text{ safestate}_{\text{op}}
\end{aligned}$$

**Fig. 5.** Operator safety for  $\mathcal{L}_0$ -commands

The second approach directly integrates the formulation of the  $\text{safestate}_{\text{op}}$ -judgement into the operational semantics and has thus more of an abstract interpretation flavor (cf. Figure 6).

For this alternative definition  $\widehat{\text{safe}}_{\text{op}}$  we first show by straightforward induction over commands that safety is preserved by reduction; in analogy to subject

<sup>3</sup>  $\langle x := e, \eta \rangle \Rightarrow \langle \text{skip}, \eta \oplus \{x \mapsto \llbracket e \rrbracket_{\eta}\} \rangle$ ,  
 $\langle \text{skip} ; c_2, \eta \rangle \Rightarrow \langle c_2, \eta \rangle$ ,  
 $\langle c_1 ; c_2, \eta \rangle \Rightarrow \langle c_1 ; c_2, \eta' \rangle$  if  $\langle c_1, \eta \rangle \Rightarrow \langle c_1', \eta' \rangle$ ,  
 $\langle \text{if } b \text{ then } c, \eta \rangle \Rightarrow \langle c, \eta \rangle$  if  $\llbracket b \rrbracket_{\eta} = \text{true}$ ,  
 $\langle \text{if } b \text{ then } c, \eta \rangle \Rightarrow \langle \text{skip}, \eta \rangle$  if  $\llbracket b \rrbracket_{\eta} = \text{false}$ ,  
 $\langle \text{while } b \text{ do } c, \eta \rangle \Rightarrow \langle \text{if } b \text{ then } (c; \text{while } b \text{ do } c), \eta \rangle$

<sup>4</sup> We also use  $\langle c, \eta \rangle \Downarrow \eta'$  to denote the result of a terminating evaluation of  $c$ , i.e.,  $\langle c, \eta \rangle \Downarrow \eta'$  iff  $\langle c, \eta \rangle \Rightarrow^* \langle \text{skip}, \eta' \rangle$ .

$$\begin{aligned}
\eta \models \widehat{\text{skip}} \text{ safe}_{\text{op}} \\
\eta \models x := e \widehat{\text{safe}}_{\text{op}} & \quad \text{iff } \eta \models e \text{ safe}_{\text{op}} \\
\eta \models \text{if } b \text{ then } c \widehat{\text{safe}}_{\text{op}} & \quad \text{iff } \eta \models b \text{ safe}_{\text{op}} \text{ and } \llbracket b \rrbracket_{\eta} = \text{true} \text{ implies} \\
& \quad \eta \models c \widehat{\text{safe}}_{\text{op}} \\
\eta \models \text{while } b \text{ do } c \widehat{\text{safe}}_{\text{op}} & \quad \text{iff } \eta \models b \text{ safe}_{\text{op}} \text{ and } \llbracket b \rrbracket_{\eta} = \text{true} \text{ implies } (\eta \models c \widehat{\text{safe}}_{\text{op}} \text{ and} \\
& \quad \langle c, \eta \rangle \Downarrow \eta' \text{ implies } \eta' \models \text{while } b \text{ do } c \widehat{\text{safe}}_{\text{op}}) \\
\eta \models c_1 ; c_2 \widehat{\text{safe}}_{\text{op}} & \quad \text{iff } \eta \models c_1 \widehat{\text{safe}}_{\text{op}} \text{ and } \langle c_1, \eta \rangle \Downarrow \eta' \text{ implies } \eta' \models c_2 \widehat{\text{safe}}_{\text{op}}.
\end{aligned}$$

**Fig. 6.** Operator safety for  $\mathcal{L}_0$ -commands (structural definition)

reduction<sup>5</sup> we call this property *safety reduction*. Note that safety reduction holds trivially for  $\text{safe}_{\text{op}}$  as defined in Figure 5.

**Lemma 1.** (*Safety Reduction*)  $\eta \models c \widehat{\text{safe}}_{\text{op}}$  and  $\langle c, \eta \rangle \Rightarrow \langle c', \eta' \rangle$  implies  $\eta' \models c' \widehat{\text{safe}}_{\text{op}}$ .

We can then show that both definitions are in fact equivalent. This is quite useful because the operational definition ( $\widehat{\text{safe}}$ ) is what we intuitively want but most proofs use the inductive definition ( $\text{safestate}$ ).

**Lemma 2.** For all  $\eta, c$ :  $\eta \models c \widehat{\text{safe}}_{\text{op}}$  iff  $\eta \models c \text{ safe}_{\text{op}}$ .

*Proof:* Use Lemma 1, and the fact that  $\eta \models c \widehat{\text{safe}}_{\text{op}}$  implies  $\eta \models c \text{ safestate}_{\text{op}}$ . ■

Both Lemma 1 and Lemma 2 are independent of the particular safety judgement and hold as long as command safety is derived from expression safety in the way described in Figure 6.

In the following we discuss arbitrary safety properties, which can be *any* mathematical relation between environments and expressions. We reserve the use of *safety judgement* for the semantic clauses defining the property. For commands, we define a safety property to be any relation,  $\_ \models \_ \text{ safe} \subseteq \text{Env} \times \text{Cmd}$ , which is defined from expression safety, according to Figure 6 (cf. Definition 5 for the stateful case).

## 2.2 Formulation of Safety Policies

A *safety policy* is a set of proof rules and auxiliary definitions which are designed to show that safe programs satisfy the safety property of interest. The intention is that a safety policy enforces a particular safety property (see Section 2.1). For source-level safety properties, the proof rules can be formalized concisely using the usual Hoare triples  $P \{c\} Q$ . We also use the notation  $\vdash^{\text{safe}} P \{c\} Q$  to

<sup>5</sup> A system is said to satisfy *subject reduction* [11] if types are preserved by term reduction, i.e., if  $M : \tau$  and  $M \Rightarrow M'$  implies  $M' : \tau$ .

denote derivability of Hoare triples, given a set of Hoare rules. Figure 7 shows the Hoare rules for operator safety. The rules are a slight modification of the standard ones; the (*assign*) axiom requires safety of the right-hand side expression, and the (*if*) and (*while*) rules require the additional hypothesis that the guard is safe under the precondition  $P$ . Figure 8 shows the definition of the auxiliary predicate  $safe_{op}$  used in the rules; note that  $safe_{op}$  is not a judgement but a function which maps expressions into formulae.

$$\begin{array}{l}
(\textit{skip}) \quad \frac{}{Q \{ \textit{skip} \} Q} \\
(\textit{assign}) \quad \frac{}{Q[e/x] \wedge safe_{op}(e) \{ x := e \} Q} \\
(\textit{if}) \quad \frac{P \Rightarrow safe_{op}(b) \quad b \wedge P \{ c \} Q \quad \neg b \wedge P \Rightarrow Q}{P \{ \textit{if } b \textit{ then } c \} Q} \\
(\textit{while}) \quad \frac{P \Rightarrow safe_{op}(b) \quad b \wedge P \{ c \} P}{P \{ \textit{while } b \textit{ do } c \} \neg b \wedge P} \\
(\textit{comp}) \quad \frac{P \{ c_1 \} R \quad R \{ c_2 \} Q}{P \{ c_1 ; c_2 \} Q} \\
(\textit{cons}) \quad \frac{P \Rightarrow P' \quad P' \{ c \} Q' \quad Q' \Rightarrow Q}{P \{ c \} Q}
\end{array}$$

**Fig. 7.** Hoare rules for  $\mathcal{L}_0$  operator safety

$$safe_{op}(e) = \begin{cases} true & \text{if } e \in Var \text{ or } e \in Const \\ safe_{op}(e_1) \wedge safe_{op}(e_2) & \text{if } e \equiv e_1 \text{ op } e_2, \text{ op} \in \{ *, +, -, = \} \\ safe_{op}(e_1) \wedge safe_{op}(e_2) \wedge e_2 \neq 0 & \text{if } e \equiv e_1 / e_2 \end{cases}$$

**Fig. 8.** Safety formula for  $\mathcal{L}_0$  operator safety

The standard Hoare rules are well-understood, unlike extensions to deal with safety. Our aim is to show how such extensions can be made automatically, while ensuring soundness and completeness. We first need to modify the standard interpretation of Hoare triples (i.e.,  $\eta \models P \{ c \} Q$  iff  $\eta \models P$  and  $\langle c, \eta \rangle \Downarrow \eta'$  together imply  $\eta' \models Q$ ) to take a safety judgement into account.

**Definition 1.**  $\models^{safe} P \{ c \} Q$  holds iff for all  $\eta \in Env$ , if  $\eta \models P$ , then  $\eta \models c$  safe, and if  $\langle c, \eta \rangle \Downarrow \eta'$ , then  $\eta' \models Q$ .

Note that the proof rules inherit an underlying logic from a system given separately; in particular, they do not say anything about the definedness of the formulae  $P$  and  $Q$  used in the Hoare triples (e.g.,  $\models^{safe} true \{ x := 0 \} 1/x \neq 100$  holds). Hence, logical definedness is unconnected to the safety policy.

### 2.3 Soundness and Completeness of Safety Policies

The crucial task is now to show that the proof rules of the safety policy are sound and complete w.r.t. the safety property of interest. Since we have defined semantic safety of a command with respect to an environment we need to show a theorem of the form  $\eta \vDash c \text{ safe} \text{ iff } \vdash^{\text{safe}} P \{c\} \text{ true}$ , for some  $P$  such that  $\eta \vDash P$ . The role of the proof obligation  $P$  is to collect all the safety information for  $c$  in  $\eta$ .

For the *only if* direction of the proof (i.e., completeness), we need the notion of *expressivity* [11] which postulates the existence of formulae which characterise particular sets of environments. More precisely, we *assume* the existence of weakest preconditions  $wpc$  for all statements. Formally, a (first-order) language  $\mathcal{L}$  is called *expressive* if, for all commands  $c \in \text{Cmd}$  and postconditions  $Q$ , there exists a formula  $wpc(c, Q)$  such that  $\eta \vDash wpc(c, Q)$  iff  $\langle c, \eta \rangle \Downarrow \eta'$  implies  $\eta' \vDash Q$ . This is a nontrivial assumption as there is no reason why an arbitrary semantic condition should be expressible by a (first-order) formula. However, the assumption is required for proof purposes only and in practice  $wpc$ s can often be computed automatically. As usual, **while**-loops pose the real problem, and here loop invariants have to be given explicitly.

Unfortunately, this standard definition of expressivity is not strong enough to show safety in all cases. Consider the example

```
i:=0;
while true do
  x:=1/(a-i); i:=i+1
```

which is safe in environments where  $a$  is negative but where the weakest precondition of the non-terminating loop is *true*, telling us nothing about its safety. Indeed, examples can be given which have *no* first-order  $wspc$ . We thus introduce the notion of *weakest safety precondition* ( $wspc$ ) to characterize safe environments.

**Definition 2.** (*Expressivity for commands*) A command  $c \in \text{Cmd}$  is called *expressible w.r.t. a safety judgement safe* if, for all postconditions  $Q$ , there exists a formula  $wspc(c, Q)$  such that

$$\eta \vDash wspc(c, Q) \text{ iff } (\eta \vDash c \text{ safe and } \langle c, \eta \rangle \Downarrow \eta' \text{ implies } \eta' \vDash Q).$$

A language  $\mathcal{L}$  is called *expressive for commands w.r.t. a safety judgement safe* if all commands are expressible.

Now a consequence of the definition of  $wspc$  is that all intermediate commands are safe, by safety reduction. However, since there is no useful notion of safe environment, it is not sufficient to simply consider the environments in which  $c$  reduces to a safe environment, or for which all intermediate environments are safe. We also need to extend expressivity to the expression level; here it assumes the existence of safety formulae, *safe*( $e$ ), compatible with the safety judgement **safe**.



**Definition 3.** (*Expressivity for expressions*) An expression  $e \in Expr$  is called expressible w.r.t. a safety judgement **safe** if there exists a formula  $safe(e)$  such that  $\eta \models e \text{ safe}$  iff  $\eta \models safe(e)$ .

By abuse of notation we will also call a given safety predicate  $safe(\_)$  expressive for a safety judgement **safe** if it satisfies the condition of Definition 3. It is then easy to show by straightforward induction over expressions that  $safe_{op}$  is expressive for **safe**<sub>op</sub>.

**Lemma 3.** For all  $e \in Expr$ ,  $\eta \models e \text{ safe}_{op}$  iff  $\eta \models safe_{op}(e)$ .

We can now characterize the weakest safety preconditions  $wspc$  (w.r.t. operator safety) for each command of  $\mathcal{L}_0$ . Lemma 4 thus gives a recursive (but due to the **while**-case unfortunately not well-founded) definition of  $wspc$ .

**Lemma 4.** Assuming all formulae exist, the following equivalences are sound:

1.  $wspc(\text{skip}, Q) \iff wpc(\text{skip}, Q)$
2.  $wspc(x := e, Q) \iff safe_{op}(e) \wedge wpc(x := e, Q)$
3.  $wspc(\text{if } b \text{ then } c, Q) \iff safe_{op}(b) \wedge (b \Rightarrow wspc(c, Q)) \wedge (\neg b \Rightarrow Q)$
4.  $wspc(\text{while } b \text{ do } c, Q) \iff safe_{op}(b) \wedge (b \Rightarrow wspc(c, wspc(\text{while } b \text{ do } c, Q))) \wedge (\neg b \Rightarrow Q)$
5.  $wspc(c_1; c_2, Q) \iff wspc(c_1, wspc(c_2, Q))$

The preceding lemma does not give a constructive definition of  $wspc$ , because of the recursion in the **while**-case.

**Lemma 5.** (*wspc properties*) For all formulas  $P$  and  $Q$ , and commands,  $c$ :

1.  $\models^{safe} wspc(c, Q) \{c\} Q$ .
2.  $\models^{safe} P \{c\} Q$  implies  $P \Rightarrow wspc(c, Q)$ .

*Proof:* 1. By definition of  $wspc$ . 2. The implication is clearly true in the model. Provability follows from completeness of the underlying logic. ■

We can now extend the definition of safety formulae to commands via a reduction to  $wspc$ . We define  $safe_{op}(c) = wspc(c, true)$ , which also yields  $\models^{safe} safe_{op}(c) \{c\} true$ , for all  $c \in Cmd$ , as a special case of Lemma 5. Moreover, we clearly have  $\eta \models c \text{ safe}_{op}$  iff  $\eta \models safe_{op}(c)$ , so can factor  $wspc$  into a functional component expressed in terms of the standard precondition  $wpc$  and a safety component  $safe_{op}(c)$ .

**Proposition 1.**  $wspc(c, Q) \iff wpc(c, Q) \wedge safe_{op}(c)$ .

Note that we choose not to define  $wspc$  this way i.e., by giving a direct definition of  $safe_{op}(c)$ . The reason is that checking safety requires a similar recursive descent over the structure of a command, similar to computing the  $wpc$ , so it is more natural to combine them into a single definition. Similarly, it is not possible to give a neat definition of  $wspc$  from  $wpc$  and safety of expressions, for the reasons given in Section 2.

**Theorem 1.** *Suppose  $c$  is expressible. Then,  $\models^{\text{safe}} P \{c\} Q$  iff  $\vdash^{\text{safe}} P \{c\} Q$ .*

*Proof:* Soundness is by induction over the derivation. For completeness, the proof structure follows that of the standard (relative) completeness proof for Hoare logic, using expressivity to get, in our case, the weakest safety preconditions which are needed to make the proof go through. The most interesting cases are for conditionals and **while**-loops.

(if) Let  $R$  denote  $\text{wspc}(\text{if } b \text{ then } c, Q)$ . Then:

$$\frac{\frac{\frac{}{R \Rightarrow \text{safe}(b)} \quad (1) \quad \frac{\frac{\overline{b \wedge R \Rightarrow \text{wspc}(c, Q)} \quad (2) \quad \frac{\overline{\text{wspc}(c, Q) \{c\} Q} \quad (3)}{b \wedge R \{c\} Q}}{\overline{-b \wedge R \Rightarrow Q}} \quad (4)}{R \{\text{if } b \text{ then } c\} Q}}{\overline{P \Rightarrow R}} \quad (5)}{P \{\text{if } b \text{ then } c\} Q}}$$

The first, second, and fourth hypotheses follow from Lemma 4, the third and fifth follow from Lemma 5 (parts 1 and 2, respectively).

(while) Suppose  $\models^{\text{safe}} P \{\text{while } b \text{ do } c\} Q$ . Let  $R$  denote  $\text{wspc}(\text{while } b \text{ do } c, Q)$ . Then:

$$\frac{\frac{\frac{\frac{}{R \Rightarrow \text{safe}_{\text{op}}(b)} \quad (1) \quad \frac{\frac{\overline{b \wedge R \Rightarrow \text{wspc}(c, R)} \quad (2) \quad \frac{\overline{\text{wspc}(c, R) \{c\} R} \quad (3)}{b \wedge R \{c\} R}}{\overline{R \{\text{while } b \text{ do } c\} -b \wedge R}} \quad (4)}{\overline{R \{\text{while } b \text{ do } c\} Q}} \quad (5)}{\overline{P \{\text{while } b \text{ do } c\} Q}}}$$

The first, second and fourth hypothesis follow from Lemma 4, the third follows from the inductive hypothesis on  $c$  and Lemma 5(1); and the fifth follows from Lemma 5(2).  $\blacksquare$

**Theorem 2.** *Assume expressivity. Then,  $\eta \models c \text{ safe}_{\text{op}}$  iff  $\vdash^{\text{safe}} \phi \{c\}$  true for some  $\phi$  such that  $\eta \models \phi$ .*

*Proof:* We show the left-to-right implication. We know that  $\models^{\text{safe}} \text{safe}_{\text{op}}(c) \{c\}$  true by Lemma 5. Hence, by Theorem 1, we have that  $\vdash^{\text{safe}} \text{safe}_{\text{op}}(c) \{c\}$  true, and since  $\eta \models c \text{ safe}_{\text{op}}$  by assumption, expressivity gives us  $\eta \models \text{safe}_{\text{op}}(c)$ .  $\blacksquare$

At this point it might look like we have built a formidable machinery to prove some less than formidable properties. However, subtle variations of the Hoare rules are possible, and finding the “right” rules (much less proving that they are right) is difficult without a formal framework like the one we have developed here. Consider, for example, the following variant of the *if*-rule

$$(if') \frac{\text{safe}_{\text{op}}(b) \wedge b \wedge P \{c\} Q \quad \text{safe}_{\text{op}}(b) \wedge \neg b \wedge P \Rightarrow Q}{P \{\text{if } b \text{ then } c\} Q}$$

in which the safety formula is “inlined” into the two hypotheses and not separated into a third hypothesis (cf. Figure 7). However, this rule variant allows

safety information to be used to determine the control flow, which makes it potentially unsound. It allows us to derive the triple

$$true \{ \text{if } 1/x \neq 1 \text{ then if } x \neq 0 \text{ then } y:=3 \} x = 1 \vee y = 3$$

which on the surface seems reasonable: either  $x$  is one and nothing can be concluded about  $y$ , or  $x$  is non-zero and  $y$  is assigned, or  $x$  is zero, the outer guard is undefined, and hence, the statement causes an exception and does not terminate properly. However, it is exactly this third alternative which causes the trouble: if division by zero does *not* cause an exception but returns a defined value (e.g., *NaN*, “not a number”), we can no longer conclude at the inner guard that the safety formula on the outer guard holds.

We note in passing that the rules in this paper are different from those in [19]. However, we believe that the rules shown here are easier to implement and apply in practice.

### 3 Stateful Safety Properties

For operator safety, the property itself was defined in terms of the original environments only. Most safety properties, however, are not that simple and require additional information: memory safety requires information about the size of arrays or the number of variable accesses, domain-specific policies such as frame safety require additional typing information which is not part of the operational semantics of the language, and so on. We now extend our framework to deal with such safety properties.

Our basic idea is to introduce a distinct auxiliary (or *shadow*) variable  $\bar{x} \in \overline{Var}$  for each variable  $x \in Var$ , which records the necessary safety information associated with  $x$ . We also introduce shadow environments  $\bar{\eta} : \overline{Var} \rightarrow \overline{Val}$ , where the shadow domain  $\overline{Val}$  depends on the safety property of interest, and extend the operational semantics to include the effects the different commands have on the values of the shadow variables. We then modify the Hoare rules to ensure that  $\bar{x}$  actually “shadows”  $x$ , i.e., that the information recorded in  $\bar{x}$  is always current.

We already adopted part of this methodology in [19]; one motivation for the present work is to formally justify it. The methodology itself is quite flexible and allows us to encode different safety properties, using different shadow domains. We illustrate our approach first for memory safety (more precisely, array bounds checks), and then show how two other, less typical safety policies can be encoded.

#### 3.1 Memory Safety

For memory safety, we need to extend our language  $\mathcal{L}_0$  by simple arrays; here, we restrict ourselves to one-dimensional arrays with a fixed lower bound of zero to simplify the presentation. Figure 9 shows the syntax of the extended language  $\mathcal{L}_1$ . As usual, we add array updates to the commands and array selects to the

expressions. However, we also require explicit array declarations of the form `var x[n]`, which declares an  $n$ -element array  $x$ .<sup>6</sup>

$$\begin{array}{l}
\text{Cmd} ::= \dots \\
\quad | \text{Var}[Expr] := Expr \\
\quad | Decl \\
\\
\text{Decl} ::= \text{var Var} \\
\quad | \text{var Var}[Const]
\end{array}
\qquad
\begin{array}{l}
\text{Expr} ::= \dots \\
\quad | \text{Var}[Expr]
\end{array}$$

**Fig. 9.** Syntax of extended *while*-language  $\mathcal{L}_1$

For memory safety, the shadow environment needs to record the size of each array; we thus have  $\bar{\eta} : \overline{\text{Var}} \rightarrow \mathbb{N}$ . Eventually, the shadow variables get their values from the declarations. This differs from the usual approach where the array bounds are represented by an extra function  $high(x)$  on the logical level.

Since we now have two environments, we have to slightly extend some parts of our machinery. This includes interpretations, the operational semantics, and the safety judgements. For interpretations, the only difference is in the case of variables, which need to be taken from the correct environment:

$$\begin{array}{l}
\llbracket x \rrbracket_{\eta, \bar{\eta}} = \eta(x) \\
\llbracket x_{hi} \rrbracket_{\eta, \bar{\eta}} = \bar{\eta}(x_{hi})
\end{array}$$

In the operational semantics, the only case interesting for memory safety is the array declaration; all other constructs leave the shadow environment unchanged.<sup>7</sup>

$$\begin{array}{l}
\langle \text{var } x, \eta, \bar{\eta} \rangle \quad \Rightarrow \langle \text{skip}, \eta, \bar{\eta} \rangle \\
\langle \text{var } x[n], \eta, \bar{\eta} \rangle \quad \Rightarrow \langle \text{skip}, \eta, \bar{\eta} \oplus \{x_{hi} \mapsto \llbracket n \rrbracket_{\eta, \bar{\eta}}\} \rangle \\
\langle x[e_1] := e_2, \eta, \bar{\eta} \rangle \Rightarrow \langle \text{skip}, \eta \oplus \{x \mapsto (x \oplus \{\llbracket e_1 \rrbracket_{\eta, \bar{\eta}} \mapsto \llbracket e_2 \rrbracket_{\eta, \bar{\eta}}\})\}, \bar{\eta} \rangle \\
\langle c, \eta, \bar{\eta} \rangle \quad \Rightarrow \langle c', \eta', \bar{\eta} \rangle, \text{ if } \langle c, \eta \rangle \Rightarrow \langle c', \eta' \rangle
\end{array}$$

As in the stateless case, we can then define the safety judgement for memory safety. Figure 10 shows both judgements for expressions and commands.

Again following the schema developed for the stateless case, we then formulate the Hoare rules of the safety policy, as shown in Figure 11; we have omitted the rules (*skip*), (*comp*), and (*cons*) which remain unchanged. In the rules (*assign*), (*if*), and (*while*), the safety predicate is changed. The (*update*)-rule is an appropriately modified version of McCarthy's original rule.

The lemmas and theorems of the previous section hold in a suitably modified form. The main change is to modify the expansions of *wspc*. The key cases are

<sup>6</sup> For consistency, we also add scalar declarations `var x`.

<sup>7</sup> We also need to specify how array selection and updates are modeled; however, this is a consequence of extending the language and is independent of any certification issues. Here, we model arrays as maps from naturals to values; hence:  $\llbracket x[e] \rrbracket_{\eta, \bar{\eta}} = (\eta(x))(\llbracket e \rrbracket_{\eta, \bar{\eta}})$

$$\begin{array}{l}
\eta, \bar{\eta} \models c \text{ safe}_{\text{mem}} \\
\eta, \bar{\eta} \models x \text{ safe}_{\text{mem}} \\
\eta, \bar{\eta} \models x[e] \text{ safe}_{\text{mem}} \quad \text{iff } 0 \leq \llbracket e \rrbracket_{\eta, \bar{\eta}} < \bar{\eta}(x_{\text{hi}}) \text{ and } \eta, \bar{\eta} \models e \text{ safe}_{\text{mem}} \\
\eta, \bar{\eta} \models e_1 \text{ op } e_2 \text{ safe}_{\text{mem}} \quad \text{iff } \eta, \bar{\eta} \models e_1 \text{ safe}_{\text{mem}} \text{ and } \eta, \bar{\eta} \models e_2 \text{ safe}_{\text{mem}} \\
\eta, \bar{\eta} \models \text{var } x \text{ safestate}_{\text{mem}} \\
\eta, \bar{\eta} \models \text{var } x[n] \text{ safestate}_{\text{mem}} \\
\eta, \bar{\eta} \models \text{skip safestate}_{\text{mem}} \\
\eta, \bar{\eta} \models e_1 := e_2 \text{ safestate}_{\text{mem}} \quad \text{iff } \eta, \bar{\eta} \models e_1 \text{ safe}_{\text{mem}} \text{ and } \eta, \bar{\eta} \models e_2 \text{ safe}_{\text{mem}} \\
\eta, \bar{\eta} \models \text{if } b \text{ then } c \text{ safestate}_{\text{mem}} \quad \text{iff } \eta, \bar{\eta} \models b \text{ safe}_{\text{mem}} \\
\eta, \bar{\eta} \models \text{while } b \text{ do } c \text{ safestate}_{\text{mem}} \quad \text{iff } \eta, \bar{\eta} \models b \text{ safe}_{\text{mem}} \\
\eta, \bar{\eta} \models c_1 ; c_2 \text{ safestate}_{\text{mem}} \quad \text{iff } \eta, \bar{\eta} \models c_1 \text{ safestate}_{\text{mem}} \\
\eta, \bar{\eta} \models c \text{ safe}_{\text{mem}} \quad \text{iff } \forall \langle c, \eta, \bar{\eta} \rangle \Rightarrow^* \langle c', \eta', \bar{\eta}' \rangle \cdot \eta', \bar{\eta}' \models c' \text{ safestate}_{\text{mem}}
\end{array}$$

**Fig. 10.**  $\mathcal{L}_1$  memory safety

$$\begin{array}{l}
(\text{decl}) \quad \frac{}{Q \{\text{var } x\} Q} \\
(\text{addecl}) \quad \frac{}{Q[n/x_{\text{hi}}] \{\text{var } x[n]\} Q} \\
(\text{assign}) \quad \frac{}{Q[e/x] \wedge \text{safe}_{\text{mem}}(e) \{x := e\} Q} \\
(\text{update}) \quad \frac{}{Q[\text{update}(x, e_1, e_2)/x] \wedge \text{safe}_{\text{mem}}(x[e_1]) \wedge \text{safe}_{\text{mem}}(e_2) \{x[e_1] := e_2\} Q} \\
(\text{if}) \quad \frac{P \Rightarrow \text{safe}_{\text{mem}}(b) \quad b \wedge P \{c\} Q \quad \neg b \wedge P \Rightarrow Q}{P \{\text{if } b \text{ then } c\} Q} \\
(\text{while}) \quad \frac{P \Rightarrow \text{safe}_{\text{mem}}(b) \quad b \wedge P \{c\} P}{P \{\text{while } b \text{ do } c\} \neg b \wedge P}
\end{array}$$

**Fig. 11.** Hoare rules for  $\mathcal{L}_1$  memory safety

$$\begin{array}{l}
\text{wspc}(\text{var } x[n], Q) \quad \iff Q[0/x_{\text{hi}}] \\
\text{wspc}(x[e_1] := e_2, Q) \quad \iff Q[\text{update}(x, e_1, e_2)/x] \wedge \text{safe}_{\text{mem}}(x[e_1]) \wedge \text{safe}_{\text{mem}}(e_2)
\end{array}$$

### 3.2 Memory Write Limits

Next, we consider a safety policy which limits the number of times values can be written into each memory location. Obviously, this is undecidable in general, but with appropriate annotations (i.e., loop invariants) it can still be very helpful. Such a policy can then be used to ensure that the physical limitations of non-volatile memory, as for example used in spacecraft, are not exceeded. For example, locations in flash memory can only be written to a finite number of times before wearing out.

$$safe_{\text{mem}}(e) = \begin{cases} true & \text{if } e \in \text{Var} \text{ or } e \in \text{Const} \\ safe_{\text{mem}}(e_1) \wedge 0 \leq e_1 < x_{\text{hi}} & \text{if } e \equiv x[e_1] \\ safe_{\text{mem}}(e_1) \wedge safe_{\text{mem}}(e_2) & \text{if } e \equiv e_1 \text{ mem } e_2, \text{ op} \in \{*, /, +, -, =\} \end{cases}$$

**Fig. 12.** Safety formula for  $\mathcal{L}_1$  memory safety

We formalize this using shadow variables  $x_{w1}$  which are initialized with zero when  $x$  is declared and incremented each time it is assigned to. As in the case of memory safety, the abstract environments map the variables to naturals,  $\bar{\eta} : \overline{\text{Var}} \rightarrow \mathbb{N}$ . However, unlike in the case of memory safety, we now need (i) shadow variables for scalars as well, and (ii) a separate shadow variable for each element of an array. While the first point is straightforward to deal with, the second seems at first more complicated. However, by introducing a complete shadow array, we get around all these problems. In the operational semantics we then see a nice symmetry between the operations on the original value environment and on the shadow environment:

$$\begin{aligned} \langle \text{var } x, \eta, \bar{\eta} \rangle &\Rightarrow \langle \text{skip}, \eta, \bar{\eta} \oplus \{x_{w1} \mapsto 0\} \rangle \\ \langle \text{var } x[n], \eta, \bar{\eta} \rangle &\Rightarrow \langle \text{skip}, \eta, \bar{\eta} \oplus \{x_{w1} \mapsto \lambda i \cdot 0\} \rangle \\ \langle x := e, \eta, \bar{\eta} \rangle &\Rightarrow \langle \text{skip}, \eta \oplus \{x \mapsto \llbracket e \rrbracket_{\eta}\}, \bar{\eta} \oplus \{x_{w1} \mapsto \bar{\eta}(x_{w1}) + 1\} \rangle \\ \langle x[e_1] := e_2, \eta, \bar{\eta} \rangle &\Rightarrow \langle \text{skip}, \\ &\quad \eta \oplus \{x \mapsto (x \oplus \{\llbracket e_1 \rrbracket_{\eta, \bar{\eta}} \mapsto \llbracket e_2 \rrbracket_{\eta, \bar{\eta}}\})\}, \\ &\quad \bar{\eta} \oplus \{x_{w1} \mapsto (x_{w1} \oplus \{\llbracket e_1 \rrbracket_{\eta, \bar{\eta}} \mapsto x_{w1}(\llbracket e_1 \rrbracket_{\eta, \bar{\eta}}) + 1\})\} \\ &\quad \rangle \\ \langle c, \eta, \bar{\eta} \rangle &\Rightarrow \langle c', \eta', \bar{\eta}' \rangle, \text{ if } \langle c, \eta \rangle \Rightarrow \langle c', \eta' \rangle \end{aligned}$$

The safety judgement  $\text{safe}_{w1}$  obviously only needs to look at assignments; it just checks that the assignment counts are still below a fixed upper limit  $\text{MAXWR}$ . Since safety reduction holds trivially, we formulate  $\text{safe}_{w1}$  directly and not via  $\text{safestate}$ .

$$\begin{aligned} \eta, \bar{\eta} \models x := e \text{ safe}_{w1} &\quad \text{iff } \bar{\eta}(x_{w1}) < \text{MAXWR} \\ \eta, \bar{\eta} \models x[e_1] := e_2 \text{ safe}_{w1} &\quad \text{iff } (\bar{\eta}(x_{w1}))(\llbracket e_1 \rrbracket_{\eta, \bar{\eta}}) < \text{MAXWR} \end{aligned}$$

Finally, we formulate the Hoare rules (cf. Figure 13); again, the only interesting cases are declarations and assignments. We thus omit an explicit definition of the safety formula and inline it instead. Note that we extend the logic for arrays by the construct  $\text{init}(x, n, k)$  which denotes the array  $x$  of size  $n$  where every element is set to  $k$ . For this, we need the axiom  $i < n \Rightarrow (\text{init}(x, n, k))(i) = k$  in the domain theory of the underlying logic (not shown here).

Again, we can show that the system is sound and complete with respect to the corresponding semantics. The proofs follow the outline in Section 2.

$$\begin{array}{l}
(\text{decl}) \quad \frac{}{Q[0/x_{w1}] \{\text{var } x\} Q} \\
(\text{adecl}) \quad \frac{}{Q[\text{init}(x_{w1}, n, 0)/x_{w1}] \{\text{var } x[n]\} Q} \\
(\text{assign}) \quad \frac{}{Q[e/x, (x_{w1} + 1)/x_{w1}] \wedge x_{w1} < \text{MAXWR} \{x := e\} Q} \\
(\text{update}) \quad \frac{}{Q \left[ \begin{array}{l} \text{update}(x, e_1, e_2)/x, \\ \text{update}(x_{w1}, e_1, x_{w1}[e_1] + 1)/x_{w1} \end{array} \right] \wedge x_{w1}[e_1] < \text{MAXWR} \{x[e_1] := e_2\} Q}
\end{array}$$

**Fig. 13.** Hoare rules for  $\mathcal{L}_1$  write limits

### 3.3 Memory Read Limits

The final safety policy we consider in this paper limits the number of times memory locations can be read. Intuitively, this is the dual of the write limit policy considered above; formally, however, it is quite different. The reason for the difference (and the source of additional complexity) is that the updates of the shadow environment are now much less localized: the evaluation of each expression can potentially change the shadow environment. This problem is not restricted to read limits but occurs whenever expression evaluation can have side effects, either in the original environment, or in the shadow environment. We thus extend the evaluation notation for expressions to take the environments into account, i.e.,  $\langle e, \eta, \bar{\eta} \rangle \Downarrow \langle v, \eta', \bar{\eta}' \rangle$ .

To simplify our notation we define a shadow environment update function  $\text{upd} : Env \times \bar{Env} \times Expr \rightarrow \bar{Env}$  which examines the expression and adds the correct number of occurrences to the shadow environment; the notation  $y \in_n e$  denotes that there are  $n$  occurrences of the variable  $y$  in  $e$ :

$$\begin{aligned}
\text{upd}(\eta, \bar{\eta}, e) = & \bar{\eta} \oplus \{x_{r1} \mapsto \bar{\eta}(x_{r1}) + n \mid x \in_n e\} \\
& \oplus \{x_{r1} \mapsto x_{r1} \oplus \{\llbracket e' \rrbracket_{\eta, \bar{\eta}} \mapsto x_{r1}(\llbracket e' \rrbracket_{\eta, \bar{\eta}}) + n\} \mid x[e'] \in_n e\}
\end{aligned}$$

We can then formulate the operational semantics concisely; the omitted cases follow easily.

$$\begin{aligned}
\langle \text{var } x, \eta, \bar{\eta} \rangle & \Rightarrow \langle \text{skip}, \eta, \bar{\eta} \oplus \{x_{r1} \mapsto 0\} \rangle \\
\langle \text{var } x[n], \eta, \bar{\eta} \rangle & \Rightarrow \langle \text{skip}, \eta, \bar{\eta} \oplus \{x_{r1} \mapsto \lambda i \cdot 0\} \rangle \\
\langle x := e, \eta, \bar{\eta} \rangle & \Rightarrow \langle \text{skip}, \eta \oplus \{x \mapsto \llbracket e \rrbracket_{\eta, \bar{\eta}}\}, \text{upd}(\eta, \bar{\eta}, e) \rangle \\
\langle x[e_1] := e_2, \eta, \bar{\eta} \rangle & \Rightarrow \langle \text{skip}, \\
& \eta \oplus \{x \mapsto (x \oplus \{\llbracket e_1 \rrbracket_{\eta, \bar{\eta}} \mapsto \llbracket e_2 \rrbracket_{\eta, \bar{\eta}}\})\}, \\
& \text{upd}(\eta, \text{upd}(\eta, \bar{\eta}, e_1), e_2) \\
& \rangle \\
\langle \text{if } b \text{ then } c, \eta, \bar{\eta} \rangle & \Rightarrow \langle c, \eta, \text{upd}(\eta, \bar{\eta}, b) \rangle \text{ if } \llbracket b \rrbracket_{\eta, \bar{\eta}} = \text{true} \\
\langle \text{if } b \text{ then } c, \eta, \bar{\eta} \rangle & \Rightarrow \langle \text{skip}, \eta, \text{upd}(\eta, \bar{\eta}, b) \rangle \text{ if } \llbracket b \rrbracket_{\eta, \bar{\eta}} = \text{false}
\end{aligned}$$

In effect, we can give the semantics in terms of the basic underlying semantics and the update function on the shadow environments: if  $\langle c, \eta \rangle \Rightarrow \langle c', \eta' \rangle$ , then

$\langle c, \eta, \bar{\eta} \rangle \Rightarrow \langle c', \eta', \text{upd}(\eta, \bar{\eta}', e_1, \dots, e_n) \rangle$  for immediate subexpressions  $e_1, \dots, e_n$  of  $c$  (extending  $\text{upd}$  to lists of expressions in the obvious way). We can also apply the same idea to the Hoare rules. Instead of an update function which is applied to the shadow environment we need an update substitution  $\text{Sub}(e)$  which is applied to the precondition; it is defined in the same way as the update function:

$$\text{Sub}(e) = [x_{r1} + n/x_{r1} \mid x \in_n e] \cup [\text{update}(x_{r1}, e', x_{r1}[e'] + n)/x_{r1} \mid x[e'] \in_n e]$$

We then define the safety formula  $\text{safe}_{r1}(e)$  in the same way: it checks that the occurrences in  $e$  do not exceed the limit  $\text{MAXRL}$ :

$$\text{safe}_{r1}(e) = \bigwedge_{x \in_n e} x_{r1} + n \leq \text{MAXRL} \quad \wedge \quad \bigwedge_{x[e'] \in e} (\text{fold}_{x[e'] \in_n e}(\text{upd}(n), x_{r1}))[e'] \leq \text{MAXRL}$$

where the folded update of  $x_{r1}$  by all *literal* occurrences  $x[e']$  in  $e$  is defined using:

$$\text{upd}(n)(x[e'], x_{r1}) = \text{update}(x_{r1}, e', x_{r1}[e'] + n).$$

The safety judgements are similar to those for write limits. The only change is that since expression evaluation can affect the shadow environment, we need to add the the safety condition outside the substitution. With this, we have all the pieces in place to formulate the Hoare rules. We only give a single rule for the **if**-statement; the other rules follow the same schema.

$$(if) \frac{b \wedge P \quad \{c\} Q \quad \neg b \wedge P \Rightarrow Q}{\text{Sub}^b(P) \wedge \text{safe}_{r1}(b) \quad \{\text{if } b \text{ then } c\} Q}$$

## 4 Automatic Derivation of Safety Policies

We now generalize the idea from Section 3.3 and derive a general way of formulating safety extensions to an operational semantics and Hoare logic, respectively, such that the results of the previous sections are preserved. The main idea is to develop a notion of *compositional* safety property, which then allows us to augment the Hoare rules in a similarly compositional manner.

We have seen that abstract environments describe how programs compute the abstract properties we are interested in for a given safety property. In order to reason about such properties in a safety policy, we need a notion of expressivity to relate environments to the logic.

**Definition 4.** *We say that a command  $c \in \text{Cmd}$  is operationally expressive, if whenever  $\langle c, \eta, \bar{\eta} \rangle \Rightarrow \langle c', \eta', \bar{\eta}' \rangle$ , then for all  $x \in (\eta' \cup \bar{\eta}')$ , there exists an expression  $e$ , such that  $\llbracket e \rrbracket_{\eta, \bar{\eta}} = \llbracket x \rrbracket_{\eta', \bar{\eta}'}$ . ■*



This formalizes the idea that whatever change a command makes to the environments can be expressed in terms of substitutions. Clearly, the expression can only contain variables from the original environments.

We use the notation  $\text{Sub}_\theta^{e_1, \dots, e_n}(P)$  to denote the substitution, applied to  $P$ , which expresses the change in environments effected by command type  $\theta$  with immediate subexpressions  $e_1, \dots, e_n$ . We are implicitly assuming particularly simple changes to the environment which can always be expressed this way, but this accounts for all our examples. For example,  $\text{Sub}_{\text{assign}}^{x,e}(P)$  is simply  $P[e/x]$  for the assignment  $x := e$ .

In general, each command has its own notion of safety. However, we want to exclude pathological examples of safety properties, so we consider, now, what sort of properties are acceptable. For atomic commands, we allow an arbitrary condition on the environments and the component expressions. For example, the safety of the assignment  $x := e$  can be any condition on  $x$  and  $e$ . We can express this as a predicate  $P \subseteq \text{Env} \times \overline{\text{Env}} \times \text{Expr} \times \text{Expr}$ . For compound commands, the key idea is that the basic data of a safety property consists of arbitrary predicates,  $\text{Cond}$ , on the immediately accessible subexpressions for each command. We will write  $\eta, \bar{\eta} \models \text{Cond}(e_1, \dots, e_n)$  to mean  $\langle \eta, \bar{\eta}, e_1, \dots, e_n \rangle \in \text{Cond}$ .

**Definition 5.** *A safety property on commands is compositional, if there exist predicates  $\text{Cond}_\theta$ , with the following properties:*

- $\eta, \bar{\eta} \models \text{var } x \text{ safe}$  iff  $\eta, \bar{\eta} \models \text{Cond}_{\text{decl}}(x)$
- $\eta, \bar{\eta} \models \text{var } x[n] \text{ safe}$  iff  $\eta, \bar{\eta} \models \text{Cond}_{\text{adec1}}(x, n)$
- $\eta, \bar{\eta} \models x := e \text{ safe}$  iff  $\eta, \bar{\eta} \models \text{Cond}_{\text{assign}}(x, e)$
- $\eta, \bar{\eta} \models x[e_1] := e_2 \text{ safe}$  iff  $\eta, \bar{\eta} \models \text{Cond}_{\text{update}}(x, e_1, e_2)$
- $\eta, \bar{\eta} \models \text{skip safe}$
- $\eta, \bar{\eta} \models \text{if } b \text{ then } c \text{ safe}$  iff  $\text{Cond}_{\text{if}}(b)$  and  $\langle b, \eta, \bar{\eta} \rangle \Downarrow \langle \text{true}, \eta', \bar{\eta}' \rangle$  implies  $\langle \eta', \bar{\eta}' \rangle \models c \text{ safe}$
- $\eta, \bar{\eta} \models \text{while } b \text{ do } c \text{ safe}$  iff  $\eta, \bar{\eta} \models \text{Cond}_{\text{while}}(b)$  and  $\langle b, \eta, \bar{\eta} \rangle \Downarrow \langle \text{true}, \eta', \bar{\eta}' \rangle$  implies  $(\langle \eta', \bar{\eta}' \rangle \models c \text{ safe and } \langle c, \eta', \bar{\eta}' \rangle \Downarrow \langle \eta'', \bar{\eta}'' \rangle \text{ implies } \langle \eta'', \bar{\eta}'' \rangle \models \text{while } b \text{ do } c \text{ safe})$ .

For sequential composition, the safety of  $c_1; c_2$  is defined as before. Although this looks fairly similar to Figure 6 it generalizes it by allowing arbitrary conditions on the expressions. Stateless safety follows as the special case where  $\eta, \bar{\eta} \models \text{Cond}_\theta(e_1, \dots, e_n)$  iff  $\eta, \bar{\eta} \models e_i \text{ safe}$ , for each  $i$ .

This notion of compositionality maintains the correspondence between **safe** and **safestate**, while allowing that safety of a command is arbitrarily expressed in terms of the safety of its subcommands. Now it should come as no surprise that we require the condition predicates to be expressible.

**Definition 6.** *We say that the  $n$ -ary predicate,  $P$ , is expressible when there exists formulas  $\phi$  such that*

$$\langle e_1, \dots, e_n \rangle \in P \text{ iff } \eta, \bar{\eta} \models \phi(e_1, \dots, e_n).$$

Finally, we are in a position to state a general completeness theorem, which generalizes the theory of stateless safety developed in Section 2. We omit the details of the proof here and just state the theorem; the proof structure is the same as for the stateless case, making use of expressivity where appropriate.

**Theorem 3.** *Given (i) a set,  $\overline{Val}$  (the shadow domain), (ii) an operational semantics,  $\langle c, \eta, \bar{\eta} \rangle \Rightarrow \langle c, \eta', \bar{\eta}' \rangle$ , and (iii) a compositional safety property, such that expressivity (operational, predicate, commands and expressions) holds, the following system is sound and complete with respect to the safety property:*

$$\begin{array}{l}
\text{(decl)} \quad \frac{}{\text{Sub}_{\text{decl}}^x(Q) \wedge \text{Cond}_{\text{decl}}(x) \{ \text{var } x \} Q} \\
\text{(adecl)} \quad \frac{}{\text{Sub}_{\text{adecl}}^{x,n}(Q) \wedge \text{Cond}_{\text{decl}}(x, n) \{ \text{var } x [n] \} Q} \\
\text{(assign)} \quad \frac{}{\text{Sub}_{\text{assign}}^{x,e}(Q) \wedge \text{Cond}_{\text{assign}}(x, e) \{ x := e \} Q} \\
\text{(update)} \quad \frac{}{\text{Sub}_{\text{update}}^{x,e_1,e_2}(Q) \wedge \text{Cond}_{\text{update}}(x, e_1, e_2) \{ x[e_1] := e_2 \} Q} \\
\text{(if)} \quad \frac{b \wedge P \{c\} Q \quad \neg b \wedge P \Rightarrow Q}{\text{Sub}_{\text{if}}^b(P) \wedge \text{Cond}_{\text{if}}(b) \{ \text{if } b \text{ then } c \} Q} \\
\text{(while)} \quad \frac{b \wedge P \{c\} P}{\text{Sub}_{\text{while}}^b(P) \wedge \text{Cond}_{\text{while}}(b) \{ \text{while } b \text{ do } c \} Q}
\end{array}$$

(with the rules (skip) and (cons) as before). ■

## 5 Related Work

A number of different techniques have been applied to program certification. The following list is certainly not exhaustive; we focus on static techniques and leave out dynamic techniques like runtime monitoring [5].

Certification tools based on static analysis are already commercially available, e.g., PolySpace [14], which uses abstract interpretation and constraint solving techniques to identify possible runtime errors. However, such tools usually have fixed built-in notions of safety and suffer from a high number of false positives.

Other approaches use expressive type systems to enforce safety policies. Rittri [16] and Kennedy [7] have extended type inference techniques to ensure the consistent use of physical dimensions in functional programs. However, both approaches exploit certain algebraic properties of dimensions and it is unclear how general they are. Xi and Pfenning [20] have used dependent types to show array bounds safety, again for functional programs. Using similar ideas, Walker [18] has developed a type system to express and enforce a number of security policies. Shankar et al. [17] have used type qualifiers [3] to detect vulnerabilities due to C's format strings. Their `tainted` and `untainted` qualifiers take the same role as the values in our shadow domains. In general, type-based approaches tend to scale better, although it is unclear when a specific expressive type inference

algorithm becomes intractable in practice. Unlike the shadow variables, however, inferred types are static, i.e., the abstract value associated with a program cannot change during execution. Moreover, structured collections like arrays are usually modeled using a single type to keep inference tractable; this makes the analysis necessarily less precise. Experiments are thus required to compare the effects and trade-offs of the different approaches in practice.

Traditionally, program verification concentrates on showing full functional equivalence between specifications and programs. This is true especially for integrated development/proof environments as for example the KIV system [15]. However, Hoare-style verification has also been used in property-oriented certification as we investigate it here. Extended static checking (ESC) [8, 4] can be thought of as an “inference-based debugger”: it uses Hoare rules, supported by program annotations, to detect a variety of potential errors, including division-by-zero and array-bounds violations. The more annotations the program contains, the more errors ESC can detect. Similarly, the SPARK Examiner [1] is a tool which uses Hoare rules to show exception freedom of Ada programs; this corresponds to a safety policy which combines more elaborate versions of operator safety (i.e., division-by-zero and overflow) and memory safety (i.e., array-bounds violations and overflow).<sup>8</sup> However, none of the systems deal with the question of correctness of their respective safety policies. Also, they typically only deal with one specific policy, whereas our framework is general.

## 6 Conclusions and Future Work

In this paper we have formalized a selection of safety properties using Hoare logic, and shown that they are sound and complete with respect to a semantic notion of safety. We have developed a generic method of doing this for arbitrary safety properties, thus showing how a safety policy can be automatically derived from a safety property and an operational semantics. The principal difficulty has been finding a general definition of safety property which enables this automatic derivation.

The rules we have presented show that safety rules can be quite complicated, even when dealing with a single policy at a time. The semantic framework developed in this paper serves as a structuring mechanism to deal with such complexity. The modularization of safety policies is a difficult problem but the present theory should serve as a starting point.

We are currently using this theory as the basis for the implementation of a VCG which is parametric with respect to a safety policy, and we are looking at a wide range of safety properties. Direct application of the theory should lead to a modular implementation.

The simple *while*-language studied here is sufficient for this because our aim is to certify synthesized code, and so we can control the language subset under

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<sup>8</sup> Note that overflows can result from arithmetic operations as well as from inconsistent use of derived types (i.e., subtypes) and thus influence both operator safety and memory safety.

consideration. Moreover, since we can generate loop invariants along with the synthesized code our safety logic need not be concerned with this.

On a theoretical side, we believe that the logical nature of Definition 5 points to some interesting connections to the theory of computation, and we are currently investigating this.

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