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# Delta-Modulator-Based Quantised Output Feedback Controller for Linear Networked Control Systems

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**ABSTRACT** This article proposes a  $\Delta$ -Modulator ( $\Delta$ -M) based quantised output feedback controller for linear networked systems. The proposed  $\Delta$ -M is essentially a 2-level quantiser, in contrast to some of the existing quantisers such as  $2^p$  level ( $p \ge 1$ ) uniform-interval-nearest-neighbour quantiser, and offers various advantages which include lower design complexity, less noisy and lower cost. The three key components of the control system: the *controller*, the *filter* and the *quantiser* are designed to achieve the desired performance. The stability conditions of the  $\Delta$ -M are derived and conditions for the existence of *zig-zag* behaviour in steady-state are determined. The performance of the proposed controller is illustrated through simulations considering practical communication network based on ZigBee protocol. The results of the simulation demonstrate that the proposed controller could effectively achieve desired performance under various imperfections of the practical communication network.

**INDEX TERMS**  $\Delta$ -modulation, quantised control, networked control systems (NCSs), output feedback control.

#### I. INTRODUCTION

During the past few decades, a new paradigm of controller design and implementation, based on a communication network, has emerged. This is called as networked control systems (NCSs) [1]. In NCS, various control system components exchange data through a communication network, and there exists a strong interaction between communication and control [2], [3]. The communication networks in NCS allow sharing of data packets among control components and therefore avoid the point-to-point wiring installation, associated with the traditional control system, which increases flexibility and maintainability of the system. Further, the well established and proven communication protocols often ensure the data packets to be successfully transmitted between the con-

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trol components and thereby makes the NCS very reliable. This has widely been used in various applications which include remote control [4], telemanipulation [5], robotics [6], process control [7] and so on.

Although this control paradigm has been very successful [8], [9], there exist many challenges such as *quantisation errors* [10], *network-induced delay* [11], *packet losses*, which may result in system instability [12]. This is because the communication networks transmit data packets in the form of digital signals after sampling and quantisation of the continuous-time signal. Moreover, the limited bandwidth of the network often causes traffic congestion which leads to delays and packet dropouts.

In the past few decades, researchers have developed several effective control design methods and have proved their stability, both for linear and nonlinear systems [13]–[19]. Some of the existing control methods such as sliding mode

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control [20], event-triggered control [21],  $H_{\infty}$  control [22], formation tracking control [23], model predictive control [24], distributed control [25] and so on have been tailored for networked control to mitigate the effects of various network imperfections and make the NCS more robust. These have been applied to many engineering applications such as fault detection, identifying cyberattacks, robot applications and many more.

One of the major issue with NCS is the bit-rate constraint (i.e. bandwidth utilisation) of the communication network. One of the possible methods to alleviate this problem is through quantisation. This is a process of mapping a large set of input values to a small set of output values where the continuous-time signals are represented by quantised signals. In the past, different types of quantisers have been proposed by researchers which include nearest neighbour quantisers [26], [27], logarithmic quantisers [28], [29], neural network quantisers [30] and so on. Note that as the number of quantisation levels decreases, the effectiveness of the quantisers in bandwidth utilisation increases. However, the accuracy of the generated control action decreases due to less number of quantisation levels (quantisation error increases). Although by using a higher number of bits could reduce the quantisation error, this increases the delay in the control action. Because all the bits need to be received in order to reconstruct the input to the controller [31].

The single-bit quantiser is a possible alternative to alleviate the problems associated with bit-rate constraints (or network bandwidth utilisation). During the past decade, various researchers have used the single-bit quantisers based on either  $\Delta$ -Modulator ( $\Delta$ -M), Delta-Sigma Modulator ( $\Delta$ -M), Hybrid-Delta Modulator ( $\Delta$ -M) [32]–[34], to develop single-bit controllers. The output of these quantisers (modulators) are called bit-streams and the associated controllers are popularly known as bit-stream controllers [35]. The stability of bit-stream controllers have been established and the guidelines to tune the controllers' parameters have been reported in [8], [36]–[39].

In these methods, instead of using a microprocessor to implement the control functions, the controllers are implemented in hardware using bit-streams inside programmable logic devices such as field-programmable gate arrays (FPGAs). This technique differs from the traditional digital implementation where the continuous-time signal is represented by a single-bit signal. Moreover, since all control elements are implemented in parallel, the addition of extra functionality to a given design will consume extra silicon area with little impact on the timing of the system, unlike micro-controller based systems which execute control functions sequentially and may exceed the available execution time as more functionality is added. The success of single-bit quantisers has been demonstrated in areas such as control, mobile communication, and biomedical applications [35], [40]–[43].

However, the applications of single-bit quantisers to NCS have received comparatively less attention from researchers

except recently in [3], where quantised controllers are designed for NCS which consist of three major components such as the controller, the filter and the quantiser. In [3], a uniform-interval-nearest-neighbour quantiser, with  $2^p$  levels, have been used and the performance of the system have been investigated by varying the number of bits p in the quantiser. This quantiser becomes a single-bit quantiser when p=1, and it gives the minimum variance of the output.

Motivated by the success of the bit-stream controllers based on  $\Delta$ -M,  $\Delta\Sigma$ -M,  $\Delta_H$ -M [8], [36]–[39], the present study proposes an alternate approach of designing a quantised controller for networked control systems using  $\Delta$ -M. Amongst all these modulators,  $\Delta$ -M offers many advantages such as lower noise, cost-effective operations, lower complexity and uses 2-levels in contrast to  $2^p$  levels as used in [3]. The goal of this study is to investigate:

- (i.) Whether it is possible to use the  $\Delta$ -M as a quantiser for networked control systems?
- (ii.) How to determine the step size (quantiser gain) of the Δ-M?
- (iii.) What are the conditions which will ensure the stability of  $\Delta$ -M?
- (iv.) Whether the proposed control strategy can achieve better performance in a ZigBee protocol based real communication network?

For this purpose, a  $\Delta$ -M based quantised output feedback controller is designed for linear networked systems. The bounds of the quantisation gain  $\lambda$  and the stability conditions are derived. The control architecture consisting of the *controller*, the *filter* and the *quantiser* is implemented in a real ZigBee protocol based communication network. The steady-state behaviour of the system is studied, and the conditions for the existence of the periodic behaviour are determined.

Note that, this study addresses the problem of designing quantised output-feedback controllers for networked control systems under disturbance. Theoretical results are derived for the design of the output-feedback controller which minimises a given cost function. The filter is designed to minimise the variance of the input signal to the  $\Delta$ -M based quantiser. Further, the value of quantiser gain which ensures both the stability of the overall closed-loop system and periodic behaviour of the modulator output.

The rest of the paper is organised as follows. Section-II describes the problem formulation and the architecture of the  $\Delta$ -M based networked control system is described in Section-III. Design procedures of designing the controller, the filter and the quantisation gain are described in Section-IV. The effectiveness of the proposed control strategy is demonstrated using a practical communication network, considering two examples in Section-V followed by conclusions in Section-VI.



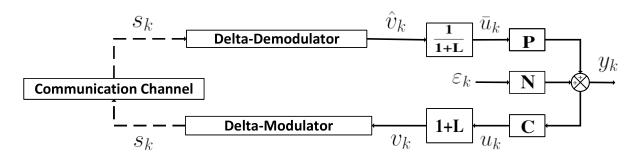


FIGURE 1. Closed-loop system with delta-modulator ( $\Delta$ -M).

#### **II. PROBLEM FORMULATION**

Consider a single-input-single-output linear time invariant (LTI) continuous-time system given by:

$$\dot{x}(t) = A_{cont} x(t) + B_{cont} u(t) + K_{cont} \varepsilon(t), \qquad (1a)$$

$$y(t) = C_{cont} x(t) + \varepsilon(t),$$
 (1b)

where  $x(t) \in \mathbb{R}^n$ ,  $u(t) \in \mathbb{R}^1$  and  $y(t) \in \mathbb{R}^1$  are the system (plant) states, system input and system output, respectively. The objective of the study is to design a controller which would stabilise the plant under bit rate constraint in the communication channel between the controller and the plant. The bit rate of the communication channel is assumed to be  $b_r$  bits per second which give the smallest possible control update period  $\delta_1$  where,

$$\delta_1 = \frac{1}{h_r}. (2)$$

The output from the plant is sampled at  $\delta_1$  seconds, and an anti-aliasing filter is deployed which operates at the same sampling period. Note that filtering of the signals at lower sampling periods is ideal for controllers.

The discrete equivalent of (1) at a sampling period of  $\delta_1$  seconds, in innovations form, [3], [44] is described as:

$$x_{k+1} = Ax_k + B\bar{u}_k + K\varepsilon_k, \tag{3a}$$

$$y_k = Cx_k + \varepsilon_k, \tag{3b}$$

where  $x_k \in \mathbb{R}^n$ ,  $\bar{u}_k \in \mathbb{R}^1$ ,  $y_k \in \mathbb{R}^1$ ,  $\varepsilon_k \in \mathbb{R}^1$  are the system (plant) states, system input, system output and innovations sequence having variance  $\sigma_s^2$ , respectively.

It is assumed that the system transfer function P(z) ( $P(z) = C(zI - A)^{-1}B$ ) has relative degree d + 1 < n. Hence, for d = 0,  $CB \neq 0$  and for d > 1,

$$CA^{i}B = 0 \quad \forall i = 0, 1, \dots, d-1; CA^{d}B \neq 0.$$
 (4)

For the rest of the paper, the following assumptions are used:

Assumption 1: The discrete-time transfer function from  $\bar{u}_k$  to  $y_k$  is stable and minimum phase.

Assumption 2: The communication between the controller and the plant are carried over a ZigBee protocol based wireless network supporting  $b_r$  bits per second.

Assumption 3: The communication channel is error-free.

Note that the assumption of error-free communication channel is only a working hypothesis. However, the results are shown by implementing the NCS on a real wireless network using ZigBee protocol where network imperfections are present.

## III. ARCHITECTURE OF THE $\Delta$ -MODULATOR BASED NETWORKED CONTROL SYSTEM

The schematic of the proposed control scheme is shown in Figure-1 which consists of the plant (P), the controller (C), the filters (L), the communication channel and the discrete-time  $\Delta$ -M. The  $\Delta$ -M consists of a transmitter (encoder or modulator) and a receiver (decoder or demodulator). In this scheme, the input signal is oversampled at a higher rate than the Nyquist rate to achieve better resolution of the input signal at the end of the demodulator. The transmitter consists of a switch (relay component) which introduces nonlinearity and add complexity to the overall system. Therefore, the stability of the overall system and  $\Delta$ -M needs to be carefully addressed [32], [33].

The relations within various signals in this modulator can be described (see Figure-2) as:

$$\hat{v}_{k+1} = \hat{v}_k + \lambda \operatorname{sgn}(\bar{v}_k), \tag{5a}$$

$$\bar{v}_k = v_k - \hat{v}_k, \tag{5b}$$

where  $\lambda > 0$ ,  $\bar{v}_k \in \mathbb{R}$  and

$$\operatorname{sgn}(\bar{v}_k) = \begin{cases} +1, & \text{if } \bar{v}_k \ge 0, \\ -1, & \text{if } \bar{v}_k < 0. \end{cases}$$

As can be seen in Figure-2, the communication channel between the modulator and the demodulator carries 1-bit signal which is defined as  $s_k = \frac{1}{2} \left[ 1 + \text{sgn}(\bar{v}_k) \right]$ . This implies  $s_k \in \{0, 1\}^T$ . For proper operation of  $\Delta$ -M, the gain  $\lambda$  must be same in both the modulator and the demodulator [40].

#### IV. DESIGN PROCEDURE

The control law is computed by minimising the cost function:

$$J_A(p) = \frac{1}{p} \sum_{i=1}^p E\left\{ y^2(k+p+d+i) \right\},\tag{6}$$



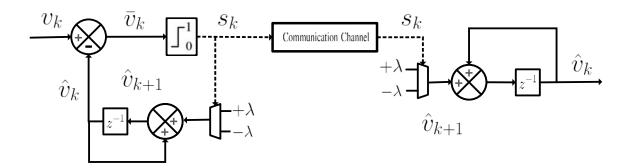


FIGURE 2. Discrete-time delta-modulator ( $\Delta$ -M).

where p, d denote respectively the number of bits of the quantiser and the relative degree of the system. This essentially represents the variance of the output.

Remark 1: It is shown in [3] that for a p-bit representation, the output of the plant is cyclostationary with a period p. Therefore, the performance of the controller is investigated by minimising the cost function (6) for different values of p at each time step k.

The schematic of the proposed control system is shown in Figure-1. This consists of three major components; the controller (C), the filter (L) and the  $\Delta$ -M. While designing the controller and the filter, it is assumed that the quantisation error  $\bar{v}_k = 0$ . The controller (C) is designed to minimise  $J_A(p)$  and the filter (L) is designed to minimise the variance of  $\bar{v}_k$  (quantisation error). The quantisation gain  $(\lambda)$  is determined such that the  $\Delta$ -M is stable.

#### A. DESIGN OF THE CONTROLLER (C)

Lemma 1: For a system with a transfer function  $C(zI - A)^{-1}B$ , the  $\Delta$ -M based optimal controller, which minimises (6), is given by

$$x_{k+1}^c = A_c x_k^c + B_c u_{k-1-d} + K_c y_k, \tag{7a}$$

$$u_k = C_c x_k^c + D_c y_k + a u_{k-1},$$
 (7b)

where,

$$A_c = (A - K C), B_c = B, K_c = K,$$
 (8a)

$$C_c = -\frac{g_2^T}{g_2^T g_2} C A^{d+1} (A - K C), \tag{8b}$$

$$D_c = -\frac{g_2^T}{g_2^T g_2} C A^{d+1} K, (8c)$$

$$a = -\frac{g_2^T g_1}{g_2^T g_2}, \ g_1 = CA^{d+1}B, \ g_2 = CA^dB.$$
 (8d)

Note that, for a system with relative degree d, d+1 < n. For d = 0,  $CB \neq 0$  and for  $d \geq 1$ ,

$$CA^{i}B = 0 \quad \forall i = 0, 1, \dots, d-1; CA^{d}B \neq 0.$$

*Proof:* From (3) and (4), the predicted output at  $k^{th}$  instant can be expressed as:

$$\hat{y}_{(k+1)+(d+1)} = CA^{d+1}(A - KC)x_k + CA^{d+1}Ky_k + CA^{d+1}Bu_{k-1} + CA^dBu_k.$$
(9)

Since the norm of the vector in R.H.S of (9) is a square function of  $u_k$ , the optimal control law  $u_k$  is given by,

$$u_k = C_c x_k + D_c y_k + a u_{k-1}. (10)$$

When the states of the plant  $x_k$  are not available, they are estimated by an observer which is given by,

$$x_{k+1}^c = Ax_k^c + Bu_{k-1-d} + K(y_k - Cx_k^c).$$
 (11)

Replacing  $x_k$  by  $x_k^c$ , in (10) gives the control law  $u_k$  in (7b).

#### B. DESIGN OF FILTERS (L)

*Lemma 2:* The optimum filter (1+L), which minimises the variance of  $\bar{v}$  (input to the  $\Delta$ -M), is given by:

$$x_{k+1}^l = A_l x_k^l + B_l u_{k-1} + K_l (u_k - a u_{k-1}),$$
 (12a)

$$v_k = C_l x_k^l + (u_k - a u_{k-1}), (12b)$$

where,

$$A_l = A + K_l C_l, B_l = B, C_l = -(C_c + D_c C).$$
 (13)

The Kalman gain  $K_l$  is computed from

$$\tilde{x}_{k+1} = \tilde{A}\tilde{x}_k + \tilde{w}_k, \tag{14a}$$

$$\tilde{\mathbf{y}}_k = \tilde{C}\tilde{\mathbf{x}}_k + \bar{\mathbf{v}}_k,\tag{14b}$$

where,

$$\tilde{A} = A, \quad \tilde{C} = C_l,$$
 (15)

and

$$E\left\{ \begin{pmatrix} \tilde{w}_k \\ \bar{v}_k \end{pmatrix} \begin{pmatrix} \tilde{w}_k^T & \bar{v}_k^T \end{pmatrix} \right\} = \begin{bmatrix} KK^T & KD_C^T \\ (KD_C^T)^T & (D_C)^2 \end{bmatrix} \sigma_{\varepsilon}^2. \quad (16)$$

The inverse filter,  $(1+L)^{-1}$  is given by:

$$x_{k+1}^{i} = Ax_{k}^{i} + B\bar{u}_{k-1} + K_{l}\hat{v}_{k}, \tag{17a}$$



$$\bar{u}_k = -C_l x_l^i + a \bar{u}_{k-1} + \hat{v}_k.$$
 (17b)

*Proof:* Consider the controller given in (7b). Since the controller state is (asymptotically) equal to the plant state, (7b) can be rewritten as:

$$u_k = C_c x_k + D_c y_k + a u_{k-1}. (18)$$

Using the plant model (3a) and (3b):

$$u_k = C_c x_k + D_c (C x_k + \varepsilon_k) + a u_{k-1}. \tag{19}$$

Further, (19) can be seen as a system with process noise

$$K\varepsilon_k \sim \mathcal{N}(0, \sigma_c^2 K K^T),$$
 (20)

and measurement noise

$$D_c \varepsilon_k \sim \mathcal{N}(0, \sigma_{\varepsilon}^2(D_c)^2).$$
 (21)

The process noise and measurement noise have cross-covariance given by  $\sigma_{\varepsilon}^2 K D_c^T$ .

This system can be rewritten in innovations form as:

$$x_{k+1}^{i} = Ax_{k}^{i} + B\bar{u}_{k-1} + K_{l}\hat{v}_{k}, \tag{22a}$$

$$\bar{u}_k = (C_c + D_c C)x_k^i + a\bar{u}_{k-1} + \hat{v}_k,$$
 (22b)

where  $K_l$  is the Kalman gain and  $v_k$  is the new innovation sequence. Note that, (22a) and (22b) describe the dynamics of the inverse filter  $\frac{1}{1+L}$  with input  $v_k$  and output  $\bar{u}_k$ . The filter (1+L), is therefore given by:

$$x_{k+1}^{l} = Ax_{k}^{l} + Bu_{k-1} + K_{l}(u_{k} - (C_{c} + D_{c}C)x_{k}^{l} - au_{k-1}),$$
(23a)

$$v_k = u_k - (C_c + D_c C)x_k^l - au_{k-1}.$$
 (23b)

This result proves (12a) and (12b). Further, since  $v_k$  is an innovations sequence, it is i.i.d with minimal variance. The inverse filter, as stated in (17a), (17b) is given by (22a) and (22b) respectively. This concludes the proof of of Lemma-2.

### C. DESIGN OF QUANTIZATION GAIN ( $\lambda$ )

Following assumption is made while designing the quantization gain  $\lambda$ .

Assumption 4: Let v(t) be the signal to be  $\Delta$ -modulated and V(f) denote its spectrum. It is assumed that the signal v(t) is bounded and its spectrum V(f) is band-limited i.e  $|v(t)| \leq V$  that V(f) = 0,  $\forall f > \mathcal{B}_v$  where, V and  $\mathcal{B}_v$  are known positive constants [45]. Further it is assumed that the sampling rate of the  $\Delta$ -M,  $f_s$  satisfies the relation

$$\frac{f_s}{2\mathcal{B}_v} > 2^{\alpha},\tag{24}$$

where  $2B_v$  is the Nyquist frequency of the signal v(t),  $\alpha$  is the over sampling ratio ( $\alpha \in \mathbb{R}^1$ ).

Remark 2: At the demodulation side, the signal-to-noiseand-distortion ratio (SNDR) is significantly improved by doubling the sampling rate  $f_s$ .

Remark 3: In standard practice, over sampling rate  $f_s$  is chosen such that  $\alpha > 5$ .

Lemma 3 [45]: Consider the signal v(t) which satisfies assumption-4. Let  $\dot{v}(t)$  be its derivative. Then,

$$|\dot{v}(t)| \le 2\pi \mathcal{B}_{v} \mathcal{V}, \quad \forall t \in \mathbb{R}^{+}.$$
 (25)

Further, let  $v_k$  denote the discrete samples of v(t) and  $f_s$  is the over-sampling frequency of  $\Delta$ -M which satisfies (24). Then,

$$|\Delta v_k| \le \frac{\pi}{2^{\alpha}} \mathcal{V},\tag{26}$$

where  $\Delta v_k = v_{k+1} - v_k$ .

Stability of the proposed control scheme is dependent on the type of the quantiser and the associated gains. The range of the stability margin of the quantiser gain is derived in the following.

Lemma 4: If the quantiser input  $\bar{v}_k$  (shown in Figure-2) satisfies the condition,  $|\bar{v}_{k+1}| \leq |\bar{v}_k|$ , and

$$\sup_{k>0} |\Delta \nu_k| < \lambda < \infty, \tag{27}$$

then.

$$|\bar{\nu}_{\infty}| \le 2\lambda < \beta,\tag{28}$$

where  $\beta$  is a positive constant and  $\lambda$  is the quantisation gain. *Proof:* From Figure-2, the dynamics  $\bar{v}_k$  can be expressed as:

$$\bar{v}_{k+1} = v_{k+1} - \hat{v}_{k+1}, 
= v_{k+1} - \hat{v}_k - \lambda \operatorname{sgn}(\bar{v}_k), 
= v_{k+1} - v_k + \bar{v}_k - \lambda \operatorname{sgn}(\bar{v}_k), 
= \bar{v}_k + \Delta v_k - \lambda \operatorname{sgn}(\bar{v}_k).$$
(29)

For a  $\Delta$ -M, it has been shown that the system is stable if  $|\bar{v}_{k+1}| \leq |\bar{v}_k|$  [32]. Let us investigate using (29), the behaviour of the trajectory for two possible cases:  $\bar{v}_k > 0$  and  $\bar{v}_k \leq 0$ .

When  $\bar{v}_k > 0$ :

$$1\bar{v}_{k+1} = \Delta v_k + \bar{v}_k - \lambda,$$
  
$$< \bar{v}_k < 2\lambda.$$

Similarly, when  $\bar{v}_k \leq 0$ , then:

$$1\bar{v}_{k+1} = \Delta v_k + \bar{v}_k + \lambda,$$
  
>  $\bar{v}_k > 2\lambda.$ 

From Lemma-3, it can be seen that  $\Delta v_k$  is function of over-sampling frequency. From the above two cases  $|\bar{v}_k|$  is bounded such that  $|\bar{v}_k| < 2\lambda$ . Further, as  $k \to \infty$ ,  $\bar{v}_k$  will be bounded as:

$$|\bar{v}_{\infty}| \leq 2\lambda < \beta$$
.

This completes the proof of Lemma-4.  $\Box$  In order to find the bound of  $\bar{v}_k$ , the following lemma is presented [45].

Lemma 5 [45]: The closed loop system (5) exhibits quasi-sliding motion from any arbitrary initial value if,

$$\lambda > \frac{\pi \mathcal{V}}{2^n},\tag{30}$$



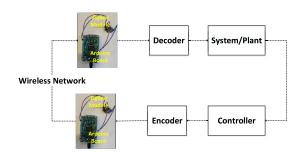


FIGURE 3. Networked control using ZigBee protocol based communication network.

and the switching function  $\bar{v}_k$  converges to a boundary layer  $\Omega(|\bar{v}_k| \leq \Omega)$  in finite time.

Remark 4: When  $\bar{v}_k$  is bounded such that  $|\bar{v}_k| \leq \Omega$ , the trajectory of  $\bar{v}_k$  will exhibit periodic (zig-zag) behaviour [45]. The period can change depending on the choice of quantisation gain  $\lambda$  and sampling time  $T_s$ .

Remark 5: When  $\bar{v}_k$  exhibits a zig-zag behaviour, then  $v_k$  and  $\hat{v}_k$  also exhibit similar behaviour.

Remark 6: If  $v_k$  is a constant signal,  $\Delta v_k = 0$ . Then, from (29),

$$\bar{v}_{k+1} = \bar{v}_k - \lambda \operatorname{sgn}(\bar{v}_{k-1}).$$

By iterating this k-1 times gives

$$\bar{v}_k = \bar{v}_0 - \lambda \sum_{i=0}^{k-1} \operatorname{sgn}(\bar{v}_i).$$

This implies that  $\bar{v}_k$  has a period of k-1.

#### **V. RESULTS**

The effectiveness of the proposed controller design and implementation framework in a networked environment is demonstrated, considering two examples under the effects of three types of noise (i.e. process noise, measurement noise, and quantisation noise).

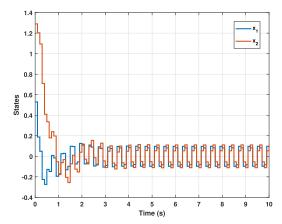
#### A. ZigBee BASED NETWORKED CONTROL SYSTEM

The block diagram of the networked control system is shown in Figure-3 where the network is implemented using the ZigBee protocol. Note that the plant, the controller, the filters and the modulators run in MATLAB/Simulink environment. The modulated signal is transmitted to the Zigbee module in Arduino board 2 from the Zigbee module in Arduino board 1. This board is connected to the computer and with the plant, the controller, the filter and the quantiser. The Zigbee module in Arduino board 2 acts as a hop device in another computer which transmits the signal back to the Zigbee module in Arduino board 1 which then transmits the signal into the demodulator.

This study follows the common practice, reported in the literature, where the wireless communication channel is implemented only on one side of the networked control system to transmit the control signal from the controller node to the system. During the simulations, it is found

**TABLE 1. Data packet format.** 

Name	Length(bytes)
Header	2
Source Port	1
Destination Port	1
Receiver Address	2
Data	$\mu$
Packet End	1



**FIGURE 4.** Behaviour of states  $x_1$  and  $x_2$  with time for  $\lambda = 0.2$  and  $\delta_1 = 0.1$ .

out that the transmission delay  $\tau$  of this network equals to 0.02 seconds.

The format of the transmitted data packet using the ZigBee protocol is given in Table-1. Note that the length of the information in the transmitted data packet is assumed to be  $\mu$  bytes. For each control action, the total length of each data packet  $(\Upsilon)$ :

$$\Upsilon = \mu + 7$$
.

The average transmission time T is given by:

$$T = \frac{8 \times \Upsilon}{\eta},\tag{31}$$

where  $\eta$  is the average rate of radio transmission of the ZigBee module. The average energy E consumed by each transmission is calculated as:

$$E = \upsilon \times \iota \times T, \tag{32}$$

where  $\upsilon$  and  $\iota$  denote respectively the operating voltage and the current. The values of the parameters of the  $\Delta$ -M and ZigBee module used in this study are:  $\Upsilon=8; \eta=250 \text{ K}$  bits/s;  $T=256 \text{ ms}; \upsilon=3.3\text{V}; \iota=0.3\text{A}; E=25.3\text{mJ}.$ 

#### **B. NUMERICAL EXAMPLE**

The dynamics of the first simulated system is given by [3]:

$$x_{k+1} = A x_k + B_c \bar{u}_k + K \varepsilon_k, \tag{33a}$$

$$y_k = C x_k + \varepsilon_k, \tag{33b}$$

where,

$$A = \begin{bmatrix} 0.9 & -0.1 \\ 0.7 & 0.8 \end{bmatrix}, \quad B = \begin{bmatrix} 0.8 \\ 1.0 \end{bmatrix},$$



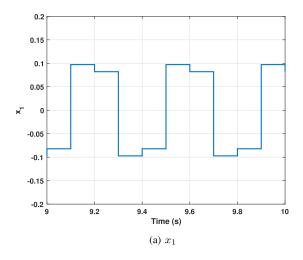


FIGURE 5. Zig-zag behaviour of states.

TABLE 2. Signal-to-noise-ratio of Example 1.

Case	SNR (dB)
Case 1	21.0690 dB
Case 2	21.1904 dB
Case 3	21.4342 dB

$$C = \begin{bmatrix} 1.0 & 0 \end{bmatrix}, \quad K = \begin{bmatrix} 1.0 \\ 1.0 \end{bmatrix}.$$

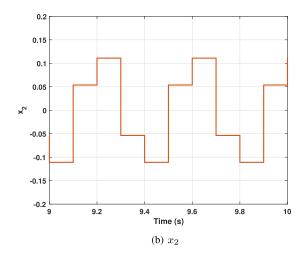
This linear time-invariant discrete-time system is stable, minimum phase and has relative degree d=1. In this article, results are shown/compared considering three cases.

- Case1: With the Δ-Modulator and Zigbee communication network.
- 2) Case2: With a  $2^p$ -level quantizer.
- 3) Case3: With ideal communication channel.

For case 1, the design of the controller, the filter and the quantisation gain is carried out following the procedures described in the previous sections. Note that the initial conditions  $x_0$  of the the plant is  $x_0 = [0.7, 1]^T$ . For this example, innovation variance is taken as 0.1, and the response of both the states are shown in Figure-4. Note that quantisation gain and sampling time are chosen to fulfil Lemma-3 and Lemma-4.

Results of case 2 have been shown in [3]. Results of the case 3 is shown in Figure-6 and Figure-7.

From case 1, it can be seen from the Figure-4 that all the states converge into a region  $\Omega$  within finite time and stay within that region indefinitely. This is further obvious from Figure-5, where the response of the states is of zig-zag nature as described in Lemma-5. This is expected due to the presence of the quantiser. Also the period of the zig-zag motion for both states are 4 (see Figure-5). As discussed in Remark-4, this period is dependent on the value of the quantisation gain and the sampling frequency, and it is, therefore, possible to get different period with other values of quantisation gain and sampling frequency. From, Table-2, it can be observed that signal-to-noise-ratio (SNR) values are similar.



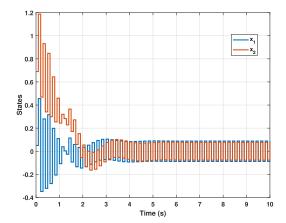


FIGURE 6. Behaviour of states for case 3.

### C. FREQUENCY CONTROL OF A SINGLE AREA POWER SYSTEM

The second example considers the load frequency control problem of a single area power system. The block diagram of this system is shown in Fig-8.

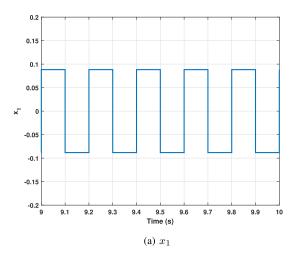
The dynamics of this system in state-space form is expressed as [46]:

$$\dot{x}(t) = F x(t) + G u(t) + H \Delta P_d, \tag{34a}$$

$$y(t) = Lx(t), (34b)$$

where,

$$F = \begin{bmatrix} -\frac{1}{T_p} & \frac{K_p}{T_p} & 0 & 0\\ 0 & -\frac{1}{T_t} & \frac{1}{T_t} & 0\\ -\frac{1}{RT_g} & 0 & -\frac{1}{T_g} & -\frac{1}{T_g}\\ K_i & 0 & 0 & 0 \end{bmatrix} G = \begin{bmatrix} 0\\ 0\\ \frac{1}{T_g}\\ 0 \end{bmatrix}$$



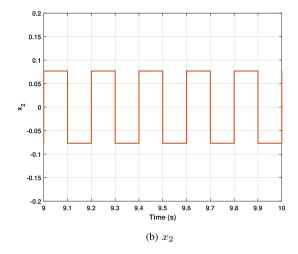


FIGURE 7. Zig-zag behaviour of states for case 3.

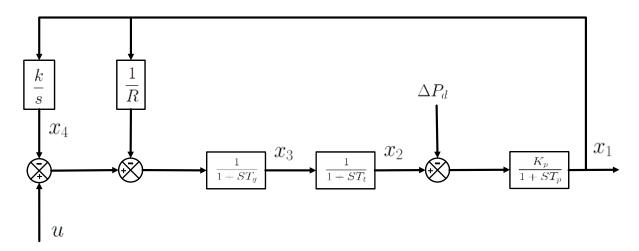


FIGURE 8. The block diagram of a single area power system.

$$H = \begin{bmatrix} -\frac{K_p}{T_p} & 0 & 0 & 0 \end{bmatrix}^T L = \begin{bmatrix} 1 & 0 & 0 & 0 \end{bmatrix}.$$

and  $\Delta P_d$  is the load disturbance. This is equivalent to the innovations sequence  $\varepsilon_k$  used in (3).

The discrete equivalent of (34) at sampling time of  $\delta_1$  = 0.02 seconds (using Euler discretisation and Lemma-5), is given by:

$$A = \begin{bmatrix} 1 - \frac{\delta_1}{T_p} & \frac{\delta_1 K_p}{T_p} & 0 & 0 \\ 0 & 1 - \frac{\delta_1}{T_t} & \frac{\delta_1}{T_t} & 0 \\ -\frac{\delta_1}{RT_g} & 0 & 1 - \frac{\delta_1}{T_g} & -\frac{\delta_1}{T_g} \\ \delta_1 K_i & 0 & 0 & 1 \end{bmatrix} B = \begin{bmatrix} 0 \\ 0 \\ \frac{\delta_1}{T_g} \\ 0 \end{bmatrix}$$

$$K = \begin{bmatrix} -\frac{\delta_1 K_p}{T_p} & 0 & 0 & 0 \end{bmatrix}^T C = \begin{bmatrix} 1 & 0 & 0 & 0 \end{bmatrix}.$$

**TABLE 3.** Parameters for single area power system.

Parameter	Value	Unit
$T_p$	20	seconds
$T_t$	0.3	seconds
$T_q$	0.08	seconds
$K_p$	120	Hz/Mw p.u.
$ \hat{K_i} $	0.6	Hz/Mw p.u.
R	2.4	Hz/Mw p.u.

The values of various parameters of the power system, used in this study, are shown in Table-3. The main objective of the load frequency controller is to ensure zero steady-state error (i.e. the deviation of the frequency from the nominal value should be zero) when the system is subjected to load disturbance. Note that the frequency of the power system is governed by the well known P-f mechanism [47] which states that a mismatch in the real power demand and generation would affect the frequency of the system. For example, when the load in the system increases/decreases, the frequency of the system decreases/increases from its nominal value.



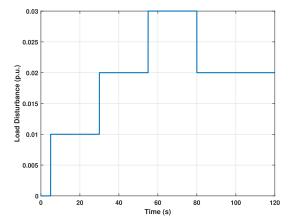


FIGURE 9. Change of load disturbance with time.

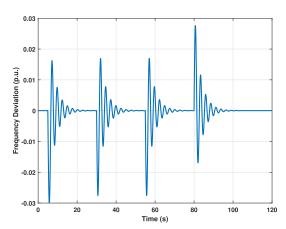


FIGURE 10. Frequency change with load disturbance.

To control these variations in frequency, sophisticated controllers are required, which determine the amount of mechanical input (e.g steam) to reduce the frequency deviations (from nominal value) to zero.

The power system is often subjected to a time-varying load disturbance shown in Figure-9. For case 1, the quantized controller is designed following the procedures discussed in earlier sections. The frequency deviation under load disturbances (Figure-9) is shown in Figure-10 for quantisation gain  $\lambda=0.01$  which is chosen according to Lemma-4.

For case 2, frequency deviation under load disturbances is shown in Figure-11. Case 3 results are shown in [46], hence not included in this manuscript.

It can be seen from the Figure-10 that the output (frequency deviation) converges towards zero within finite time (settling time) under time-varying load disturbances. Also, it is evident that overshoot of the networked system is within industry-accepted limits (i.e. less than 5% p.u.). The output variable, i.e. frequency deviation converge into a region  $\Omega$  within finite time. From, Table-2, it can be observed that SNR values are similar for all three cases.

From both the examples, it can be seen that proposed architecture works well with the ZigBee protocol based wireless communication channel under various network constraints

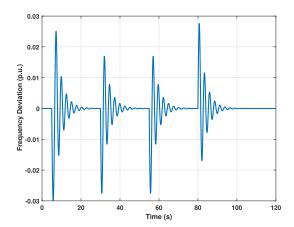


FIGURE 11. Frequency change with nearest neighbour quantiser (p = 3).

TABLE 4. Signal-to-noise Ratio of Example 2.

Case	SNR (dB)
Case 1	17.5320 dB
Case 2	17.6352 dB
Case 3	18.1397 dB

like packet loss, transmission delay, random packet delay etc. It is evident that single-bit control is sufficient to keep the system stable and required control is achieved within acceptable time limits.

#### VI. CONCLUSION

The single-bit output feedback controller is designed for linear networked systems where  $\Delta$ -M is used as 2-level quantiser. The step size (quantiser gain  $\lambda$ ) and oversampling frequency ( $f_s$ ) are determined which ensures the stability of the  $\Delta$ -M. The effectiveness of the control strategy is shown considering a real ZigBee protocol based communication network with inherent imperfections such as bit-rate constraints, packet losses, transmission delays and so on using two simulated examples.

#### **FUTURE RECOMMENDATIONS**

Due to the current advancements of communication technology, most of the current control schemes need to be reexamined for their effectiveness; if they are to be used for the networked environment. In this study, we focus on designing output-feedback controller for networked systems assuming that the system is linear and single-input-single-output. The extension of the proposed controller to multi-input-multi-output (MIMO) systems and nonlinear systems is immediate future research work. Further, the design of such controllers for event-trigger networked control systems may be a better method from the perspective of bandwidth utilisation and will be investigated in future and be reported separately.

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