Depairing Current at High Magnetic Fields in Vortex-Free High Temperature Superconducting Nanowires

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Abstract

Superconductors are essential in many present and future technologies, from largescale devices for medical imaging, accelerators, or fusion experiments to ultralow-power superconducting electronics.¹ However, their potential applicability, and particularly that of high-temperature superconductors (HTS), is severely affected by limited performances at large magnetic fields and high temperatures, where their use is most needed. One of the main reasons for these limitations are the presence of quantized vortices, whose movements result in losses, internal noise, and reduced performances.^{2,3} The conventional strategy to overcome flow of vortices is to pin them along artificial defects.^{3–7} Here we theoretically and experimentally demonstrate that critical-current density in high-temperature superconductors can reach unprecedented high values at high fields and temperatures, by preventing vortex entry. By tailoring the geometry, i.e. reducing the width, W, of nanowire-patterned HTS films, the range of the Meissner state, for which no vortices are present, is extended up to very large applied field values, on the order of ~ 1T. Current densities of the order of the depairing current can be sustained under high fields, for a wide range of temperatures. Results may be relevant both for devising new conductors carrying depairing-current values at high temperatures and large magnetic fields and for reducing flux noise in sensors and quantum systems.

The discovery of high-temperature superconductors hold great promise to revolutionize the applications of superconducting technologies, strongly reducing system operating costs. Still, the potential of HTS materials is far from being fully achieved. The fundamental limits of HTS performance are delimited by the depairing critical-current density, $J_{c,dp}$, upper critical magnetic field, $\mu_0 H_{c2}$, and critical temperature, T_c . In particular for $YBa_2Cu_3O_{7-\delta}$ (YBCO) cuprate superconductors, with characteristic coherence length, $\xi_0 = \xi(T = 0 \text{K}) \sim 1.5\text{-}2\text{nm}$, and penetration depth, $\lambda_0 = \lambda(0 \text{K}) \sim 150\text{-}200\text{nm}$, one obtains a Ginzburg-Landau depairing current, $J_{c,dp}(0K) \sim 100-300 MA/cm^2$, $\mu_0 H_{c2}(0K) > 100T$, and $T_c \sim 92 {\rm K}.^{2,31}$ However, energy dissipation in the mixed state, in which vortices penetrate the superconductor, severely limits their superconducting performance, thus becoming a critical issue in most practical applications. Motion of vortices under the Lorentz force determines the value of J_c , which is specially reduced at high temperatures, where thermal effects become very relevant, and high applied fields, which are the situations where HTS could offer more advantages.² An additional important fact is that most common HTS materials are thin strips (e. g. coated conductors), with very large demagnetizing fields. As a result, in practice any value however small of applied magnetic field perpendicular to the plane of the superconductor, results in vortex penetration. Enhancing current-density values in the mixed state has been a long-standing challenge. To reduce the J_c dependence on magnetic field, artificial vortex pinning sites are added to the superconductors, requiring a difficult optimization of the pinning landscape for the specific operating conditions (temperature, and magnetic field value and orientation).^{5–7} Moreover, vortex motion leads to flux noise that limits the operation of sensitive superconducting electronics⁹ and quantum applications.¹⁰ Therefore, achieving large currents in the superconductor in a vortex-free regime becomes crucial for cutting-edge applications for both large and small scales.

Reduction of the dimensionality in oxide functional materials has received growing scientific and technological interest due to the range of new properties and obtained functionalites, as compared with the bulk.^{12,13,15} Thanks to advances in high-resolution lithography techniques, it is now possible to fabricate complex nanostructures in epitaxial functional oxides, without damaging their physical properties.¹⁶ When a superconducting film is shrunk to mesoscopic length scales, both the supercurrent distribution and the properties of vortex matter are strongly influenced by the sample topology and size.^{17–19} In particular, there are two effects that may be specially relevant for technological applications.

On the one hand, the critical current density may be enhanced up to the depairing limit, by reducing the cross section to the characteristic lengths ξ and λ . This effect has been theoretically predicted in several studies? and experimentally observed in both conventional type II superconductors and high temperature superconductors. Some examples in conventional superconductors include Al,? Nb or Mo_{0.7}Ge_{0.3}? thin films, with thickness $t \sim 20$ - 50 nm, patterned with micrometric wires, $W \sim 1 - 2 \mu m$. Moreover, robust values of self-field $J_{c,dp}$, over the full superconducting temperature range, could also be achieved in complex structures consisting of two-dimensional Nb thin films (t=11nm) patterned with nanomeshes (34nm spatial periodicity). Although being a more challenging task, theoretical depairing currents at zero applied field can also be observed in high temperature superconductors. For example in YBCO nanowires with cross sections of 50x50nm², ascribed to current crowding effects^{22?} or (Ba,K)Fe₂As₂ microbridges ($t = 2 \mu m$) with nanoscale thickness ($t \sim 90 nm$).

On the other hand, geometry constrains are known to be responsible for vortex exclusion and rearrangement processes. It is theoretically predicted that vortices are completely expelled from a thin superconducting strip, of width W larger than the in-plane penetration depth, below a critical field $B_0 \sim 1/W^2$, that only depends on the width of strip with a pre-factor that slightly depend on the model considered.^{10,11,26} This effect have been experimentally proved in micrometric Nb and YBCO thin strips through vortex imaging techniques. In the first case, by reducing the width of the strip down to $W = 1.6 \ \mu m$ first vortex penetration was observed at $\sim 2 \mathrm{mT}$,¹⁰ whereas in the later vortex expulsion until \sim 0.1 mT was obtained in strips of $t = 6 \ \mu \text{m.}^{26}$ In both cases, vortex arrangement in single row at the centre of the strip was observed, when the applied field reached B_0 . These experimental al theoretical works anticipated the potential of nano-metric structures to eliminate noise linked to vortex motion up to high magnetic fields. First vortex penetration at fields of $\sim 1 \text{T}$ was experimentally demonstrated on W-based nanowires of t = 30 nm and W =50 nm,²⁴ by means of magnetoresistance measurements. In the same work recovery of the superconducting state at even large fields was reported due to vortex self-arresting processes, opining the path for the design of loss-less superconducting devices at high fields and temperatures. Magnetoresistance measurements were also used to study geometrical effects on the vortex lattice in high temperature superconducting nanowires.²³ All this studies were performed close to the irreversibility line by using low applied currents.

However, up to now, experimental evidence of the two effects - i.e., vortex-free state in a wide range of the H-T phase diagram , in nanowires sustaining large depairing critical current densities, has not been achieved yet. In this work provide experimental evidence that, by combining this two well known geometrical effects, in technologically relevant YBCO nanowires with properly adjusted dimensions, key $J_c(H)$ performances may be achieved. Nanowires of widths $W \sim 80\text{-}100\text{nm}$, and thickness, t = 150nm, show a basically temperature-independent $J_c(H)$ plateau, with critical current density values approaching particularly large YBCO depairing limit, up to $\mu_0 H_a \sim 1$ T. Such values of current at high fields are above those ever been achieved using conventional flux-pinning strategies. It is worth noting that the obtained results are not limited to a thin film geometry offering enhanced flexibility in the device design. We first present the theoretical basis of how a tailored geometry at the nanoscale leads to vortex exclusion up to high fields, and then experimentally confirm the validity of the proposed strategy in actual superconducting YBCO nanowires.

We consider a superconducting slab extending in the y direction, occupying the region -W/2 < x < W/2, with thickness $t \gg W$ along the z axis. A uniform external magnetic field $\mu_0 H_a$ is applied along the z direction. We use $\kappa = \lambda/\xi$, being λ and ξ the superconductor London penetration and coherence lengths, respectively. We assume $W \gg \xi$ and $\lambda \gg \xi$ $(\kappa \gg 1)$. In this geometry, for which demagnetizing effects are negligible $(t \gg W)$, it is energetically favourable to have vortices in the superconductor when the applied field reaches the first critical field $\mu_0 H_{c1}$, given by ²⁸

$$\mu_0 H_{c1} = \frac{\Phi_0}{4\pi\lambda^2} \left(\log \kappa + 0.081 - 2\sum_{n=1}^{\infty} (-1)^{n+1} K_0\left(\frac{Wn}{\lambda}\right) \right) \left(1 - \operatorname{sech}\left(\frac{W}{2\lambda}\right) \right)^{-1}, \quad (1)$$

where Φ_0 is the flux quantum, μ_0 is the vacuum permeability, and $K_i()$ are the modified Bessel functions of the second kind and *i*-th order. The expression for $\mu_0 H_{c1}$ has the limits

$$\mu_0 H_{c1}(W \ll \lambda) = \frac{2\Phi_0}{\pi W^2} \left[\log \left(1.78 \frac{W}{\pi \xi} \right) + 0.081 \right], \tag{2}$$

$$\mu_0 H_{c1}(W \gg \lambda) = \frac{\Phi_0}{4\pi\lambda^2} \left(2e^{-\frac{W}{2\lambda}} + 1 \right) (\log \kappa + 0.081).$$
(3)

For perfectly flat surfaces, surface barriers²⁹ prevent vortices entering the slab up to a larger superheating field, $\mu_0 H_s$, given by³⁰

$$\mu_0 H_s = \frac{\Phi_0}{2\pi\lambda^2} \cosh\left(\frac{W}{2\lambda}\right) \operatorname{csch}\left(\frac{1}{\kappa} - \frac{W}{2\lambda}\right) \sum_{n=0}^{\infty} \left[K_1\left(\frac{2Wn}{\lambda} + \frac{2}{\kappa}\right) - K_1\left(\frac{2W(n+1)}{\lambda} - \frac{2}{\kappa}\right)\right],\tag{4}$$

We plot in Fig. 1 as solid lines the dependence of $\mu_0 H_s$, $\mu_0 H_{c1}$, and the limiting expres-

sions upon the superconductor width W (normalized to λ_0), given by the above analytic expressions, considering typical ξ_0 and λ_0 values for YBCO. Interestingly, Eq. (2) shows that for widths W much smaller than λ the field $\mu_0 H_{c1}$ for vortex penetration depends basically as Φ_0/W^2 , which can reach values on the order of 1T for nanowires with sizes of tens of nanometers. Theory also indicates that $\mu_0 H_{c1}$ depends very little upon the temperature [only trough the factor ξ inside the logarithm in Eq. (2)].

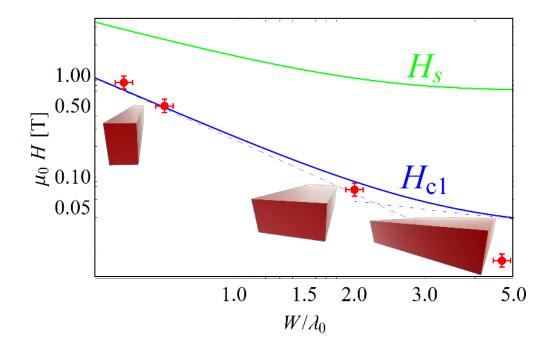


Figure 1: Magnetic fields for the entering of vortices: (blue) first penetration field $\mu_0 H_{c1}$ with the limiting expressions (dashed-blue); (green) supercooling field $\mu_0 H_s$. Red dots corresponds to the experimental values of $\mu_0 H_{crit}$. Figures depict the aspect ratio of different nanowires.

To experimentally confirm the theoretical predictions, we fabricate YBCO nanowires of different widths ranging from W=80-700nm, with a fixed length of 1µm and thickness of 150nm (see Fig. 2a and Methods). All patterned nanowires had values of $T_c \sim 87$ -92K, indicating no significant degradation of the bulk superconducting properties. Figure 2b shows the self-field critical-current density, $J_{c,sf}$, at 5K, for several micro and nanowires of different widths. A systematic enhancement of $J_{c,sf}$ is observed when reducing W, reaching values that approach the Ginzburg-Landau depairing current, $J_{c,dp}$, in the limit $W < 2\lambda_0$. Although many of narrow nanowires show very high values of $J_{c,sf}(5K) \sim 40{\text -}60 \text{ MA/cm}^2$, the reproducibility of samples sustaining critical-current density values close to the depairing is low. At these small dimensions, sample inhomogeneities (porosity, nano-precipitates, or amorphisation during the patterning process) may damage some of the properties and reduce the effective nanowire cross section.

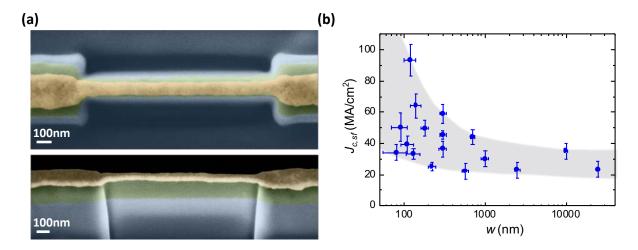


Figure 2: (a) False colour top-view (Top) and cross-section (Bottom) SEM image of a 80 nm-width, 150nm-thick nanowire. (b) $J_{c,sf}$ at 5K for several YBCO micro and nanowires of different width.

The magnetic field dependence of $J_c/J_{c,sf}$ at 65K for nanowires of different widths is presented in Fig. 3a. Results for a wider microwire of $W = 30\mu$ m are added for comparison. For all nanowires J_c is constant up to a critical magnetic field value, $\mu_0 H_{crit}$. There is a systematic enhancement of $\mu_0 H_{crit}$ with reducing W, with a plateau extended up to $\mu_0 H_{crit} \sim 1$ T for nanowires with W = 80-100nm. In macroscopic systems, the J_c plateau is associated to a single-vortex pinning regime dominated by vortex-defect interactions, crossing over to a collective regime above $\mu_0 H_{crit}$ when vortex-vortex interactions become important.^{32,33} In those samples, $\mu_0 H_{crit}$ decreases with increasing the temperature due to thermally activated processes.³⁴ However, in our nanowires the plateau can unambiguously be associated to the absence of vortices in the superconductor. A first evidence can be seen in Fig. 1 where we

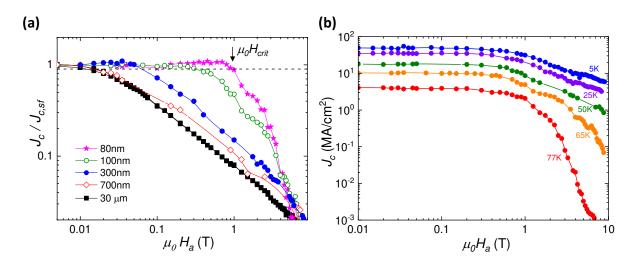


Figure 3: (a) Normalized critical-current density as a function of the applied magnetic field for nanowires of different widths and a microwire at 65K. Dashed line shows the criteria used to determine $\mu_0 H_{crit}$ (depicted with an arrow for the nanowire of 80nm). (b) Applied-field dependence of $J_c(H_a)$ obtained for a nanowire of W=100nm at different temperatures.

plot the experimental values of $\mu_0 H_{crit}$ for the nanowires with different widths W together with the theoretical prediction for the field of vortex penetration. The experimental values match very well the theoretical $\mu_0 H_{c1}$ dependence; remarkably, this is a zero-parameter fit, since theoretical values only depend upon experimentally determined quantities. Only the experimental point at $W/\lambda_0 \sim 5$ (W = 700nm) departs from the expected line because in this case the original assumption of $t \gg W$ does not hold. Theory also predicts that if the smoothness of the nanowires surfaces is eventually improved, one could raise the field of penetration further up to values close to the superheating field values, $\mu_0 H_s$ in Fig. 1.

A further confirmation that vortices are absent in our nanowires comes from confirming the temperature independence of $\mu_0 H_{crit}$ predicted by the theory. In Fig. 3b it is seen that $\mu_0 H_{crit}$ for a nanowire with W = 100nm remains basically constant in a large temperature window from 5K to 77K. The temperature-independence of $\mu_0 H_{crit}$ is also evidenced in Fig. 4a, where $\mu_0 H_{crit}(T)$ obtained for nanowires of different widths and a microwire with W = 30μ m is plotted as a function of temperature. $\mu_0 H_{crit}$ remains essentially constant throughout all temperature range for all nanowires, whereas for the microwire $\mu_0 H_{crit}$ monotonically drops with the temperature, as expected considering thermally activated vortex motion.^{32,33} This confirms that vortices do not penetrate in our superconducting nanowires until the applied field reaches the basically temperature-independent $\mu_0 H_{crit}$ [Eq. (2)].

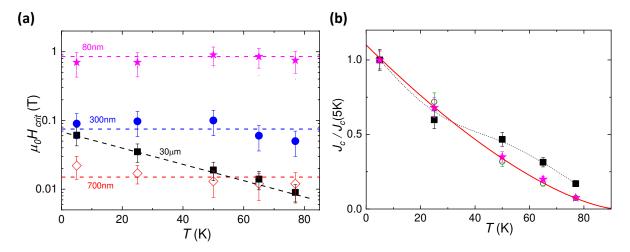


Figure 4: (a) Temperature dependence of $\mu_0 H_{crit}$ for different nanowires and a microwire. Dashed lines are guide for the eye illustrating the $\mu_0 H_{crit}(T)$ dependence. (b) Temperature dependence of self-field current density $J_{c,sf}$ normalized to its value at 5K, for two nanowires with W=80nm (pink stars) and 100nm (green open circles), and a microwire with $W=30\mu$ m (black squares). Solid red line shows the temperature dependence of the depairing current density, $J_{c,dp}$. Dashed black line is a guide for the eye

Figure 4b shows the self-field J_c , normalized to its value at 5K, for two nanowires with W = 80nm and 100nm and, for comparison, a microwire with $W = 30\mu$ m. The experimentally observed temperature dependence of both nanowires can be well fitted with the Ginzburg-Landau depairing current temperature dependence, $J_{c,dp} \sim 1/(\lambda^2(T)\xi(T))$, providing evidence that critical-current density is achieving the theoretical depairing values. In contrast, the J_c temperature dependence for the microwire, governed by vortex pinning, displays a different pattern.

Our results may have important consequences in practical applications. On the one hand, the fact that no vortices are present in our nanowires makes them excellent candidates to use them in noise-sensitive devices such as SQUIDs, superconducting qubits or sensors.^{9,10} On the other hand, the very large values of current density achieved at high applied fields and temperatures may be also relevant at large scale, with potential to improve the performance of actual thin strips (e. g. coated conductors). Large currents could eventually obtained by densely packing such nanowires, which may have large thickness, with barely any magnetic interaction among them. In conclusion, we have provided a novel avenue towards achieving unprecedentedly high values of currents at high field and temperatures in superconductivity applications, by tailoring the geometry to prevent vortex penetration rather than the conventional strategy of pinning vortices by defects.

Methods

Superconducting nanowires were fabricated by a top-down approach. The initial system consists of a c-axis oriented $YBa_2Cu_3O_{7-\delta}$ 150nm-thick film grown on LaAlO₃ substrate via Chemical Solution Deposition methodology.³⁵ Silver contact pads were sputtered on the thin films and annealed under oxygen atmosphere at 450° C for 1 hour to ensure good electrical contacts with resistances below 10 $\mu\Omega$. Photolithography and chemical etching were used to define microwires of width W=25 μ m and length L=100 μ m, allowing current-voltage, I-V, characteristic measurements with the standard four-point method. Patterned microwires were protected with a 50nm gold layer grown by sputtering. The width of the microwires was finally reduced to the nanoscale by using Focused Ion Beam (FIB) [Zeiss 1560XB Cross Beam] lithography, following the process described in,³⁶ enabling to obtain nanowires with good superconducting performances. In field transport measurements were conducted in a commercial 9T Physical Property Measurement System (PPMS). The magnetic field was always applied parallel to the c-axis of the superconductor in a maximum Lorentz Force configuration. Critical temperature, T_c , was measured by recording the voltage under $10\mu A$ current. Critical-current density, J_c , was obtained by means of current bias transport measurements and using a 200nV-criteria. The critical magnetic field value $\mu_0 H_{crit}$ discussed below was determined at $0.9J_{c,sf}$.

The theoretical formulas for the penetration fields in a type-II superconducting slab used in the text have been derived following analogous procedures as those by $Abrikosov^{28}$ and Shmidt,³⁰ which dealt with the case of the slab in an applied magnetic field. In our case of transport currents, since the experimental values of currents have been on the order of ~ 10mA, they generate a self-field of ~ 2mT, much less than the involved $\mu_0 H_{c1}$ or $\mu_0 H_s$ values. For this reason, the field relevant for studying penetration has been considered to be the applied field and not the self-field of the transport currents.

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Graphical TOC Entry

