

# Design and Determination of Optimum Coefficients of IIR Digital Highpass Filter using Analog to Digital Mapping Technique

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## ABSTRACT

In Digital Signal Processing (DSP), there are two types of filters used to perform the filtering operations and they are the Infinite Impulse Response (IIR) filter and the Finite Impulse Response (FIR) filter. The present output sample of an IIR filter depends on the present input samples, past input samples and past output samples, i.e. IIR filter is of recursive type. Now to design the Digital IIR filter, the coefficients are essentially required. There are a number of techniques available for designing the IIR filter. In this paper, the design and determination of the IIR filter coefficients are introduced using a computer-based approach. The program based on the algorithm proposed in this paper is simulated in Matlab which provides with the satisfactory results.

**Keywords:** IIR filter, Digital filters, coefficient, High pass filter, Butterworth filter, Chebyshev filter, analog-to-digital mapping.

## 1. Introduction

In Digital Signal Processing (DSP) the filters are very essential because they are practically introduced to filter out the desired frequency as per requirement to be used in different areas of interest, for example Communication Engineering i.e. to process the desired signal from one point to another. In that case, the total band of frequency is transmitted through the transmitter and the user selects the desired frequency to get the particular information, for example Radio receivers. Now to select the proper frequency band, the filter is used [1][2][3].

In DSP, there are typically two systems. The first one, i.e. the Digital filters, which performs the filtering operation in the time domain [4][5][7]. The second one is the Spectrum analyzer, which represents the signal in the frequency domain [4][5][21]. The impulse response of an IIR filter is of infinite duration whereas the impulse response of a FIR filter is of finite duration. The response of an IIR filter is much better than that of a FIR filter having the same number of coefficients [8][16].

There are certain properties of IIR filter such as pass-band width, stop-band width, maximum allowable pass-band and stop-band ripples [4][5][8][16]. Generally the filters are designed with resistors, capacitors and op-amps to get the desired filtering operation because the op-amp is

able to show the recursive effect and so it is very useful in designing the IIR filter as it is actually a recursive filter. Whereas the FIR filter is a non-recursive filter [5][8][16]. The IIR filter that is designed with resistors, capacitors and op-amp is actually an analog filter. That is the transfer function we will get from this type of filter is the transfer function in the analog plane. The digital filter has several features such as high accuracy and reliability, small physical size and reduced sensitivity to component tolerances or drift [4][9][13]. So to get the transfer function in the digital plane or in other words to design a digital filter from the pre-designed analog filter, we need to introduce the frequency transformation. So for this purpose, the analog to digital frequency transformation technique is applied, it is called the analog to digital mapping technique [4][5][6][7].

## 2. Design of IIR Filter

The IIR filter can be designed by active or passive elements as described in the introductory part. Now there are many types of IIR filters such as Butterworth filter, Chebyshev filter, Elliptic filter etc [4][8][14][15]. The design of the IIR filters are shown from Fig.1 to Fig.2 [4][9][10].

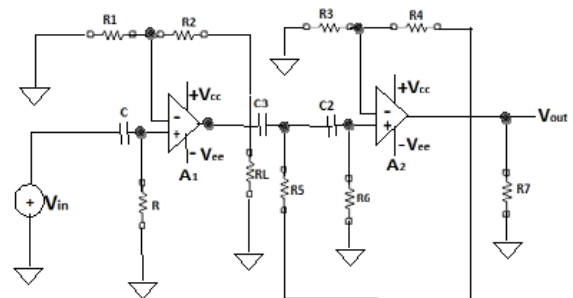


Fig.1 IIR High pass Butterworth Filter

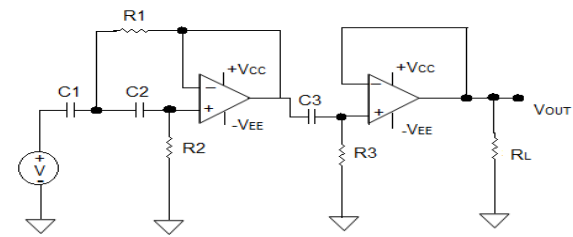


Fig.2 IIR High pass Chebyshev Filter

In DSP, in case of filter design, when the components like resistors, capacitors etc are connected, this is referred to as a passive filter until and unless the active components like voltage source is connected. When a voltage source is connected with that passive filter circuit, the filter becomes a active filter[9][11]. At the time of observation of the output response of filter, the order of FIR filter is higher than that of the IIR filter for the same magnitude response. So to reduce the order of a filter and to reduce the signal processing time, it is important to design the IIR filter rather than designing FIR filter[8].

Fig.1 and Fig.2 shows the design of Butterworth filter and Chebyshev filter respectively. Basically the available filter orders are 1<sup>st</sup> order filter and 2<sup>nd</sup> order filter. Now by cascading these two, the several high order filters can be designed. For example, if the 9<sup>th</sup> order filter will have to be designed, the fashion will be the cascading of four 2<sup>nd</sup> order filter and one 1<sup>st</sup> order filter[9]. The higher the order of a filter, the better in the response.

After designing the analog filter, the pole-zero locations as well as the stability of the filter must be determined. In that case, the analog filter must be converted to an equivalent digital filter with the application of analog-to-digital mapping technique. There are many methods for this mapping, for example Bilinear transformation, Impulse invariance method[8][16][21]. After successful mapping, the transfer function of the digital filter can be obtained or in other words the filter coefficients can be calculated which is one of the most important for digital filter design[4][5][18][20]. In case of analog filter, the transfer function can be found in s-plane but for digital filter, the transfer function is obtained in z-plane[10][11].

### 3.Realization of IIR Digital Filter

IIR Filter has a network or system which is of recursive type. Such recursive system can be described by the following Difference equation where  $x(n)$  is the input of the system and  $y(n)$  is the output of that system[8][16][17],

$$y(n) = -\sum_{k=1}^N a_k y(n-k) + \sum_{k=0}^M b_k x(n-k) \dots \dots (1.1)$$

$$y(n) = -a_1 y(n-1) - a_2 y(n-2) \dots \dots - a_{N-1} y(n-N+1) - a_N y(n-N) + b_0 x(n) + b_1 x(n-1) \dots \dots + b_M x(n-M) \dots \dots (1.1a)$$

Let,

$$w(n) = b_0 x(n) + b_1 x(n-1) \dots \dots + b_M x(n-M) \dots \dots (1.1b)$$

Equation (1.1a) can be represented as,

$$y(n) = -a_1 y(n-1) - a_2 y(n-2) \dots \dots - a_{N-1} y(n-N+1) - a_N y(n-N) + w(n) \dots \dots (1.1c)$$

Now taking the Z-Transform in the both sides of equation (1.1a), we get

$$Y(z) = -a_1 z^{-1} Y(z) - a_2 z^{-2} Y(z) \dots \dots - a_{N-1} z^{-(N-1)} Y(z) - a_N z^{-N} Y(z) + b_0 X(z) + b_1 z^{-1} X(z) \dots \dots \dots + b_M z^{-M} X(z) \dots \dots (1.2)$$

Rearranging the equation (1.2), we get,

$$Y(z) = -(a_1 z^{-1} + a_2 z^{-2} \dots \dots + a_{N-1} z^{-(N-1)} + a_N z^{-N}) Y(z) + (b_0 + b_1 z^{-1} \dots \dots + b_M z^{-M}) X(z) \dots (1.2a)$$

So, from the Equation (1.1a) it is clear that in a IIR system, the present output depends upon the past inputs and the past outputs. Now taking the Z-Transform of Equation (1.1a) the structure of the IIR Filter can be determined with the application of Direct form-I Realization. This method is used to separate delays for both input and output. This realization requires  $M+N+1$  multiplications,  $M+N$  additions and  $M+N+1$  memory locations. There are various form of realizations[8] of an IIR Filter such as,

1. Direct form - I realization
2. Direct form - I realization
3. Transposed direct form realization
4. Cascade form realization
5. Parallel form realization
6. Lattice-Ladder structure realization

The structure of Direct form – I realization for an IIR Filter described in equation (1.1a) is shown in Fig.3[ 8][16][19].

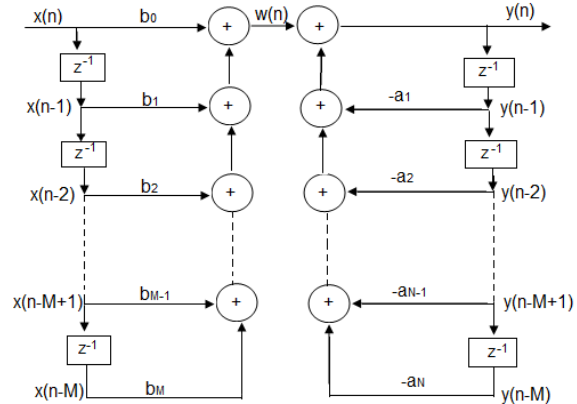


Fig.3 Direct Form-I realization of IIR Digital filter

### 4.Determination of the Transfer Function

The impulse response is actually the ratio of the produced output to the given input, that is more elaborately, the transfer function  $H(z)$  is actually the ratio of the Z-Transform of output to the input.

The impulse response  $h(n)$  for a realizable filter is[8][16],

$$h(n) = 0 \quad \text{for } n \leq 0 \quad \dots \dots (1.3)$$

where  $h(n)$  must satisfy the following condition to ensure the stability of a filter[8][16],

$$\sum_{n=0}^{\infty} |h(n)| < \infty \quad \dots \dots (1.4)$$

Now, the Z-Transform of the impulse response  $h(n)$  i.e.  $H(z)$  is referred to as the transfer function of a digital filter. So, our interest goes to the determination of the  $H(z)$  because the  $H(z)$  contains the numerator and denominator

coefficients which are essentially required to design the Digital filter.

Now to determine the transfer function of the IIR filter, we rewrite the equation (1.2a) and get the required H(z) equation i.e.,

$$H(z) = \frac{\sum_{n=0}^M b(n)z^{-n}}{1 + \sum_{n=1}^N a(n)z^{-n}} \dots \dots (1.5)$$

$$= \frac{B(z)}{A(z)} = \frac{b(0)+b(1)z^{-1}+b(2)z^{-2}+\dots\dots+b(M)z^{-M}}{1+a(1)z^{-1}+a(2)z^{-2}+\dots\dots\dots+a(N)z^{-N}} \dots \dots (1.5a)$$

Where,

- b(n) = Numerator coefficient of the filter
- a(n) = Denominator coefficient of the filter
- H(z)= Y(z) / X(z) = B(z) / A(z) = Transfer function

After successful determination of the transfer function, we can calculate the coefficients of the filter i.e. the determination of b(n) and a(n). Actually the Transfer Function refers that for giving some input how much output can be obtained. There are various techniques available to design and calculate the coefficients of an IIR Digital high pass filter. The algorithm referred here can be used for smarter determination of IIR Digital high pass filter and is much more helpful for the determination of the transfer function as well as the coefficients. The flowchart is given in Fig.4.

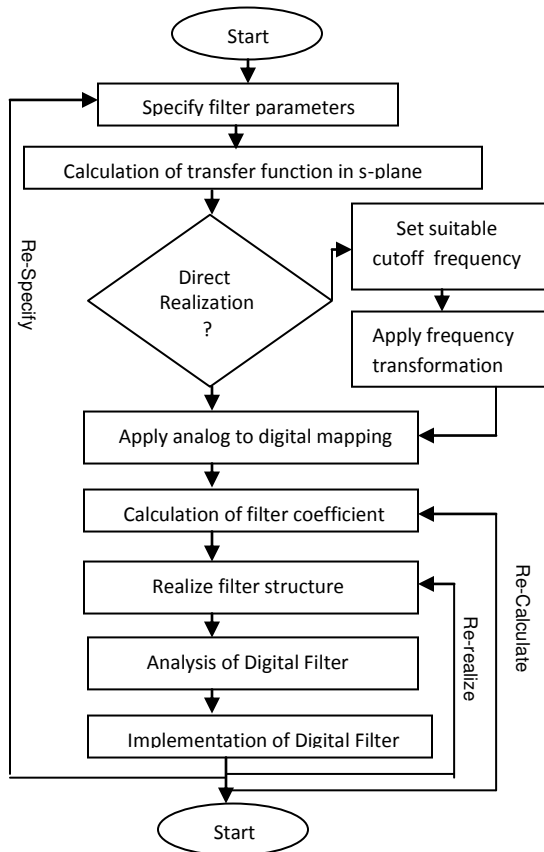


Fig.4 Proposed algorithm

Now, applying the proposed algorithm and specifying the necessary parameters, the designer can suitably design the IIR Digital high pass filter. The algorithm has a special feature that it provides the user with two techniques of realization of IIR Digital high pass filter

1. Direct realization
2. Indirect realization

In indirect realization[4], based on the specified cutoff frequency, the IIR Digital high pass filter can be designed by using the frequency transformation technique.

In the direct method of realization[4], first the transfer function of an analog filter is to be calculated, i.e. the transfer function in s-plane. Then by applying the analog-to-digital mapping technique, the transfer function in z-plane is obtained. Now, the transfer function that is obtained in z-plane is actually the transfer function of the digital filter.

So, first we design the network for the IIR Digital high pass filter, then realize the structure of that and after that by applying the analog-to-digital mapping technique i.e. s-plane to z-plane transformation, we finally achieved the transfer function in z-plane or in other words, the transfer function of the predesigned IIR Digital high pass filter.

### 5. S-plane to Z-plane transformation

S-plane to z-plane transformation simply means that analog-to-digital mapping where the stable poles of s-plane can be successfully mapped into z-plane. Fig.1 shows a IIR filter network. If we perform the Laplace transform on the impulse response h(t) of that network, we get the transfer function H(s) in s-plane i.e.[8][10] ,

$$H(s) = L\{h(t)\} = \int_0^{\infty} h(t).e^{-st} dt \dots \dots (1.6)$$

where,

$$s = \text{complex variable} = \sigma + j\Omega \dots \dots (1.6a)$$

Now, to get the discrete signal h(n) from the h(t), we substitute ,

$$t = nT \dots \dots (1.7)$$

where,

$$T = \text{sampling time}$$

If T=1sec, then we get t = n from equation (1.7). After substitution, we can easily achieve h(n) from h(t) where h(n) is the discrete form of the time domain continuous signal h(t). If we perform the z-transform on h(n), we can get the transfer function in z-plane i.e. H(z)[8][16],

$$H(z) = Z\{h(n)\} = \sum_{n=-\infty}^{\infty} h(n)z^{-n} \dots \dots (1.8)$$

So, in this way, the transfer function of an IIR Digital filter can be obtained. Basically the relation between the s-plane and z-plane [8] can be described by the equation,

$$z = e^{sT} \dots \dots (1.9)$$

Let,

$$z = re^{j\omega} \quad \dots \dots (1.10)$$

and,

$$s = \sigma + j\Omega \quad \dots \dots (\text{from equation (1.6a)})$$

Now, substituting equation (1.10) and (1.6) into equation (1.9), we get,

$$z = e^{sT} = e^{(\sigma + j\Omega)T} = e^{\sigma T} e^{j\Omega T} \quad \dots \dots (1.11)$$

Comparing equation (1.10) and (1.11), we get that,

$$|z| = e^{\sigma T} \quad \dots \dots (1.12)$$

and

$$\omega = \Omega T \quad \dots \dots (1.13)$$

We can consider three cases to determine the values of  $|z|$  with respect to the values of  $\sigma$  as follows[8],

Case I:  $\sigma = 0$  gives  $|z| = 1 \rightarrow$  Stable system

Case II:  $\sigma < 0$  gives  $|z| < 1 \rightarrow$  Stable system

Case III:  $\sigma > 0$  gives  $|z| > 1 \rightarrow$  Unstable system

For the value of  $\sigma=0$  and  $\sigma<0$ , the  $j\Omega$  axis of  $s$ -plane maps into the unit circle on the  $z$ -plane and for  $\sigma>0$ , The right half of  $s$ -plane maps outside the unit circle on the  $z$ -plane. So, to ensure the stability of the system, we must consider the values of  $\sigma$  as  $\sigma < 0$  and  $\sigma = 0$ . The Fig.5 shows the concept of  $s$ -plane to  $z$ -plane mapping for stable system and the Fig.6 shows the mapping for unstable system[8].

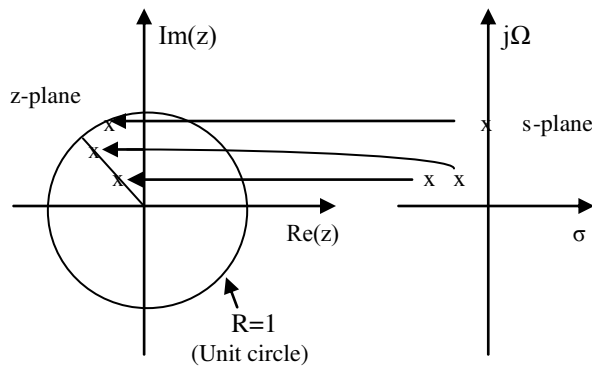


Fig.5 Mapping of poles from s-plane to z-plane  
( For stable system)

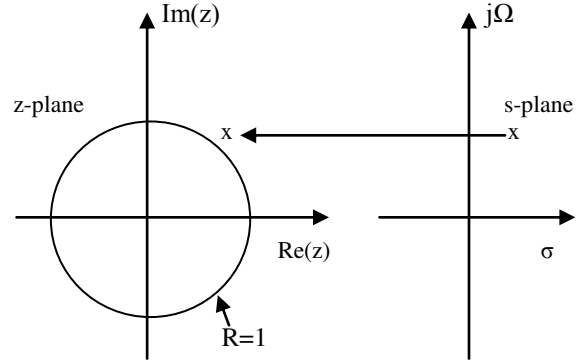


Fig.6 Mapping of poles from s-plane to z-plane  
( For unstable system)

## 6. Comparison of Butterworth and Chebyshev Filter

Butterworth filter and Chebyshev filter both are used for the high frequency filtering or low frequency filtering operation. These two filters are used in some specific area of interests. So, there are the reason where we use the Butterworth filter and where the Chebyshev filter. In some cases, the Butterworth filter is better than the Chebyshev filter and vice versa. Which filter will be used in which area, depends upon their properties such as magnitude response, pole locations, order of filter etc. Here is some comparisons in between these two filters[8][12].

- The magnitude response of the Butterworth filter decreases monotonically as the frequency increases from 0 to  $\infty$  whereas the magnitude response of the Chebyshev filter exhibits ripples in the passband or stopband depending upon the type. Chebyshev Type-I filter exhibits equiripple behavior in passband and Chebyshev Type-II filter exhibits that of in stopband.
- The poles of Butterworth filter lies on a circle whereas the poles of the Chebyshev filter lies on an ellipse.
- Butterworth filter contains poles and zeros both but Chebyshev Type-I filter contains only poles and for that reason Chebyshev Type-I filter is called All Pole Filter.
- The transition band is more in Butterworth filter compared to the Chebyshev filter.
- For the same filter parameter specification, the number of poles are more in Butterworth filter than Chebyshev filter. This indicates that, with same specifications, the order of Butterworth filter is higher than that of the Chebyshev filter. That means, to construct the Chebyshev filter, less numbers of discrete components are required compared to the Butterworth filter.

## 7. Simulation result

The programs to design the IIR Butterworth Highpass filter and IIR Chebyshev Highpass filter(Type-I and Type-II) are simulated in Matlab7. The proper specifications, such as the passband frequency, the stop band frequency, the passband and stopband ripples, are chosen in those programs, to have the perfect result in the magnitude response, phase response, impulse response and pole-zero plotting and moreover for the coefficient determination. The pole-zero plotting shows the stability of the system. The output graphs are shown from Fig.7 to Fig.22.

Table 1 gives us the result of the coefficients for IIR Butterworth Highpass filter and IIR Chebyshev Highpass filter(Type-I and Type-II) for order=4 and order=6.

Filter name	Filter order	Type	Numerator coefficient	Denominator coefficient
Butterworth Highpass Filter	4	N/A	0.6531, 0.461 0.122, 0.01436	0.3731,-0.8696, 1.304, -1.348
	6		1.806,1.764, 0.7177, 0.1558, 0.01902, 0.001239	0.9612,-2.601, 4.399,-5.155, 4.454,-2.729
Chebyshev Highpass Filter	4	I	0.01281, -0.0297,0.0259, -0.01003	0.2972,-1.545, 3.091, -2.827
	6		0.00071, -0.0025,0.0037, -0.0029,0.0013, -0.0031	0.1342,-1.081, 3.681,-6.795, 7.173,-4.11
	4	II	1.245,-4.358, 6.085,-2.922	1.253,-4.358, 6.085,-3.931
	6		1.021,-5.277, 11.88,-14.74, 10.57,-4.137	1.278,-6.825, 15.79,-20.06, 14.7,-5.875

### i) Magnitude response (Filter order=4)

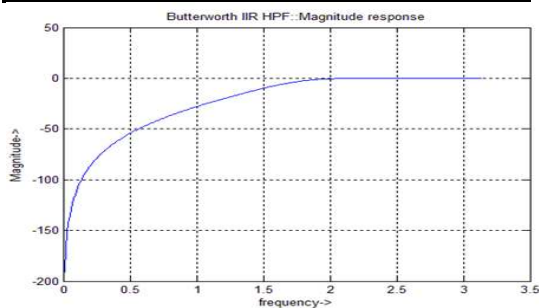


Fig.7 Butterworth Highpass filter Magnitude response

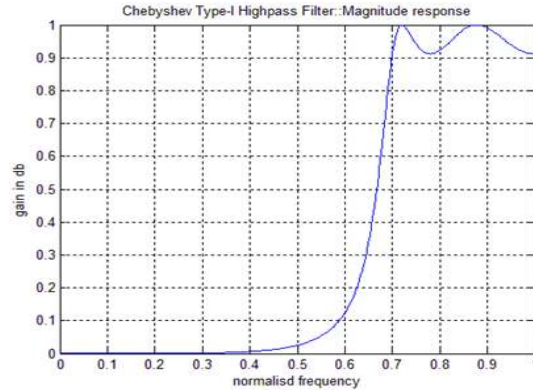


Fig.8 Chebyshev(Type-I) Highpass filter Magnitude response

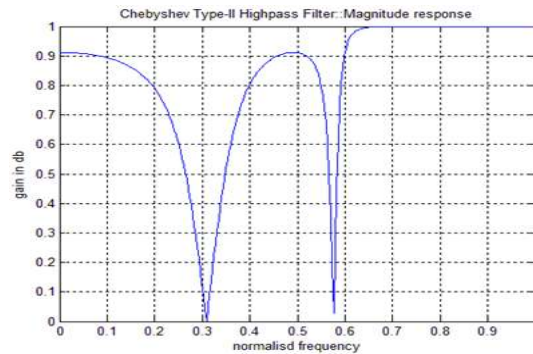


Fig.9 Chebyshev(Type-II) Highpass filter Magnitude response

### ii) Impulse response (Filter order = 4)

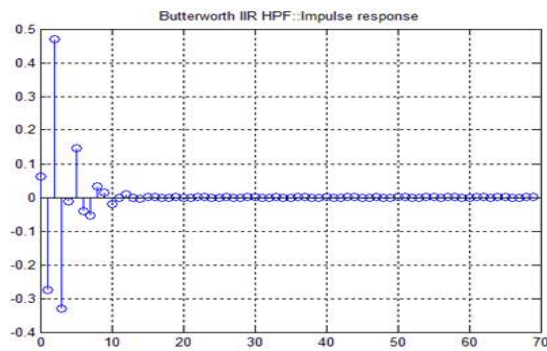


Fig.10 Butterworth Highpass filter Impulse response

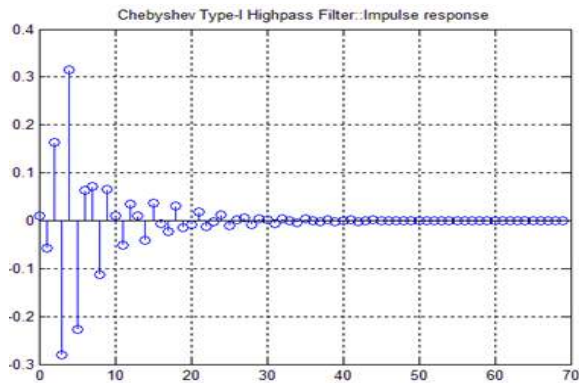


Fig.11 Chebyshev(Type-I) Highpass filter Impulse response

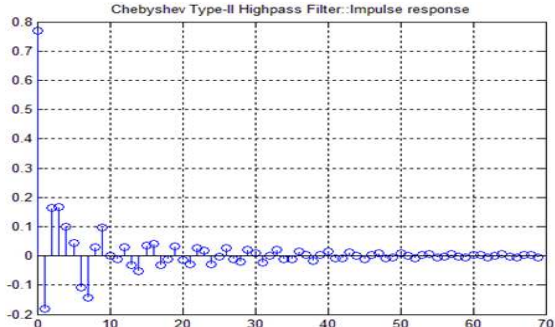


Fig.11 Chebyshev (Type-II) Highpass filter Impulse response

**iii) Pole-Zero plot (Filter order = 4)**

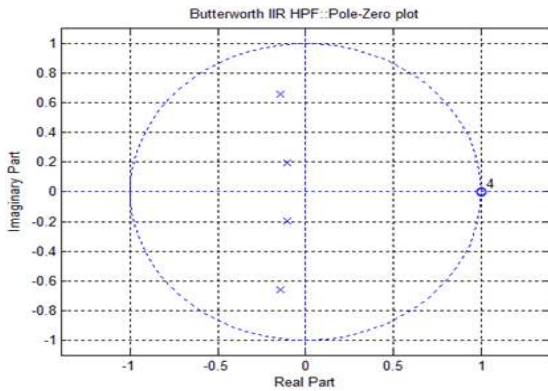


Fig.12 Butterworth Highpass filter pole-zero plot

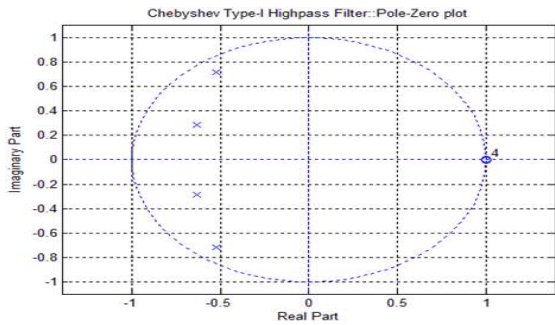


Fig.13 Chebyshev (Type-I) Highpass filter pole-zero plot

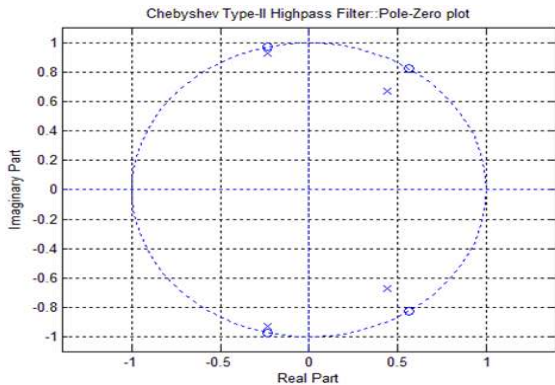


Fig.13 Chebyshev (Type-II) Highpass filter pole-zero plot

**iv) Magnitude response (Filter order=6)**

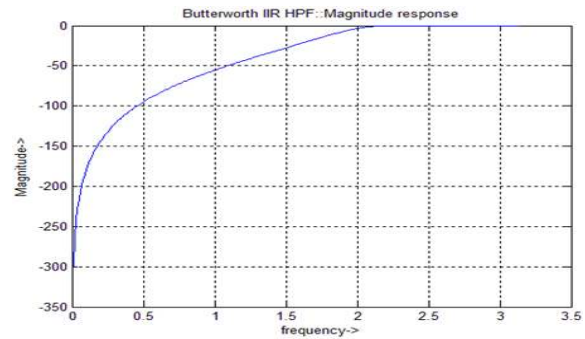


Fig.14 Butterworth Highpass filter Magnitude response

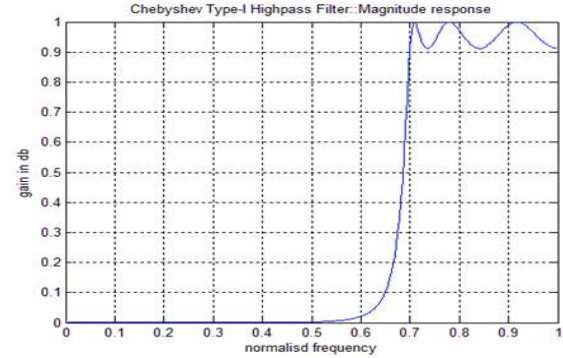


Fig.15 Chebyshev (Type-I) Highpass filter Magnitude response

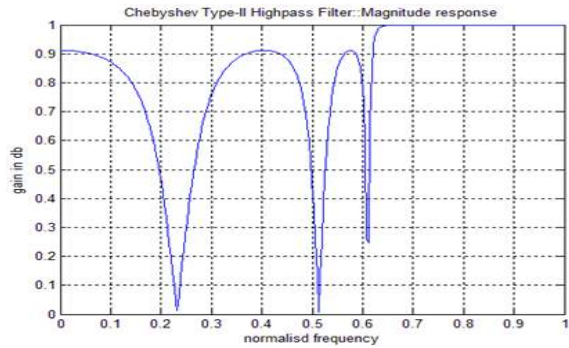


Fig.16 Chebyshev (Type-II) Highpass filter Magnitude response

**v) Impulse response (Filter order = 6)**

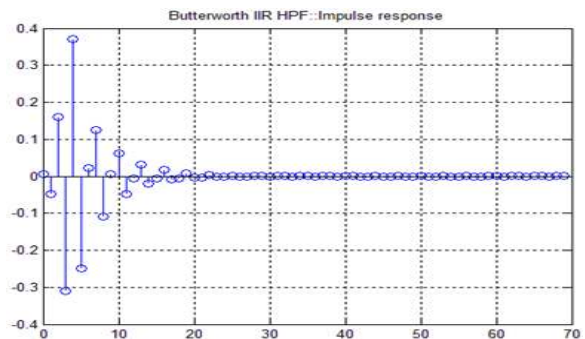


Fig.17 Butterworth Highpass filter Impulse response

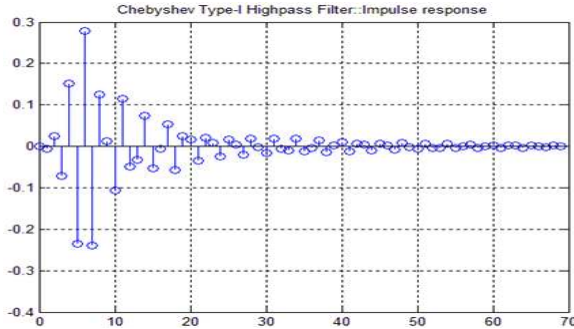


Fig.18 Chebyshev(Type-I) Highpass filter Impulse response

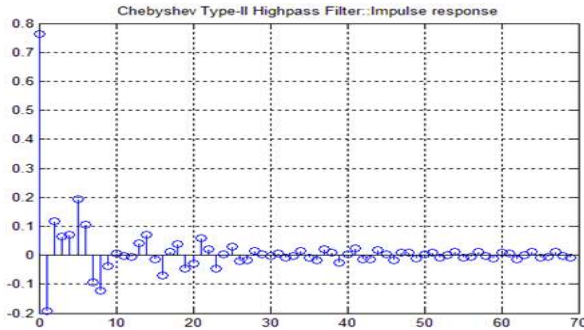


Fig.19 Chebyshev(Type-II) Highpass filter Impulse response

**vi) Pole-Zero plot(Filter order = 6)**

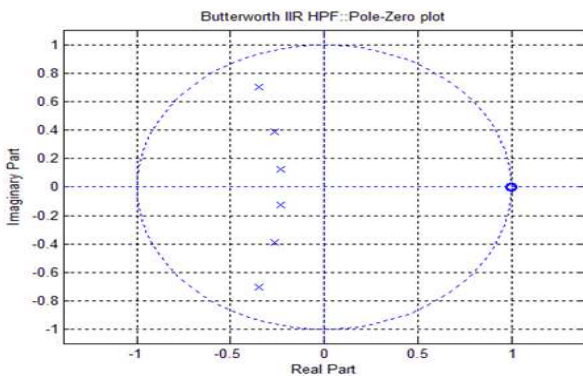


Fig.20 Butterworth Highpass filter pole-zero plot

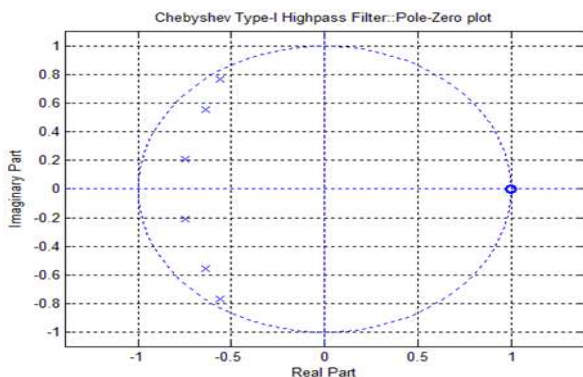


Fig.21 Chebyshev(Type-I) Highpass filter pole-zero plot

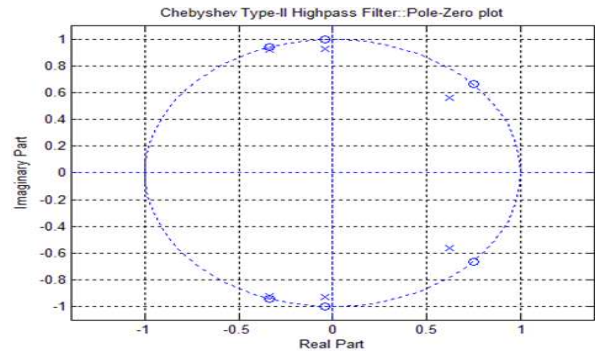


Fig.22 Chebyshev(Type-II) Highpass filter pole-zero plot

**8. Conclusion**

In this paper, the design and the calculation for coefficients of IIR Digital High pass filter are shown. The algorithm shown in Fig.4 helps us to determine the optimum coefficients of the IIR Digital High pass filter. Those coefficients are truly required to design the digital filter. After designing the IIR Digital High pass filter from the analog filter with the application of analog-to-digital mapping technique, we obtain the required coefficients as well as the magnitude response, impulse response and the pole-zero plot of the filter by simulating the necessary programs in Matlab7. The pole-zero plot shows the stability of the system. Here we can see that the IIR Digital High pass filter(Chebyshev Type-I, Chebyshev Type-II, Butterworth filter), designed with the application of the algorithm, are stable in nature. So, we can design a stable IIR Digital High pass filter by applying the algorithm.

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