Design and Simulation of RF MEMS Capacitive type Shunt Switch & its Major Applications

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Abstract: This article details about the design of a K-band RF-MEMS capacitive type shunt switch and some of its major applications. The electrostatic and electromagnetic analyses of the designed structure have been performed using MATLAB and commercially available EM solvers. FEM and MOM both tools have been used extensively for full-wave analysis. Two major applications (SPDT switch and reconfigurable Antenna) have been studied here. The design is based upon 675 μ m thick high resistive Silicon ($\rho > 8K\Omega$ -cm).

Keywords - Coplanar waveguide, MEMS Shunt switch, Reconfigurable Antenna, RF MEMS, Single Pole double throw (SPDT)

I. INTRODUCTION

A micro-electromechanical system (MEMS) is a process technology used to produce minute integrated devices or systems that merge mechanical and electrical components. RF-MEMS switches are playing a crucial role in the growth of the modern communications systems. The MEMS switches have the capability to combine advantages of electro-mechanical and semiconductor technologies based switches. In particular, MEMS switches offer the high RF performance and low DC power consumption of electro-mechanical switches but with the size and cost features of semiconductor switches [1]. RF MEMS switch found extensive applications in switching networks, T/R modules, phase shifters, reconfigurable antennas, tunable and switched filter banks etc. A MEMS switch can be made in several different configurations depending on the signal path (series or shunt), the actuation mechanism (electrostatic, magneto static, thermal etc), the type of contact (Ohmic or capacitive) and the type of structure (cantilever or bridge). Various kinds of MEMS switches have been extensively studied in series and shunt structures [2]-[3]. In 1996, the first practical capacitive shunt switch was presented by Raytheon based on a fixed-fixed metal beam design [2]. Since then, several studies have been conducted on capacitive shunt switches to achieve better performance and lower actuation voltage [4]-[9]. In this paper, design of RF MEMS switches based on simple fixed-fixed beam has been carried out. A shunt-capacitive MEMS switch consists of a thin metal membrane "bridge" suspended over the center conductor of a coplanar waveguide (CPW) [10]-[11] or microstrip line and fixed at both ends to the ground conductors of the CPW line as shown in fig. 1(a). In this paper, electrostatic and electromagnetic simulation results of RF MEMS capacitive shunt switches for K-band (18-26.5 GHz) applications have been carried out. MATLAB has been used for Electrostatic simulation and ADS & HFSS have been used extensively for EM simulations. RF MEMS switches have been extensively used for reconfigurable integrated circuits [12]. Furthermore a SPDT MEMS switch and a Ka-band Reconfigurable patch antenna with the use of integrated MEMS switches are also presented. The experimental results show the insertion loss of SPDT switch is below 0.3 dB and the isolation obtained is better than 18 dB entirely up to K – band. The organization of the paper is as follows. RF MEMS switch with its selection criteria, parameters are discussed in Section 2. Electrostatic and Electromagnetic analysis of Capacitive Shunt switches are discussed in Section 3 and Section 4 respectively. Section 5 presents applications of MEMS switch.

II. RF MEMS SWITCH

2.1 RF MEMS Capacitive Shunt switch for K band

Switches are the fundamental and imperative parts of RF systems. RF MEMS switch are highly attractive for low loss, high isolation, high linearity, wide band, low power consumption and ease of integration. These switches can be mainly categorized as Capacitive and Ohmic contact and the signal can flow in series and shunt configuration as shown in table I. There are two main forces for the actuation purpose of RF MEMS switch: Electromagnetic and Electrostatic and the properties of these two actuation methods are given in table II.

Table I Classification of Switch

4.	D		0 1		
Actu	ation propertie	es Vs Actuatio	on Method		
Table II					

Parameters	Type	
Contact type	Series Configuration	
	Shunt Configuration	
Mechanical structure	Cantilever	
	Fixed-fixed beam	
Form of Contact	Capacitive	
	Ohmic	

Actuation	Power	Force	Speed
method	usage	Generated	
Thermal	High	Moderate	m sec
Electromagne	Medium/Hi	High	μ sec /
tic	gh		m sec
Electrostatic	low	Moderate	μ sec

As it can be seen from the table II, in case of the electromagnetic actuation, the usage of power and the force generated are high as compared to electrostatic actuation method. So for the microwave and mm-wave regions, electrostatic switches are the most common switches that are used [13].

2.2 Switch Selection

Different parameters are considered for switch selection as shown in Table I. The main criteria for the selection of the switch are frequency of operation and application. Out of the two configuration, shunt is preferred over series due to minimal parasitic involved and can handle four times more RF power compared to series switch. The series configuration mostly applied at the lower frequencies. In series configuration, the t-line is open when no bias voltage is applied (up-state position). The open contact of the transmission lines at K-band frequencies propagates the surface modes which hinders the operation of series switches at higher frequencies. The advantage of implementing shunt switch are ease of fabrication, for the same specifications the value of down-state capacitance is low for series configuration as compared to shunt type, resulting in poor isolation of series switches at high frequencies, Shunt configuration in comparison to series have fewer parasitic due to continuous t-line and mainly in the down-state, the isolation of the series switch design is limited by the coupling between the two open conductors. Fig. 1(a) and fig. 1(b) shows the cross sectional view and isometric view of RF MEMS capacitive shunt switch respectively for the high frequency applications which is designed and simulated electro-statically and electromagnetically using 2.5D and 3D full wave EM simulators [14].

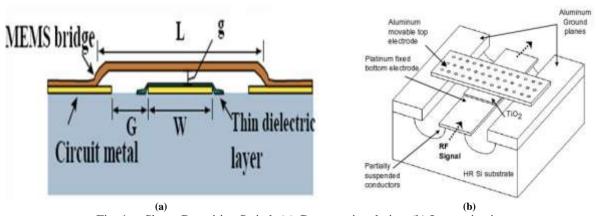


Fig. 1 Shunt Capacitive Switch (a) Cross sectional view (b) Isometric view

III. ELECTROSTATIC ANALYSIS OF SWITCH

Capacitive switches are two-state capacitors. The typical switch is a metal bridge suspended above a CPW signal line with both ends of the bridge anchored to ground. The switch has the minimal loading effect on the signal line in this state (the passing state) and signals propagate with minimal reflection. The switch is snapped down when a voltage exceeding the electrostatic pull-in voltage is applied between the bridge and the signal line. In this state (the down-state) incident signals are reflected due to the formation of a low impedance path through the dielectric and the switch bridge to ground. The level of isolation depends on the relative value of the shunt impedance to the characteristic impedance of the transmission line, *Zo*. The ratio of the blocking state (down-state) capacitance to the passing state (up-state) capacitance, Con/Coff, is often used as a performance metric known as figure of merit, which should be more than 50. Generally, capacitive switches are used in the shunt configuration.

3.1 Coplanar Waveguide

Coplanar waveguide (CPW) is a Planar transmission line. In this Signal line and two ground lines are on the same plane. It is a substrate height independent transmission line. The Z_0 of the line is determined by G/W/G dimension. It is easy for Calibration. The thickness of dielectric in ideal CPW is infinite. There are various configurations of CPW and these can be classified as Simple CPW, CB-CPW (Conductor backed-Coplanar waveguide), FG-CPW (Finite ground-Coplanar waveguide), FG-CPW (Finite ground-Conductor backed-Coplanar waveguide) where w is center strip width and S is slot width and C_r is relative dielectric constant of the substrate dielectric. This design uses the silicon substrate of $C_r = 11.8$, and CPW center conductor width of $C_r = 120 \mu M$, space line is $C_r = 11.8$, and the height of the substrate is $C_r = 11.8$, and $C_$

3.2 Switch Parameters

Increasing the substrate permittivity and thinning the substrate reduces loss, however thinning these substrates are difficult to handle and can result in high yield loss. So in the given scenario High Resistive Silicon ($\rho > 8K\Omega - cm$) has been opted. The modeled parameters of the shunt switch using the above CPW line are shown in table III. As per the values chosen for length (l),width of membrane(w), Central electrode width of the CPW line (W),thickness (t),gap-height (g_0), some another parameters had been calculated and given in table IV.

Table III Modeled values of switch

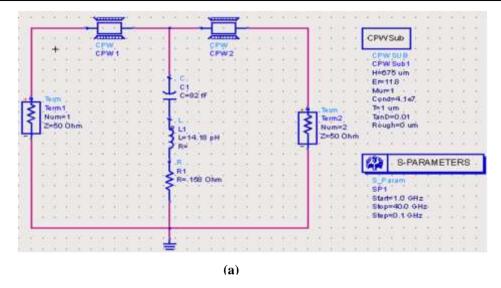
Parameters	Values	
Up-state Capacitance	82 fF	
(C_u)		
Down-state capacitance	6.36 pF	
(C_d)		
Inductance (L)	14.18 pH	
Resistance (R)	0.158 Ω	
Length (l)	300 μm	
Width (w)	100 μm	
Initial air-gap (g _o)	3 μm	
Thickness	0.5 μm	
Membrane	Gold(Au),	
	Aluminium(Al)	
CPW (G/W/G)	90/120/90 μm	
Substrate	High Resistive Silicon	
	$(\rho > 8K\Omega - cm)$	
Actuation area	$120 \times 100 \ \mu\text{m}^2$	

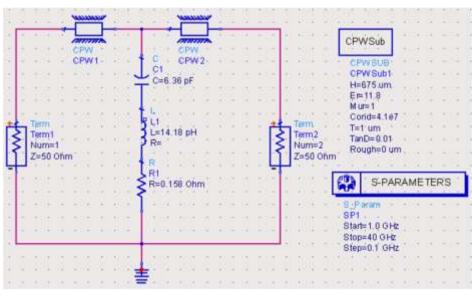
Table IV
Calculated Parameters

Parameters Values				
Values				
Gold	Aluminium			
Membrane	Membrane			
14.93	17.33			
35	37.5			
3.1	3.3			
38	41			
8.49				
11				
	Val Gold Membrane 14.93 35 3.1 38 8.4			

1V. ELECTROMAGNETIC ANALYSIS OF SWITCH

The key characteristics of the RF MEMS switch are insertion loss, isolation, and actuation voltage. The aim is to increase the RF parameters while reducing the actuation voltage. A MEMS shunt capacitive switch in upstate and downstate position is shown in fig. 2(a) and fig. 2(b) respectively. The layout designs of switch are shown in fig. 3(a) and fig. 3(b). The switch is suspended at a height 'g' above the dielectric layer on a t-line and the dielectric thickness is t_d with the dielectric constant \in_r . The switch is L µm long, w µm wide and t µm thick. The width of t-line is W µm. The switch is modeled by two short sections of t-line and a lumped CLR model of the bridge with a variable capacitor (up-state, down-state).





(b)

Fig. 2 Schematic model of Shunt capacitive switch in (a) Upstate position (b) Downstate position

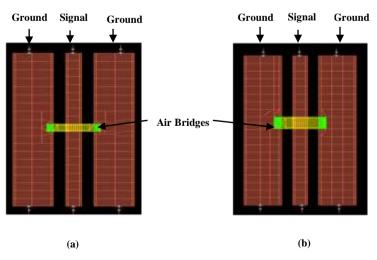


Fig. 3 Layout structure of Shunt capacitive switch in (a) Upstate position (b) Downstate position

4.1 2.5- D and 3-D Simulation of RF Switch

Upstate Simulation: The switch structure is simulated in the upstate position by the ADS and HFSS simulators. There is CPW which contains transmission line, a substrate, and grounds. There are two anchors situated on top of the CPW and connects the membrane to the Ground [9]. The area of the membrane is $120 \times 100 \mu m^2$. The S parameters are taken from the range of 0-40 GHz using the ADS. As can be seen from fig. 4(a), the Return loss (S_{11}) is better than 10 dB in the frequency range of DC - 35 GHz, Insertion loss (S_{12}) is less than 0.3 dB . In HFSS simulator, the Return loss (S_{11}) is better than 10 dB in the frequency range of DC - 30 GHz, Insertion loss (S_{12}) is less than 0.3 dB as shown in fig. 6.

Downstate simulation: The switch in its downstate position does not have anchors because the membrane should be situated on top of the dielectric layer [9]. The S-parameters are taken from the range of 0-16 GHz using the ADS. As can be seen from fig. 4(b), Isolation (S_{21}) is better than 20 dB in frequency band of 5-40 GHz.

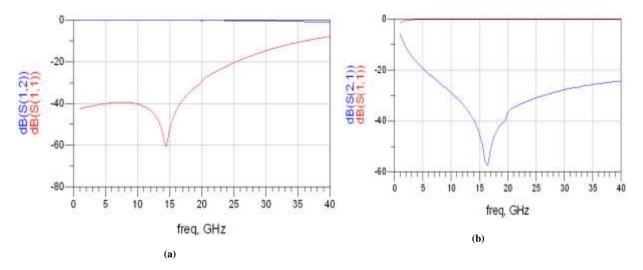


Fig. 4 (a) Return loss S(1,1), Insertion loss S(1,2) (b) Isolation S(2,1)

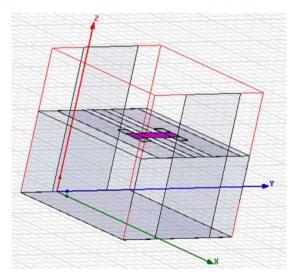


Fig. 5 RF switch structure in HFSS Simulator

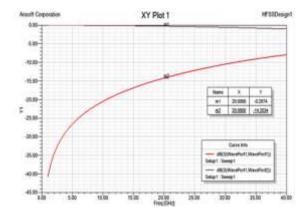


Fig. 6 Measured Return loss S(1,1) and Insertion loss S(1,2) of Shunt capacitive switch

V. APPLICATIONS OF RF MEMS SWITCH

To have faster, cheaper, highly tunable Communication system which is having wider bandwidth for increased data rates as per the requirement for which MEMS devices have originated new field of applications. Reconfigurable antennas, filters and tuners, high-Q passives and resonators, low loss planar THz waveguide components and low loss phase shifters having low loss and high isolation are a few applications of RF MEMS switches. Out of many applications major applications are designed and simulated.

5.1 SPDT MEMS Switch

The single-pole double-throw (SPDT) switches have been broadly employed in the microwave and millimeter wave communication systems [15], such as the signal routing in transmit and receive applications, the switched-line phase shifters, phased array antennas and the wide-band tuning networks. Many SPDT switches were implemented using MEMS technology for replacing the conventional solid state semiconductor switches. In this paper, a MEMS SPDT switch has been employed to work in K-band applications (10-30 G Hz).

5.2 Design and EM Simulation of MEMS SPDT Switch

The layout structure of capacitive SPDT switch in state 1 and state 2 shown in fig. 7(a) and fig. 7(b) respectively. When the switch pair S_1 is open and the S_2 is closed, the RF power flows from the input to the RF out 1 and vice versa. When the SPDT switch is in state 1 then insertion loss is at S_{21} terminal and Isolation is at S_{31} terminal and the same way when this SPDT switch is in state 2 Insertion loss is at S_{31} terminal and Isolation is at S_{21} terminal. The SPDT switch consists of two pairs of capacitive MEMS shunt switches and a power splitter. The pairs of MEMS switches are used to enhance the isolation between the two output ports, and they are at a quarter-wavelength distance from the T-junction. The quarter-wavelength transmission lines between the T-junction and the switch pairs are capacitively loaded with two air bridges in order to make the SPDT switch more compact in size. RF performance of the SPDT switch has been analyzed using 2.5D planar electromagnetic software, ADS.

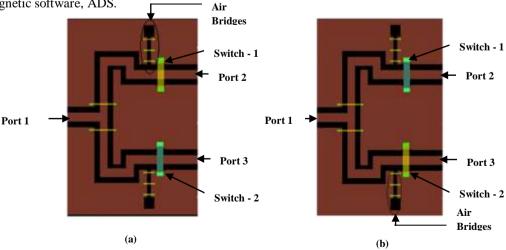


Fig. 7 Layout structure of the capacitive MEMS SPDT switch (a) State-1 (b) State-2

As shown in Fig. 8(a)-8(c), in state 1, the simulated return loss is better than 10 dB in the frequency range of 10-25 GHz, Insertion loss is less than 0.3 dB, Isolation is between two ports i.e. (S_{32}) is better than 18 dB entirely upto K – band and the simulated Insertion loss is also less than 0.3 dB in state 2 as shown in fig. 8 (d)

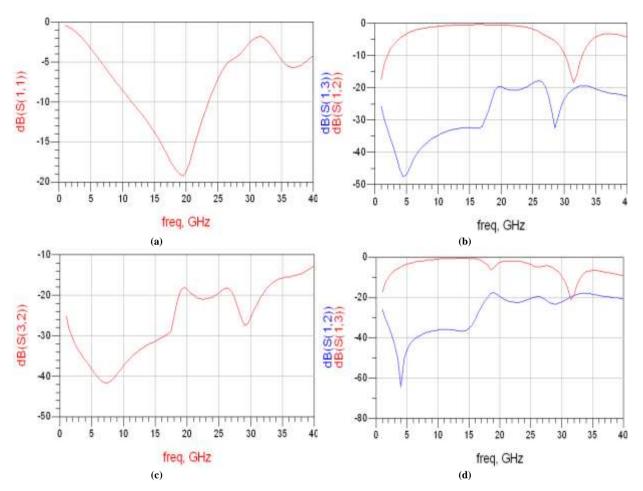


Fig. 8 Result of SPDT switch in State -1 (a)Return loss S(1,1) (b) Insertion loss S(1,2) (c) Isolation S(3,2) (d) SPDT switch in State -2

5.3 Reconfigurable Patch Antenna

The new generation of antennas is Reconfigurable antenna that will not be limited to a certain function or resonance but will change their functionality depending on the requirements of implementation. As compared to broadband antennas, reconfigurable antennas offer many advantages, such as compact size, radiation patterns for all designed frequency bands are somewhat similar, and frequency selectivity which helps in reducing adverse effects like jamming and co-site interference.

5.4 Design and EM Simulation of Reconfigurable Patch Antenna

A new field of modern world microwave is the reconfigurability of the already used devices in the system chain. It is gaining maximum potentiality because of its versatile utility according to desired environments [16]. A Ka-band reconfigurable microstrip Patch antenna has been designed and simulated using EM Simulators as shown in fig.9, based on a structure with RF MEMS switches mounted at different slots. The states of the switches change the functionality of the antenna cutting or allowing certain frequencies of operation. The main feature of this approach is (1) for providing polarization diversity, the capability of a square patch to dynamically reconfigure the polarization from circular to linear, thus and (2) the MEMS switches with the dimensions of length= $400\mu m$, width= $100\mu m$, thickness= $1\mu m$ is mounted within the patch and does not require supplementary space. The MEMS switches consists of a moveable metal overpass suspended over a T shaped Metal stub. The overpass is supported at both ends by metalized vias which are electrically connected to the patch antenna. The metal overpass is actuated by an electrostatic force of attraction set up by a voltage applied between the overpass and the T- shaped metal stub. A dielectric layer having high resistive silicon

substrate of 0.675 mm deposited over the metal stub prevents stiction when the surfaces come in contact. The metal strip of length 570 μ m and width 50 μ m attached to the metal stub behaves as a parallel plate Capacitor shown in fig. 9 .The switch is in the ON state when the overpass is pulled down by the electrostatic force due to the bias, and the capacitance of the metal strip appears in shunt with the input impedance of the patch antenna. This capacitance tunes the patch to a lower operating frequency. When both the switches are in the OFF state (upstate), the patch resonates at its nominal operating frequency (f_o) of about 29.0 GHz. When both switches are in the ON state (downstate), the f_{ON} is 28.5 GHz. The reconfigurable patch antenna structure is shown in fig.10 and measured return loss demonstrating frequency reconfigurability is about -13.3 dB in HFSS simulator .

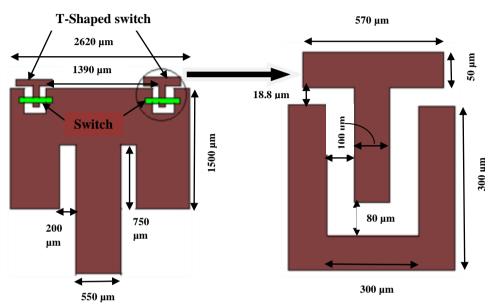


Fig. 9 Layout structure of Frequency Reconfigurable Patch antenna element with two independent Switches

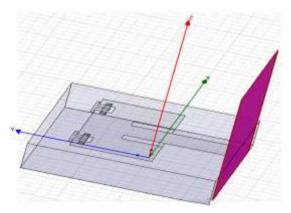


Fig. 10 A Ka-Band Reconfigurable Patch antenna structure in HFSS simulator

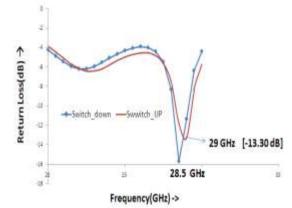


Fig. 11 Measured return loss demonstrating frequency reconfigurability with MEMS switches

VI. CONCLUSION

This article presents the design and simulation of RF MEMS capacitive type shunt switch and K- Band SPDT switch and Ka-Band Reconfigurable patch antenna as major applications to validate our design concepts. The electromagnetic simulation results indicated that return loss is better than 10 dB at lower frequency and Insertion loss is less than 0.3 dB and Isolation is better than 20 dB which is good enough for the reconfigurable devices. These results indicate that the proposed SPDT switch is of small size and offers low loss by using the capacitive-contact RF MEMS switch. A novel frequency reconfigurable patch antenna with integrated MEMS switches was also simulated. This patch has a reconfigurability bandwidth of 500 MHz (28.5 GHz to 29 GHz).

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