

Research Article

Design of Asymmetrical Relay Resonators for Maximum Efficiency of Wireless Power Transfer

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This paper presents a new design method of asymmetrical relay resonators for maximum wireless power transfer. A new design method for relay resonators is demanded because maximum power transfer efficiency (PTE) is not obtained at the resonant frequency of unit resonator. The maximum PTE for relay resonators is obtained at the different resonances of unit resonator. The optimum design of asymmetrical relay is conducted by both the optimum placement and the optimum capacitance of resonators. The optimum placement is found by scanning the positions of the relays and optimum capacitance can be found by using genetic algorithm (GA). The PTEs are enhanced when capacitance is optimally designed by GA according to the position of relays, respectively, and then maximum efficiency is obtained at the optimum placement of relays. The capacitance of the second resonator to *n*th resonator and the load resistance should be determined for maximum efficiency while the capacitance of the first resonator and the source resistance are obtained for the impedance matching. The simulated and measured results are in good agreement.

1. Introduction

Wireless power transfer (WPT) is very useful and applicable technology in many areas, for example, smart phone [1], smart car [2], home appliances, medical devices [3-6], automated logistics, and robots such as drone. In particular, WPT will be more needed to apply to Internet of things (IoT) and wearable devices in the near future. So far, induction method WPT application has been actively commercialized, for example, charging of smart phone, charging of electric toothbrush, and powering of automated logistics, but magnetic resonance WPT could hardly be commercialized and is currently being developed. However, magnetic resonance WPT application will be actively developed and commercialized since The Alliance for Wireless Power (A4WP) and Power Matters Alliance (PMA) are consolidated in June 2015 which are standards of magnetic and induction resonance WPT, respectively [7].

Magnetic resonance WPT [8] has an advantage compared with induction WPT. Magnetic resonance WPT can transfer

power to longer distance of tens of centimeters effectively by using the resonance of a transmitter and a receiver whereas the inductive coupling method is used in short distance of many millimeters. However, magnetic resonance WPT cannot prevent the efficiency from dropping gradually as the distance between a transmitter and a receiver is longer [9]. Therefore, the researches to improve PTE have been conducted actively. The researches of an optimum load, a metamaterial slab, and a frequency-tuning method are demonstrated [10–15].

Another approach to improve the power transfer efficiency at long distance is to employ the relay resonators [16–27]. Many researches on relay resonators of magnetic resonance WPT have been conducted to extend the distance of power transfer. By adding relay resonators, PTE is improved at long distance between transmitting and receiving resonator [16]. The optimum position of one relay between transmitting and receiving resonators was investigated [17] and number of relays and distance between relays were optimized for

maximum PTE [18]. In addition, it is possible to control power flow by employing relay resonators [19, 20].

The analysis of PTE in a relay resonator system was complicated. In [21, 22], nonadjacent coupling of relays was ignored because nonadjacent coupling is smaller than adjacent coupling. However, the analysis ignoring nonadjacent coupling could not give birth to an exact result. The research of relay resonators by considering nonadjacent coupling was conducted [23]. It has been proved that the maximum PTE is not achieved at the resonance frequency of unit resonator due to magnetic couplings between nonadjacent resonators [23]. Therefore, it is clear that a new design method of relay resonators is required to obtain the maximum PTE. The design method of optimization of capacitance of each resonator was employed in case of symmetric relay configuration [24] and the optimum placement of the asymmetrical relay resonators was found as the positions of the relays are scanned [25]. However, the PTE of the asymmetrical relay resonators is not maximized because the capacitance of each resonator is not designed optimally.

In this paper, the optimum design of asymmetrical relay resonator is presented by optimizing the capacitance of each resonator as well as the placement of asymmetrical relay resonator. Additional improvement of PTE is achieved using the optimum capacitance determined by both PTE equation and GA after optimizing the placement of resonator. Finally, the maximum power transfer efficiency can be obtained by finding both the optimum positions and the optimum capacitance of the asymmetrical relay resonators.

2. Equivalent Circuit of Asymmetrical Relay Resonators

Figure 1 shows a structure of asymmetrical relay resonators. There are four resonators: a transmitter, two relays, and a receiver. The distance between a transmitter and a receiver is fixed to be 75 cm. The 2 relays are added between a transmitter and a receiver. The 2 relays can be rearranged between a transmitter and a receiver.

Figure 2 shows the equivalent circuit of asymmetrical relay resonators with a source and a load. R_i , L_i , and C_i (i = 1, 2, ..., n) are the resistance, self-inductance, and lumped capacitance connected in series to the *i*th resonator, respectively. M_{ij} $(i, j = 1, 2, ..., n, i \neq j)$ is the mutual inductance between *i*th resonator and *j*th resonator. R_s and R_L are source and load resistance, respectively. I_i (i = 1, 2, ..., n) is the current flowing on the *i*th resonator. V_s is source voltage and Z_{in} is input impedance. It is noted that R_i , L_i , and M_{ij} are constants because the size of resonators and distance are fixed. Therefore, the design variables become $C_1, ..., C_n, R_s$, and R_L . The purpose of this work is to find the optimum design variables for maximum power transfer of asymmetrical relay resonators.

Kirchhoff's voltage law (KVL) equations of the equivalent circuit of Figure 2 are given in the following matrix form:

$$\begin{bmatrix} \left(R_{s}+R_{1}+j\left(\omega L_{1}-\frac{1}{\omega C_{1}}\right)\right) & j\omega M_{12} & \cdots & j\omega M_{1,n-1} & j\omega M_{1,n} \\ j\omega M_{21} & \left(R_{2}+j\left(\omega L_{2}-\frac{1}{\omega C_{2}}\right)\right) & \cdots & j\omega M_{2,n-1} & j\omega M_{2,n} \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ j\omega M_{n-1,1} & j\omega M_{n-1,2} & \cdots & \left(R_{n-1}+j\left(\omega L_{n-1}-\frac{1}{\omega C_{n-1}}\right)\right) & j\omega M_{n-1,n} \\ j\omega M_{n,1} & j\omega M_{n,2} & \cdots & j\omega M_{n,n-1} & \left(R_{n}+R_{L}+j\left(\omega L_{n}-\frac{1}{\omega C_{n}}\right)\right)\right) \end{bmatrix} \begin{bmatrix} I_{1} \\ I_{2} \\ \vdots \\ \vdots \\ I_{n-1} \\ I_{n} \end{bmatrix}$$

$$\begin{bmatrix} V_{s} \end{bmatrix}$$

$$(1)$$

where ω is an angular frequency.

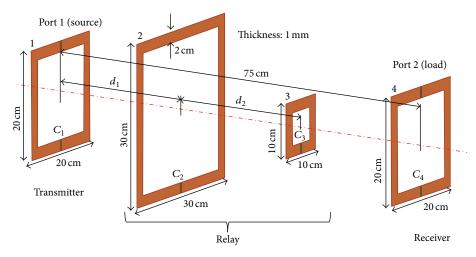


FIGURE 1: Structure of the asymmetrical relay resonators.

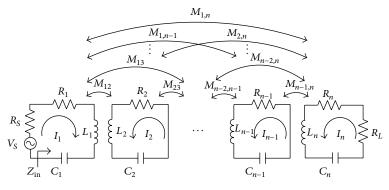


FIGURE 2: Equivalent circuit of asymmetrical relay resonators.

The efficiency of wireless power transfer is defined as the ratio of the dissipated power (P_L) at the load to the input power (P_{in}) which can be obtained by adding the total dissipated power in the resonators and the load. The PTE is given by [24]:

$$\eta = \frac{P_L}{P_{\text{in}}} = \frac{(1/2) R_L |I_n|^2}{(1/2) R_1 |I_1|^2 + (1/2) R_2 |I_2|^2 + \dots + (1/2) R_{n-1} |I_{n-1}|^2 + (1/2) (R_n + R_L) |I_n|^2} = \frac{R_L}{R_1 |I_1/I_n|^2 + R_2 |I_2/I_n|^2 + \dots + R_{n-1} |I_{n-1}/I_n|^2 + (R_n + R_L)}.$$
(2)

To calculate the efficiency of (2), the current ratio should be obtained from (1). The current column $[I_1 \cdots I_n]^T$ of (1) should be normalized by I_n . Then, the first row $[(R_s + R_L + j(\omega L_1 - 1/\omega C_1)) \cdots j\omega M_{1,n}]$ and $[V_s]$ of (1) can be erased and

the last column $[j\omega M_{2,n}\cdots (R_n + R_L + j(\omega L_n - 1/\omega C_n))]^T$ can be transposed to the right hand side. Therefore, the last row of the current matrix $[I_n/I_n]$ is erased. Then, it becomes as follows:

$$\begin{aligned} j\omega M_{21} & \left(R_{2} + j\left(\omega L_{2} - \frac{1}{\omega C_{2}}\right)\right) \cdots j\omega M_{2,n-1} \\ \vdots & \vdots & \ddots & \vdots \\ j\omega M_{n-1,1} & j\omega M_{n-1,2} & \cdots \left(R_{n-1} + j\left(\omega L_{n-1} - \frac{1}{\omega C_{n-1}}\right)\right) \\ j\omega M_{n,1} & j\omega M_{n,2} & \cdots & j\omega M_{n,n-1} \end{aligned} \right) \begin{bmatrix} \frac{I_{1}}{I_{n}} \\ \frac{I_{2}}{I_{n}} \\ \vdots \\ \frac{I_{n-1}}{I_{n}} \end{bmatrix} \\ = \begin{bmatrix} -j\omega M_{2,n} \\ \vdots \\ -j\omega M_{n-1,n} \\ -\left(R_{n} + R_{L} + j\left(\omega L_{n} - \frac{1}{\omega C_{n}}\right)\right) \end{bmatrix}. \end{aligned}$$
(3)

From (3), the current ratio I_1/I_n ,..., and I_{n-1}/I_n can be obtained by using inverse matrix and they are substituted for (2). Therefore, the PTE (η) becomes a function of C_2 ,..., C_n and R_L as follows:

$$\eta = f\left(C_2, \dots, C_n, R_L\right). \tag{4}$$

Note that C_1 and R_s are not related to PTE (η) but are related to impedance matching. Our purpose is to determine C_2, \ldots, C_n and R_L for maximum PTE. C_1 and R_s are also important for impedance matching that will be determined by impedance matching condition.

3. Optimum Design of Asymmetrical Relay Resonators

First, the optimum resistance of $R_{L,opt}$ should be expressed as a function of C_2, C_3, \ldots , and C_{n-1} to satisfy the equation of $\partial \eta(C_2, \dots, C_n, R_L) / \partial R_L = 0$ [17]. The equation of $\overline{R}_{L,\text{opt}} =$ $f(C_2, C_3, \ldots, C_{n-1})$ is substituted to (4) and, then, PTE (η) becomes a function of C_2, C_3, \ldots , and C_n . To obtain the optimum capacitance of $C_{2,opt}, C_{3,opt}, \ldots$, and $C_{n,opt}$ for maximum efficiency, the equations of $\partial \eta / \partial C_2 = 0, ...,$ and $\partial \eta / \partial C_n = 0$ should be simultaneously solved but they are very complicated. Therefore, $C_{2,opt}$,..., and $C_{n,opt}$ could be determined by GA and PTE equation of (4). GA is a search heuristic that mimics the process of natural selection. It used to generate useful solution to optimization and search problems. GA finds the minimum value of fitness function by repetition of selection, crossover, and mutation which are inherent process [24, 28]. This GA procedure is summarized in Figure 3 and GA options are listed in Table 1. The minimum value of fitness function, that is, negative PTE equation of (4), is found through the repetition of selection, crossover, and mutation which are inherent process of GA. Finally, the minimum value of negative PTE corresponding to maximum PTE is found when the optimum capacitance is determined. The optimization algorithm of GA was implemented by MATLAB code. After $C_{2,opt}, \ldots$, and $C_{n,opt}$ are determined, $R_{L,opt}$ is

obtained by the equation of $R_{L,opt} = f(C_2, C_3, ..., C_{n-1})$. Lastly, C_1 and R_s are chosen to match the imaginary part and real part of input impedance, respectively. The input impedance (Z_{in}) is obtained from the equivalent circuit of Figure 2 and is given by

$$Z_{\rm in} = R_1 + j \left(\omega L_1 - \frac{1}{\omega C_1} \right) + j \omega M_{12} \left(\frac{I_2}{I_1} \right)$$
$$+ j \omega M_{13} \left(\frac{I_3}{I_1} \right) + \dots + j \omega M_{1,n-1} \left(\frac{I_{n-1}}{I_1} \right) \qquad (5)$$
$$+ j \omega M_{1,n} \left(\frac{I_n}{I_1} \right).$$

 $C_{1,\text{opt}}$ is determined for Im(Z_{in}) to be zero and $R_{S,\text{opt}}$ is set to be Re(Z_{in}) from (5).

4. Results

Figure 4 shows the calculated PTE when using the optimum capacitance determined by GA and using the conventional capacitance determined by $C_i = 1/(\omega_0^2 L_i)$ (*i* = 2, 3, 4). Note that C_1 is given by $Im(Z_{in}) = 0$ for impedance matching condition. The structure is shown in Figure 1 when d_1 and d_2 scan from 16 to 40 cm and from 13 to 37 cm, respectively. The PTEs when using the optimum capacitance are much higher and flatter than those using conventional capacitance. The average and standard deviation of PTE are 75.3% and 5.38%, respectively, when the optimum capacitance is used. On the other hand, when the conventional capacitance [25] is used, the average and standard deviation are 51.8% and 16.43%, respectively. These results clearly indicate that our method is to find the maximum PTE in the asymmetrical nresonator system, compared with the previous method [25] in the modified paper. The operating frequency of 6.78 MHz is standardized frequency by A4WP for magnetically coupled wireless power transfer system.

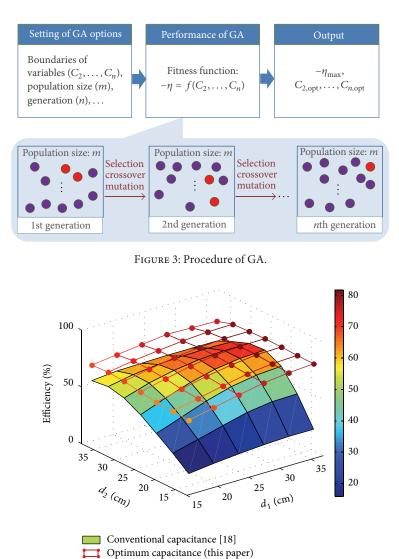


FIGURE 4: Optimum power transfer efficiency versus the various placement of two relay resonators (f = 6.78 MHz).

Six cases according to relay position of d_1 and d_2 are chosen arbitrarily to demonstrate that our method is generally applied for the various cases. The distances between resonators are shown in Figure 5. Figure 6 shows the photograph of measurement setup. There are four resonators with capacitors in series whose configuration is the case of #1 in Figure 5. Two feeding loops near resonators 1 and 4 are shown as a source and a load, respectively. The space between resonator 1 and feeding loop (port 1) determines source resistance of R_s and that between feeding loop (port 2) and resonator 2 determines load resistance of R_L for the impedance matching. Power transfer efficiency (PTE) is given by $|s_{21}|^2/(1 - |s_{11}|^2)$ measured by a vector network analyzer shown in the photograph.

Figure 5 shows the PTEs of six cases, both simulation and measurement. Note that case #4 is the optimum placement of the relays when d_1 and d_2 are found to be 37 cm and 26 cm, respectively. It is clearly shown that the PTE is improved by using the optimum capacitance at the optimum placement.

The values of C_1, \ldots, C_4 , R_S , and R_L are specified in Table 2 in both cases of optimum capacitance and conventional capacitance. The measured results tend to agree with the simulated results.

However, the efficiencies of measurement are small, compared with the simulation results. The measured errors are thought to be caused by the following reasons. First, the conductivity of the fabricated resonators is lower than that of pure copper used in the simulation. Second, the capacitors have a loss that is not considered in simulation. Lastly, the parameters such as R_i , L_i , and M_{ij} ($i, j = 1, 2, 3, 4, i \neq j$) from HFSS simulator may be slightly different from the fabricated resonator values. It could generate some errors in finding the optimum capacitance value because GA code uses the R_i , L_i , and M_{ij} from HFSS simulator.

Figure 7 shows the calculated dissipation power of four resonators and the load for the six cases when 1 W power is injected. It is clear that the small dissipated power of four resonators and the large load power indicate the good transfer

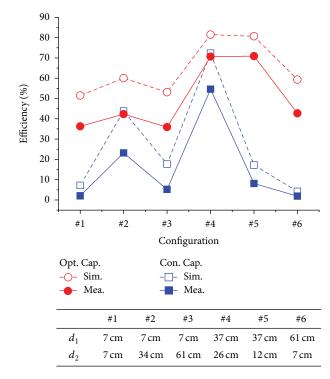


FIGURE 5: Power transfer efficiency for six cases.

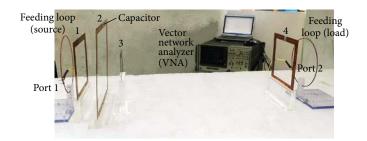


FIGURE 6: Photograph of experimental setup.

TABLE 1:	GA	options.
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Classification	Values		
Variables	C_2, C_3, C_4		
	<i>C</i> ₂ : 700~900 pF		
Boundary of variables	<i>C</i> ₃ : 3000~7000 pF		
	<i>C</i> ₄ : 1400~2000 pF		
Population	1000		
Generation	20		
Selection	Tournament		
Crossover	Scattered		
Mutation	Adaptive Feasible		
Fitness function	$-\eta = f\left(C_2, C_3, C_4\right)$		

efficiency. The power has a relationship to the equation of $P_i = (1/2)|I_i|^2/R_i$ (i = 1, ..., 4) and $P_L = (1/2)|I_4|^2/R_L$. Therefore, the dissipated powers are related to the currents on each resonator since R_i (i = 1, 2, 3, 4) is fixed. In cases

of using conventional capacitance, if the distances between two adjacent resonators are longer, the power cannot be transferred well but can be dissipated in the first resonator between them as seen in cases such as cases #1, #3, and #6 in Figure 7. By using optimum capacitance, the currents of resonators can be optimized. For example, the more current is flowing on the large loop of resonator 2 to transfer power more efficiently. Therefore, the more power is to be dissipated in the load. Case #4 is the most optimized placement (d_1 and d_2) for the maximum PTE in this configuration. By using the optimum capacitance, the PTE is further improved. To be summarized, the optimum capacitance of the asymmetrical relays should be designed to obtain maximum PTE as well as the optimum positions of relay resonators.

5. Conclusion

This work presents a new design method of asymmetrical relay resonators for efficient wireless power transfer. The

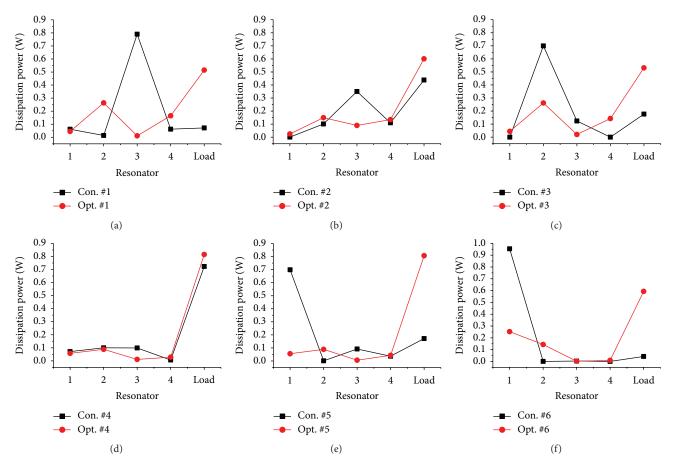


FIGURE 7: Dissipated power at four resonators and the load ($P_{in} = 1$ W).

TABLE 2: Designed parameters: (a) conventional design by $C_i = 1/(\omega_0^2 L_i)$ (*i* = 2, 3, 4) and (b) optimum design by GA.

 $\langle \rangle$

			(a)					
	#1	#2	#3	#4	#5	#6		
$R_L(\Omega)$	0.021	0.071	54.26	1.97	0.083	3.99		
$R_{S}\left(\Omega\right)$	0.289	42.37	383.59	0.252	0.026	0.019		
C_1 (pF)	1841	618.5	836	1483	1496	1495		
C_2 (pF)	821.6							
C_3 (pF)	5018							
C_4 (pF)	1495							
			(b)					
	#1	#2	#3	#4	#5	#6		
$R_L(\Omega)$	0.056	0.08	0.067	0.518	0.335	1.165		
$R_{S}\left(\Omega\right)$	0.397	0.717	0.401	0.311	0.323	0.07		
C_1 (pF)	1001	1004	1008	1494	1494	1495		
C_2 (pF)	758	764	765	819	813	800		
C_3 (pF)	3405	4960	3878	4597	4151	2584		
<i>C</i> ₄ (pF)	1495	1495	1454	1467	1493	1069		

optimum design of asymmetrical relay is performed by both optimum distances between the relays and optimum

capacitance of resonators. The optimum locations of the asymmetrical relay resonators were found as the positions of the relays are scanned. To further improve the PTE, the optimum capacitance of the resonators was determined by GA. To find optimum capacitance, GA can replace the complicated simultaneous equations. The PTEs when using the optimum capacitance are much higher and flatter than those using conventional capacitance as the positions of the relays are scanned. C1 and RS are not related to the PTE but are related to the impedance matching. The dissipated powers of each resonator and load are investigated, which assures that optimum currents of resonators are set by the optimum capacitance to transfer power more efficiently. The design method can be extended and applied to different configurations, that is, the relay resonators including a source and a load.

Competing Interests

The authors declare that they have no competing interests.

Acknowledgments

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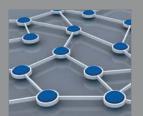
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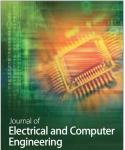
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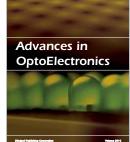


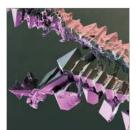


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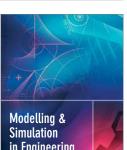


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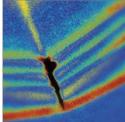




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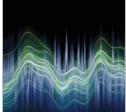


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