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# Dual yolk-shell structure of carbon and silica-coated silicon for high-performance lithium-ion batteries

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# Dual yolk-shell structure of carbon and silica-coated silicon for high-performance lithium-ion batteries

## Abstract

Silicon batteries have attracted much attention in recent years due to their high theoretical capacity, although a rapid capacity fade is normally observed, attributed mainly to volume expansion during lithiation. Here, we report for the first time successful synthesis of Si/void/SiO<sub>2</sub>/void/C nanostructures. The synthesis strategy only involves selective etching of SiO<sub>2</sub> in Si/SiO<sub>2</sub>/C structures with hydrofluoric acid solution. Compared with reported results, such novel structures include a hard SiO<sub>2</sub>-coated layer, a conductive carbon-coated layer, and two internal void spaces. In the structures, the carbon can enhance conductivity, the SiO<sub>2</sub> layer has mechanically strong qualities, and the two internal void spaces can confine and accommodate volume expansion of silicon during lithiation. Therefore, these specially designed dual yolk-shell structures exhibit a stable and high capacity of 956 mA hg<sup>-1</sup> after 430 cycles with capacity retention of 83%, while the capacity of Si/C core-shell structures rapidly decreases in the first ten cycles under the same experimental conditions. The novel dual yolk-shell structures developed for Si can also be extended to other battery materials that undergo large volume changes.

## Keywords

silica, yolk, coated, silicon, high, performance, lithium, ion, batteries, shell, structure, carbon, dual

## Disciplines

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## Dual yolk-shell structure of carbon and silica-coated silicon for high-performance lithium-ion batteries

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Silicon batteries have attracted much attention in recent years due to their high theoretical capacity, although a rapid capacity fade is normally observed, attributed mainly to volume expansion during lithiation. Here, we report for the first time successful synthesis of Si/void/SiO<sub>2</sub>/void/C nanostructures. The synthesis strategy only involves selective etching of SiO<sub>2</sub> in Si/SiO<sub>2</sub>/C structures with hydrofluoric acid solution. Compared with reported results, such novel structures include a hard SiO<sub>2</sub>-coated layer, a conductive carbon-coated layer, and two internal void spaces. In the structures, the carbon can enhance conductivity, the SiO<sub>2</sub> layer has mechanically strong qualities, and the two internal void spaces can confine and accommodate volume expansion of silicon during lithiation. Therefore, these specially designed dual yolk-shell structures exhibit a stable and high capacity of 956 mA h g<sup>-1</sup> after 430 cycles with capacity retention of 83%, while the capacity of Si/C core-shell structures rapidly decreases in the first ten cycles under the same experimental conditions. The novel dual yolk-shell structures developed for Si can also be extended to other battery materials that undergo large volume changes.

Recently, the rapidly rising price of petroleum and growing concerns about global warming have brought a great deal of attention to lithium-ion batteries with high capacity and energy density for future electric vehicles and portable electronics<sup>1–6</sup>. Nevertheless, the performance of currently commercialized lithium-ion batteries must be further improved to meet the increasing demand for high energy storage capacity. Therefore, novel electrode materials with higher capacity and higher power density are urgently needed.

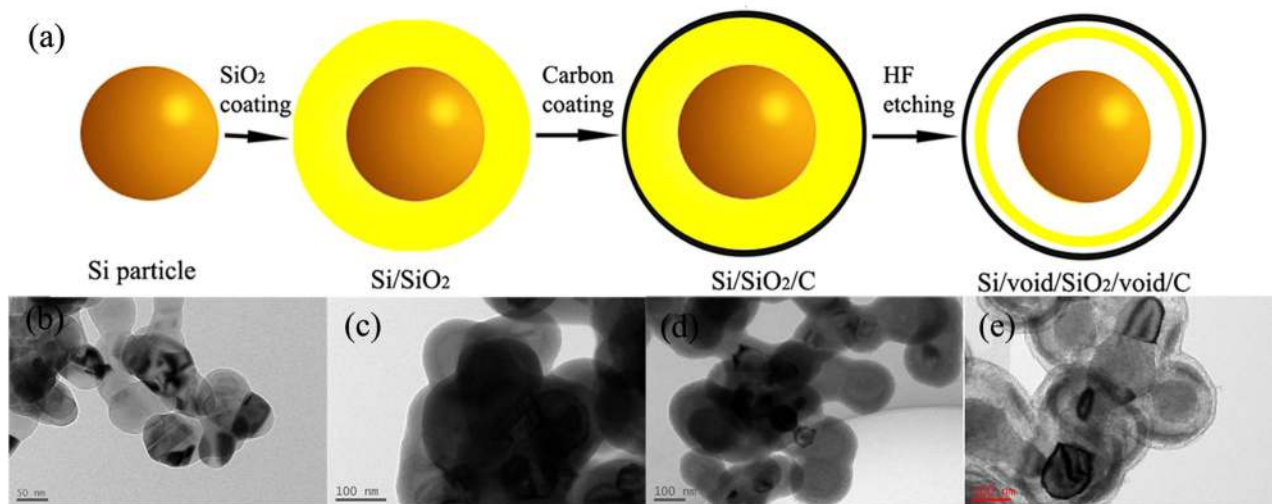
Among the various anode materials, silicon is one of the most promising candidates due to its high theoretical capacity (~3580 mA h g<sup>-1</sup>, Li<sub>15</sub>Si<sub>4</sub>) among alloy type anode materials and relatively low discharge potential (~0.4 V vs. Li/Li<sup>+</sup>)<sup>4,5</sup>. Despite these advantages, silicon anodes have two major disadvantages that have prevented their widespread use. First, the large volume changes (~300%) in silicon upon insertion and extraction of lithium-ions lead to severe electrode pulverization, which results in the loss of contact between the active materials and the current collector, leading to rapid capacity fading. Second, the continual pulverization of silicon during cycling causes the electrode surface to be cyclically exposed to the electrolyte. This generates continual formation of solid-electrolyte interphase (SEI) films, contributing to capacity fading and poor coulombic efficiency.

In an attempt to overcome these limitations of silicon, much attention has been devoted to the design and fabrication of silicon nanostructures, such as silicon nanowires<sup>6–8</sup> and nanotubes<sup>9,10</sup>, three-dimensional (3D) porous silicon<sup>11–14</sup>, and silicon in composites with carbon or oxides<sup>15–20</sup>. Encouraging results have been achieved through these efforts. In particular, Si/SiO<sub>x</sub> composites<sup>21,22</sup>, Si/SiO<sub>2</sub>/C<sup>23–25</sup>, and yolk-shell

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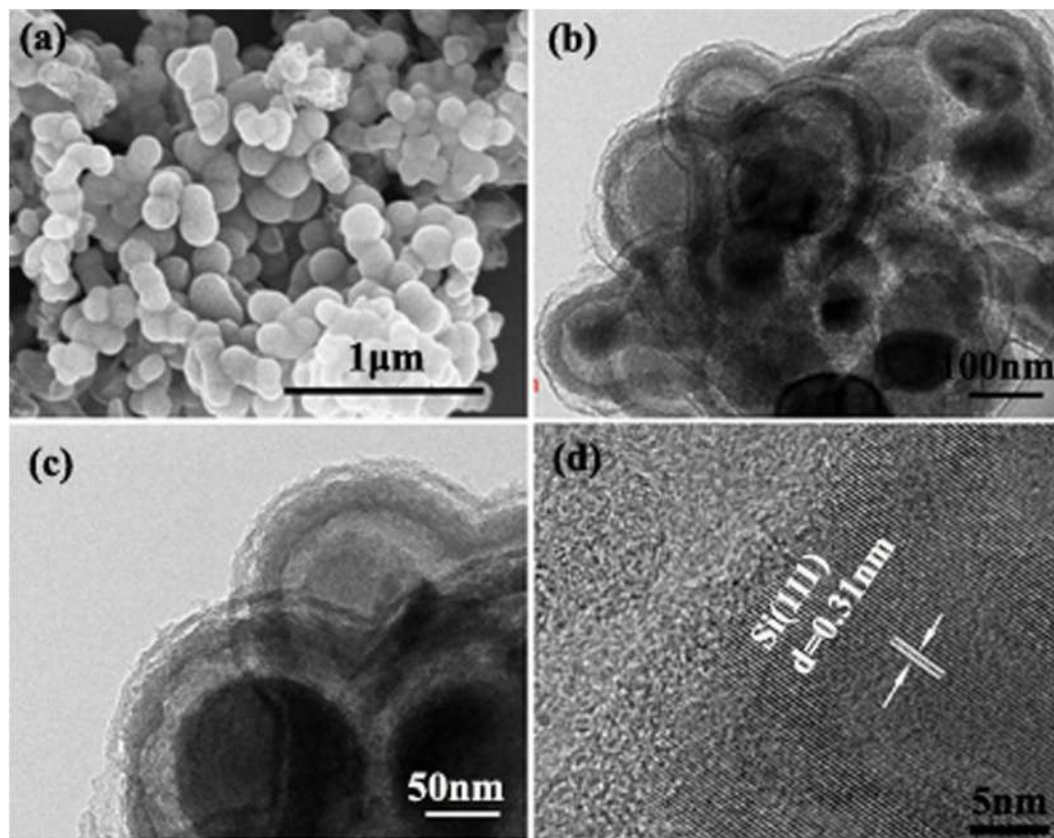
**Figure 1.** (a) Schematic illustration of the fabrication process for the dual yolk-shell structure. (b), (c), (d), and (e) Corresponding TEM images of Si, Si/SiO<sub>2</sub>, Si/SiO<sub>2</sub>/C, and Si/void/SiO<sub>2</sub>/void/C spheres.

structured Si/C<sup>26–28</sup> have demonstrated excellent electrochemical performance. These performances can be ascribed to the C or SiO<sub>x</sub> shell on the outside of the silicon, which can offer a static surface for the formation of a thin and stable SEI, preserving the anode from irreversible reaction with the electrolyte<sup>29–31</sup>. Additionally, the existing hollow structures in the yolk-shell structure can provide extra space for the volume expansion of silicon, which guarantees the structural integrity of the electrode<sup>29–31</sup>. These distinguishing features provide enlightened guidance for nanostructured design of high-performance silicon batteries.

In this paper, we uniformly coated silicon with a silica layer via the Stöber method and then coated Si/SiO<sub>2</sub> composites with a carbon layer. Taking advantage of the inhomogeneous nature of silica shells prepared by the Stöber method<sup>32</sup>, proper etching conditions were chosen for selectively etching the SiO<sub>2</sub> shell by means of a hydrofluoric acid (HF) treatment. By this process, a small portion of the outer layer and a large portion of the interior layer of the SiO<sub>2</sub> shell were removed. The novel Si/void/SiO<sub>2</sub>/void/C structures were obtained. The advantages of dual yolk-shell silicon structures lie in the presence of internal void spaces and the mechanically strong SiO<sub>2</sub> layer, which limits the degree of volume expansion of silicon during lithiation. In addition, the amorphous SiO<sub>2</sub> and C intrinsically have advantages as shell materials due to their chemical inertness, porous structure, and size-selective permeability<sup>33–37</sup>. They can provide a double barrier to prevent the electrolyte from reaching the surface of the silicon nanoparticles and protect the anode from subsequent irreversible reaction with the electrolyte<sup>31</sup>. With the help of the dual yolk-shell structures, the capacity of the silicon half-cell could be stabilized at 956 mA h g<sup>-1</sup> at 0.46 A g<sup>-1</sup> after 430 cycles with capacity retention of 83%, while the capacity of the Si/C core-shell structures rapidly decreases in the first ten cycles under the same experimental conditions. Therefore, such dual yolk-shell structures can also be extended to other battery materials that undergo large volume changes.

## Results

The schematic flowchart in Fig. 1 illustrates the major process steps employed in the present work. A SiO<sub>2</sub> layer was first coated on Si seeds using the Stöber method. The carbon layer was then obtained through a pyrolytic decomposition of polyvinylidene difluoride (PVDF), which had been already coated on the Si/SiO<sub>2</sub> nanoparticles. Finally, Si/void/SiO<sub>2</sub>/void/C nanostructures could be prepared by selectively etching the SiO<sub>2</sub> layer using an HF solution with a proper concentration. The corresponding transmission electron microscope (TEM) images are presented in Fig. 1b,c,d,e. Silica has been frequently used as a source of shell materials<sup>33–36</sup>, and the Stöber method has attracted much interest for the scalable fabrication of silica shells on nanoparticles via the facile hydrolysis of tetraethyl orthosilicate (TEOS). In addition, the SiO<sub>2</sub> layer has some unique chemical properties. For instance, Chen's group has demonstrated that the silica shell on nanoparticles formed by the Stöber method is intrinsically inhomogeneous. The outer layer of the shell is chemically more robust than the inner layer. They obtained multiple yolk-shell nanostructures through selectively etching the silica layer with hot water<sup>32</sup>. Also, Yin's group used only TEOS to generate core-shell structures, and they showed that polyvinyl pyrrolidone (PVP) could protect the outside of the silica layer to allow selective etching of the inner section<sup>38</sup>. These properties can be exploited for novel synthetic control of silica nanostructures. In our paper, the SiO<sub>2</sub> layer remains inhomogeneous after the coating with the carbon layer. Therefore, a thin layer (about 4–6 nm) on the robust outside of the silica, and at the same time, a thick layer (about 10–30 nm) on the soft inside of the silica can be etched away by the HF solution.



**Figure 2.** (a) SEM, (b) TEM, (c) magnified TEM, and (d) HRTEM images of Si/void/SiO<sub>2</sub>/void/C composite spheres.

Figure 2 shows scanning electron microscope (SEM) images of the dual yolk-shell Si nanocomposite spheres, which exhibit an average diameter of 190 ( $\pm 10$ ) nm. TEM and high resolution TEM (HRTEM) (Fig. 2b,c, d) observations confirm that dual yolk-shell silicon structures with two hollow buffers and two amorphous shells were obtained.

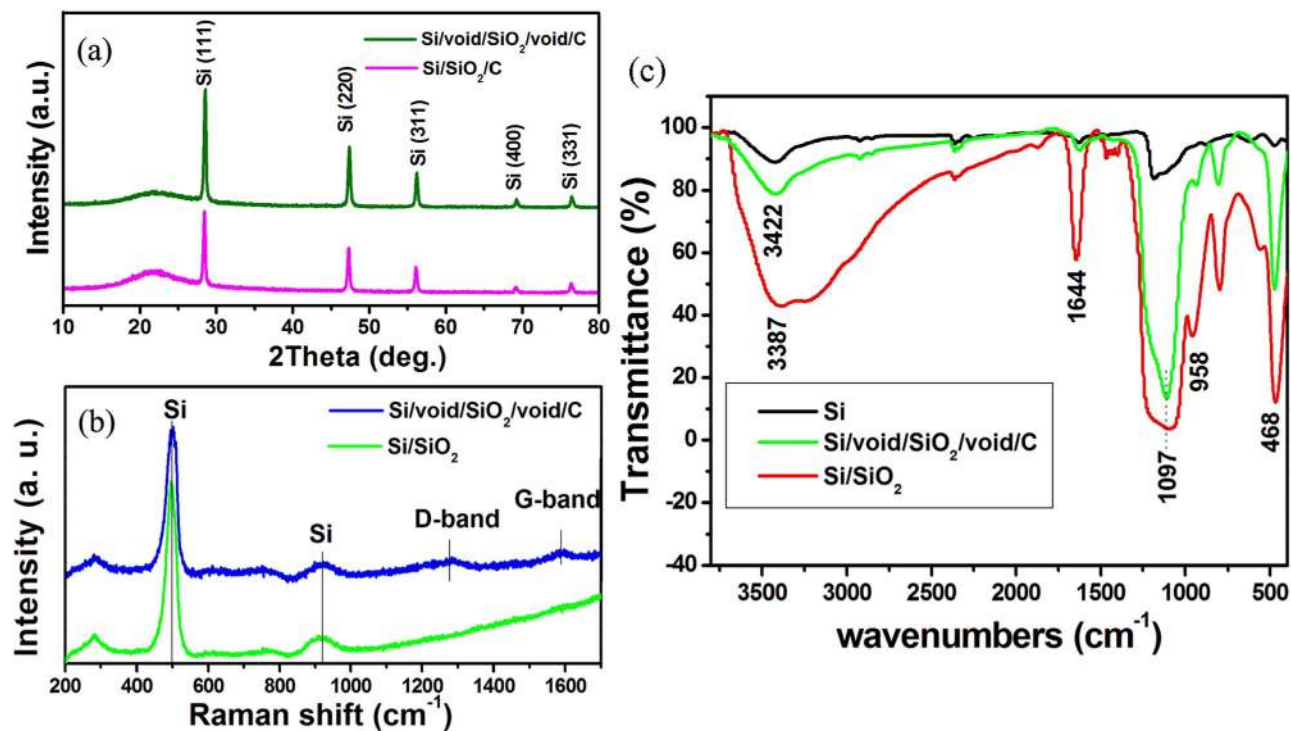
The X-ray diffraction (XRD) patterns of the Si/SiO<sub>2</sub>/C nanocomposite before and after the selective etching process with HF solution are shown in Fig. 3a. The diffraction peaks appearing at 28.4, 47.3, 56.1, 69.1, and 76.3° can be indexed respectively to the (111), (220), (311), (400), and (331) planes of Si crystallites (ICDD JCPDS no. 27-1402). A broad peak appearing at around 22° indicates the amorphous character of the silica shell. After the selective etching of the silica, a drop in the intensity ratio of amorphous SiO<sub>2</sub> to Si is clearly observable, indicating the decreased amount of SiO<sub>2</sub>. Figure 3b shows the Raman spectra of the Si/SiO<sub>2</sub> and Si/void/SiO<sub>2</sub>/void/C nanocomposites. It can be seen that due to the presence of Si nanoparticles, these composites exhibit Si peaks at around 500 cm<sup>-1</sup> and 918 cm<sup>-1</sup><sup>39</sup>. The observation of two peaks at 1282 cm<sup>-1</sup> (known as the D band) and 1591 cm<sup>-1</sup> (known as the G band)<sup>25,40</sup> are characteristic of the presence of carbon materials in Si/void/SiO<sub>2</sub>/void/C samples.

The Fourier transform infrared spectroscopy (FTIR) spectra in Fig. 3c show the characteristic transmittances of Si, Si/SiO<sub>2</sub>, and Si/void/SiO<sub>2</sub>/void/C powders. Si/SiO<sub>2</sub> nanocomposite shows peaks at 3387 cm<sup>-1</sup>, 1644 cm<sup>-1</sup>, 1097 cm<sup>-1</sup>, 958 cm<sup>-1</sup>, and 468 cm<sup>-1</sup>, corresponding to -OH, -OH, Si-O, Si-OH, and Si-O-Si stretching, respectively<sup>25,41</sup>. The peaks at 3387 cm<sup>-1</sup> and 1644 cm<sup>-1</sup> in the spectrum of the Si/void/SiO<sub>2</sub>/void/C powders are reduced, which can be attributed to the decomposition of the -OH band during the carbonization to produce the carbon coating on Si/SiO<sub>2</sub>. The presence of Si-O and Si-O-Si in the products demonstrates the formation of SiO<sub>2</sub> in the Si/SiO<sub>2</sub> and Si/void/SiO<sub>2</sub>/void/C nanocomposites.

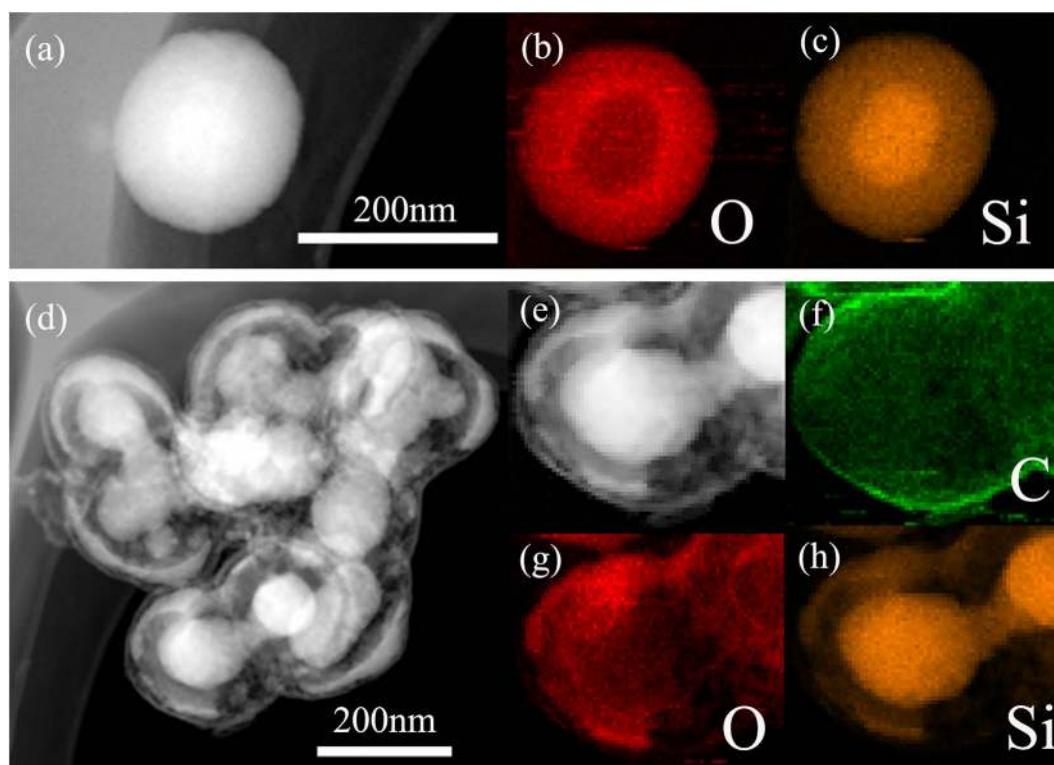
Further evidence for Si/SiO<sub>2</sub> and Si/void/SiO<sub>2</sub>/void/C nanostructures was identified by dark-field scanning transmission electron microscopy (STEM) images and energy dispersive X-ray (EDX) element mappings (Fig. 4). The SiO<sub>2</sub> coating can be distinguished in the Si/SiO<sub>2</sub> nanostructures from the O and Si mappings. The void space between Si and SiO<sub>2</sub> in the Si/void/SiO<sub>2</sub>/void/C nanostructures can be seen from the Si and O mappings, and the void space between C and SiO<sub>2</sub> can be seen in the STEM image. The TEM, XRD, Raman, and FTIR investigations, as well as the element mapping data, indicate that the nanocomposites consist of a silicon core, a shell of amorphous SiO<sub>2</sub>, two void spaces, and a carbon layer in the Si/void/SiO<sub>2</sub>/void/C structures.

To test the electrochemical performance of the dual yolk-shell Si nanocomposite, a two-electrode coin cell using the nanocomposite as the electrode material and lithium metal as counter electrode was

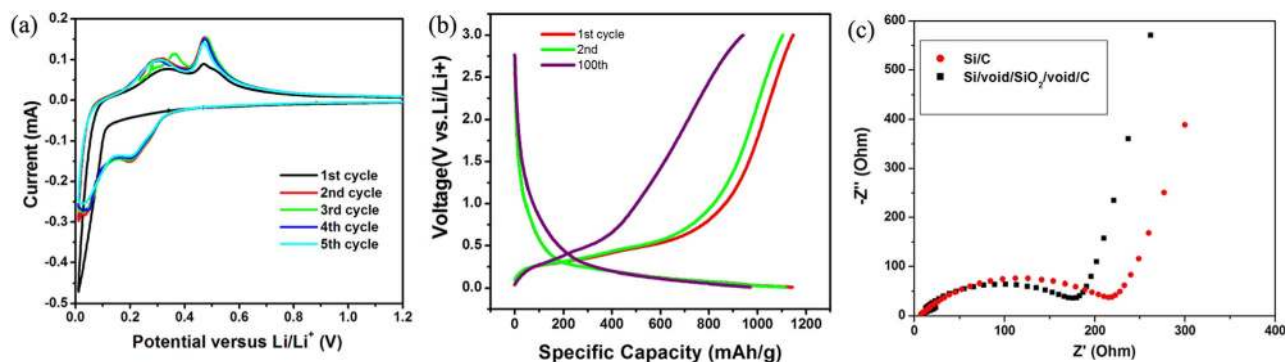




**Figure 3.** (a) XRD patterns of Si/SiO<sub>2</sub>/C composites before and after HF treatment. (b) Raman spectra of Si/SiO<sub>2</sub> and Si/void/SiO<sub>2</sub>/void/C composites. (c) FTIR spectra of Si, Si/SiO<sub>2</sub>, and Si/void/SiO<sub>2</sub>/void/C composite.



**Figure 4.** (a) STEM image of Si/SiO<sub>2</sub>. (b), (c) corresponding EDX mapping images of O (red) and Si (orange). (d, e) STEM image of Si/void/SiO<sub>2</sub>/void/C. (f), (g), (h) corresponding EDX mapping images of C (green), O (red), and Si (orange).

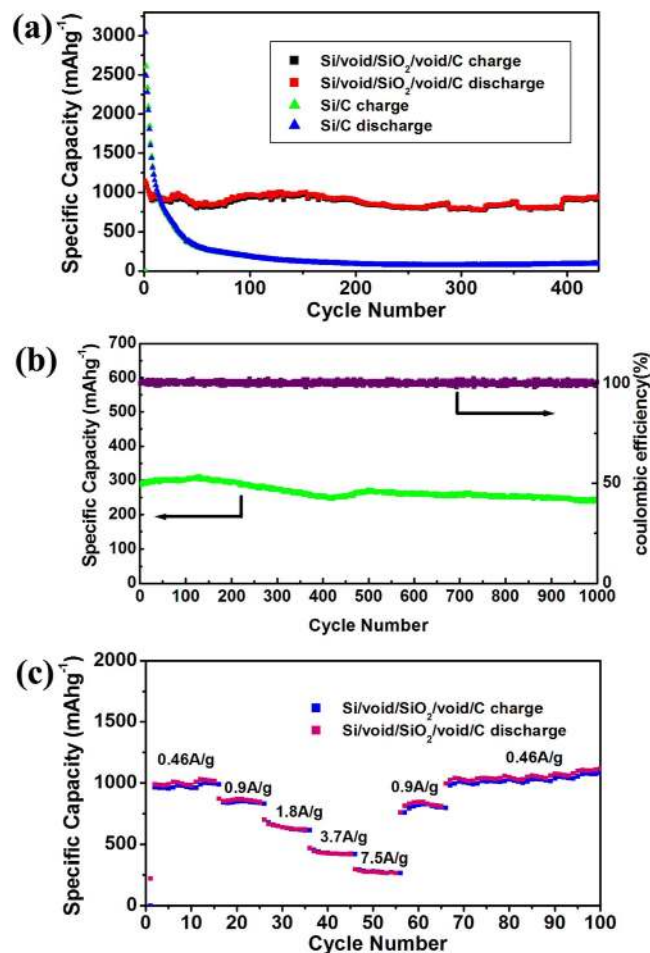


**Figure 5.** (a) Cyclic voltammograms (CVs) from the first 5 cycles for Si/void/SiO<sub>2</sub>/void/C from 0.01 V to 3 V (with only 0.01–1.2 V shown) at a scan rate of 0.05 mV s<sup>-1</sup>. (b) Charge and discharge voltage profiles of Si/void/SiO<sub>2</sub>/void/C composite for the 1<sup>st</sup>, 2<sup>nd</sup>, and 100<sup>th</sup> cycles tested between 0.01 and 3 V at a rate of 0.46 A g<sup>-1</sup>. (c) EIS results for Si/void/SiO<sub>2</sub>/void/C and Si/C composites.

fabricated. Cyclic voltammetry experiments (Fig. 5a) show lithiation and delithiation peaks at potentials typical of the reactions of Si<sup>42</sup>. The peak at 0.19 V in the cathodic branch (lithiation) corresponds to the conversion of amorphous Si to Li<sub>x</sub>Si. In the anodic branch (delithiation), the two peaks at 0.31 V and 0.47 V are attributed to the delithiation of Li<sub>x</sub>Si back to amorphous Si<sup>43</sup>. No SEI formation peak (0.34–0.36 V)<sup>44</sup> can be observed in the first cycle in the CV curves. Moreover, an activation process occurs, as indicated by the increase in the CV peak intensity. Similar phenomena have been observed elsewhere<sup>8,26,44,45</sup>. The phenomena can be ascribed to the presence of C or an oxide shell outside of the silicon, which can offer a static surface for the formation of a thin and stable SEI, preserving the anode from irreversible reactions with the electrolyte<sup>29–31,44</sup>. In addition, in the work of Li's group<sup>44</sup>, SEI formation on hollow carbon nanospheres/silicon/alumina core-shell structures was not observed, because the insulating Al<sub>2</sub>O<sub>3</sub> layer suppresses electron transfer from Si to the electrolyte. The decomposition of LiPF<sub>6</sub> to form LiF and PF<sub>5</sub> is the only possible mechanism that can occur<sup>44</sup>. Thus only a small amount of LiF is formed due to the decomposition of LiPF<sub>6</sub> with heat and moisture. Since this process does not involve Li<sup>+</sup>, this may not appear in the CV curves, unlike conventional SEI formation on Si surfaces<sup>44</sup>. Their work provides a new understanding of the mechanism of SEI formation. In our work, the formation of the SEI film on C and oxide shells may be a slow process during the first few cycles, so that the SEI formation peaks were not obvious in the first few cycles in the CV curves. Another possibility is that there is no SEI formation during the cycling, because the insulating SiO<sub>2</sub> layer has the same effect as the Al<sub>2</sub>O<sub>3</sub> layer in the work of Li's group. In the activation process, it may take a few cycles for the outer layers of SiO<sub>2</sub> and C to become ionically conductive, allowing each Si particle to become lithiated. The activation process can also be observed in our cycling performance in Fig. 6b, as the capacity increases in the first few cycles. Figure 5b shows the 1<sup>st</sup>, 2<sup>nd</sup>, and 100<sup>th</sup> voltage profiles in the charge and discharge processes in the potential window of 0.01–3 V vs Li/Li<sup>+</sup>. The first cycle at a current rate of 0.46 A g<sup>-1</sup> shows charge and discharge capacity of 1147 and 1143 mA h g<sup>-1</sup>, respectively. After 2 and 100 cycles, the capacities are 1113 mA h g<sup>-1</sup> and 968 mA h g<sup>-1</sup>, respectively. The electrochemical impedance spectroscopy (EIS) technique was also utilized to clarify the electrochemical performance of the dual yolk-shell Si nanocomposite compared with Si/C nanocomposite<sup>46</sup>. The charge-transfer resistance parameters of the Si/C electrode are obviously larger than those of the Si/void/SiO<sub>2</sub>/void/C nanocomposite electrode (Fig. 5c).

The Si/void/SiO<sub>2</sub>/void/C electrode displays stable cycling performance and maintains a reversible discharge capacity of 956 mA h g<sup>-1</sup> after 430 cycles at current density of 0.46 A g<sup>-1</sup> (Fig. 6a), which is 2.5 times higher than the theoretical capacity of graphite. At the same time, Si/C nanocomposite was also tested in lithium-ion batteries as comparison samples. The initial capacity of Si/C nanocomposite reaches up to 3150 mA h g<sup>-1</sup> at current density of 0.46 A g<sup>-1</sup>, although a rapid capacity fade is observed upon further cycling. This can be attributed to the expansion/contraction of silicon during the charge and discharge, which results in the pulverization and disruption of the microstructure of the electrode. Figure 6b presents the long-term cycling performance at a charge/discharge rate of 5.8 A g<sup>-1</sup>, which shows stable capacity around 250 mA h g<sup>-1</sup>. Capacity degradation is almost negligible, demonstrating the good stability of the dual yolk-shell silicon structures. Figure 6c shows the charge/discharge capacity at different current densities. Capacity above 950, 830, 610, 410, and 260 mA h g<sup>-1</sup> is retained at current density of 0.46, 0.9, 1.8, 3.7, and 7.5 A g<sup>-1</sup>, respectively, and when the current density is changed back to 0.46 A g<sup>-1</sup>, the specific capacity recovers to 1000 mA h g<sup>-1</sup>.

From Table 1, it can be seen that although our sample shows worse cyclability than the Si-C yolk shell structure, it is better than those of Si-SiO<sub>x</sub> core-shell nanowire, Si-SiO<sub>2</sub> core@shell nanowire, silicon@carbon hollow core-shell, Si@SiO<sub>x</sub>/C nanocomposite, and silicon@TiO<sub>2-x</sub>/carbon microfiber. Considering that the loading of active materials for coin cell testing is about 1.5 mg cm<sup>-2</sup> and the thickness of the



**Figure 6.** (a) Cycling behavior of Si/void/SiO<sub>2</sub>/void/C and Si/C composites at a current density of 0.46 A g<sup>-1</sup>. (b) Cycling performance and coulombic efficiency of Si/void/SiO<sub>2</sub>/void/C at a current density of 5.8 A g<sup>-1</sup>. (c) Rate capability of Si/void/SiO<sub>2</sub>/void/C composite at different current densities.

Comparison of electrochemical properties of silicon with different structures					
sample	current density	Capacity (mAh g <sup>-1</sup> ) (cycle number)	capacity retention	mass of active materials	thickness of active materials
3D mesoporous silicon@graphene <sup>16</sup>	1 A g <sup>-1</sup>	1200 (200)	89.1%	–	–
silicon@TiO <sub>2-x</sub> /carbon microfiber <sup>19</sup>	200 mA g <sup>-1</sup>	1050(50)	90%	–	–
Si/CNT/C composite <sup>20</sup>	1 A g <sup>-1</sup>	2100 (100)	95.5%	0.6-0.8 mg cm <sup>-2</sup>	–
Si-SiO <sub>2</sub> core@shell nanowire <sup>21</sup>	420 mA g <sup>-1</sup>	3371 (50)	62%	–	–
Si-SiO <sub>x</sub> Core-Shell Nanowire <sup>22</sup>	740 mA g <sup>-1</sup>	1910 (100)	95%	–	–
Si@SiO <sub>x</sub> /C Nanocomposite <sup>24</sup>	150 mA g <sup>-1</sup>	1100 (60)	–	–	–
Si-C yolk shell structure <sup>26</sup>	420 mA g <sup>-1</sup>	2800 (1000)	74%	–	–
Silicon@carbon hollow core-shell <sup>28</sup>	50 mA g <sup>-1</sup>	625.3 (40)	76.8%	–	–
Present work	460 mA g <sup>-1</sup>	1147 (430)	83%	1.5 mg cm <sup>-2</sup>	200 μm

**Table 1.** Comparison of electrochemical properties of silicon with different structures.

active materials is 200 μm, our sample indeed shows excellent performance as anode material for lithium ion batteries.

## Discussion

The improved cyclability could be ascribed to the characteristics of such dual yolk-shell structures. First, the void spaces in the dual yolk-shell structures allow for some volume expansion of silicon. At the



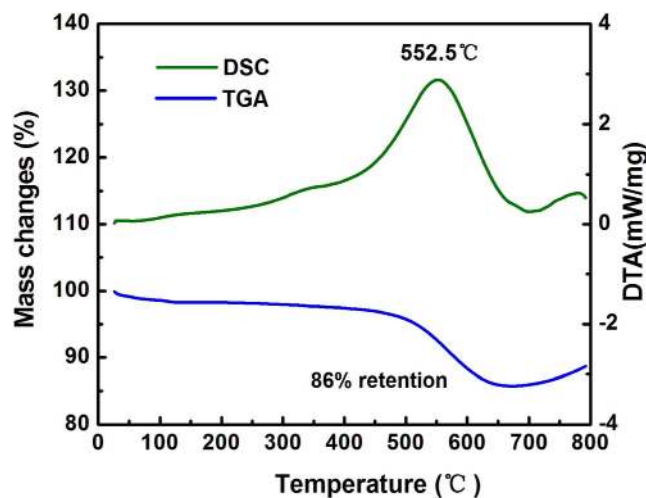


Figure 7. TGA and DSC curves for Si/void/SiO<sub>2</sub>/void/C composites.

same time, the SiO<sub>2</sub> layer is mechanically strong and can successfully prevent the Si from expanding, while still allowing lithium ions to pass through. Secondly, the SiO<sub>2</sub> layer combined with the outside C layer could provide double protection for the anode from irreversible reaction with the electrolyte, and a stable SEI could be built on the outside surface. As a result, these dual yolk-shell silicon structures show stable cyclability.

In the Si/void/SiO<sub>2</sub>/void/C nanocomposite, the electron and ion transport pathways may be a problem due to the void spaces. Numerous nanocomposites containing voids, however, have been reported for use in lithium-ion batteries. We suppose that the electrons and ions are transported through the contact points between the core and SiO<sub>2</sub> shell, as well as between the SiO<sub>2</sub> shell and the C shell in our work. So, the decrease in the capacity at large current density (Fig. 6) can be partly ascribed to the slow diffusion of Li<sup>+</sup> in the Si/void/SiO<sub>2</sub>/void/C structures.

Thermogravimetric analysis (TGA) of the dual yolk-shell silicon structures was carried out in air atmosphere from 30 °C to 800 °C (Fig. 7). The weight content of carbon is about 14%. It is assumed that there was no loss of silicon during the whole synthesis process of the Si/void/SiO<sub>2</sub>/void/C nanocomposite (since losses were avoided as far as possible in the process of collecting products in each step, and in addition, Si is inert compared to SiO<sub>2</sub> in the reaction with HF). Therefore, the weight content of silicon can be estimated by the data recorded in each step. The weight ratio of silicon in the Si/void/SiO<sub>2</sub>/void/C nanocomposite is about 64%, and that of silica is about 22%.

## Methods

**Material preparation.** Si/void/SiO<sub>2</sub>/void/C nanospheres were synthesized in three steps. Firstly, 0.4 g silicon nanoparticles (with an average diameter of ~100 nm) were dispersed in a mixture of 74 ml ethanol and 10 ml water by ultrasonication. Then 0.15 g polyvinylpyrrolidone (PVP) and 3 ml ammonia water (30%) were added into this solution. 6 ml tetraethyl orthosilicate (TEOS) was added dropwise into the solution under vigorous stirring, and the reaction was incubated at room temperature under stirring for 1 h. The resulting Si/SiO<sub>2</sub> nanoparticles were isolated by centrifugation. Secondly, polyvinylidene fluoride (PVDF) powder was dissolved in N-methyl-2-pyrrolidinone (NMP) in a weight ratio of 6%<sup>18</sup>. Then, the Si/SiO<sub>2</sub> powders were added into the solution and stirred for 24 h. The obtained suspension was dried at 90 °C under vacuum for 48 h to vaporize the NMP solvent. The dried substance was transferred into a furnace and pyrolyzed at 650 °C under Ar atmosphere for 2 h to obtain Si/SiO<sub>2</sub>/C composite. Then, the Si/SiO<sub>2</sub>/C nanoparticles were milled in an agate mortar and sieved for subsequent use. Finally, the obtained Si/SiO<sub>2</sub>/C composite was immersed in 0.8 M HF solution for 1 h to selectively etch the SiO<sub>2</sub> layer. The resulting solid was collected after washing with water four times by centrifugation. Dual yolk-shell structures were obtained.

**Characterization.** The morphology and diameter of the Si/void/SiO<sub>2</sub>/void/C nanospheres were characterized with a scanning electron microscope (SEM, FEI Nova Nano SEM 230) and a transmission electron microscope (TEM, JEOLJEM-2100F). Powder X-ray diffraction (XRD) patterns were obtained using an X-ray diffractometer (XRD, Rigaku D/max 2500 XRD with Cu-K $\alpha$  radiation,  $\lambda = 1.54178 \text{ \AA}$ ). The amount of C in the Si/void/SiO<sub>2</sub>/void/C nanoparticles was confirmed by a combined differential scanning calorimetry (DSC) and thermogravimetric analysis (TGA) instrument (SDT, Q600) in air atmosphere with a heating rate of 5 °C/min. Raman (LabRam HR-800) and Fourier transform infrared spectroscopy (FTIR) (NICOLET 6700) were also conducted.

**Electrochemical Measurements.** The electrodes were prepared by coating Cu foil with slurries containing the Si/void/SiO<sub>2</sub>/void/C nanoparticles (70%) as active material, a conducting agent (acetylene carbon, 20%), and polyacrylic acid binder (PAA, 10%), dissolved in N-methyl-2-pyrrolidinone. After coating, the electrode was dried under vacuum at 90 °C for 10 h. The loading of active materials for the coin cell testing was about 1.5 mg cm<sup>-2</sup>, and the thickness of the active materials was about 200 μm. The coin-type half-cells were assembled in an Ar-filled glove box. A polyethylene membrane was used as separator, and the electrolyte was 1 M LiPF<sub>6</sub> dissolved in a mixed solvent of ethylene carbonate (EC) and dimethyl carbonate (DMC) (1:1, v/v). The cells were galvanostatically charged and discharged in the voltage range of 0.01–3 V vs. Li/Li<sup>+</sup> using a Land Battery Tester (Land CT 2001 A, Wuhan, China) at different current densities at room temperature. Cyclic voltammetry (CV) was carried out on an electrochemical workstation (Chi604e, China) at a scan rate of 0.05 mV s<sup>-1</sup> in the voltage range of 0.01–3 V vs. Li/Li<sup>+</sup>. The electrochemical impedance spectroscopy (EIS) was performed on a ZAHNER-IM6ex electrochemical workstation (ZAHNER Co. Germany) in the frequency range of 100 kHz to 10 mHz on a cell in as-assembled condition.

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## Author Contributions

J.L., L.Y.Y., H.Z.L., Z.Q.S. and M.L. wrote the main manuscript text, and S.S.T. prepared Fig. 2. All authors reviewed the manuscript.

## Additional Information

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