

# Effectively modulating thermal activated charge transport in organic semiconductors by precise potential barrier engineering

Yinan Huang<sup>1,7</sup>, Xue Gong<sup>2,3,7</sup>, Yancheng Meng<sup>2,3</sup>, Zhongwu Wang<sup>1</sup>, Xiaosong Chen<sup>1</sup>, Jie Li<sup>1</sup>, Deyang Ji<sup>1,6</sup>, Zhongming Wei<sup>4</sup>, Liqiang Li<sup>1,5</sup>✉ & Wenping Hu<sup>1,5</sup>

The temperature dependence of charge transport dramatically affects and even determines the properties and applications of organic semiconductors, but is challenging to effectively modulate. Here, we develop a strategy to circumvent this challenge through precisely tuning the effective height of the potential barrier of the grain boundary (i.e., potential barrier engineering). This strategy shows that the charge transport exhibits strong temperature dependence when effective potential barrier height reaches maximum at a grain size near to twice the Debye length, and that larger or smaller grain sizes both reduce effective potential barrier height, rendering devices relatively thermostable. Significantly, through this strategy a traditional thermo-stable organic semiconductor (dinaphtho[2,3-b:2',3'-f]thieno[3,2-b]thiophene, DNNT) achieves a high thermo-sensitivity (relative current change) of 155, which is far larger than what is expected from a standard thermally-activated carrier transport. As demonstrations, we show that thermo-sensitive OFETs perform as highly sensitive temperature sensors.

<sup>1</sup>Tianjin Key Laboratory of Molecular Optoelectronic Sciences, Department of Chemistry, Institute of Molecular Aggregation Science, Tianjin University, 300072 Tianjin, China. <sup>2</sup>School of Nano-Tech and Nano-Bionics, University of Science and Technology of China, Hefei 230026 Anhui, China. <sup>3</sup>Suzhou Institute of Nano-Tech and Nano-Bionics (SINANO), Chinese Academy of Sciences, Suzhou 215123 Jiangsu, China. <sup>4</sup>State Key Laboratory of Superlattices and Microstructures, Institute of Semiconductors, Chinese Academy of Sciences, 100083 Beijing, China. <sup>5</sup>Joint School of National University of Singapore and Tianjin University, International Campus of Tianjin University, Binhai New City, 350207 Fuzhou, China. <sup>6</sup>Present address: Beijing National Laboratory for Molecular Sciences, 100190 Beijing, China. <sup>7</sup>These authors contributed equally: Yinan Huang, Xue Gong. ✉email: [lilq@tju.edu.cn](mailto:lilq@tju.edu.cn)

Organic semiconductor has been demonstrated to be one highly promising material for the future-generation electronics because of light weight, low cost, mechanical flexibility, and tunable structure and property<sup>1–4</sup>. Electronic charge transport property of organic semiconductor materials is the basis for some applications, and usually exhibits temperature-dependent behavior<sup>5–7</sup>. The temperature dependence of charge transport in organic semiconductors dramatically affects and even determines their performance and applications. For example, the weak temperature dependence makes the properties of semiconductors thermo-stable, which is generally desirable for the applications of power devices and integrated circuits<sup>8–10</sup>. In contrast, the strong temperature dependence generates thermo-sensitive material, which may be suitable for sensing<sup>11,12</sup>. However, until now, the effective and reliable modulation of the temperature dependence of charge transport in organic semiconductor is still a great challenge.

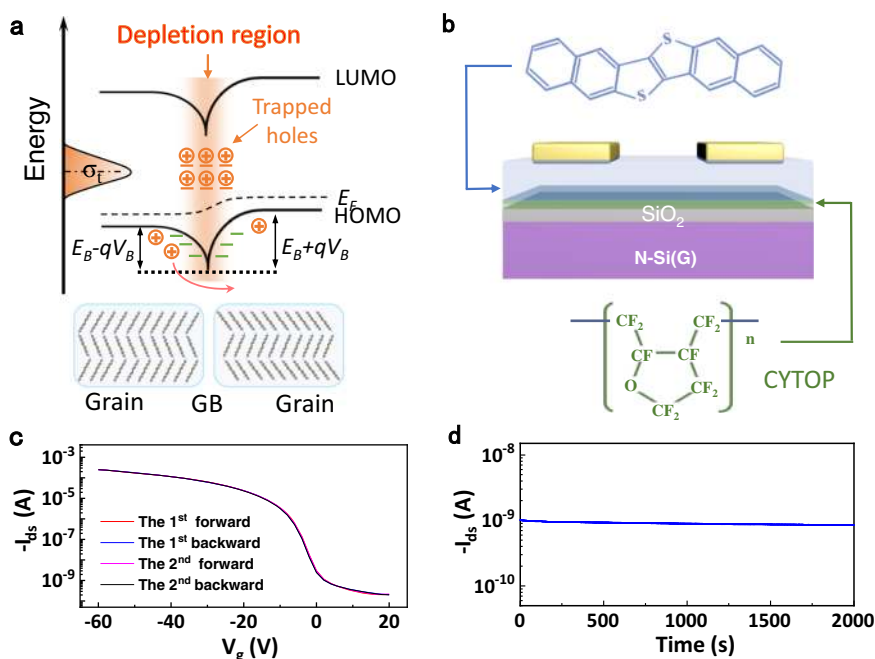
Organic semiconductors generally form polycrystalline film with grain boundaries (GBs) by the commonly-used vacuum or solution preparation method<sup>13–15</sup>. GB, a kind of local trap, in polycrystalline film significantly affects the charge transport<sup>16–20</sup>. Because of the existence of traps, some charges will be trapped at GB. The trapped charges repel other charges of the same sign, which leads to the formation of space-charge region (SCR) with built-in potential<sup>21–23</sup>. The SCR in p-type organic semiconductor is hole depletion, and thus produces a downward local band bending (Fig. 1a), whose degree depends on the height of potential barrier ( $E_B$ ) of SCR<sup>24,25</sup>. The carriers around the local banded band can hop across the potential barrier through a thermal activated way, i.e., thermionic emission<sup>26,27</sup>. The height of the potential barrier may be varied with bias voltage<sup>22,28</sup>, so the effective height of the potential barrier ( $E_{Be}$ ) plays an important role in the thermal activated charge transport. Therefore, we may modify the temperature dependence of charge transport in organic semiconductors through designing and regulating the effective height of the potential barrier. However, this strategy has

not been realized previously due to the great difficulty in the precise modulation of  $E_{Be}$ .

Here, we have realized the precise potential barrier engineering by elaborately tuning the grain size of polycrystalline film. On this basis, we have successfully achieved the effective modulation of the temperature dependence of thermal activated charge transport in organic semiconductor. Significantly, through the above strategy, a traditional thermo-stable organic semiconductor (dinaphtho[2,3-b:2',3'-f]thieno[3,2-b]thiohene, DNTT) has been transformed into highly thermo-sensitive when  $E_{Be}$  reaches the maximum at the point of grain size ( $l$ ) near to twice of Debye length ( $l_D$ ) (i.e.,  $l \approx 2l_D$ ). Whereas, larger or smaller grain size both reduce  $E_{Be}$ , which enables these DNTT devices to be relatively thermo-stable. In addition, we also have implemented the modulation of thermal activated charge transport in pentacene by the same strategy, demonstrating the universality of this strategy. As a temperature sensor<sup>29,30</sup>, DNTT device shows a high sensitivity (defined as the relative current change value) of 155. To our knowledge, it is the best performance among the reported temperature sensors based on organic field-effect transistor (OFET)<sup>31–35</sup>. As demonstrations, the DNTT OFET has achieved detection of ambient infrared (IR), identification of human touch and mapping of temperature distribution.

## Results

**Theoretical analysis of potential barrier engineering.** Figure 1a shows the energy-level diagram including band bending at GB of p-type organic semiconductor. The positive charges trapped at GB repel other charges of the same sign, and are screened by bared negative charges to remain macroscopic electrical neutrality, which results in a built-in potential  $\phi_B$ <sup>36–40</sup>. The SCR of hole depletion is thus formed, which produces a downward band bending of local energy level at GB, just like two back-to-back Schottky barriers<sup>22,27,39</sup>. The carriers at GB are severely restricted



**Fig. 1** Energy band diagram, OFET structure and operational stability data. **a** Schematic diagrams of energy band model at grain boundary (GB) of polycrystalline semiconductor with a bias voltage. **b** Schematic illustration of OFET and molecular structure of DNTT and CYTOP. **c** OFET transfer curves during the two cycle tests.  $V_{ds} = -60$  V. **d** Bias stress curve of the OFET.  $V_{gs} = 0$  V, and  $V_{ds} = -60$  V. The data in (c, d) denote the excellent operational stability of DNTT OFET.

by potential barrier of SCR and basically can only hop across the barrier through thermionic emission. According to Bethe's thermionic emission theory<sup>28,41</sup>, the net current in Schottky barrier is a superposition of forward current and reverse current. As shown in Fig. 1a, the effective height of potential barrier (mentioned as  $E_{Be}$ ) on the forward bias side is  $E_B - qV_B$ , and that on the reverse bias is  $E_B + qV_B$ . Therefore, charge carriers on the reverse bias can hardly cross the higher potential barrier, so the corresponding current is negligible. Therefore, the current density across a GB can be given by

$$J = A^* T^2 \exp\left(-\frac{E_F - E_V + E_B - qV_B}{k_B T}\right), \quad (1)$$

where  $A^* = \frac{4\pi q m^* k_B^2}{h^3}$  is effective Richardson constant,  $q$  is elementary charge,  $m^*$  is the effective mass of the carrier,  $k_B$  is the Boltzmann's constant,  $h$  is Planck constant,  $E_V$  is the energy level of HOMO without bending,  $V_B$  is the voltage drop across a GB, and  $E_B$  is the height of potential barrier at GB.  $E_F - E_V$  makes no difference for devices fabricated by the same materials and processes, so  $E_{Be} = E_B - qV_B$  is the key parameter that plays an important role in charge transport. These two parameters  $E_B$  and  $V_B$  can be given by theoretical model.

We assume that the DNTT film is composed of identical crystallites with a grain size of  $l$ . The minority carrier (i.e., electron) is negligible and all discussions about carriers are for majority carrier (i.e., hole). According to theoretical model of GBs<sup>27,42</sup>,  $E_B$  is dependent on the total carrier density  $n$  cm<sup>-3</sup>, trap density at GBs  $Q_t$  cm<sup>-2</sup>. Its boundary conditions can be given by the relation between grain size  $l$  and Debye length  $l_D$ <sup>23</sup>. If  $l < 2l_D$ , the crystallite is completely depleted of carriers and the traps are partially filled (i.e.,  $nl < Q_t$ ),

$$E_B = q\phi_B = \frac{q^2 l^2 n}{8\epsilon_s \epsilon_0} \quad l < 2l_D. \quad (2)$$

If  $l > 2l_D$ , the crystallite is partly depleted and the traps are fully filled (i.e.,  $nl > Q_t$ ), which enable  $E_B$  to be independent of  $l$ ,

$$E_B = q\phi_B = \frac{q^2 Q_t^2}{8\epsilon_s \epsilon_0 n} \quad l > 2l_D, \quad (3)$$

where  $\epsilon_0$  and  $\epsilon_s$  are vacuum permittivity and relative permittivity, respectively.  $l_D = \sqrt{\epsilon_s \epsilon_0 k_B T / q^2 n}$  is the characteristic length of SCR and screen length against the trapped charges at the GBs<sup>43,44</sup>.

The resistance of a polycrystalline material consists of the contributions from the grain-boundary region ( $R_B$ ) and the bulk of the crystallite ( $R_C$ ). Assuming that the grains are seamless connection, the channel (with length of  $L$ ) contains  $L/l$  grains and GBs, and then  $V_B$  can be described as  $V_{ds} \frac{R_B}{R_B + R_C} \frac{L}{l}$ , where  $V_{ds}$  is the voltage drop of the channel. If we define  $\beta = \frac{R_B}{R_B + R_C}$ ,  $V_B$  becomes  $\frac{\beta V_{ds}}{L} l$ . Using Eqs. (2), (3) and  $V_B = \frac{\beta V_{ds}}{L} l$  together, the effective height of barrier  $E_{Be}$  can be expressed as follow

$$E_{Be} = E_B - qV_B = \frac{q^2 n}{8\epsilon_s \epsilon_0} l^2 - \frac{q\beta V_{DS}}{L} l, \quad l < 2l_D. \quad (4)$$

$$E_{Be} = E_B - qV_B = \frac{q^2 Q_t^2}{8\epsilon_s \epsilon_0 n} - \frac{q\beta V_{DS}}{L} l, \quad l > 2l_D. \quad (5)$$

On the basis of the above equations and analysis, the precise design of grain size  $l$  in a certain range can tune the effective height of barrier  $E_{Be}$  by controlling  $E_B$  and  $V_B$ , which can thus modulate temperature dependence of charge transport according to Eq. (1). However, the above theoretical analysis has not been realized in organic semiconductors.

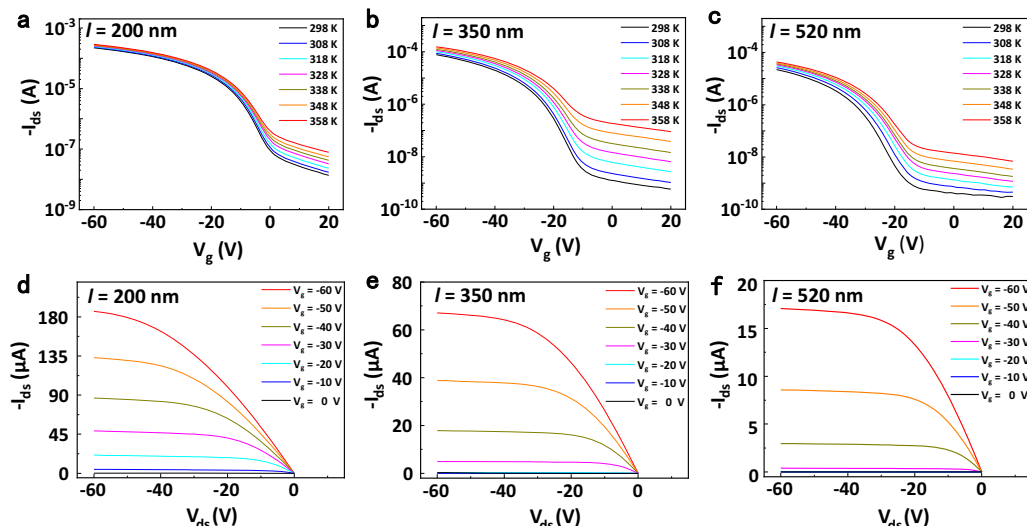
**Fabrication and characterization of OFETs.** To prove the feasibility and efficiency of potential barrier engineering for modulating thermal activated charge transport, it would be desirable to transform the thermo-stable semiconductor to be thermo-sensitive. It has been widely reported that dinaphtho[2,3-b:2',3'-f]thieno[3,2-b]thiophene (DNTT) is a good thermo-stable field-effect semiconductor and the charge transport property barely changes from 293 K to 363 K in the OFET<sup>45,46</sup>. Therefore, DNTT is one kind of good model compound for this study.

To test the charge transport property of DNTT and its temperature dependence, the DNTT OFETs were prepared as shown in Fig. 1b. The perfluoro(1-butenyl vinyl ether) polymer (CYTOP) as modification layer between the semiconductor and SiO<sub>2</sub>/Si was used to reduce the interfacial traps introduced by some polar groups and semiconductor disorder<sup>47,48</sup>. The active layer was fabricated by vacuum evaporation using multiple purified high quality DNTT, which renders the low density of impurities in the polycrystalline semiconductor film. Subsequently, the Au electrodes were deposited on the top of the DNTT polycrystalline film. All these processes aim to reduce the density of various traps except GBs as low as possible, and to avoid introducing the traps caused by these defects in the band gap. Through these strategies, DNTT OFETs exhibit standard and steady field-effect transfer characteristics without visible hysteresis during the cycle tests (Fig. 1c), and source-drain current almost remains constant under long-term bias condition (Fig. 1d). These two experimental data indicate that the devices show good operational stability without bias stress, which is the prerequisite for obtaining authentic temperature dependence of charge transport property as well as for the application in temperature sensors. In addition, the negligible bias stress indicates that there are few deep traps that may cause the bias stress.

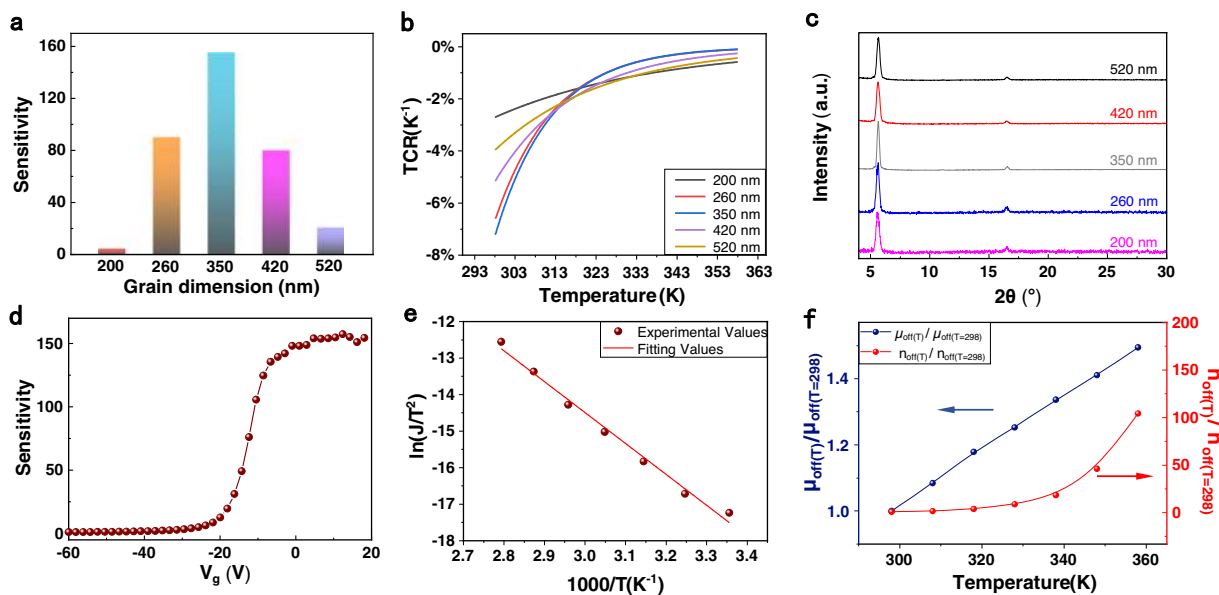
### Temperature dependence of charge transport of DNTT films.

To testify the effect of grain size on the temperature dependence of charge transport, five polycrystalline DNTT films with different grain sizes were fabricated through elaborately regulating the process of deposition and growth of DNTT molecules (including evaporation rate, growth time and deposition temperature, for details, see Methods). Atomic force microscope (AFM) was used to characterize the morphology of the films and measure the average grain size  $l$  (Supplementary Fig. 1a–e). The distribution of grain sizes in the five films are in the range of 200–520 nm. Subsequently, the temperature dependence of charge transport of the OFETs with these DNTT films was respectively investigated by measuring transfer characteristic and output characteristic curves in the temperature range of 298–358 K (for details, see Methods). The typical characteristic curves of OFETs with grain sizes of 200 nm (minimum), 350 nm (middle) and 520 nm (maximum) are shown in Fig. 2, and that of OFETs with grain sizes of 260 nm and 420 nm are shown in Supplementary Figs. 1, 2. The transfer curves at variable temperature (Fig. 2a–c, Supplementary Fig. 1g, i) and output curves (Fig. 2d–f, Supplementary Fig. 2g, i) at fixed temperature of these devices display good field-effect characteristic, indicating that the OFETs sustain the steady working status in this temperature range. The cycle-scan transfer curves at variable temperature exhibit slight hysteresis (Supplementary Fig. 3), which indicates the negligible bias stress and good operation stability, and guarantees the credibility of the investigation of thermal activated charge transport.

The current of these transfer curves increases with the increasing temperature, revealing that thermal activated behavior dominates charge transport of the polycrystalline DNTT OFETs<sup>38,49–51</sup>. The statistical relationship between grain size and the relative current change  $\{[I(V_g, T) - I(V_g, T_0)]/I(V_g, T_0)\}$ ,



**Fig. 2** The characteristic curves of the DNTT OFETs with different grain sizes. **a–c** The temperature dependent transfer curves of OFETs with grain sizes of about 200 nm, 350 nm and 520 nm.  $V_{ds} = -60$  V. **d–f** The output curves of OFETs with different DNTT grain sizes corresponding (**a–c**). The character  $l$  in all figures denotes grain size. All electrical measurements were performed in dark ambient unless otherwise specified.



**Fig. 3** Effect of the grain size on temperature dependence of OFETs. **a** Bar chart of the relationship between sensitivity and grain sizes. Sensitivity is defined as  $[I(V_{gr}, T) - I(V_{gr}, T_0)] / I(V_{gr}, T_0)$ , where  $V_{gr} = 20$  V,  $T_0 = 298$  K,  $T = 358$  K. **b** Temperature coefficient of resistance (TCR) of the OFETs with different grain sizes. **c** X-ray diffraction patterns of DNTT polycrystalline films with different grain sizes. **d** The correlation between the sensitivity (measured when  $V_{ds} = -60$  V) and the gate voltage of the OFET. **e** The fitting plot of  $\ln(I/I_0)$  as a function of  $1000/T$ . **f** The relative change rate of mobility and carrier density in the off-state of the OFET. The curves of (**d–f**) were measured by the OFET with grain size of 350 nm. The data of (**a, b** and **e–f**) were measured in the off-state ( $V_g$  is 20 V) when  $V_{ds}$  is  $-60$  V.

defined as sensitivity in this work) in the off-state ( $V_g$  is 20 V) from 298 to 358 K in the transfer curves is summarized in Fig. 3a and Supplementary Table 1. As expected, the devices show significant difference: the devices with the grain size distributed between 260 nm and 420 nm exhibit very strong temperature-dependent characteristic. The sensitivity of these devices is about two orders of magnitude, and the highest value is more than two and a half orders of magnitude at the grain size of about 350 nm, which value is significantly larger than the standard thermal-activated carrier transport<sup>12,31–35</sup>. If the typical activation energy of a standard thermally-activated carrier transport is in the range of 100–200 meV for intrinsic organic semiconductors<sup>12,31</sup>, the expected temperature sensitivity is only around 1–3 from 298 K to

358 K. On the contrary, the electrical properties of the devices with the grain size  $<260$  nm or larger than 420 nm are lightly changed with the varied temperature. For easy comparison with other kinds of thermosensitive materials or devices, we also calculated the temperature coefficient of resistance (TCR) as sensitivity (for details, see Supplementary Note 4). The TCR curves of the OFETs are shown in Fig. 3b, which is higher than, at least compared to, most of conventional thermosensitive materials. For example, TCR for most metals and other conductive materials is between 0.1 and 1%  $K^{-1}$ <sup>52–54</sup>, and most oxide ceramics<sup>55,56</sup> and silicon-based electronics<sup>57–62</sup> show the TCR value of 1–4%  $K^{-1}$  at room temperature. This fact indicates that the DNTT OFETs with proper grain size achieve high thermo-sensitivity.

X-ray diffraction patterns of these five films (Fig. 3c) reveal that all these DNTT films have similar crystallinity, suggesting that the different temperature dependence of charge transport does not stem from the molecular ordering. The results show that well designing the grain size successfully achieves the remarkable modulation of thermal activated charge transport in organic semiconductors.

Furthermore, in single OFET, the temperature dependence of charge transport can be further tuned by the gate voltage. As shown in Fig. 3d, sensitivity shows the tiny change in the off-state (0–20 V) and on-state (–20 to –60 V), but decreases significantly in subthreshold region. Generally, the larger gate voltage leads to smaller sensitivity, which is due to the fact that the vast charges induced by gate voltage screen the potential of GBs (as expressed in Eq. (3)). The tunable sensitivity is very useful for the practical applications in the complicated environment<sup>63</sup>.

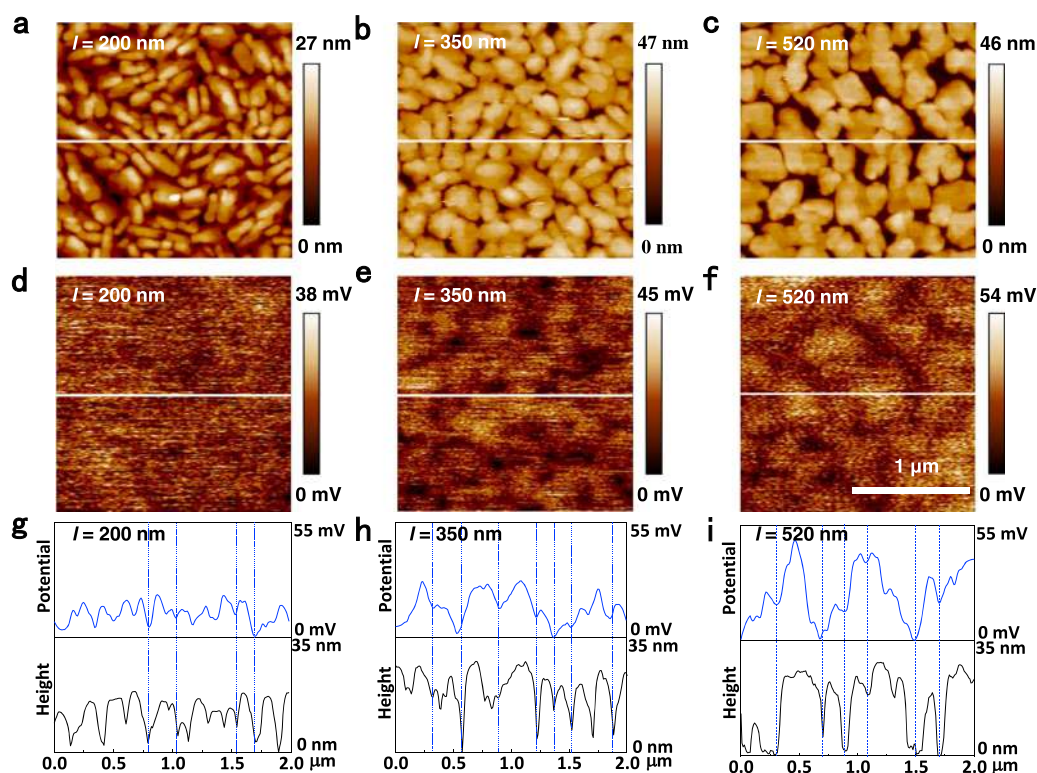
**Modulating mechanism of the temperature dependence of DNTT polycrystalline film.** To elucidate the mechanism of the thermal activated charge transport in the polycrystalline organic semiconductor, the electrical property of the DNTT OFET at different temperature was examined. If charge carrier is mainly restrained by the potential barrier of GBs, current density should obey Eq. (1). Dividing both sides by  $T^2$  and taking the logarithm, Eq. (1) becomes

$$\ln \frac{J}{T^2} = \ln A^* - \left( \frac{E_F - E_V + E_B - qV_B}{k_B} \right) \frac{1}{T} \quad (6)$$

which suggests  $\ln \frac{J}{T^2} \propto \frac{1}{T}$ . Then, we draw the plot of  $\ln \frac{J}{T^2}$  as a function of  $1000/T$  for the device with grain size of 350 nm in Fig. 3e (others are shown in Supplementary Fig. 4). As expected, the curve shows good linearity, indicating that the charge transport in the DNTT film accords with the thermionic emission

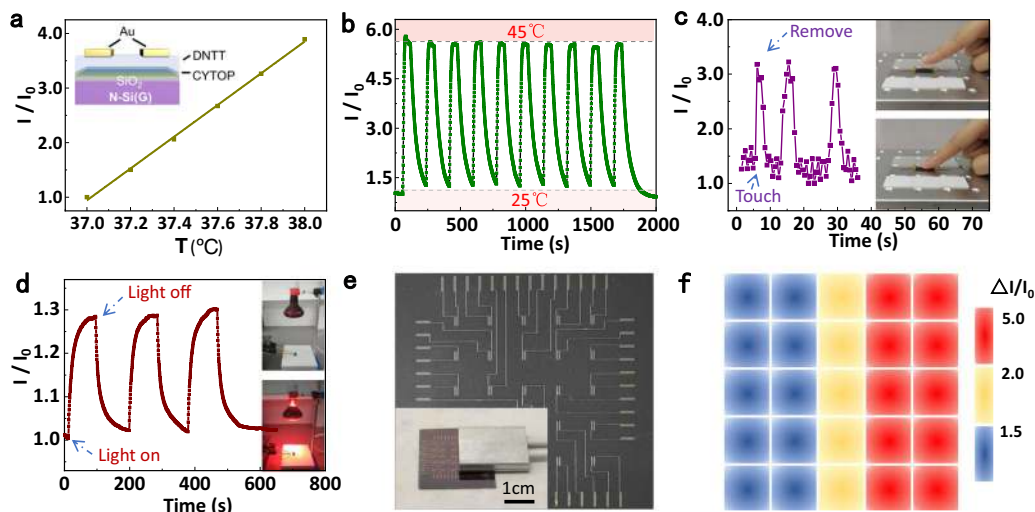
model. The critical parameter  $\frac{E_F - E_C + E_B - qV_B}{k_B}$  can be obtained from the slope of these fitting curves. In addition, these results also show that steeper slope indicates higher temperature sensitivity (i.e., 350 nm > 520 nm > 200 nm), which will be discussed detailly in the Discussion Section below. Moreover, the relative change of field-effect mobility ( $\mu$ ) and carrier density ( $n$ ) in the off-state at different temperature were calculated (Fig. 3f), respectively. The mobility in saturated region was extracted by field-effect method (see below). The drift equation  $J = qn\mu E$ , where  $q$  is the elementary charge and  $E$  is electric field intensity, is a classical equation to describe the motion of carriers under electric field. I-V characteristics in linear and saturation regime of FET are derived from this equation<sup>28,64,65</sup>. Therefore, the carrier concentration in the off-state ( $n_{off}$ ) can be calculated by off-state current in transfer curves (Fig. 2) or I-V curve of two-terminal measurement (Supplementary Fig. 5) as long as the mobility in the off-state ( $\mu_{off}$ ) is obtained by FET method.

In order to extract  $\mu_{off}$ , the dependence of mobility on gate voltage should be considered. DNTT is a thienothiophene small molecule organic semiconductor. Some studies suggest that the organic semiconductors with high degree of delocalization and order, like thienothiophene, show weak dependence of mobility on gate voltage<sup>66–69</sup>. The dependence of  $\mu$  on  $(V_g - V_{th})$  in this work was examined. As shown in Supplementary Fig. 6, the DNTT OFET exhibits the weak dependency of  $\mu$  on  $V_g$ . Therefore, it is reasonable to approximate  $\mu$  at  $V_g - V_{th} = 0$  as  $\mu_{off}$  for estimation of  $n_{off}$ . Using this approximation,  $n_{off}$  was calculated to be about  $1 \times 10^{14} \text{ cm}^{-3}$  by drift equation, which is lower than that of organic single crystal<sup>70</sup>. (for details, see Supplementary Note 8). In addition, the carrier concentration of devices in different batches are derived to be in the small range (for details, see Supplementary Table 2). The relative change rate of  $\mu_{off}$  and  $n_{off}$  with the increasing temperature is shown in Fig. 3f,



**Fig. 4** The topography and surface potential characteristics of DNTT polycrystalline films measured by Kelvin probe force microscopy (KPFM).

**a–c** Topography images of DNTT polycrystalline films with grain sizes of 200 nm, 350 nm and 520 nm, respectively. **d–f** Surface potential images of DNTT polycrystalline films. **g–i** Line section profile of topography and surface potential.



**Fig. 5 Applications of the thermo-sensitive OFET for temperature sensing.** **a** Temperature resolution curve of sensor measured at 37–38 °C with a precision of 0.2 °C. The inset shows the device structure. **b** Cyclic stability of temperature sensor from 25 °C to 45 °C. **c** Response of temperature sensor to finger touch. **d** Response of temperature sensor to infrared heat source. **e** A photo of the OFET temperature sensor array. **f** Temperature mapping measured by the sensor array.

which conforms to the description of thermionic emission theory. Furthermore, it can be found that  $n_{off}$  exhibits larger change, indicating that more carriers obtain enough energy to cross the barriers with the increase of temperature.

From the estimation of  $n_{off} \approx 1 \times 10^{14} \text{ cm}^{-3}$  by drift equation (detailed discussion in Supplementary Note 8) and the reported value of DNTT's  $\epsilon_s \approx 37^1$ , the twice of Debye length ( $2l_D$ ) was determined to be about 400 nm. It is among the range of DNTT's grain sizes of 200–520 nm in this work. Therefore, the potential barrier ( $E_B$ ) of GBs should be discussed in two conditions. As expressed in Eqs. (2) and (3), when  $l < 2l_D$ ,  $E_B \propto l^2$ . As the grain size increases to more than  $2l_D$  ( $l > 2l_D$ ),  $E_B$  becomes independent of  $l$ . It should be noted that the method of precise calculation of Debye length is not established in organic semiconductors because of the uncertain concentration of dopants. Based on some reasonable assumption and approximation, a way to estimate Debye length is used in this work. It might be not absolutely accurate, but it is in the reasonable range and is enough to qualitatively explain the anomalous temperature dependence in this work. The development of precise calculation methods needs further research in the future.

To further certify the relationship between potential barrier of GB and grain size, the potential of GB should be directly observed. Kelvin probe force microscopy (KPFM) has been proved as a powerful tool for investigating interface electrical structure of organic devices, which can record surface and interface potential distribution of polycrystalline film<sup>72–74</sup>. The potential  $\phi_B$  of DNTT films with different grain sizes can be obtained directly through KPFM. Figure 4a–c show the AFM images of the DNTT films with grain sizes of 200 nm, 350 nm and 520 nm, respectively, and the corresponding potential distribution images are shown in Fig. 4d–f. The value represents the potential difference between substrate and grains. The curves of height and potential show almost the same profile in these three samples, which directly manifests the congruent relationship between grain sizes and potential (Fig. 4g–i), and confirms that the measured potential is produced from GBs.

The maximum value of potential barrier ( $E_B = q\phi_B$ ) is measured to be about 17 meV, 27.8 meV and 47.7 meV for three DNTT films with grain sizes of 200 nm, 350 nm and 520 nm, respectively, which demonstrates the effectiveness of modulating

barrier through grain size. In theory,  $E_B$  of 350 nm DNTT film is estimated to be about 9.3 meV by Eq. (2). Although the theoretical value of  $E_B$  is less than the experimental one, they are in the similar and reasonable range, which can prove the credibility of the theoretical deduction. The analysis of this point was discussed in Supplementary Note 9. Correlating the potential barrier and grain size with the temperature dependence of charge transport of these three DNTT film (Fig. 3a), it can be found that the OFETs with smaller or larger  $l$  are relatively thermo-stable, while the OFET with middle  $l$  shows great thermo-sensitivity. The reason for this result will be explicitly explained by the effective height of potential barrier in the Discussion Section below.

#### Applications of the thermo-sensitive OFET for temperature sensing.

The OFET prepared by thermo-stable semiconductor DNTT has been modulated to be a device with strong thermal dependent property, which is the core performance required by temperature sensors for health monitoring. As a temperature sensor, the OFET shows an high sensitivity of 155 (Fig. 3a, 350 nm). To our knowledge, it is far higher than that of the reported temperature sensors based on OFET<sup>31–35</sup>. In addition, it should be noted that this high sensitivity achieved in the off-state (with current level of about nA) of OFETs produces high input impedance, which usually leads to large noise. This problem could be well-solved by circuit design such as integration of low-noise amplifiers<sup>75</sup>. Benefiting from the high sensitivity, the sensor realizes a quite precise resolution of 0.2 °C with great relative current change from 37 °C to 38 °C (Fig. 5a). It should be pointed out that huge  $\Delta I/I_0$  in such small temperature range suggests that our sensor may hold promise for higher resolution. However, due to the limitation of our experimental setup, we could not obtain smaller temperature change in a continuous and reliable mode. Moreover, the cycle stability of the sensor was examined on a semiconductor cooling-heating stage in the temperature range of 25–45 °C. As shown in Fig. 5b, the response signal is stable after multiple cycles. In addition, the sensor also shows quick response and recovery, which is evidenced by two tests. As shown in Supplementary Fig. 7, when finger touched on/off the front side of sensor covered with polyimide film (about 0.06 mm), the response and recover time are 0.58 s and 1.76 s, respectively

(Supplementary Movie 1). When finger touched on/off the back side (about 450  $\mu\text{m}$  silicon), the response and recovery are 2.6 s and 8.2 s, respectively. If without the encapsulation or substrate layer, the sensor may show quicker response and recovery speed.

To demonstrate the ability of our device for some future application such as health care and human-machine interaction, the sensor was used to identify human touch and ambient thermal radiation. The sensor can tolerate the repeated human touch and shows good identification through simple packing with tape (Fig. 5c). Utilizing an IR light as thermal radiation from environment (Supplementary Movie 2), the sensor shows high signal-to-noise ratio and good repeatability (Fig. 5d), which suggests the good capability to sense thermal radiation. In consideration of large-scale integration required by practical application, the temperature sensors were fabricated with a  $5 \times 5$  matrix (Fig. 5e) to demonstrate the feasibility for multipoint measurements. The normalized currents of the array were measured when a hot plate was placed on it (inset in Fig. 5e). The mapping of the temperature distribution via the measurement of the normalized drain currents is shown in Fig. 5f, which matches well with the setting position of hot plate. All the application demonstrations indicate the precise temperature sensing capability of the sensor and sensor matrix, suggesting its potential for the practical applications.

## Discussion

To interpret why the OFETs with middle grain sizes show the prominent temperature dependence of charge transport around room temperature, the current density across GB by thermionic emission is investigated theoretically. Through Eq. (6) or definition of differential sensitivity (for details, see Supplementary Note 11), it can be obviously found that the sensitivity at certain temperature shows positive correlation with  $(E_F - E_V + E_B - qV_B)$ .

As we discussed in Theoretical analysis Section,  $E_F - E_V$  makes no difference for the devices with the same materials and processes, so the key parameter influencing sensitivity is the effective height of barrier,  $E_{Be} = E_B - qV_B$ .

Derived from Eqs. (4) and (5), the Schematic diagram of relationship between  $E_{Be}$  and  $l$  is shown in Supplementary Fig. 8, which shows two main regimes: (1)  $E_B$  generally increases as a quadratic function of  $l$  in the case of  $l < 2l_D$  (Eq. (4)). (2) When  $l > 2l_D$ , the traps at GBs are completely filled, rendering  $E_B$  to be independent of  $l$ . However,  $V_B$  still enlarges, so  $E_{Be}$  begins to decline with  $l$  (Eq. (5)). Therefore, maximum of  $E_{Be}$  (i.e., sensitivity) should be obtained when  $l$  is around  $2l_D$ , which further causes the strong temperature dependence of charge transport.

In our experiment,  $2l_D$  is estimated to be 400 nm, and grain size  $l$  varies from 200 nm to 520 nm. Sensitivity increases firstly and then decreases. The maximum sensitivity is obtained when  $l$  is 350 nm (Fig. 3a), which is near to  $2l_D$  of about 400 nm. These results are consistent with theoretically analysis. To test the universality of this strategy, the OFETs with pentacene as semiconductor layer also implemented the modulation of temperature dependence of charge transport (for method, see Supplementary Note 12). As shown in Supplementary Fig. 9, pentacene film with proper grain size also achieved the larger thermo-sensitivity, demonstrating that the barrier potential regulation is effective to tune thermal activated charge transport of organic semiconductor.

In conclusion, this work has developed an effective strategy to modulate the temperature dependence of thermal activated charge transport in organic semiconductor by precisely tuning the effective height of potential barrier ( $E_{Be}$ ) at GBs (i.e., potential barrier engineering), which has not been reported previously.  $E_{Be}$  reaches the maximum when  $l \approx 2l_D$ , which thus makes the device

highly thermo-sensitive. While refining or coarsening crystalline grain can both reduce  $E_{Be}$ , which thus enhances the thermo-stability of devices. This result indicates that the grain size should be well optimized into the proper size range according to the thermosensitive or thermostable requirement of applications. The mechanism of modulation for charge transport has been meticulously elucidated by experimental demonstration and theoretical deduction. The strategy shows good universality for different organic semiconductors. Through the strategy, the DNTT OFET shows an high thermo-sensitivity and can achieve detection of ambient IR, identification of human touch and mapping of temperature distribution. This work brings useful inspiration for both mechanism research of thermal activated charge transport and devices design of integrated circuits and high-performance sensors.

## Methods

**Fabrication of DNTT polycrystalline OFETs.** DNTT was purchased from Sigma-Aldrich and purified three times. CYTOP was purchased from Asahi Glass Corporation. The OFETs adopted a bottom-gate top-contact configuration. The highly doped Si wafers (500  $\mu\text{m}$  thick) with 300 nm-thick thermal oxide layer were used as substrate. In order to reduce interface traps, the CYTOP was spin-coated (4000 rpm, 30 s, 180 nm) onto the surface of  $\text{SiO}_2$  and annealed at 100  $^\circ\text{C}$  for 30 min. All DNTT polycrystalline films were vapor deposited on the insulating layer CYTOP surface below  $10^{-4}$  Pa. The grain size was controlled by the evaporation rate of DNTT and the substrate temperature, and the thickness was controlled at 20 nm by crystal oscillator. The 260 nm, 350 nm, 420 nm and 520 nm grain sizes were deposited at a rate of about 0.008 nm/s, 0.006 nm/s, 0.004 nm/s and 0.002 nm/s and a substrate temperature of 65  $^\circ\text{C}$ , respectively. The 200 nm grain size was deposited at a rate of about 0.01 nm/s and a substrate temperature of 25  $^\circ\text{C}$ . The gold-plated electrodes with a thickness of 20 nm were the source-drain electrodes under a vapor deposition rate of about 0.005 nm/s, and the channel length and width were 50  $\mu\text{m}$  and 1000  $\mu\text{m}$ , respectively.

**Characterization of OFETs.** The electrical characterizations of the temperature OFETs were carried out in dark conditions using a Keithley 4200-SCS. In the measurement of transfer characteristic, the drain-source voltage was -60 V, and the gate voltage was scanned from 20 V to -60 V at intervals of 1 V. In finger touch experiment, a finger touched the surface of the sensor which was packaged with tape film. The KPFM measurements were carried out in dark conditions on a Dimension ICON (Bruker), PtIr-coated rectangle Si probe was used. The grain sizes were measured by using the average length of all grains' along the long axes in  $2 \times 2 \mu\text{m}$  region.

## Data availability

The data presented in this study are available from the corresponding authors on reasonable request.

Received: 22 February 2020; Accepted: 17 November 2020;

Published online: 04 January 2021

## References

- Chortos, A., Liu, J. & Bao, Z. Pursuing prosthetic electronic skin. *Nat. Mater.* **15**, 937–950 (2016).
- Liu, J. et al. Relieving the photosensitivity of organic field-effect transistors. *Adv. Mater.* **32**, 1906122 (2019).
- Wang, B. H. et al. High- $k$  gate dielectrics for emerging flexible and stretchable electronics. *Chem. Rev.* **10**, 5690–5754 (2018).
- Wu, X., Mao, S., Chen, J. & Huang, J. Strategies for improving the performance of sensors based on organic field-effect transistors. *Adv. Mater.* **30**, 1705642 (2018).
- Ji, D. et al. Band-like transport in small-molecule thin films toward high mobility and ultrahigh detectivity phototransistor arrays. *Nat. Commun.* **10**, 12 (2019).
- Tsurumi, J. et al. Coexistence of ultra-long spin relaxation time and coherent charge transport in organic single-crystal semiconductors. *Nat. Phys.* **13**, 994–998 (2017).
- Groves, C. Simulating charge transport in organic semiconductors and devices: a review. *Rep. Prog. Phys.* **80**, 026502 (2017).
- Smits, E. C. P. et al. Bottom-up organic integrated circuits. *Nature* **455**, 956–959 (2008).

9. Crone, B. et al. Large-scale complementary integrated circuits based on organic transistors. *Nature* **403**, 521–523 (2000).
10. Ren, X. et al. Organic field-effect transistor for energy-related applications: low-power-consumption devices, near-infrared phototransistors, and organic thermoelectric devices. *Adv. Energy Mater.* **8**, 1801003 (2018).
11. Zhang, F., Zang, Y., Huang, D., Di, C. A. & Zhu, D. B. Flexible and selfpowered temperature-pressure dual-parameter sensors using microstructureframe-supported organic thermoelectric materials. *Nat. Commun.* **6**, 8356 (2015).
12. Ren, X. et al. A low-operating-power and flexible active-matrix organic-transistor temperature-sensor array. *Adv. Mater.* **28**, 4832–4838 (2016).
13. Galindo, S., Tamayo, A., Leonardi, F. & Mas-Torrent, M. Control of polymorphism and morphology in solution sheared organic field-effect transistors. *Adv. Funct. Mater.* **27**, 1700526 (2017).
14. Janneck, R. et al. Highly crystalline C8-BTBT thin-film transistors by lateral homo-epitaxial growth on printed templates. *Adv. Mater.* **29**, 1703864 (2017).
15. Katz, H. E. et al. A soluble and air-stable organic semiconductor with high electron mobility. *Nature* **404**, 478–481 (2000).
16. Mottaghi, M. & Horowitz, G. Field-induced mobility degradation in pentacene thin-film transistors. *Org. Electron.* **7**, 528–536 (2006).
17. Mahapatro, A. K., Sarkar, N. & Ghosh, S. Grain-boundary-controlled current transport in copper phthalocyanine. *Appl. Phys. Lett.* **88**, 162110 (2006).
18. Reid, O. G., Yang, M., Kopidakis, N., Zhu, K. & Rumbles, G. Grain-size limited mobility in methylammonium lead iodide perovskite thin films. *ACS Energy Lett.* **1**, 561–565 (2016).
19. Chu, Z. et al. Impact of grain boundaries on efficiency and stability of organic-inorganic trihalide perovskites. *Nat. Commun.* **8**, 2230 (2017).
20. Schön, J. H. & Batlogg, B. Modeling of the temperature dependence of the field-effect mobility in thin film devices of conjugated oligomers. *Appl. Phys. Lett.* **74**, 260 (1999).
21. Rani, V., Sharma, A., Kumar, P., Singh, B. & Ghosh, S. Charge transport mechanism in copper phthalocyanine thin films with and without traps. *RSC Adv* **7**, 54911–54919 (2017).
22. Qian, C. et al. Crystal-domain orientation and boundary in highly ordered organic semiconductor thin film. *J. Phys. Chem. C* **119**, 14965–14971 (2015).
23. Huang, H., Wang, H., Zhang, J. & Yan, D. Surface potential images of polycrystalline organic semiconductors obtained by Kelvin probe force microscopy. *Appl. Phys. A* **95**, 125–130 (2009).
24. Pfeiffer, M., Leo, K. & Karl, N. Fermi level determination in organic thin films by the Kelvin probe method. *J. Appl. Phys.* **80**, 6880–6883 (1996).
25. Jeong, C. Y. et al. Investigation of the charge transport mechanism and subgap density of states in p-type Cu<sub>2</sub>O thin-film transistors. *Appl. Phys. Lett.* **102**, 082103 (2013).
26. Levinson, J. et al. Conductivity behavior in polycrystalline semiconductor thin film transistors. *J. Appl. Phys.* **53**, 1193–1202 (1982).
27. Seto, J. Y. W. The electrical properties of polycrystalline silicon films. *J. Appl. Phys.* **46**, 5247–5254 (1975).
28. Sze, S. M., Ng, K. K. *Physics of semiconductor devices.* (John Wiley & sons, 2006).
29. Lou, Z. et al. Ultrasensitive and ultraflexible e-skins with dual functionalities for wearable electronics. *Nano Energy* **38**, 28–35 (2017).
30. Nakata, S., Arie, T., Akita, S. & Takei, K. Wearable, flexible, and multifunctional healthcare device with an ISFET chemical sensor for simultaneous sweat pH and skin temperature monitoring. *ACS Sens.* **2**, 443–448 (2017).
31. Ren, X., Chan, P. K. L., Lu, J., Huang, B. & Leung, D. C. W. High dynamic range organic temperature sensor. *Adv. Mater.* **25**, 1291–1295 (2013).
32. Nakayama, K. et al. Organic temperature sensors and organic analog-to-digital converters based on p-type and n-type organic transistors. *Org. Electron.* **36**, 148–152 (2016).
33. Abdullah, S. M., Ahmad, Z. & Sulaiman, K. A solution-based temperature sensor using the organic compound CuTsPc. *Sensors* **14**, 9878–9888 (2014).
34. Wang, D., Tan, Q., Liu, J. & Liu, Z. A stable europium metal-organic framework as a dual-functional luminescent sensor for quantitatively detecting temperature and humidity. *Dalton Trans.* **45**, 18450–18454 (2016).
35. Jung, S., Ji, T. & Varadan, V. K. Temperature sensor using thermal transport properties in the subthreshold regime of an organic thin film transistor. *Appl. Phys. Lett.* **90**, 062105 (2007).
36. Tello, M., Chiesa, M., Duffy, C. M. & Sirringhaus, H. Charge trapping in intergrain regions of pentacene thin film transistors. *Adv. Funct. Mater.* **18**, 3907–3913 (2008).
37. Street, R. A., Knipp, D. & Völkel, A. R. Hole transport in polycrystalline pentacene transistors. *Appl. Phys. Lett.* **80**, 1658–1660 (2002).
38. Podzorov, V. et al. Intrinsic charge transport on the surface of organic semiconductors. *Phys. Rev. Lett.* **93**, 086602 (2004).
39. Verlaak, S., Arkhipov, V. & Heremans, P. Modeling of transport in polycrystalline organic semiconductor films. *Appl. Phys. Lett.* **82**, 745–747 (2003).
40. Wang, X. & Dodabalapur, A. Trapped carrier scattering and charge transport in high-mobility amorphous metal oxide thin-film transistors. *Ann. Phys. (Berlin)* **530**, 1800341 (2018).
41. Bethe, H. A. *Theory of the boundary layer of crystal rectifiers.* 43-12 (MIT Radiation Laboratory Rep., 1942).
42. Baccarani, G., Riccò, B. & Spadini, G. Transport properties of polycrystalline silicon films. *J. Appl. Phys.* **49**, 5565–5570 (1978).
43. Riess, I. Conditions for neglecting space charge effects on distributions of point defects and I–V relations. *Solid State Ionics* **69**, 43–52 (1994).
44. Gil, Y., Umurhan, O. & Riess, I. Properties of solid state devices with mobile ionic defects. Part I: The effects of motion, space charge and contact potential in metal|semiconductor|metal devices. *Solid State Ionics* **178**, 1–12 (2007).
45. Za'aba, N. K., Morrison, J. J. & Taylor, D. M. Effect of relative humidity and temperature on the stability of DNTT transistors: A density of states investigation. *Org. Electron.* **45**, 174–181 (2017).
46. Xie, W. et al. Temperature-independent transport in high-mobility dinaphtho-thieno-thiophene (DNTT) single crystal transistors. *Adv. Mater.* **25**, 3478–3484 (2013).
47. Haas, S., Takahashi, Y., Takimiya, K. & Hasegawa, T. High-performance dinaphtho-thieno-thiophene single crystal field-effect transistors. *Appl. Phys. Lett.* **95**, 022111 (2009).
48. Wang, C. et al. Organic field-effect transistors with a bilayer gate dielectric comprising an oxide nanolaminate grown by atomic layer deposition. *ACS Appl. Mater. Interfaces* **8**, 29872–29876 (2016).
49. Novikov, S. V., Dunlap, D. H., Kenkre, V. M., Parris, P. E. & Vannikov, A. V. Essential role of correlations in governing charge transport in disordered organic materials. *Phys. Rev. Lett.* **81**, 4472–4475 (1998).
50. Shuai, Z., Wang, L. & Li, Q. Evaluation of charge mobility in organic materials: from localized to delocalized descriptions at a first-principles level. *Adv. Mater.* **23**, 1145–1153 (2011).
51. Horowitz, G. Organic field-effect transistors. *Adv. Mater.* **10**, 365–377 (1998).
52. Giancoli, G. D. *Physics: Principles with Applications*, 4th edn. (Prentice Hall, 1995).
53. Hong, S. Y. et al. Stretchable Active Matrix Temperature Sensor Array of Polyaniline Nanofibers for Electronic Skin. *Adv. Mater.* **28**, 930–935 (2016).
54. Kwon, I. W., Son, H. J., Kim, W. Y., Lee, Y. S. & Lee, H. C. Thermistor behavior of PEDOT: PSS thin film. *Synth. Met.* **159**, 1174–1177 (2009).
55. Lv, Y., Hu, M., Wu, M. & Liu, Z. Preparation of vanadium oxide thin films with high temperature coefficient of resistance by facing targets d.c. reactive sputtering and annealing process. *Surf. Coat. Technol.* **201**, 4969–4972 (2007).
56. Moreno, M., Kosarev, A., Torres, A. & Ambrosio, R. Fabrication and performance comparison of planar and sandwich structures of microbolometers with Ge thermo-sensing layer. *Thin Solid Films* **515**, 7607–7610 (2007).
57. Paivi Heimala, P. K., Aarnio, Jaakko & Heinamaki, Arja Thermally Tunable Integrated Optical Ring Resonator with Poly-Si Thermistor. *J. Lightwave Technol.* **14**, 2260 (1996).
58. Wu, Z. et al. Polysilicon-based flexible temperature sensor for brain monitoring with high spatial resolution. *J. Micromech. Microeng.* **27**, 025001 (2017).
59. Hashim, Y. & Sidek, O. Effect of temperature on the characteristics of silicon nanowire transistor. *J. Nanosci. Nanotechnol.* **12**, 7849–7852 (2012).
60. Hashim, Y. & Sidek, O. Simulation study of temperature sensitivity of silicon nanowire transistors with different types of orientations. *IEEE Trans. Electron. Eng.* **7**, 458–460 (2012).
61. Lei, W.-D., Bai, Y., Tian, H. & Zhao, F. A Single Device Temperature Sensor Based on Amorphous Silicon Thin Film Transistor. *IEEE Sensors J.* **19**, 10236–10242 (2019).
62. Shimanovich, K., Mutsafi, Z., Roizin, Y. & Rosenwaks, Y. CMOS compatible SOI nanowire FET with charged dielectric for temperature sensing applications. *J. Phys. D Appl. Phys.* **53**, 065101 (2020).
63. Wang, Z. et al. The semiconductor/conductor interface piezoresistive effect in an organic transistor for highly sensitive pressure sensors. *Adv. Mater.* **31**, 1805630 (2019).
64. Zaumseil, J. & Sirringhaus, H. Electron and Ambipolar Transport in Organic Field-Effect Transistors. *Chem. Rev.* **107**, 1296–1323 (2007).
65. Newman, C. R. et al. Introduction to Organic Thin Film Transistors and Design of n-Channel Organic Semiconductors. *Chem. Mater.* **16**, 4436–4451 (2004).
66. Illig, S. et al. Reducing dynamic disorder in small-molecule organic semiconductors by suppressing large-amplitude thermal motions. *Nat. Commun.* **7**, 10736 (2016).
67. Peng, B., Huang, S., Zhou, Z. & Chan, P. K. L. Solution-Processed Monolayer Organic Crystals for High-Performance Field-Effect Transistors and Ultrasensitive Gas Sensors. *Adv. Funct. Mater.* **27**, 1700999 (2017).
68. Liu, C. et al. A unified understanding of charge transport in organic semiconductors: the importance of attenuated delocalization for the carriers. *Mater. Horiz.* **4**, 608–618 (2017).



69. Yang, T. et al. Understanding, Optimizing, and Utilizing Nonideal Transistors Based on Organic or Organic Hybrid Semiconductors. *Adv. Funct. Mater.* **30**, 1903889 (2019).
70. Kajji, T., Takenobu, T., Morpurgo, A. F. & Iwasa, Y. Organic Single-Crystal Schottky Gate Transistors. *Adv. Mater.* **21**, 3689–3693 (2009).
71. Hayashi, T., Take, N., Tamura, H., Sekitani, T. & Someya, T. Alternating current admittance of DNTT-based metal-insulator-semiconductor capacitors. *J. Appl. Phys.* **115**, 093702 (2014).
72. Sharma, I. & Mehta, B. R. Enhanced charge separation at 2D MoS<sub>2</sub>/ZnS heterojunction: KPFM based study of interface photovoltage. *Appl. Phys. Lett.* **110**, 061602 (2017).
73. Shao, Y. et al. Grain boundary dominated ion migration in polycrystalline organic-inorganic halide perovskite films. *Energy Environ. Sci.* **9**, 1752–1759 (2016).
74. Ruiz, A. et al. Work function fluctuations in polycrystalline TiN observed with KPFM and their impact on MOSFETs variability. *Appl. Phys. Lett.* **114**, 093502 (2019).
75. Xu, J., Mitra, S., Van Hoof, C., Yazicioglu, R. F. & Makinwa, K. A. A. Active Electrodes for Wearable EEG Acquisition: review and Electronics Design Methodology. *IEEE Rev. Biomed. Eng.* **10**, 187–198 (2017).

## Acknowledgements

The authors are grateful to National Key Research and Development Program (2018YFA0703200, 2016YFB0401100), National Natural Science Foundation of China (52073210, 21905199, 21573277, 51633006, 62004138), Natural Science Foundation of Tianjin City (19JCZDJC37400, 194214030036), Beijing National Laboratory for Molecular Sciences (BNLMS202006) and Key Research Program of Frontier Sciences of Chinese Academy of Sciences (QYZDB–SSW–SLH031). The authors highly appreciate Prof. Jun Kang (Beijing Computational Science Research Center), Dr. Wenchong Wang (University of Munster), Prof. Xiaojun Guo (Shanghai Jiao Tong University), Prof. Chengliang Wang (Huazhong University of Science and Technology), Prof. Tao Li (Shanghai Jiao Tong University), Prof. Yanyan Fu (Shanghai Institute of Microsystem and Information, Chinese Academy of Sciences), who provided valuable and constructive comments and suggestions for the theoretical analysis in this work.

## Author contributions

X.G. and Y.H. performed the sensor fabrication and measurements. Y.M., Z.W., X.C., J.L. and D.J. assisted in the experiments. Z.W. and W.H. made valuable suggestions on this

work. L.L., Y.H. and X.G. conceived, analyzed and wrote the paper. L.L. conceived and supervised this work.

## Competing interests

The authors declare no competing interests.

## Additional information

**Supplementary information** is available for this paper at <https://doi.org/10.1038/s41467-020-20209-w>.

**Correspondence** and requests for materials should be addressed to L.L.

**Peer review information** *Nature communications* thanks Shun Watanabe and the other, anonymous, reviewer(s) for their contribution to the peer review of this work. Peer reviewer reports are available.

**Reprints and permission information** is available at <http://www.nature.com/reprints>

**Publisher's note** Springer Nature remains neutral with regard to jurisdictional claims in published maps and institutional affiliations.



**Open Access** This article is licensed under a Creative Commons Attribution 4.0 International License, which permits use, sharing, adaptation, distribution and reproduction in any medium or format, as long as you give appropriate credit to the original author(s) and the source, provide a link to the Creative Commons license, and indicate if changes were made. The images or other third party material in this article are included in the article's Creative Commons license, unless indicated otherwise in a credit line to the material. If material is not included in the article's Creative Commons license and your intended use is not permitted by statutory regulation or exceeds the permitted use, you will need to obtain permission directly from the copyright holder. To view a copy of this license, visit <http://creativecommons.org/licenses/by/4.0/>.

© The Author(s) 2021