# Electrostatic pull-in application in flexible devices: A review

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## Review

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## **Abstract**

The electrostatic pull-in effect is a common phenomenon and a key parameter in the design of microscale and nanoscale devices. Flexible electronic devices based on the pull-in effect have attracted increasing attention due to their unique ductility. This review summarizes nanoelectromechanical switches made by flexible materials and classifies and discusses their applications in, among others, radio frequency systems, microfluidic systems, and electrostatic discharge protection. It is supposed to give researchers a more comprehensive understanding of the pull-in phenomenon and the development of its applications. Also, the review is meant to provide a reference for engineers to design and optimize devices.

# Introduction

It has become more and more difficult for traditional electronic devices made of rigid substrates to meet the needs of flexible and low-cost applications in complex environments. Flexible electronics have great potential for applications such as portable displays, electronic skin, and wearable healthcare. With the development of new materials and microelectromechanical and nanoelectromechanical systems (MEMS/NEMS), MEMS devices have become an essential part of flexible electronic systems. Common flexible MEMS devices are based on electrostatic, piezoelectric, and thermal actuation. Electrostatic actua-

tion is one of the earliest and most mature actuation mechanisms used in flexible MEMS devices. Because of its simple structure and convenience for integrated manufacturing, it is one of the most actively researched driving actuation [1].

There are typical pull-in phenomena in electrostatic MEMS devices. Previous research has established that the pull-in is a widely acknowledged instability condition that restricts the normal operating range of the devices. For instance, the pull-in between the combs can cause the leakage of the energy, which

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needs to be avoided in the design of microcombs [1]. Meanwhile, devices using pull-in as the working mechanism have attracted researcher's attention because of the low cost and the simple structures.

Extensive research has shown that modeling the bending of the flexible devices is only limited to the mechanical behavior of the flexible interconnect lines, and hardly involves the structure of the flexible devices. Nanoelectromechanical (NEM) switches made of carbon nanotubes (CNTs) [2-4], graphene (GR) [5-7], nanowires (NWs) [8-10], and other flexible materials are the most basic devices for a variety of component and system level applications, such as low-loss switches, phase shifts, and relays.

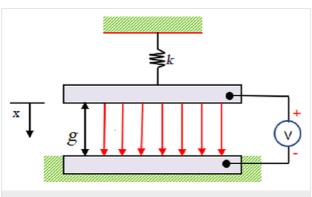
In this paper, the state of the art of electrostatic pull-in phenomena in flexible devices is discussed, and the influence of different electrode structures in NEM switches is classified and discussed. In addition, the applications of NEM switches in radio frequency (RF), electrostatic discharge (ESD), microfluidic, physical unclonable function (PUF), and microscale energy devices are summarized in this review, which will help researchers to understand upcoming trends regarding the pull-in effect and help engineers to further optimize the devices.

## Review

# Mechanism of the pull-in effect

The pull-in effect is a common phenomenon occurring in magnetostatic actuators, dielectric elastomer actuators, and electrostatic actuators, which can cause failure [1]. Electrostatic pull-in is a nonlinear effect caused by intensive electromechanical coupling, a unique characteristic for MEMS/NEMS devices, which break down before the pull-in occurs on the macroscale.

Figure 1 shows the lumped parameters model [11] of electrostatically driven parallel plate actuators. The structure consists of two parallel plates. The upper plate is suspended by a spring above the lower plate, which is fixed. The voltage between the



**Figure 1:** The lumped parameters model of the pull-in phenomenon between parallel plates. Figure 1 was redrawn from [11].

plates creates an electrostatic force that causes the upper plate to move down, and the spring provides a restoring force that prevents it from moving. When the applied voltage is lower than the pull-in voltage, the electrostatic force and mechanical recovery force on the upper plate are balanced. As the voltage continues to increase, the upper plate moves rapidly downward and the two plates eventually contact, known as pull-in.

The critical voltage [11] that leads to the pull-in phenomenon is:

$$V_{\text{Pull-in}} = \sqrt{\frac{8kg^3}{27\varepsilon_0 A}},\tag{1}$$

where k is the elastic modulus of the flexible material, g is the initial distance between the two plates,  $\varepsilon_0$  is the vacuum permittivity, and A is the area of the two plates. It can be seen from Equation 1 that the material, size, and structure of the electrodes affect properties such as voltage, response time, and life cycles. When designing an ideal MEMS device, these parameters should be considered first.

#### Electrostatic NEM switches

The simplest application of the pull-in phenomenon are NEM switches, which are the basis for the other component or system level applications. NEM switches represent a class of nanoscale devices, integrating both electrical and mechanical functionality of nanostructures to process external stimuli applied to the device controlling the electrical current [12]. The lower pull-in voltage and the improved durability of the NEM switches require electrode materials with high Young's modulus, conductivity, and Poisson's ratio. The flexible suspension electrode materials include CNTs, GR, and silicon/germanium nanowires.

#### **CNT-NEM** switches

CNTs are an ideal flexible material for NEM electrodes because of the high aspect ratio, current density, and excellent tensile strength. In 2000, Rueckes et al. [13] observed the electromechanical conversion in CNTs for the first time. They used single-walled carbon nanotubes (SWCNTs) to prepare a suspended cross bistable switch array. Table 1 summarizes the structures and the voltage of CNT-NEM switches described in the literature. In addition to bridge and cantilever structures, the vertical structure further increases the device density and can be used in nonvolatile high-density memory devices. The properties such as diameter, length, morphology, and structure of CNTs directly affect the pull-in voltage and repeatability.

**Bridge model:** The typical structure of the bridge model is shown in Figure 2a. In 2006, Kaul et al. [15,16] prepared a two-

Table 1: Structu	res and pull-in voltage	es of CNT switches.				
Ref	Structure	S/MCNT	Diameter [nm]	Length [nm]	Gap [nm]	Voltage [V]
[14]	bridge	М	20–40	800	40–60	3.6
[4,15,16]	bridge	S	1–3	130	20	3.5-4.5
[2]	bridge	S	_	_	_	2.5-3
[17]	cantilever	M	22	115	4	2.9
[18]	vertical	M	70	1400	100	20
[19,20]	vertical	М	50-70	_	100	4.5

terminal SWCNT NEM switch. The response time of the switch is nanoseconds, and the voltage is 3.5–4.5 V. Cha et al. [21] prepared a multiwalled carbon nanotube (MWCNT) NEM switch. The switch length is 800 nm, the diameter is 20–40 nm, the initial gap is 40–60 nm, and the critical voltage is about 3.6 V. Abbasi et al. [2] prepared a bistable switch based on SWCNTs, with a pull-in voltage of 2.5–3 V, which is suitable for nonvolatile memory applications.

Cantilever model: A typical cantilever structure is shown in Figure 2b. Lee et al. [22,23] prepared a three-terminal cantilever MWCNT NEM switch. The diameter of the CNTs is 50 nm, the length is 2  $\mu$ m, and the gap is 150 nm. When  $V_{\rm ds}$  is 0.5 V,  $V_{\rm gs}$  is in the range of 6–30 V. Loh et al. [24] prepared a cantilever SWCNT and diamond electrode structure with a pull-in voltage of 23.4 V. C–C bonds can greatly improve the life cycles of the switch. The MWCNTs prepared by Dujardin et al. [17] had a diameter of 22 nm and a length of 115 nm. The pull-in voltage was reduced to 2.9 V as the gap was only 4 nm.

**Vertical model:** On the basis of the cantilever model, Jang et al. [18-20] and Kaul et al. [4] aligned CNTs vertically on the substrate. The vertically aligned MWCNTs were prepared by various methods, such as the constriction of CNT growth by nanoscale pockets and DC plasma-enhanced chemical vapor

deposition (PECVD), as shown in Figure 2c. Using the vertically aligned CNT switches provides a high density for nonvolatile storage memory. However, vertically aligned switches require a pull-in voltage of more than 20 V, higher than the usual horizontally distributed switches.

CNT-NEM switches have great potential in nonvolatile memory devices. In addition, CNTs used with metals in composite electrodes, as a bottom contact electrodes [25,26], or intermediate layer [27], and can greatly improve the life cycles of switches. However, CNTs prepared by CVD are randomly oriented, and positioning CNTs at the desired location is a challenge that hinders scalable manufacturing. Ward et al. [28] and Cha et al. [21] used spin coating to disperse carbon nanotubes on a substrate by coating with a solution carbon nanotube powder in dichlorobenzene. In addition, a more general solution deposition process is AC dielectrophoretic technology [2,23]. The dielectrophoretic technology requires low voltage, high frequency, and can quickly realize positioning and alignment of CNTs in 10–60 s.

#### **GR-NEM** switches

Based on the large-scale fabrication of graphene (GR) using CVD and oxygen plasma etching, GR-NEM switches have attracted the attention of researchers. Table 2 summarizes

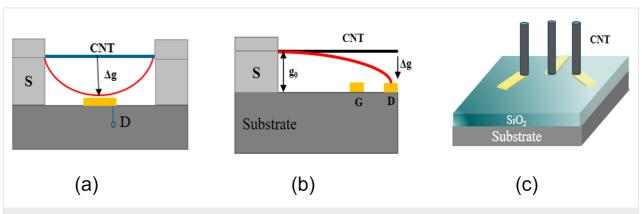


Figure 2: Structures of CNT switches. (a) Bridge model, (b) cantilever model, and (c) vertical model. Figure 2a,b,c were redrawn from [21], [22], and [18], respectively.

Ref	Structure	Length [µm]	Width [μm]	Thickness [layers]	Voltage [V]
[29]	GR-GR	20	3	1	5
[30]	GR-Au	1	4	2	4–5
[31]	GR-Au	1	2.5	2	5-7.5
[32]	GR-Au	1	_	20	10-12.5
[33,34]	GR-Au	2.5	0.5	2	2
[7,35]	GR-Au	1	2	1	1.92
[6]	GR-Au	1	2	10	3.8
[5]	GR-GR	1.5	0.8	1	6

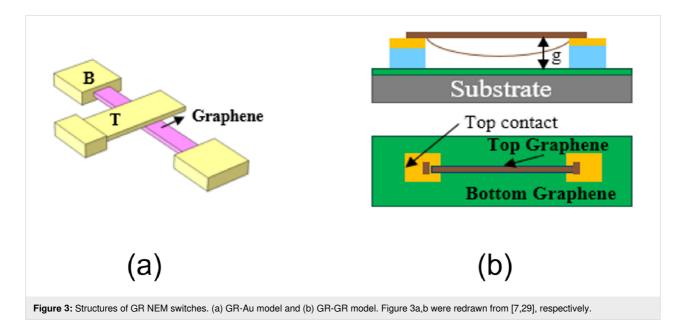
GR-NEM switch structures described in the literature. Two common electrode materials are GR-Au and GR-GR. By adjusting the size of the graphene sheets, the number of layers and the gap, different pull-in voltages can be obtained.

**GR-Au:** A typical GR-Au structure is shown in Figure 3a. As the structure requires a sacrificial layer to keep GR suspended and avoid collapse, the preparation technology is complex. Li et al. [30] prepared cantilever GR-NEM switches with two or three terminals using amorphous silicon as sacrificial layer. Sun et al. [6] prepared a three-terminal switch using PMMA polymer as the sacrifice layer. The graphene beam area was  $2.5~\mu m \times 0.5~\mu m$ , the gap was 170 nm, and the pull-in voltage was about 2 V. In 2016, Sun et al. [6] prepared a graphene cantilever switch with a thickness of about 10 layers, as shown in Figure 3a. The graphene sheet has an area of 1  $\mu m \times 2~\mu m$ , a gap of 1401 nm, and a critical voltage of 3.8 V. In addition, Liu et al. [34] proposed a GR-NEM switch structure with three-terminal circular clamping. The significant advantages of this ge-

ometry are the elimination of tearing and the limitation of stiction, providing a new direction for achieving reliable GR switches.

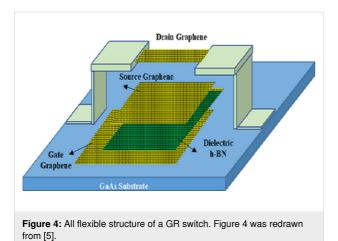
**GR-GR:** Figure 3b shows a typical GR-GR structure. Li et al. [33] used molecular dynamics to compare the adsorption energies of GR-Au and GR-GR structures. The GR-GR structure has a higher adsorption energy of 307 mJ/m<sup>2</sup>. Mizuta et al. [7,35] assumed that the use of a GR-GR electrode structure could avoid the uncontrollable microscale interactions between GR and the outer coating interface of the metal electrode. In addition, the use of a GR-GR electrode structure can improve the flexibility of the whole switch, laying a good foundation for fully flexible devices.

In 2009, Milaninia et al. [29] reported the first NEM switch with a GR-GR electrode structure. The bridge monolayer graphene electrode is 20  $\mu$ m long and 3  $\mu$ m wide, the electrode gap is 500 nm, and the pull-in voltage is less than 5 V. As the



electrode material was polycrystalline graphene, fracture of the GR electrode was observed after 4–5 life cycles, resulting in device failure.

In 2018, Huynh Van et al. [5] prepared a three-terminal fully flexible graphene switch for the first time, as shown in Figure 4. The switch is made of fully flexible materials. The three electrodes are all made of 1–2 layers of graphene, and the insulating medium layer is made of h-BN. The pull-in voltage at room temperature is less than 6 V, and the switch fails after 12 life cycles.



Graphene NEM switches can be used as phase shifters in RF MEMS systems due to the stable lattice and hydrophobic surface of graphene. In addition, GR-NEM switches can also be used as ESD devices in integrated circuits, which can greatly simplify the circuit volume of traditional PN junctions and increase the circuit integration.

#### **NWs-NEM** switches

Single-crystal nanowires are excellent candidates for NEM switches due to their uniform chemical and physical structure, and good reproducibility of structure and composition. Elec-

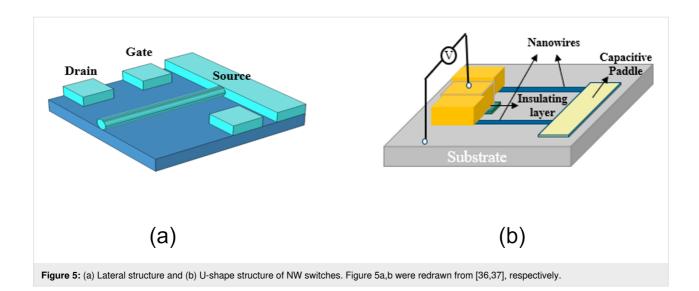
trode materials include diamond, SiC, Si, and Ge. Table 3 summarizes nanowire materials commonly used for NEM switches.

The research on NWs-NEM switches can be classified into two types, namely manufacturing techniques and in situ techniques. In the former, the switches are first processed by top-down or bottom-up processes and, subsequently, their parameters are tested. In the latter, the pull-in effect of NWs is directly studied through atomic force microscopy or transmission electron microscopy using nanomanipulators. This allows one to explore different working states without having to manufacture multiple devices repeatedly.

Manufacturing technique: Liao et al. [36] first proposed the concept of a lateral switch structure in 2010, as shown in Figure 5a. The structure uses bilateral electrodes to assist recovery and improve lifetime. The flexible element uses a diamond NW as cantilever with cross section of  $14 \mu m \times 400 \text{ nm}$ . The gap is 300 nm, and the pull-in voltage is 70 V. As the width is reduced to 200 nm and the gap is reduced to 100 nm, the pull-in voltage is reduced to 5 V. The switching current ratio is greater than  $10^6$ , and the number of life cycles is greater than  $10^5$ . Jasulaneca et al. [8] made a CuO NWs switch,  $3 \mu m \log_7 80 \text{ nm}$  in diameter, and 120 nm in the gap, with a pull-in voltage of 12.5 V. Feng et al. [40] prepared SiC nanowire NEM switches using bottom-up techniques. The pull-in voltage ranges from one to several volts and the response time is below microseconds with a switching ratio of about  $10^3$ .

Qian et al. [37] produced a U-shaped NEM switch with two Si nanowires, which support a square capacitive plate to form a U-shaped removable electrode, as shown in Figure 5b. The length of the silicon nanowires is 5  $\mu$ m, the cross section is  $90 \times 90$  nm square, the lateral gap is 2  $\mu$ m, the gap between the electrode and the substrate is 145 nm, and the voltage is 1.12 V. However, the critical voltage fluctuation range is 1 V, and only five life cycles were achieved. Boodhoo et al. [38] used heavily

Date	Ref	Terminal	Material	Length [µm]	Gap [nm]	Voltage [V]
2010	[14]	two	$Mo_6S_3I_6$	15	3000	21
2010	[36]	three	diamond	14	300	70
2010	[36]	three	diamond	14	100	5
2012	[37]	two	silicon	5	145	1.12
2015	[38]	three	silicon	1.8	50	1.8
2016	[39]	two	Bi <sub>2</sub> Se <sub>3</sub>	4	620	2.8
2019	[9]	two	Ge <sub>0.91</sub> Sn <sub>0.09</sub>	14.8	570	5
2021	[8]	two	CuO	3	120	12.5



doped Si NWs that were 1800 nm long, 42 nm wide and 50 nm thick, with a lateral spacing of 50 nm. The pull-in voltage was -1.8 V and the switching ratio was 10<sup>5</sup>. However, the introduction of heavy doping will increase the risk of failure.

In situ technique: In 2010, Andzane et al. [14] used the in situ technique to characterize  $Mo_6S_3I_6$  NW electrodes. The  $Mo_6S_3I_6$  beam was 15  $\mu$ m in length and 3  $\mu$ m in radius. The initial gap was 3  $\mu$ m, and the pull-in voltage was over 21 V. In another publication [41], the authors used in the situ technique to glue Ge nanowires to electrochemically etched gold tips to form NEM switches. The pull-in voltage was 40–45 V with an initial electrode gap of 6  $\mu$ m.

Kosmaca et al. [39] studied the electrostatic pull-in of  $Bi_2Se_3$  NW electrodes and determined that the pull-in voltage was 3 V. This technique provides a new method for the connection between the intermediate layer of ITO and the electrode. In [10], they also used the in situ technique for resonance detection of a GeSn alloy. Meija et al. [42] proposed the technique of using resonance aid to reduce the pull-in voltage of  $Ge_{0.91}Sn_{0.09}$  NWs. By applying 610.8 kHz and 0.45 V AC voltage to a GeSn alloy nanowire, the pull-in voltage can be reduced from 13.8 to 5 V. This method can effectively reduce the pull-in voltage as required by materials that are not resistant to higher voltages, such as  $Bi_2Se_3$ , and broaden the selection range of materials.

The influence of switch structure on the pull-in effect The current NEM switches are still in the laboratory development stage, and the main obstacle to commercialization is the repeatability. Commercial switches require at least 10<sup>15</sup> working cycles, but the current level is still 10<sup>4</sup>–10<sup>8</sup>. And the life cycles based on two-dimensional flexible materials such as graphene are only about a decade, far from meeting the needs of

commercialization. NEM switches with low voltage and a high number of switching cycles are the main direction of research.

Structure optimization: The design of NEM switches with low voltage mainly considers the number of electrodes, mechanical structure, and driving mode. According to the number of electrodes, NEM switches can be divided into two-terminal, threeterminal, or multiterminal switches. The simple structure of the two-terminal switches requires fewer electrodes. Using additional electrodes for multiterminal switches reduces the elastic requirement for the materials and ensures a sufficiently large initial gap. According to the mechanical structure, NEM switches can be divided into cantilever (single clamp) and bridge (double clamp). Cantilevers are more sensitive with a lower critical voltage, while the stability of the beam is stronger when both ends are fixed. Samaali and Najar [43] proposed a double-beam RF switch, and the study showed that the pull-in voltage of the double-beam structure was 23% lower than that of the general structure. Prasanth et al. [44] designed structures with non-uniform shape and found that the pull-in voltage increased as the taper degree increased. Almitani et al. [45], Abdelrahman et al. [46], and Eltaher et al. [47,48] proposed a perforated-beam structure, based on cantilever and bridge structures. Their studies showed that the filling rate of holes significantly affected the resonant frequency of the beam. Guha et al. [49] and Sravani et al. [50] proposed a structure of perforated parallel plates supported by four serpentine springs. The elastic coefficient can be significantly reduced by the serpentine meander structure. Also, the capacitance and viscous air damping can be effectively reduced by the perforated structure, thus reducing the pull-in voltage.

**Lifetime extension:** Understanding the failure of the switch is the premise to improve the number of life cycles of switches.

Mechanical tear, burn, and stiction are the main problems affecting the lifetime of NEM switches. A pressure of up to 30 GPa [51] when the switch is closed can easily cause wear and tear of the switches, especially for CNT [16] and GR [29] switches. Permanent adhesion caused by the dielectric charging [52,53] and chemical bonding [24] are the focus of the research on improving the life cycles. At present, it is mainly solved by reducing the electrode contact area [14] or increasing the electrode gap [54]. Due to the high voltage and the rapid local heating caused by the electrostatic discharge, the burning and melting electrode material will also affect the life cycles of the switch. Using AC voltage can reduce the need for high-temperature resistance of the electrodes. The performance of the switches can also be affected by the surrounding environment. Capillary forces are occurring due to the humid environment during wet etching [24]. In addition, the design of NEM switches needs to consider the unique characteristics of the materials. For example, the shape of CNTs not only affects the pull-in voltage, but also filled single-wall and bamboo-shaped MWCNTs show higher breaking fields than empty MWCNTs [55].

## Applications of NEM switches

NEM switches are basic components and are widely used in component-level and system-level applications. NEM switches can be used as low-power switches, phase shifters, and other basic components for RF communication. At the same time, there is extensive research on NEM switch structures in the preparation of microvalves, micropumps, or physical unclonable functions.

## RF switches and phase shifters

Traditional RF MEMS systems use metal as the removable element to prepare RF switches. The response time of metal RF switches is about  $1{\text -}15~\mu s$ , and the pull-in voltage is about tens of volts, which cannot meet the demand. Also, the metal lattice stability is poor. Electromigration or micro-welding adhesion failure might easily occur. GR-NEM switches with low power consumption can effectively reduce the pull-in voltage, improve the response speed, and overcome the shortcomings of metal switches, such as slow response and high critical voltage.

Graphene NEM capacitor switches have been combined with coplanar waveguide (CPW) transmission lines as analog and digital phase shifters. Phase shifters are key components in smart antennas, and beam steering or scanning applications. The distributed microelectromechanical system transmission line (DMTL) is the most common type of phase shifter based on a load line structure. A DMTL consists of a CPW and a GR-NEM switching load. The GR-NEM switching load can affect the characteristic impedance of the transmission line, thus control-

ling the speed and generating a phase shift. A typical phase shifter structure is shown in Figure 6.

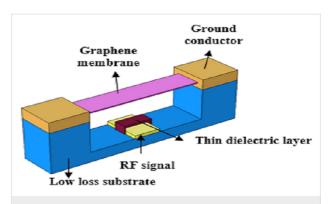


Figure 6: The structure of a GR-CPW phase shifter. Figure 6 was redrawn from [56].

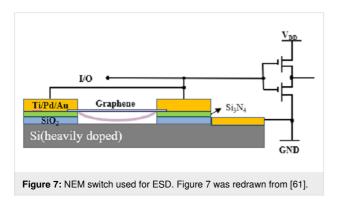
Dragoman et al. [57] first used a GR switch in a RF system. They deposited three gold bands onto a silicon substrate to form a CPW, and then prepared a suspended graphene bridge structure above the CPW with a pull-in voltage of 2 V and a wide operating frequency of 60 GHz. Sharma et al. [56,58] tested the isolation and insertion loss of graphene in microwave and millimeter-wave bands. At frequencies of 1-60 Ghz, the isolation of the single-layer graphene switch was 10 dB, and that of multilayer graphene was 20 dB. In addition, the isolation of the GR RF switch can be regulated by the applied voltage. Moldovan et al. [59] used multilayer graphene switches prepared by CVD for analog and digital bandwidth phase shifters. The phase shift was 355°/dB in the analog design at 2.4 GHz and 138°/dB in the digital design. Anjum et al. [60] used GR/MoS<sub>2</sub> composite materials as beam to decrease the pull-in voltage below 1 V, which can be used in RF applications requiring low driving voltage.

#### ESD circuits

ESD protection is an indispensable part of integrated circuit (IC) design. Traditional in silico PN-junction-based on-chip ESD protection will cause parasitic capacitance, leakage, noise, a large consumption of silicon area, and other negative effects. This is unacceptable in the scale down of advanced IC designs. GR-NEM switches can be used as a new and unique ESD protection structure to replace PN junctions in integrated circuits. The high carrier mobility and the heat transfer coefficient allow for a low conduction resistance and can avoid device burn during ESD.

ESD protection based on GR NEM switch structures can be integrated into the back end of the substrate, as shown in Figure 7. The anode and cathode of the switches are connected to input/output pads and ground pad on a chip, respectively.

When electrostatic discharge occurs, the suspended graphene will be pulled into the bottom electrode, and the normally closed GR-NEM switch will be opened to release electrostatic discharge pulses.

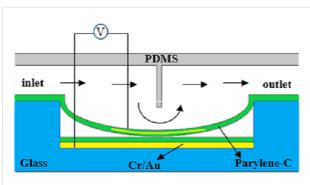


Ma et al. [61] proposed that a model of an ESD circuit in which GR-NEM structures were used. The ESD voltage is 7–17.5 V, the response time is 200 ps, and the repetition cycle is more than 30 times. Chen et al. [62-64] tested GR-NEM ESD circuits using transmission line pulse (TLP) technology and assumed that transient ESD behavior was related to size and TLP waveform. Zhang et al. [65] explored the electrothermal characteristics of a Cu-GR interconnect under ESD, and the maximum temperature of the interconnect with a thickness of 10 nm could be reduced by 30%. Ng et al. [66] conducted stress tests on the prepared GR-NEM switch and proposed a nail structure to improve reliability. Li et al. [67-70] compared the effects of graphene materials on ESD performance. Due to the removal of grain boundaries, the single-crystal GR devices are four times more capable of handling current than polycrystalline GR devices. Shen et al. [71] prepared GR ESD switches and analyzed their electrical characteristics and reliability. The switch did work only once. Therefore, ESD protection based on the GR NEM switch structure needs to be further studied.

#### Microfluidic systems

Microfluidic systems composed of microvalves, microsensors, and micropumps are widely used in chemical engineering and life sciences. Electrostatically actuated microvalves reduce the required volume and are beneficial to the miniaturization of microfluidic systems. Electrostatically actuated microvalves contain a closing electrode, an opening electrode, and a flexible removable diaphragm. Bae et al. [72] fabricated a bidirectional microvalve using flexible polyimide as the valve diaphragm. The response time is 50 μs and can withstand 126 kPa pressure. Yıldırım et al. [73,74] used poly(*para*-xylene) to prepare a microvalve used in lab-on-chip devices. The pull-in voltage is 150 V and the valve can withstand pressure of 40 kPa. Desai et al. [75] and Patrascu et al. [76] designed a microvalve based on

PDMS with a pull-in voltage of 150 V and a pressure capacity of 23 kPa and a life cycle of 400 times. Atik et al. [77] prepared a normally closed valve on glass substrates as shown in Figure 8. The valve required an average voltage of 221 V, the response time was less than microseconds, and the lifecycles was more than 50 times. The combination of flexible valves and PDMS microchannels optimizes the optical transparency and durability of microvalves.



**Figure 8:** Schematic diagram of a flexible microvalve structure. Figure 8 was redrawn from [77].

A pneumatically coupled graphene membrane pump was designed by Davidovikj et al. [78], which is selectively permeable to gases in a controllable way, as shown in Figure 9. The dumb-bell-shape chamber consists of a narrow trench connecting two circular cavities, both of which are covered by few-layer graphene. Local electrodes at the bottom of each cavity allow to drive each graphene membrane individually, changing the pressure of the chamber through a small voltage and controlling the airflow between the two cavities. The system can also be used for molecular sieving of gases and the aspiration and dispensing of liquids by introducing pores in the graphene membranes.

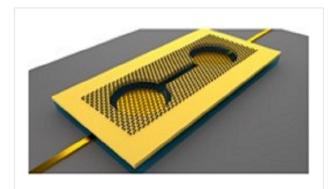
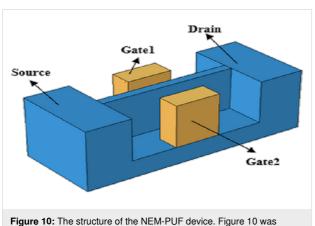


Figure 9: Schematic diagram of a GR pump. Figure 9 was republished from [78] (D. Davidovikj et al., "Graphene gas pumps", 2D Materials, vol. 5, article no. 031009, published on 11 May 2018; <a href="https://doi.org/10.1088/2053-1583/aac0a8">https://doi.org/10.1088/2053-1583/aac0a8</a>); © 2018 IOP Publishing. Reproduced with permission. All rights reserved. This content is not subject to CC BY 4.0.

# NEM physical unclonable functions

Just as NEM switches use the pull-in instability in microdevices as working principle, static stiction can be used in NEM switches as a mechanism to achieve a physical unclonable function (PUF) to ensure the reliability and stability of data. A PUF extracts the intrinsic physical parameters of a device and divides these parameters into the binary states "0" and "1". Instead of storing encrypted information in digital memory, NEM-PUF creates a unique chip that takes advantage of the process differences inevitable in integrated circuit processing. Even though the mask and process are same, each chip is slightly different due to normal manufacturing variability.

Boodhoo et al. [38] investigated the influence of the doping level on the gate voltage by preparing bilateral-gate Si NW transistors. Figure 10 shows the NEM-PUF structure prepared by Hwang and co-workers [79]. The device uses Si NWs as a moving electrode and the two gates (G1/G2) are symmetrically distributed. Si NWs in contact with G1 is defined as "0", and Si NWs in contact with G2 is defined as "1". The state of the PUF is determined by the current difference after G1/G2 conduction. In 2019, Hwang et al. [80] further improved the technique by increasing the states and expanding the encryption capacity by applying different doping levels on the source and drain sides of Si NWs. A NEM-PUF is ideal for outdoor and military applications due to the unique encryption formed by the random stiction of the devices during manufacturing.



redrawn from [79].

#### Microscale energy systems

Electrostatically driven switches are known to require very high voltages. On the one hand, researchers are trying to optimize the size of the switches to find the optimal solution to reduce the voltage [81-84]. On the other hand, microswitches based on cantilever beam or bridge structures can be combined with triboelectric [85], pyroelectric [86], piezoelectric [87-89], and other power generation forms to form a microscale energy

system. In this system, microswitches can be used as either rectifier to connect the power source to the load circuit or as an actuator structure, which plays an important role in the microscale energy system.

Triboelectric power generation: Two materials with different work functions will gain or lose electrons when they are in contact with each other, resulting in the triboelectric effect [90]. When the switch is closed, the electrode contact friction causes the surfaces to carry charges, forming a dielectric charging effect. Molinero et al. [52,53] characterized the dielectric charging when the switch electrodes were contacted and proved that the surface dielectric charging caused by friction would lead to a shift of the switch voltage and shorten the life cycles of the switch.

Based on the coupling of triboelectrification and electrostatic induction, the switch can be used as a triboelectric generator (TEG) [90]. The TEG usually contains an insulated diaphragm and the switch is normally closed. The combination of triboelectrification and the elastic restoring force causes the switch to open. After the circuit conduction or charge disappears, the switch is pulled in again. By analyzing the electric field in the comb structure, He et al. [91] showed that the side electrode would generate a repulsive force without introducing a current. Pallay et al. [85] proposed a sensor-switch system as shown in Figure 11. The TEG consists of two aluminum electrodes and a PDMS-insulated diaphragm placed on a fixed electrode. When the TEG is impacted, the switch is opened with the help of the bottom side electrode. The TEG provides a way to solve the problem of high voltage of electrostatic switches. In addition, Cui et al. [92-95] showed that the self-powered triboelectrification nanogenerator (TENG) can also be used for human health detection and wound healing.

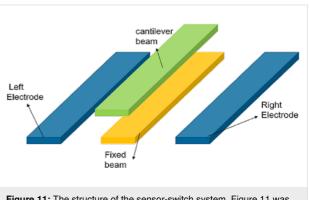
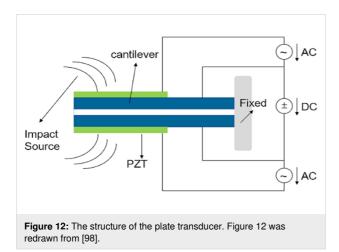


Figure 11: The structure of the sensor-switch system. Figure 11 was redrawn from [86].

**Piezoelectric power generation:** Piezoelectric materials can produce very precise tiny motions and have significant charac-

teristics of high force transmission [96], which is very suitable for MEMS applications. When the piezoelectric material is pressed, a potential difference will be generated between the two ends of the material. This potential difference can be used to assist the driving voltage of the NEM switch, thus reducing the need for external excitation.

Ultrasonic transducers based on capacitive switch structures are widely used in medical imaging, underwater tests, and obstacle detection. Saadatmand and Kook [97] conducted a theoretical analysis on electrostatically driven ultrasonic transducers with one-side and two-side circular plate structures and modified the voltage calculated by the lumped parameter model. Raeisifard et al. [96] proposed a comprehensive model of cantilever switches under electrostatic and piezoelectric excitation. They pointed out that piezoelectric excitation would reduce the demand for voltage of electrostatic switches, but would introduce additional manufacturing cost. Rutsch et al. [98] combined piezoelectric materials with NEM switches to prepare ultrasonic transducers, as shown in Figure 12. The structure retained piezoelectric bending and a DC voltage could be used to adjust the resonant frequency of the electrostatic switch to expand the bandwidth of the ultrasonic transducer. İkizoğlu et al. [99] combined a piezoelectric energy harvester with a cantilever beam switch for RF systems. Under vibrations, the piezoelectric material produces charges that are transferred via a switch rectifier to a capacitor to load it.



**Pyroelectric power generation:** Pyroelectric crystals can generate electricity with a change in temperature owing to the net change in polarization of the crystal units [100]. Therefore, it can be an ideal high-voltage source for MEMS/NEMS devices. Similar to the triboelectric effect, the pyroelectric effect can also be used as an energy source for microscale energy systems with NEM switches for corresponding sensors and actuators.

Microscale robotics can be used to navigate and explore narrow spaces and manipulate microscale parts. A pyroelectric highvoltage generator (PHVG) system can produce voltages up to several kilovolts, which is an important power supply system for microscale robotics. Ni et al. [100] proposed a PHVG system composed of lithium niobate and a PZT cantilever switch, as shown in Figure 13. Lithium niobate can generate voltages with the help of a single-chip microcomputer containing heating and cooling functions. When the voltage exceeds the PZT cantilever switch pull-in voltage, the switch will be closed to complete the actuator task. In 2020, the authors [100] prepared a high-voltage source with 1.03 kV output voltage using 3D printed conductive polylactic acid polymer. In 2021, they [101] integrated a lithium niobate crystal, a piezoelectric cantilever switch, and a capacitive load into a single printed circuit board (PCB). The output voltage ranges from 15.5 to 1480 V, and the cantilever switch acts as a rectifier connected to PHVG to charge the capacitive load.

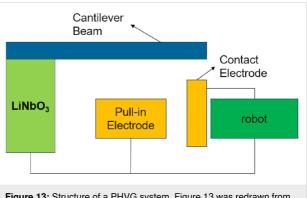


Figure 13: Structure of a PHVG system. Figure 13 was redrawn from [101].

## Conclusion

The pull-in effect is a unique phenomenon in microscale and nanoscale devices, which is widely used in flexible devices. This review describes the preparation of NEM switches based on CNTs, GR, and NWs. By selecting material, structure, and other parameters of the flexible electrode, the performance of the switches can be improved. The effect has great potential in the fields of low-voltage applications and low power consumption. At the same time, as the most basic component, NEM switches can be used in RF systems, as ESD protection for integrated circuits, as microvalves and micropumps in microfluidic systems, and as PUFs to implement data encryption for outdoor and defense systems. In addition, NEM switches can also be combined with triboelectricity, pyroelectric, piezoelectric effects into microscale energy systems as sensors and actuators. However, most of the current research focuses on the use of flexible components. Also, there is some research on fully flexible devices, which still need further development.

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# References

- Zhang, W.-M.; Yan, H.; Peng, Z.-K.; Meng, G. Sens. Actuators, A 2014, 214, 187–218. doi:10.1016/j.sna.2014.04.025
- Abbasi, S. A.; Kim, T.-h.; Somu, S.; Wang, H.; Chai, Z.; Upmanyu, M.; Busnaina, A. J. Phys. D: Appl. Phys. 2020, 53, 23LT02. doi:10.1088/1361-6463/ab7e61
- Cole, M.; Milne, W. Materials 2013, 6, 2262–2273. doi:10.3390/ma6062262
- Kaul, A. B.; Kowalczyk, R.; Megerian, K.; von Allmen, P.; Baron, R. L. *MRS Online Proc. Libr.* 2008, 1081, 1506. doi:10.1557/proc-1081-p15-06
- Huynh Van, N.; Muruganathan, M.; Kulothungan, J.; Mizuta, H. Nanoscale 2018, 10, 12349–12355. doi:10.1039/c7nr08439k
- Sun, J.; Muruganathan, M.; Kanetake, N.; Mizuta, H. *Micromachines* 2016, 7, 124. doi:10.3390/mi7070124
- Mizuta, H.; Hammam, A.; Kulothungan, J.; Suzuki, S.; Schmidt, M. E.; Sun, J.; Muruganathan, M. Recent Progress of Graphene-Based Nanoelectronic Devices and NEMS for Challenging Applications. In 2016 13th IEEE International Conference on Solid-State and Integrated Circuit Technology (ICSICT), 2016; pp 474–477. doi:10.1109/icsict.2016.7998955
- Jasulaneca, L.; Livshits, A. I.; Meija, R.; Kosmaca, J.; Sondors, R.; Ramma, M. M.; Jevdokimovs, D.; Prikulis, J.; Erts, D. Nanomaterials 2021, 11, 117. doi:10.3390/nano11010117
- Meija, R.; Livshits, A. I.; Kosmaca, J.; Jasulaneca, L.; Andzane, J.; Biswas, S.; Holmes, J. D.; Erts, D. *Nanotechnology* 2019, 30, 385203. doi:10.1088/1361-6528/ab2b11
- Kosmaca, J.; Meija, R.; Antsov, M.; Kunakova, G.; Sondors, R.; latsunskyi, I.; Coy, E.; Doherty, J.; Biswas, S.; Holmes, J. D.; Erts, D. Nanoscale 2019, 11, 13612–13619. doi:10.1039/c9nr02740h
- Rochus, V.; Rixen, D. J.; Golinval, J.-C. Nonlinear Anal. 2005, 63, e1619–e1633. doi:10.1016/j.na.2005.01.055
- Jasulaneca, L.; Kosmaca, J.; Meija, R.; Andzane, J.; Erts, D. Beilstein J. Nanotechnol. 2018, 9, 271–300. doi:10.3762/bjnano.9.29
- Rueckes, T.; Kim, K.; Joselevich, E.; Tseng, G. Y.; Cheung, C.-L.; Lieber, C. M. Science 2000, 289, 94–97. doi:10.1126/science.289.5476.94
- Andzane, J.; Prikulis, J.; Dvorsek, D.; Mihailovic, D.; Erts, D. Nanotechnology 2010, 21, 125706. doi:10.1088/0957-4484/21/12/125706
- Kaul, A. B.; Wong, E. W.; Epp, L.; Bronikowski, M. J.; Hunt, B. D. MRS Online Proc. Libr. 2006, 924, 604. doi:10.1557/proc-0924-z06-04
- Kaul, A. B.; Wong, E. W.; Epp, L.; Hunt, B. D. Nano Lett. 2006, 6, 942–947. doi:10.1021/nl052552r
- Dujardin, E.; Derycke, V.; Goffman, M. F.; Lefèvre, R.; Bourgoin, J. P. Appl. Phys. Lett. 2005, 87, 193107. doi:10.1063/1.2126805

- Jang, J. E.; Cha, S. N.; Choi, Y.; Amaratunga, G. A. J.; Kang, D. J.; Hasko, D. G.; Jung, J. E.; Kim, J. M. Appl. Phys. Lett. 2005, 87, 163114. doi:10.1063/1.2077858
- Jang, J. E.; Cha, S. N.; Choi, Y.; Butler, T. P.; Kang, D. J.;
  Hasko, D. G.; Jung, J. E.; Jin, Y. W.; Kim, J. M.; Amaratunga, G. A. J.
  Appl. Phys. Lett. 2008, 93, 113105. doi:10.1063/1.2983743
- Jang, J. E.; Cha, S. N.; Choi, Y. J.; Kang, D. J.; Butler, T. P.;
  Hasko, D. G.; Jung, J. E.; Kim, J. M.; Amaratunga, G. A. J.
  Nat. Nanotechnol. 2008, 3, 26–30. doi:10.1038/nnano.2007.417
- Cha, S. N.; Jang, J. E.; Choi, Y.; Amaratunga, G. A. J.; Kang, D.-J.;
  Hasko, D. G.; Jung, J. E.; Kim, J. M. Appl. Phys. Lett. 2005, 86, 083105. doi:10.1063/1.1868064
- Lee, S. W.; Lee, D. S.; Morjan, R. E.; Jhang, S. H.; Sveningsson, M.; Nerushev, O. A.; Park, Y. W.; Campbell, E. E. B. Nano Lett. 2004, 4, 2027–2030. doi:10.1021/nl049053v
- Lee, S. W.; Lee, D. S.; Yu, H. Y.; Campbell, E. E. B.; Park, Y. W. Appl. Phys. A: Mater. Sci. Process. 2004, 78, 283–286. doi:10.1007/s00339-003-2363-6
- Loh, O.; Wei, X.; Sullivan, J.; Ocola, L. E.; Divan, R.; Espinosa, H. D. Adv. Mater. (Weinheim, Ger.) 2012, 24, 2463–2468. doi:10.1002/adma.201104889
- Jo, E.; Seo, M.-H.; Pyo, S.; Ko, S.-D.; Kwon, D.-S.; Choi, J.;
  Yoon, J.-B.; Kim, J. ACS Appl. Mater. Interfaces 2019, 11, 18617–18625. doi:10.1021/acsami.9b02747
- Bull, T. G.; McBride, J. W. Technologies 2019, 7, 69. doi:10.3390/technologies7040069
- Wang, Y.; Yang, Z.; Xu, Q.; Chen, W.; Ding, G.; Zhao, X. J. Micromech. Microeng. 2015, 25, 085012. doi:10.1088/0960-1317/25/8/085012
- Ward, J. W.; Meinhold, M.; Segal, B. M.; Berg, J.; Sen, R.; Sivarajan, R.; Brock, D. K.; Rueckes, T. A Nonvolatile Nanoelectromechanical Memory Element Utilizing a Fabric of Carbon Nanotubes. In *Proceedings. 2004 IEEE Computational Systems Bioinformatics Conference*, IEEE: Orlando, FL, USA, 2004; pp 34–38. doi:10.1109/nvmt.2004.1380799
- Milaninia, K. M.; Baldo, M. A.; Reina, A.; Kong, J. Appl. Phys. Lett. 2009, 95, 183105. doi:10.1063/1.3259415
- Li, P.; You, Z.; Cui, T. Appl. Phys. Lett. 2012, 101, 093111. doi:10.1063/1.4738891
- Grushina, A. L.; Ki, D.-K.; Morpurgo, A. F. Appl. Phys. Lett. 2013, 102, 223102. doi:10.1063/1.4807888
- AbdelGhany, M.; Szkopek, T. Appl. Phys. Lett. 2014, 104, 013509. doi:10.1063/1.4861059
- Li, P.; You, Z.; Cui, T. Sens. Actuators, A 2014, 217, 56–61. doi:10.1016/j.sna.2014.06.010
- Liu, X.; Suk, J. W.; Boddeti, N. G.; Cantley, L.; Wang, L.; Gray, J. M.; Hall, H. J.; Bright, V. M.; Rogers, C. T.; Dunn, M. L.; Ruoff, R. S.; Bunch, J. S. Adv. Mater. (Weinheim, Ger.) 2014, 26, 1571–1576. doi:10.1002/adma.201304949
- Mizuta, H. Recent Progress of Graphene-Based Nanoelectronic and NEM Device Technologies for Advanced Applications. In 2016 IEEE International Conference on Semiconductor Electronics (ICSE), 2016; A1–A2. doi:10.1109/smelec.2016.7573572
- Liao, M.; Hishita, S.; Watanabe, E.; Koizumi, S.; Koide, Y. Adv. Mater. (Weinheim, Ger.) 2010, 22, 5393–5397. doi:10.1002/adma.201003074
- Qian, Y.; Lou, L.; Julius Tsai, M.; Lee, C. Appl. Phys. Lett. 2012, 100, 113102. doi:10.1063/1.3693382

- Boodhoo, L.; Crudgington, L.; Chong, H. M. H.; Tsuchiya, Y.;
  Moktadir, Z.; Hasegawa, T.; Mizuta, H. Microelectron. Eng. 2015, 145, 66–70. doi:10.1016/j.mee.2015.02.047
- Kosmaca, J.; Andzane, J.; Baitimirova, M.; Lombardi, F.; Erts, D. ACS Appl. Mater. Interfaces 2016, 8, 12257–12262. doi:10.1021/acsami.6b00406
- Feng, X. L.; Matheny, M. H.; Zorman, C. A.; Mehregany, M.; Roukes, M. L. Nano Lett. 2010, 10, 2891–2896. doi:10.1021/nl1009734
- Andzane, J.; Prikulis, J.; Meija, R.; Kosmaca, J.; Biswas, S.; Holmes, J. D.; Erts, D. *Mater. Sci. (Medžiagotyra)* 2013, 19, 5. doi:10.5755/j01.ms.19.3.3086
- Meija, R.; Kosmaca, J.; Jasulaneca, L.; Petersons, K.; Biswas, S.; Holmes, J. D.; Erts, D. *Nanotechnology* **2015**, *26*, 195503. doi:10.1088/0957-4484/26/19/195503
- Samaali, H.; Najar, F. Microsyst. Technol. 2017, 23, 5317–5327. doi:10.1007/s00542-016-3264-x
- Prasanth, C.; Harsha, C. S.; Pratiher, B.
  Proc. Inst. Mech. Eng., Part C 2018, 232, 3337–3350. doi:10.1177/0954406217736079
- Almitani, K. H.; Abdelrahman, A. A.; Eltaher, M. A. Structures 2020, 28, 1413–1426. doi:10.1016/j.istruc.2020.09.055
- Abdelrahman, A. A.; Esen, I.; Özarpa, C.; Eltaher, M. A. *Appl. Math. Modell.* 2021, 96, 215–235. doi:10.1016/j.apm.2021.03.008
- Eltaher, M. A.; Abdraboh, A. M.; Almitani, K. H. Microsyst. Technol. 2018, 24, 3925–3937. doi:10.1007/s00542-018-3910-6
- Eltaher, M. A.; Kabeel, A. M.; Almitani, K. H.; Abdraboh, A. M. *Microsyst. Technol.* 2018, 24, 4881–4893. doi:10.1007/s00542-018-3905-3
- Guha, K.; Laskar, N. M.; Gogoi, H. J.; Borah, A. K.; Baishnab, K. L.; Baishya, S. Solid-State Electron. 2017, 137, 85–94. doi:10.1016/j.sse.2017.08.007
- Sravani, K. G.; Rao, K. S.; Guha, K. Microelectron. J. 2021, 117, 105264. doi:10.1016/j.mejo.2021.105264
- Ke, C.; Espinosa, H. D. Small 2006, 2, 1484–1489.
  doi:10.1002/smll.200600271
- Molinero, D.; Cunningham, S.; DeReus, D.; Morris, A. Dielectric Charging Characterization in MEMS Switches with Insulator-Insulator Contact. In 2014 IEEE International Reliability Physics Symposium, IEEE: Waikoloa, HI, USA, 2014; 5C.3.1–5C.3.4. doi:10.1109/irps.2014.6860668
- Molinero, D.; Cunningham, S.; DeReus, D.; Morris, A. S. J. Microelectromech. Syst. 2016, 25, 737–743. doi:10.1109/jmems.2016.2573742
- Ziegler, K. J.; Lyons, D. M.; Holmes, J. D.; Erts, D.; Polyakov, B.;
  Olin, H.; Svensson, K.; Olsson, E. Appl. Phys. Lett. 2004, 84, 4074–4076. doi:10.1063/1.1751622
- Andzane, J.; Tobin, J. M.; Li, Z.; Prikulis, J.; Baxendale, M.;
  Holmes, J. D.; Erts, D. *Nano* 2007, 3, 10. doi:10.2240/azojono0123
- Sharma, P.; Perruisseau-Carrier, J.; Moldovan, C.; Ionescu, A. M. IEEE Trans. Nanotechnol. 2014, 13, 70–79. doi:10.1109/tnano.2013.2290945
- Dragoman, M.; Konstantinidis, G.; Dragoman, D.; Neculoiu, D.; Cismaru, A.; Coccetti, F.; Plana, R.; Harnagel, H.; Kostopoulos, A.; Buiculescu, R. RF NEMS Based on Carbon Nanotubes and Graphene. 2008 International Semiconductor Conference; 2008; Vol. 1, pp 103–106. doi:10.1109/smicnd.2008.4703339

- Sharma, P.; Perruisseau Carrier, J.; Ionescu, A. M.
  Nanoelectromechanical Microwave Switch Based on Graphene. In 2013 14th International Conference on Ultimate Integration on Silicon (ULIS), 2013; pp 189–192. doi:10.1109/ulis.2013.6523516
- Moldovan, C. F.; Vitale, W. A.; Tamagnone, M.; Ionescu, A. M. Graphene RF NEMS Shunt Switches for Analog and Digital Phase Shifters. In 2015 Transducers - 2015 18th International Conference on Solid-State Sensors, Actuators and Microsystems (TRANSDUCERS), 2015; pp 2029–2032. doi:10.1109/transducers.2015.7181354
- Anjum, A.; Madhewar, M.; Mohite, S. S.; Sawant, V. B. Graphene/MoS2 Based RF-NEMS Switches for Low Actuation Voltage and Enhanced RF-Performance. In 2018 9th International Conference on Computing, Communication and Networking Technologies (ICCCNT), 2018; pp 1–7. doi:10.1109/icccnt.2018.8494166
- Ma, R.; Chen, Q.; Zhang, W.; Lu, F.; Wang, C.; Wang, A.; Xie, Y.-H.;
  Tang, H. *IEEE Electron Device Lett.* 2016, 37, 674–676.
  doi:10.1109/led.2016.2544343
- Chen, Q.; Li, C.; Ng, J.; Lu, F.; Wang, C.; Zhang, F.; Ma, R.;
  Xie, Y.-H.; Wang, A. Transient Characterization of Graphene NEMS Switch ESD Protection Structures. In 2017 IEEE Electron Devices Technology and Manufacturing Conference (EDTM), 2017; pp 95–96. doi:10.1109/edtm.2017.7947523
- 63. Chen, Q.; Li, C.; Lu, F.; Wang, C.; Zhang, F.; Wang, A.; Ng, J.; Xie, Y.-H. TLP Measurement and Analysis of Graphene NEMS Switches for On-Chip ESD Protection. In 2017 IEEE 12th International Conference on Nano Micro Engineered and Molecular Systems (NEMS), 2017; pp 370–374. doi:10.1109/nems.2017.8017044
- Chen, Q.; Ma, R.; Zhang, W.; Lu, F.; Wang, C.; Liang, O.; Zhang, F.;
  Li, C.; Tang, H.; Xie, Y.-H.; Wang, A. *IEEE Trans. Electron Devices* 2016, 63, 3205–3212. doi:10.1109/ted.2016.2582140
- Zhang, R.; Zhao, W.-S.; Hu, J.; Yin, W.-Y. IEEE Trans. Nanotechnol. 2015, 14, 205–209. doi:10.1109/tnano.2014.2381775
- 66. Ng, J.; Zhang, W.; Xie, Y.-H.; Chen, Q.; Ma, R.; Wang, A. Optimization of Suspended Graphene NEMS Devices for Electrostatic Discharge Applications. In 2017 IEEE 12th International Conference on Nano Micro Engineered and Molecular Systems (NEMS), IEEE: Los Angeles, CA, USA, 2017; pp 364–369. doi:10.1109/nems.2017.8017043
- Li, C.; Chen, Q.; Ng, J.; Zhang, F.; Wang, H.; Di, M.; Wang, C.;
  Xie, Y.; Wang, A. Graphene-Based on-Chip ESD Protection. In 2019 Electron Devices Technology and Manufacturing Conference (EDTM), 2019; pp 176–178. doi:10.1109/edtm.2019.8731152
- Li, C.; Chen, Q.; Ng, J.; Zhang, F.; Wang, H.; Di, M.; Pan, Z.; Wu, T.; Zhang, K.; Xie, X.; Xie, Y.; Wang, A.
   IEEE Trans. Device Mater. Reliab. 2021, 21, 331–337. doi:10.1109/tdmr.2021.3090311
- Li, C.; Di, M.; Pan, Z.; Wang, A. A Study of Materials Impacts on Graphene Electrostatic Discharge Switches. In 2021 5th IEEE Electron Devices Technology & Manufacturing Conference (EDTM), IEEE: Chengdu, China, 2021; pp 1–3. doi:10.1109/edtm50988.2021.9420816
- Li, C.; Di, M.; Pan, Z.; Zhang, F.; Chen, Q.; Wang, A.
  IEEE J. Electron Devices Soc. 2021, 9, 1172–1180.
  doi:10.1109/jeds.2021.3121995

- Shen, L.; Lv, Y.; Jiang, L.; Kong, Z.; Lu, Y.; Chen, Q.; Wang, A.; Cheng, Y. A Study of the Electrical and Mechanical Reliability Properties of Suspended Graphene NEMS Devices for ESD Protection Applications. In 2021 5th IEEE Electron Devices Technology & Manufacturing Conference (EDTM), IEEE: Chengdu, China, 2021; pp 1–3. doi:10.1109/edtm50988.2021.9420840
- Bae, B.; Han, J.; Masel, R. I.; Shannon, M. A.
  J. Microelectromech. Syst. 2007, 16, 1461–1471. doi:10.1109/imems.2007.907782
- Yildirim, E.; Kulah, H.; Arikan, M. A. S. An Electrostatically Actuated Parylene Microvalve for Lab-on-a-Chip Applications. In 2011 16th International Solid-State Sensors, Actuators and Microsystems Conference, IEEE: Beijing, China, 2011; pp 250–253. doi:10.1109/transducers.2011.5969441
- Yildırım, E.; Arıkan, M. A. S.; Külah, H. Sens. Actuators, A 2012, 181, 81–86. doi:10.1016/j.sna.2012.05.008
- Desai, A. V.; Tice, J. D.; Apblett, C. A.; Kenis, P. J. A. Lab Chip 2012, 12, 1078. doi:10.1039/c2lc21133e
- Patrascu, M.; Gonzalo-Ruiz, J.; Goedbloed, M.; Brongersma, S. H.; Crego-Calama, M. Sens. Actuators, A 2012, 186, 249–256. doi:10.1016/j.sna.2012.03.006
- Atik, A. C.; Özkan, M. D.; Özgür, E.; Külah, H.; Yıldırım, E. J. Micromech. Microeng. 2020, 30, 115001. doi:10.1088/1361-6439/aba16f
- Davidovikj, D.; Bouwmeester, D.; van der Zant, H. S. J.; Steeneken, P. G. 2D Mater. 2018, 5, 031009. doi:10.1088/2053-1583/aac0a8
- Hwang, K.-M.; Park, J.-Y.; Bae, H.; Lee, S.-W.; Kim, C.-K.; Seo, M.;
  Im, H.; Kim, D.-H.; Kim, S.-Y.; Lee, G.-B.; Choi, Y.-K. ACS Nano
  2017, 11, 12547–12552. doi:10.1021/acsnano.7b06658
- Hwang, K.-M.; Kim, W.-K.; Jin, I. K.; Lee, S.-W.; Choi, Y.-K. Small 2019, 15, 1803825. doi:10.1002/smll.201803825
- Shingare, K. B.; Kundalwal, S. I. Appl. Math. Modell. 2020, 81, 70–91. doi:10.1016/j.apm.2019.12.021
- Radi, E.; Bianchi, G.; Nobili, A. Appl. Math. Modell. 2021, 91, 1211–1226. doi:10.1016/j.apm.2020.10.031
- Mukherjee, B.; Sen, S. Microelectron. Reliab. 2019, 94, 32–40. doi:10.1016/j.microrel.2019.01.012
- Lin, M. X.; Lee, S. Y.; Chen, C. K. J. Mech. 2019, 35, 767–778. doi:10.1017/jmech.2018.41
- Pallay, M.; Ibrahim, A. I.; Towfighian, S. A MEMS Threshold Acceleration Switch Powered by a Triboelectric Generator. In Volume 4: 23rd Design for Manufacturing and the Life Cycle Conference; 12th International Conference on Micro- and Nanosystems, American Society of Mechanical Engineers: Quebec City, Quebec, Canada, 2018; V004T08A010. doi:10.1115/detc2018-85543
- Ni, D.; Davaji, B.; Shepherd, R.; Lal, A. 3D Printed Pyroelectric Lithium-Niobate High Voltages Source with Pull-in Regulated Output. In 2020 IEEE 33rd International Conference on Micro Electro Mechanical Systems (MEMS), IEEE: Vancouver, BC, Canada, 2020; pp 586–589. doi:10.1109/mems46641.2020.9056438
- Masoumi, A.; Amiri, A.; Vesal, R.; Rezazadeh, G. *Proc. Inst. Mech. Eng., Part C* 2021, 235, 7821–7835. doi:10.1177/0954406221997481
- Sharma, R.; Agarwal, R.; Dubey, A. K.; Arora, A. Pragmatics of Capacitive Micromachined Ultrasonic Transducer. In 2017 International Conference on Emerging Trends in Computing and Communication Technologies (ICETCCT), IEEE: Dehradun, 2017; pp 1–4. doi:10.1109/icetcct.2017.8280328

- Darbasi, S.; Shourcheh, S. D.; Rezazadeh, G.; Afrang, S. Mechanical Behavior of a Capacitive Tunable Ultrasound Transducer for Bio Diagnostic Application. In *Electrical Engineering (ICEE), Iranian Conference on*, IEEE: Mashhad, 2018; pp 336–341. doi:10.1109/icee.2018.8472639
- Ibrahim, A.; Ramini, A.; Towfighian, S. J. Sound Vib. 2018, 416, 111–124. doi:10.1016/j.jsv.2017.11.036
- He, S.; BenMrad, R. IEEE Trans. Ind. Electron. 2005, 52, 974–983. doi:10.1109/tie.2005.851682
- Cui, X.; Cao, S.; Yuan, Z.; Xie, G.; Ding, J.; Sang, S.; Zhang, H. Energy Technol. 2019, 7, 1800931. doi:10.1002/ente.201800931
- 93. Guo, R.; Zhang, H.; Cao, S.; Cui, X.; Yan, Z.; Sang, S. *Mater. Des.* **2019**, *182*, 108025. doi:10.1016/j.matdes.2019.108025
- Hao, R.; Yang, S.; Yang, K.; Zhang, Z.; Wang, T.; Sang, S.; Zhang, H. ACS Appl. Energy Mater. 2021, 4, 14700–14708. doi:10.1021/acsaem.1c03328
- Wan, D.; Yang, J.; Cui, X.; Ma, N.; Wang, Z.; Li, Y.; Li, P.; Zhang, Y.;
  Lin, Z.-H.; Sang, S.; Zhang, H. Nano Energy 2021, 89, 106465.
  doi:10.1016/j.nanoen.2021.106465
- Raeisifard, H.; Nikkhah Bahrami, M.; Yousefi-Koma, A.;
  Raeisi Fard, H. Eur. J. Mech., A: Solids 2014, 44, 116–124.
  doi:10.1016/j.euromechsol.2013.10.012
- Saadatmand, M.; Kook, J. J. Braz. Soc. Mech. Sci. Eng. 2020, 42, 468. doi:10.1007/s40430-020-02551-8
- Rutsch, M.; Dali, O. B.; Jager, A.; Allevato, G.; Beerstecher, K.; Cardoletti, J.; Radetinac, A.; Alff, L.; Kupnik, M. Air-Coupled Ultrasonic Bending Plate Transducer with Piezoelectric and Electrostatic Transduction Element Combination. In 2019 IEEE International Ultrasonics Symposium (IUS), IEEE: Glasgow, United Kingdom, 2019; pp 147–150. doi:10.1109/ultsym.2019.8925701
- İkizoğlu, S.; Özgül Ertanır, A. J. Vibroengineering 2014, 16, 1106–1113.
- 100.Ni, D.; Vinayakumar, K. B.; Pinrod, V.; Lal, A. Multi Kilovolt Lithium Niobate Pyroelectric Cantilever Switched Power Supply. In 2019 IEEE 32nd International Conference on Micro Electro Mechanical Systems (MEMS), IEEE: Seoul, Korea (South), 2019; pp 970–973. doi:10.1109/memsys.2019.8870799
- 101.Ni, D.; Gund, V.; Lal, A. PCB Integrated Linbo3 Pyroelectric High Voltage Supply with Electrostatic Switch Regulator. In 2021 21st International Conference on Solid-State Sensors, Actuators and Microsystems (Transducers), IEEE: Orlando, FL, USA, 2021; pp 968–971. doi:10.1109/transducers50396.2021.9495642

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