

## Epitaxial Graphene Surface Preparation for Atomic Layer Deposition of Al<sub>2</sub>O<sub>3</sub>

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High-k dielectrics such as Al<sub>2</sub>O<sub>3</sub>, HfO<sub>2</sub>, Ta<sub>2</sub>O<sub>5</sub>, and TiO<sub>2</sub>, are required for realizing graphene-based top-gated electronic devices and for scaling field effect transistors to sizes < 100 nm. Atomic layer deposition (ALD), a method based on two self-limiting surface half-reactions, is a preferred technique to deposit high-quality, conformal, stoichiometric dielectric films with precise thickness control. Unfortunately, direct deposition of oxides on pristine or non-functionalized graphene by ALD, using H<sub>2</sub>O-based precursors, is hindered by the highly hydrophobic and chemically inert nature of graphene, resulting in no coverage or non-uniform coverage [1-4]. Here, a simple *ex-situ* graphene surface treatment based on wet chemistry is developed to render the otherwise chemically inert graphene surface more suitable for thin dielectric deposition. Using this approach, 30 nm thick Al<sub>2</sub>O<sub>3</sub> films are deposited, using trimethylaluminum and triply-distilled H<sub>2</sub>O as precursors, onto epitaxial graphene grown on the Si-face of silicon carbide. The resulting films show excellent morphology and uniformity over large (~8 mm<sup>2</sup>) areas (i.e., the entire sample area), as determined by atomic force microscopy and scanning electron microscopy. X-ray photoelectron spectroscopy revealed a nearly stoichiometric film with markedly reduced impurity content. Moreover, capacitance-voltage measurements reveal an extracted dielectric constant of ~7.3 and a positive Dirac voltage shift of ~ 1V. The mobility of free carriers in the graphene layer, as determined by van der Pauw Hall measurements (1000-1200 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>), was not affected by either the surface pre-treatment or the deposited dielectric although in each case, the sheet charge density changed.

### References

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