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Experimental Demonstration of Direct Terahertz Detection at Room-Temperature in AlGaN/GaN Asymmetric Nanochannels

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ABSTRACT: The potentialities of AlGaN/GaN nanodevices as THz detectors are analyzed. Nanochannels with broken symmetry (so called Self Switching Diodes) have been fabricated for the first time in this material system using both recess-etching and ion implantation technologies. The responsivities of both types of devices have been measured and explained using Monte Carlo simulations and non linear analysis. Sensitivities up to 100 V/W is obtained at 0.3 THz with a 280 pW/Hz^{1/2} Noise Equivalent Power.

KEYWORDS: Self-Switching Diode, Nanoelectronic devices, Terahertz detection, GaN nanodiodes.

The field of Terahertz Science and Technology is gaining international interest due to its broad potential applications, ranging from ultra high speed wireless transmission systems to medical diagnostic, industrial quality control, security-screening tools, THz astronomy, high speed communications or pharmacology [1, 2]. In the field of electronics, the efforts in order to reach the THz frequencies have been devoted to the top-down approach, reducing the size of devices down to the nanometer range in order to increase the operational speed. However, the “THz gap” is not covered yet by transistors, since the reduction of the gate length does not lead to a sufficient increase of the cut-off frequency as predicted by the traditional scaling rules. On the other hand, since the photon energy of terahertz radiation is

smaller than the thermal energy at room temperature, photonic devices as quantum cascade lasers are operational only at cryogenic temperatures. The development of room temperature, compact and low-cost semiconductor devices covering the “THz gap” is still one of the challenges of nanoelectronics and will be of great interest for many applications. Great efforts are under investigation to achieve room temperature compact THz emitters and detectors [3]. In direct THz detection systems such as Golay cells [4, 5], pyroelectric detectors [6], or microbolometer elements (VOx [7], Bi [8], Nb [9]) which are currently used in THz focal plane arrays, the response time is relatively long and responsivities are not very high. Present state-of-the-art for detection of high frequency radiation is achieved by Schottky diodes [10]. These devices allow electrical rectification of the incident electromagnetic radiation through nonlinear current-voltage characteristic. However, Schottky diodes operating at THz frequencies require advanced materials and challenging fabrication (ultra-low electron transit times and parasitic elements to be able to work at these frequencies). Among emerging electronic detectors coming from nanotechnology, nanowire field-effect transistors [11] based on the plasma wave theory [12] first proposed by Dyakonov and Shur, are a possible alternative. Based on a different concept, the self-switching diode [13] (SSD) has been proposed to produce planar asymmetric non-linear devices. The SSD consists of two insulating L-shaped trenches which define an asymmetric nanochannel in a semiconductor material. Adjusting the nanochannel width results in a non-linear, diode like, current voltage (I-V) characteristic. These components (based on GaAs and InGaAs heterojunctions) have been demonstrated as room temperature detectors at millimeter [14] and terahertz frequencies [15].

In this Letter, we propose for the first time to exploit AlGaIn/GaN nanochannels potentialities to realize room temperature THz nanodetectors working at zero bias. This is the first step towards the development of a complete room-temperature integrated THz emitter/detector system since SSDs are also theoretically expected to produce THz emission by means of Gunn oscillations [16]. Here we experimentally demonstrate their detection capabilities.

Figure 1a shows the typical geometry of the SSDs, where the etching of the trenches is used to define an asymmetric nanochannel in an AlGaIn/GaN heterostructure. Figure 1b shows the geometry of the simulation domain with the parameters involved in the “top-view” Monte Carlo (MC) simulation: the “virtual” doping N_{Db} and a negative surface charge density σ . L and W are the length and width of the nanochannel, respectively, W_H the width of the horizontal trench, and W_V the width of the vertical trench.

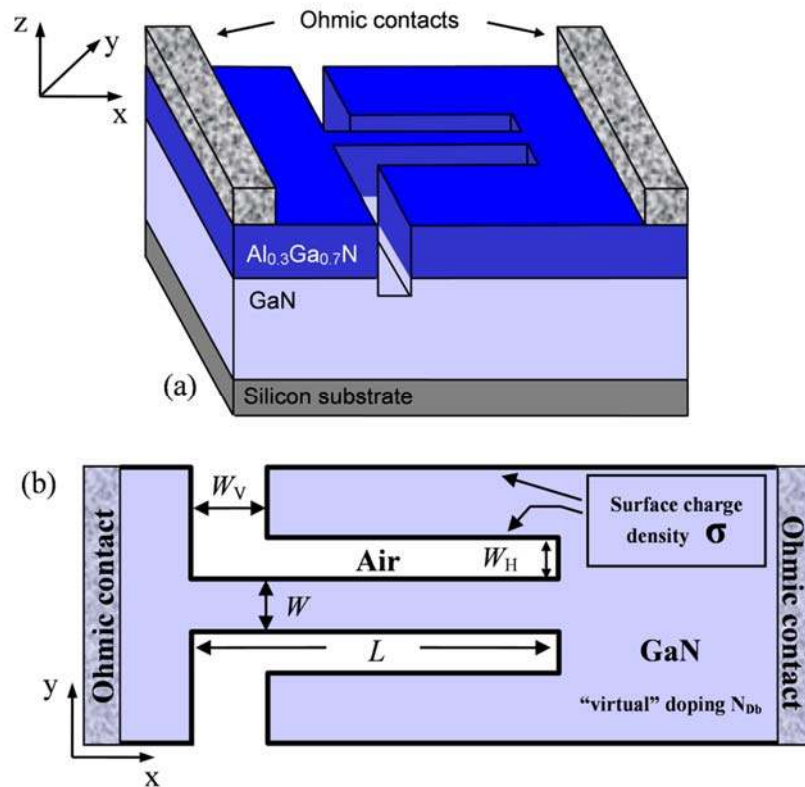


Figure 1. a) Three-dimensional geometry of the SSDs. b) “Top view” geometry as considered in the MC simulations.

To explain and analyze the nanochannel behavior, a semi-classical MC simulation self-consistently coupled with a Poisson solver is used [17]. The modeling of the SSDs would require a 3D simulation to exactly describe the influence of the lateral surface charges and the actual layer structure. To avoid the complexity of 3D models, instead we performed “top-view” 2D simulations of the channel only, and the influence of the fixed charges present in the layer structure (essentially negative charges at the top surface of AlGaIn and positive piezoelectric charges at the bottom in the AlGaIn/GaN heterojunction) is accounted for by means of a “virtual” background doping N_{Db} . In order to include the effect of the depletion originated by charges present in surface states, a negative charge density σ is considered at the semi-conductor-dielectric boundaries of the insulated trenches. Further details of the model can be found in ref [18]. In this simulation a self-consistent surface charge model is used, in which the local value of the surface charge is updated self-consistently with the carrier dynamics near the interface during the simulation, so that the surface charge at a given position is adapted to the carrier density in the nearby region.

SSDs were fabricated using an Al_{0.3}Ga_{0.7}N/GaN heterojunction (Ga-face) grown by metal oxide chemical vapor deposition (MOCVD) on a high resistivity silicon (111) substrate. The two-dimensional

electron gas (2DEG) in the quantum well was 23 nm below the surface. The carrier density of the 2DEG and mobility at room temperature are $n_s = 6.04 \times 10^{12} \text{ cm}^{-2}$ and $\mu = 1200 \text{ cm}^2/\text{V.s}$, respectively. The epitaxial layer consists of 5 nm of SiN (in-situ passivation), 23 nm of $\text{Al}_{0.3}\text{Ga}_{0.7}\text{N}$, 1.8 μm of GaN and the Silicon substrate thickness is 300 μm . The ohmic contacts ($R_c=0.4 \text{ }\Omega\cdot\text{mm}$) [19] were formed by fast annealing Ti/Al/Ni/Au metal layers at 900°C, then the device is isolated by ion implantation (He^+). Due to the aspect ratio between recess line depth (45 nm) and thickness (50 nm), specific technological steps to achieve these dimensions have been developed, mainly on the definition of the resist and the etching conditions. Using 240 nm positive poly (methyl methacrylate) (PMMA) e-beam resist thickness and Inductive Coupled Plasma (ICP) chlorine based technology [20] (average etch rate: 140 nm/min), we have realized nanodevices down to 50 nm and 90 nm for the recess and channel widths, respectively. Finally the top metal layer (Ti/Pt/Au) is deposited to form a coplanar wave guide access line.

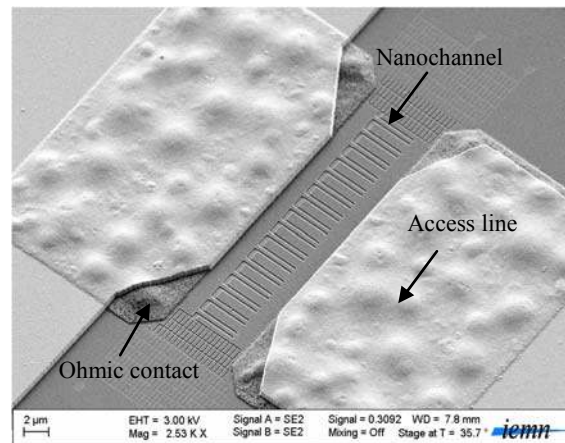


Figure 2. SEM view of an array of 16 nanochannels.

As it is well known, nanodevices present high impedances. In our case, a single SSD has a typical impedance around 15 k Ω at zero bias. To overcome this limitation, many devices are fabricated in parallel without the need of any interconnection (passive structures are difficult to realize at sub-THz/THz). In this way the extrinsic parasitic elements can be limited, which is an important advantage at this frequency range. A Scanning Electron Microscope (SEM) image of an array of 16 nanochannels is shown in Figure 2, where the overall impedance is reduced down to 1.5 k Ω .

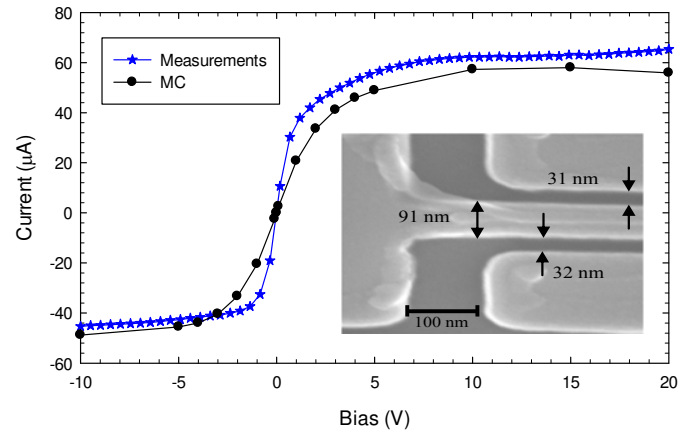


Figure 3. Comparison of the measured I-V curve (blue stars) with the one obtained with MC simulation (black circles) corresponding to the device shown in the inset (the length and width of the channel are about 1 μm and 90 nm, respectively).

The SEM image of a single nanochannel is shown in the inset of Figure 3. The channel width is defined by the two etched horizontal trenches and the device symmetry is broken by vertical trenches. To ensure that the current only flows through the channel, the depth of the etched trenches is enough for making them insulating. Based on surface states along the sidewalls of the trenches and electrostatic effects when biasing the device, the AlGaIn/GaN SSD exhibits a slightly asymmetrical current-voltage characteristic as shown in Figure 3. Once developed, the simplicity of the technological process used for the fabrication of these nanodiodes is remarkable, since it only involves the etching of insulating recess lines on a semiconductor surface. Another key aspect of these nanodevices is their planar geometry, which provides important advantages over traditional diodes (Schottky barrier) used for today's THz systems. Figure 3 shows the good agreement between DC measurements and MC simulations, which are able to reproduce the experimental behavior of the current, both for the high-bias (saturation) and low-bias regions. Similar agreement is obtained for nanochannels with different values of channel length and width.

To evaluate the detection responsivities of the fabricated SSDs, a Rhode & Schwartz vector network analyzer (ZVA-24) with WR 5.1 and WR 3.4 frequency extenders for G and J bands, respectively (140-220 and 220-325 GHz) has been used as signal source. The VNA output power is first calibrated using a PM4 calorimeter from Erickson Instruments. The signal generated by the frequency extender is applied to the nanochannels using a Cascade Microtech infinity coplanar probe within the ground-signal-ground configuration (50 μm pitch). Probes transmission losses have been measured separately in order to determine the power injected to the nanochannel (around -25 dBm). The DC voltage across the devices is measured through the bias network.

MC simulations of signal rectification/detection were also performed. For this sake, harmonic voltage signals of increasing frequency f are superimposed to the DC bias (V_{DC}) between the ohmic contacts (thus the applied voltage is $V=V_0\sin(2\pi ft)+V_{DC}$, with $V_0=0.25$ V and $V_{DC}=0$ V) and the output mean current, I_{rect} , is evaluated. We extracted first the intrinsic responsivity, S_{int} , by converting the rectified current into voltage V_{rect} (by using the value of the ohmic resistance at equilibrium, R_{DC} , $V_{rect}=I_{rect}\times R_{DC}$) and dividing it by the active power dissipated at the intrinsic SSD [calculated as $P_{int}=\text{Real}(V_0^2/2Z^*)$], so that $S_{int}=V_{rect}/P_{int}$. Z is the frequency dependent complex impedance of the intrinsic SSD, which is extracted from the MC simulation taking as a base the values obtained for $I(t)$ and following [add new reference G. M. Dunn and M J Kearney, *Semicond. Sci. Technol.* **18**, 794 (2003)]. Then, the extrinsic responsivity S_{ext} is obtained by means of the reflection coefficient of the device when inserted in the coplanar access line with characteristic resistance Z_0 as $S_{ext}=S_{int}\times(1-|\Gamma|^2)$, with $\Gamma=(Z-Z_0)/(Z+Z_0)$. In our case, the measured values of Z_0 (shown in the inset of Figure 4) have been used for the calculation of the value of Γ for the lower frequencies. Above 320 GHz, a constant value of $Z_0=(40+6j)\Omega$, has been considered. As we can see in Figure 4, there is a good overall agreement of extrinsic responsivity between simulations and measurements. Also plotted are the MC results obtained by considering a constant value of $Z_0=50\Omega$, showing the influence of the non ideality of the coplanar line, which reduces the responsivity at intermediate frequencies. It is also remarkable that if the value of Z_0 used in the calculations is increased to 75Ω , the value of the responsivity is significantly increased, thus showing that the responsivity of our devices can be improved by a modified design of the coplanar accesses (high impedance lines), which allow for a better power matching.

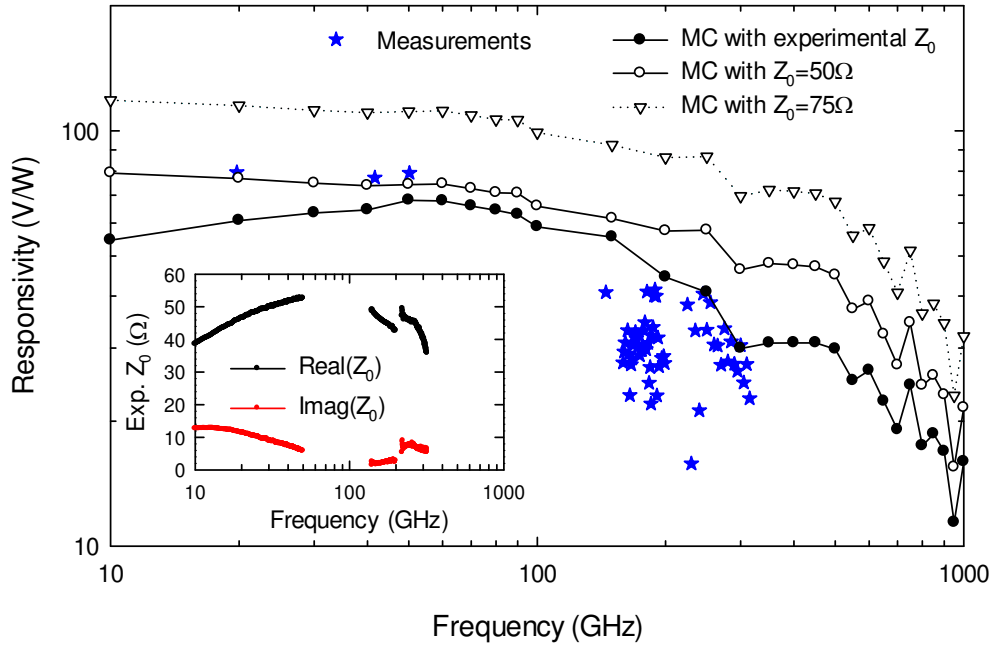


Figure 4. Comparison of the measured (blue stars) and MC simulated extrinsic responsivity of one single GaN nanochannel. The MC results calculated by using the experimental value of the characteristic impedance of the coplanar line accesses (plotted in the inset), shown in black symbols, can be compared with those obtained by using real values for Z_0 corresponding to ideal lines of $Z_0=50\Omega$ (white circles) and $Z_0=75\Omega$ (white triangles).

However, a clear cut off frequency is not identified in the measurements, so that 320 GHz should not be the upper operational limit of our devices. In fact, the simulations show that the devices could still be used at frequencies approaching the THz range.

We have fabricated and tested several SSDs with different channel widths, and the best rectification (detection) is obtained for a 90 nm wide nanochannel. For wider channels, the strength of the lateral field effect induced by the two trenches is decreased, thus producing a weaker non linearity, as predicted by MC simulations [21]. To improve current rectification and therefore the responsivity, the channel width has to be reduced; this is not easily achieved with dry etching recess processing. To overcome this challenge and obtain effective nanochannel widths lower than $W=90$ nm, we have replaced the etching technology by an ion implantation step [22] (Ar^+ ; energy: 150 keV). On the one hand, the penetration depth of the ion implantation is able to reach the GaN layer and with appropriate implant fluence and a depth profile of argon-ion implant [23] the geometry of the insulating trenches is correctly defined. The implantation energy was determined using the software Stopping and Range of Ion in Matter 2008 [24]. On the other hand, the Ar^+ implantation process induces large ion diffusion, resulting in an effective nanochannel narrower than the one defined in layout. However, knowing the width of

the implanted regions (W_I) we can infer the effective channel width, W_{EFF} . To evaluate W_{EFF} , we have fabricated ion implanted SSDs with different projected channel widths W , from 75 to 200 nm. By analyzing the dependence of the conductance ($1/R_{DC}$) as a function of the projected width of the channel (Figure 5a), we observe that the minimum width for having a conducting channel is about 172 nm. Therefore, the implantation width W_I and the possible depletion associated to the defects and surface charges created at the limits of the implanted region result in a non-conducting zone of about 86 nm at each side of the channel. To verify this assumption and compare the two technological processes, we have performed DC measurements on arrays of 16 etched and implanted SSDs with similar values of effective channel width, W_{EFF} : an implanted nanodiode with a projected width of $W=200$ nm ($W_{EFF}\approx 28$ nm) and a recessed nanodiode with a 90 nm wide channel ($W_{EFF}\approx 30$ nm, since the depletion region in this type of devices is about 30 nm wide, see ref [16]). Figure 5(b) shows that the zero bias resistance is similar and also the current saturation level. Surprisingly the implanted devices present a much stronger saturation, that can be due to the presence of a large amount of surface states that are charged as the bias is increased, thus progressively closing the channel and counteracting the increase of the current.

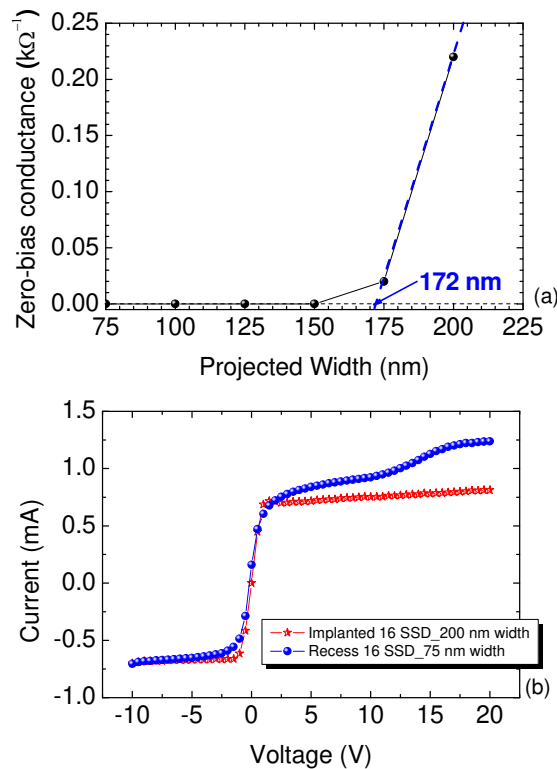


Figure 5. (a) Dependence of the zero-bias conductance as a function of the projected width, W , of the ion implanted SSDs. (b) Comparison of the I-V curves of arrays of 16 ion implanted ($W=200$ nm) and etched ($W=90$ nm) SSDs for approximately the same W_{EFF} of about 30 nm.

Figure 6 shows the intrinsic responsivity of arrays of 16 ion implanted SSDs with different channel widths (the result for the etched SSD with $W=90$ nm is also shown for comparison). As expected, the maximum responsivity is obtained for a projected channel width of $W=175$ nm (corresponding to $W_{\text{EFF}} \approx 3$ nm). For channel widths lower than 175 nm, there is a very low carrier density in the channel and its resistance is very high ($\sim M\Omega$) so that the responsivity decreases sharply for the SSDs with $W=150$ nm. Figure 6 also shows that, as expected from their analogous I-V curves [Figure 5(b)] and W_{EFF} , the responsivity of the etched SSD ($W=90$ nm) and the implanted one with $W=200$ nm, are very similar. SSDs, which were already demonstrated in a variety of materials, like Silicon-on-insulator [25] and both organic [26] and metal oxide [27] thin films, are for the first time realized using this new technological process that allows us to reduce the nanochannel width. Moreover, as the insulating trenches are made of a material with high dielectric constant (the same semiconductor), the lateral field effect is improved as the effective width of the trenches is largely reduced [28].

The array of 16 ion implanted SSDs with $W=175$ ($W_{\text{EFF}} \approx 3$ nm) is able to detect at room temperature with zero-bias a sub-millimeter wave signal of -25 dBm power at 0.3 THz with a responsivity of 100 V/W. This value does not seem to decrease for higher frequencies, which confirms its possible operation up to THz frequencies. As our detectors operate at zero bias, the noise generated by the nanochannel is essentially due to thermal fluctuations (Johnson-Nyquist noise) since $1/f$ noise is absent at equilibrium. Therefore the noise power spectral density per Hertz is given by $4K_B T R_{\text{DC}}$, where K_B is Boltzmann's constant, T the absolute temperature and R_{DC} the channel resistance [29]. This SSD array has a resistance of approximately 50 k Ω , giving a voltage noise spectral density of ~ 28 nV/Hz $^{1/2}$. Using the measured responsivity, the noise equivalent power (NEP) is about 280 pW/Hz $^{1/2}$ at 300 GHz. These results demonstrate the good performance of our ion implanted AlGaIn/GaN SSDs, with still much room for improvement by using more optimized designs.

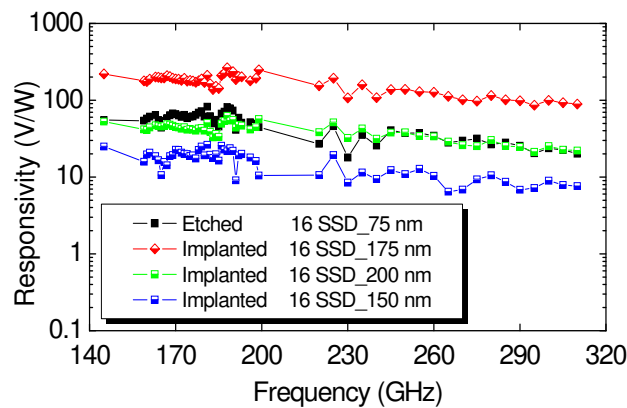


Figure 6. Zero bias intrinsic responsivity of arrays of 16 implanted SSDs with different projected

channel widths W , from 150 nm to 200 nm. Measurements for the array of etched SSDs with $W=90$ nm are also shown for comparison

In order to explain the mechanism for the terahertz detection of the SSD, the non-linearity of their I-V characteristics has been studied. Since these are unbiased detectors, the quantities of interest are the zero bias resistance, the zero-bias non linearity, and the curvature coefficient [30] γ , which is one the most important figures of merit for non linear applications such as high-speed switching. Taking into account the low power amplitude of THz radiation across the devices, the voltage excursion on the I-V curve has been estimated to be from -50 to +50 mV approximately. In this region, the I-V characteristic has been fitted with a fifth order polynomial [31], Figure 7(a). Then we have calculated the parameters corresponding to an individual SSD: resistance defined as $R=dV/dI$, plotted in Figure 7(b); non linearity defined as the second derivative of the current [32] d^2I/dV^2 , Figure 7(c) and finally, the curvature coefficient defined as the ratio of the non linearity to the nanochannel conductance, $\gamma=(d^2I/dV^2)/(dI/dV)$, Figure 7(d). It appears that when decreasing the channel width of the ion implanted SSDs from $W=200$ nm to 175 nm, the nanochannel resistance increases by several orders of magnitude leading to higher values of the curvature coefficient and therefore to a higher voltage responsivity as observed in Figure 6. The parameters for the ion implanted SSD with $W=150$ nm are not represented as the current level was too small to fit correctly its I-V curve.

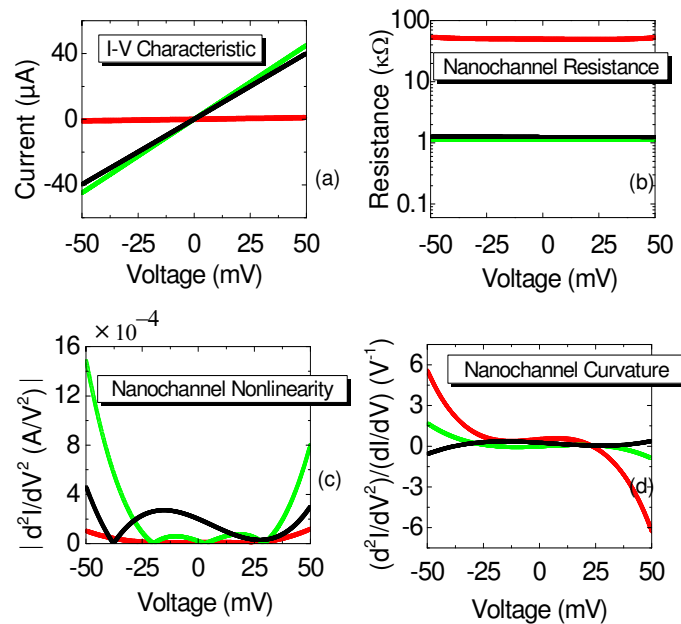


Figure 7. Comparison of the nonlinearity of the arrays of 16 SSDs fabricated by ion implantation ($W=200$ nm, green line, and $W=175$ nm, red line) and etching ($W=90$ nm, black line). (a) I-V characteristic fitted with a fifth order-order polynomial, (b) resistance, (c) nonlinearity, and (d) curvature coefficient γ .

In summary, we investigated the potentialities of AlGaIn/GaN based SSDs for being used as THz detectors. We have fabricated this type of nanochannels for the first time using two different technologies, recess etch and ion implantation, and its operation as direct sub-terahertz/terahertz detectors up to 320 GHz has been demonstrated at room temperature. These nanodevices exhibit good responsivity and noise level and the results are well explained using both Monte Carlo simulations and an I-V non linearity study. Furthermore, regarding the thermal noise, which is proportional to the resistance, the noise equivalent power of our devices can be strongly reduced by using larger arrays of nanochannels connected in parallel. This kind of GaN-based direct nanodetector could be a competitive candidate for nanometer scale terahertz detection.

Acknowledgments

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