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Experimental Demonstration of Multi-vendor and Multi-domain Elastic Optical Network with data and control interoperability over a Pan-European Test-bed

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Abstract— The operation of multi-domain and multi-vendor EONs can be achieved by interoperable Sliceable Bandwidth Variable Transponders (SBVTs), a GMPLS / BGP-LS-based control plane and a planning tool. The control plane is extended to include the control of SBVTs and Elastic Cross-Connects (EXCs), which combine a large port-count Fiber-Switch (optical backplane) and Bandwidth-Variable Wavelength Selective Switches (BV-WSSs), enabling the end-to-end provisioning and recovery of network services. A multi-partner testbed is built to demonstrate and validate the proposed end-to-end architecture. Interoperability among S-BVTs is experimentally tested between different implementations. In this case, transponders are configured using the proposed control plane. The achieved performance with hard-decision and soft-decision FECs using only the information distributed by the control plane is measured against the performance of single-vendor implementation, where proprietary information is used, demonstrating error-free transmission up to 300 km.

Index Terms—Interoperability, SBVT, EON, FEC, standardization, DSP, data-plane

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I. INTRODUCTION

E lastic Optical Networks (EONs) are envisioned as the base of next generation transport networks. Recent studies [1][2] show that EONs will be able to cope with the imminent capacity crunch thanks to their flexibility and scalability resulting in an efficient bandwidth allocation. The pillars allowing this evolution are the flexible bandwidth-variable transponders (BVTs), capable of transmitting / receiving signals with configurable physical parameters (i.e., bitrate, modulation format, FEC, etc.) and the spectrum-selective switches (SSSs), capable of switching frequency slices in multiples of 12.5 GHz. By using these two elements, EONs provide the potential to enable fully configurable multi-bitrate lightpaths, thus increasing service-oriented flexibility and overall network capacity.

Key factors for the successful deployment of EONs are the presence of solutions for the subdivision of the network in different domains (administrative, geographical, vendor, etc.) and for the interworking among the different implementations. These are seen as the main requirements emerging from the network operators that have to cope with the growing of traffic and the emerging of new services on one hand and with market competition on the other. It is therefore clear that adequate control plane operations are required to achieve lightpath provisioning, along with additional EON-specific procedures, such as elastic operation and re-optimization (e.g., defragmentation) in such a complex environment.

In this paper, we present a multi-domain, multi-vendor EON integrating both control and data plane. Protocol extensions are evaluated in a distributed multi-partner control plane test-bed, fully enabling the interoperability of several implementations of Sliceable Bandwidth Variable Transponders (S-BVTs), which are experimentally validated.

The S-BVTs of each implementation are configured using only the information distributed by the control plane. The performance, in terms of BER, is compared against the singlevendor implementation case, where the information used by the S-BVTs is not restricted to the one disseminated by the

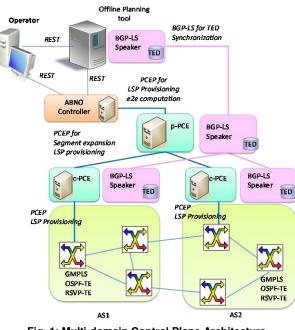


Fig. 1: Multi-domain Control Plane Architecture

control plane and proprietary FEC and DSP algorithms can be employed.

II. CONTROL ARCHITECTURE AND PROCEDURES

The control plane architecture and interfaces designed for the multi-domain EON extends the ones previously presented [3] in order to include the control of SBVTs, as illustrated in Fig. 1. The control plane enables the end-to-end provisioning and recovery of network services (either a flexi-grid network media channel or a constant bit rate service between transceivers with different bit rates and dynamic ODUFlex grooming).

In this section, we detail the functional elements of the architecture, we sketch the control plane procedures and we summarize the protocol extensions that have been proposed.

From a bottom-top approach, each domain deploys an extended GMPLS control plane including, notably, the OSPF-TE protocol for topology dissemination and the RSVP-TE protocol for the signaling of the Label Switched Paths (LSPs). On top of the GMPLS control plane, each domain deploys an active stateful Path Computation Element (AS-PCE), for the purposes of both optimal path computation and service provisioning within its domain. Thus, multi-domain path computation and provisioning is carried out by means of a Hierarchical Path Computation Element (H-PCE), with the parent PCE (pPCE), coordinating the procedures between children PCEs (cPCE): the interface between pPCE and domain cPCEs (based on PCEP protocol) is thus used by the pPCE for path computation and instantiation. The pPCE operates under guidance from the ABNO controller [4], which ultimately exports a high level REST based API to applications (e.g. to the operator NMS).

Regarding topology management, each cPCE is aware of the detailed topology and resources within its domain, and it is augmented with a BGP Link-State speaker so each cPCE is responsible for the aggregation of its domain network topology and for communicating the abstracted information towards the pPCE. For this, the BGP-LS protocol Update

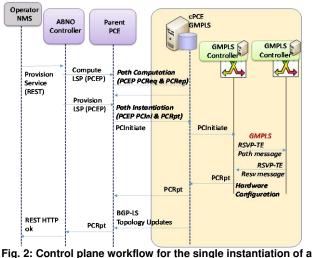


Fig. 2: Control plane workflow for the single instantiation of a service, path computation is performed by the H-PCE

messages are used, conveying Traffic Engineering (TE) attributes of the domain nodes and links as well as of the interdomain links, including the capabilities of the deployed multiflow transponders of the different vendors. The pPCE uses this information to obtain an aggregated multi-domain topology of the overall network, allowing the selection of the optimal domain sequence. Finally, the architecture is completed with an off-line planning tool, mainly for the purposes of performing CPU intensive and advanced path computation as well as global concurrent optimization where multiple services are involved [5][6]. The planning tool also relies on BGP-LS to obtain the network topology, although this does not preclude the use of other protocols and interfaces.

From the perspective of the procedures involved in the dynamic provisioning, the network operator ultimately initiates the processes. We have considered mainly two different workflows, as detailed next.

The first workflow (see Fig. 2) is used when dynamically provisioning a single service, and is characterized by the fact that path computation is carried out by the H-PCE. Upon reception of a REST request from the NMS, the ABNO controller first proceeds to obtain the end to end path (Explicit Route Object or ERO) from the pPCE, which, after the initial domain sequence selection, requests the cPCE in each domain to expand the path segments in each domain into the actual nodes and links. This involves the use of the PCEP protocol request (PCReq) and response (PCRep) messages, as shown in Fig 2. Once the ERO is computed, the ABNO controller requests the actual provisioning by means of the PCEP Initiate message [7], including the path, which is sent to the pPCE. The pPCE forwards this message to the cPCEs so each cPCE requests the head end node of its domain to initiate the GMPLS signalling process, using the RSVP-TE Path and Resv messages. During the signalling processes, the nodes end up configuring the underlying optical hardware: flexi-grid ROADMs are configured with the frequency slot parameters and S-BVTs are configured with the allocated transmission parameters (bit rate, modulation format, FEC, etc.) It is responsibility of the pPCE to ensure that the selected endpoints and allocated frequency slot parameters are compatible across the involved domains.

The second workflow is characterized by the use of the planning tool to perform the computation of a batch of services, described in Fig. 3. The establishment of a batch of services is again driven by the NMS which, this time, requests the computation and provisioning to the planning tool, via a dedicated REST interface. The planning tool performs the joint computation of the paths for each service and, once computed, proceeds with their instantiation using the interface provided by the ABNO controller.

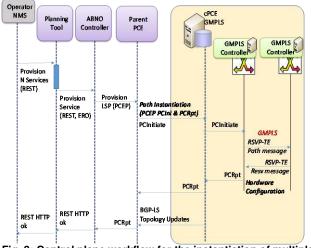


Fig. 3: Control plane workflow for the instantiation of multiple services, path computation performed by the planning tool

The main difference between them is how they scale with the number of domains and the protocol requirements. The second workflow requires the planning tool to be updated in real time with information from all the domains, stressing the update of topology/resource usage information. The first workflow is less stringent in the BGP-LS requirements and do not need the exchange of detailed information.

Control plane extensions affected all the protocols of the GMPLS suite together with the ones adopted as northbound interfaces between the cPCEs and the pPCE (i.e. PCEP and

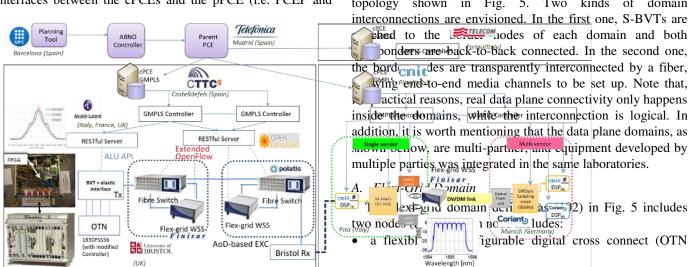


Fig. 4: IDEALIST Multi-partner Pan-European Test-bed integrating control and data

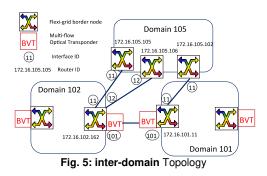
BGP-LS). The main purpose of these extensions is the handling of the media layer in which switching is based on a frequency slot described as central frequency and a slot width. A new 64-bits label format includes the grid type (assigned a value of 3 to indicate ITU-T Flex), the channel spacing (a value of 5 indicates a 6.25 GHz granularity), the n parameter that expresses the slot central frequency according to the formula Frequency (THz) = 193.1 + n * 0.00625 and m is the slot width as in Slot Width (GHz) = 12.5 * m. The new format is used in all the objects carrying а label (GENERALIZED LABEL, SUGGESTED LABEL, LABEL SET, ERO, etc.)

3

Bandwidth and traffic specification (when referring to optical spectrum) conveys the m parameter (notably during the path computation and signaling). It can be observed that 'm' is a parameter both of the GMPLS Flexigrid label and of the Flexigrid TSpec and Flowspec. The overlap comes from the fact that in a Flexigrid system the label value, that defines what is switched, indicates the slot width, therefore affecting also the bandwidth supported by an LSP. A new sub-object of the EXPLICIT_ROUTE object was defined to describe MF-OTPs or SBVTs capability of generating multiple optical flows. It is formed by a list of TLVs describing the sub-carrier attributes and appears only at the beginning and the end of the ERO to convey specific information about the configuration of the MF-OTPs at the path endpoint [8]. Both OSPF-TE and BGP-LS were extended similarly, to describe the capabilities of the S-BVTs and for the dissemination of the availability of nominal central frequencies using a bitmap encoding.

III. TEST-BED DESCRIPTION

In order to demonstrate the feasibility and performance of a multi-domain multi-vendor EON network, a pan-European test-bed with both control and data capabilities has been developed by the IDEALIST project [1]. This test-bed, illustrated in Fig. 4, is built by the interconnection of different components, both hardware and software, physically distributed within labs. The test-bed encompasses three Flexigrid domains with different capabilities, one hierarchical PCE, an ABNO Controller and the PLATON planning tool. The domains are interconnected resulting in the inter-domain topology shown in Fig. 5. Two kinds of domain



switching fabric) for client mapping and centralized control of S-BVT. To this end, the hardware controller has been modified to adapt the commands from the restful server to the registers of the FPGA-based S-BVT;

- real-time S-BVT modules carrying multiple OTU2 tributaries into a single flexible OTU container (which follows the "beyond 100G" OTN standards recommendation thanks to programmable FPGAs and a multi-flow optical front end that can adapt its data rate such as 107 Gb/s or 53.5Gb/s per carrier [9] and its number of carriers according to the reach, physical impairments or capacity demand. The FPGA platform is a Xilinx Virtex 7 580HT with high speed output serializer/deserializer GTZ interfaces up to 28 Gbit/s;
- all-optical matrix. Provides flexibility through architectureon-demand (AoD) in terms of synthesis of fiber switching cross-connections (~1dB/cross-connection, 192 x 192 Polatis switch series 6000), optical bandwidth switching with bandwidth-variable WSSs (4x16 Finisar Waveshaper)[10];
- an offline coherent receiver (Fig. 6), able to recover the transmitted signals using an all-optical coherent front-end (polarization beam splitters, 90° optical hybrid and balanced receivers) and an offline DSP (OMA Keysight N4391A).

The GMPLS control plane is thus able to configure the underlying hardware (via the connection control interface) by using dedicated REST interfaces, as mandated by the signaling process. This covers the cross-connection configuration, the WSS filters and the S-BVT. A deployed middleware translates the high-level REST interfaces to the actual low-level hardware interface. In particular, it can configure the number of physical OTU2 tributaries/lanes interfaced to the elastic BVT, and the required symbol-rate. So if there are five OTU2 lanes to be carried, the BVT is configured at 13GBd PM-QPSK. The middleware contain RESTful Servers e.g. running as a northbound interface on top of a SDN OpenDayLight (ODL) controller with their southbound interface based on extended OpenFlow Protocol (OFP). The transport equipment (e.g., Fiber Switch and BV-WSS) has an OFP agent capable to translate the received OFP commands to specific equipment APIs (e.g., TL1).

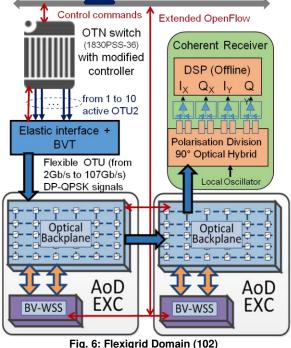
B. Multi-vendor Flexi-grid domain

The second domain (101) (Fig. 4 bottom right) includes integrated data and control plane.

The data plane setup consists of four nodes flexgrid network (based on configurable spectrum selective switches- SSS), a CNIT/Ericsson DSP unit at the TX and two different ones

(CNIT/Ericsson and Coriant) at the RX as part of an optical coherent test-bed. The TX is able to provide a super-channel with different configurable number of carriers and capacity (i.e. 1 carrier for 100G, 3 carriers for 400G and 7 carriers for 1T). At 1 Tb/s, we adopted PM-16QAM Nyquist-shaped signals shaped by a roll-off = 0.05 and symbol rate = 23 GBd.

Restful server (ODL: EXC/BV-WSS)



A 64 GSa/s Digital-to-Analog Converter (DAC) was used with 3dB bandwidth of 13 GHz and ENOB 5.5. In order to mitigate DAC bandwidth limitations, digital pre-emphasis was applied to ensure proper rectangular shape. Two single polarization IQ-modulators were used to modulate the even and odd sub-carriers of the super-channel. Subcarrier spacing was set to 25 GHz. Next, the single polarization signals went through the polarization multiplex emulation stage where they were first split into two orthogonal polarizations, delayed with respect to each other for de-correlation and finally combined by a PBC resulting in a polarization multiplexed signal. The even and odd sub-carriers were multiplexed via 3dB coupler resulting in a multiple sub-carrier super-channel. The test-bed was equipped with an optical re-circulating loop consisting of 2×80km SSMF spans, EDFAs to compensate for span losses and a wave-shaper acting as a loop filter to perform gain equalization and suppress accumulated out of band ASE noise. In addition, a polarization scrambler (PS) was also employed to evenly distribute polarization dependent loop effects. After the loop circulation, the desired sub-carrier was selected via a tunable band-pass filter, then demodulated by employing coherent phase- and polarization-diversity detection, and finally setting the local oscillator (LO) at the nominal central frequency of the selected sub-carrier. The received optical signal is mixed with the LO through a polarization-diversity 90° hybrid optical coupler, whose outputs are sent to four couples of balanced photo-diodes. The four photo-detected signals are sampled and digitized through a 20 GHz 50 GSa/s real-time oscilloscope. The sampled signals were saved for off-line receiver DSP. The line rate of 1.28 Tb/s allows us to assume a 23% FEC-OH plus 5% framing OH. Such value would guarantee proprietary soft-decision FEC with 23% OH and a pre-FEC BER of 3.37×10^{-2} .

The control plane is performed via Linux-based C++ GMPLS controllers located at each physical node of the network. Controllers run PCEP establishing session with the domain active stateful cPCE and RSVP-TE to perform signaling. Controllers are connected with the data plane testbed (i.e., SSS, TX and RX) by means of USB, serial and GPIB interfaces. Based on RSVP-TE messages, controllers are able to automatically configure SSS (i.e., filter shape as the reserved frequency slot), transponders parameters (i.e., symbol rate, number of carriers, sub-carrier central frequencies) and DSP parameters (i.e., modulation format, FEC).

Domain active stateful child PCE, developed in C++ in a Linux box, performs intra-domain topology export advertisement by means of BGP-LS, impairment-aware path computation [11], multi-action re-optimization, adaptation and instantiation by means of PCEP [12][13].

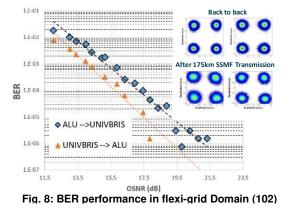
Multi-vendor interoperability between CNIT/Ericsson and Coriant DSPs was achieved by the exchange of novel Application Code (AC) and Transponder Class (TC) attributes in BGP-LS and PCEP/RSVP-TE protocols, in line with ITU recommendations [14]. The AC attribute, exported by BGP-LS within the TE link attribute extensions, defines the whole network scenario of application (i.e., in terms of wavelength range, type of fiber, dispersion compensation, presence of amplifiers, system rate, etc). The TC attribute identifies the transponder features and compatibility by implicitly defining a large set of available and allowed physical tx/rx parameters values and ranges. In particular, the TC has been encoded as a novel subTLV specification inside the MF-OTP extensions [8] of PCEP and RSVP-TE specifying the super-channel description (i.e., in terms of sub-carriers nominal central frequencies and width, modulation, FEC). Path computation allows transponder-transponder end point assignment only if they both belong to the same TC or to compatible TCs. The same policy is enforced in the RSVP-TE signaling at the endpoint nodes, during the actual transponder selection and configuration.

C. Emulated Flexi-grid domain

The third domain, defined as (105), is composed of six nodes running a GMPLS control plane on dedicated Linux boxes. No data plane is employed: therefore, media layer devices (i.e. SSS) are emulated within the same boxes running the GMPLS controllers. An active stateful child PCE, enhanced with BGP-LS speaker features, collects local topology information and coordinates with the pPCE for path calculations and LSPs provisioning inside the domain. Persistent PCEP sessions between the cPCE and all the border nodes are configured to accomplish this.

D. Hierarchical Stateful PCE and ABNO Controller

The parent PCE (pPCE) is built using the open source



Netphony Hierarchical Stateful PCE suite [15], developed in Java, and is deployed in a virtual machine that is connected to the control plane network. The pPCE uses the Netphony BGPspeaker and TED [16] to obtain the domain LS interconnection and the TE details of both Border Nodes and Inter-domain Links. Thus, when the pPCE is used for end toend computations, runs an algorithm to select the domains and performs a distributed RSA when end-to-end media channel is requested. When the inter-domain links are a transponder interconnection, the pPCE does not need to perform the RSA. A redis database is used to store persistently the LSP and Traffic Engineering databases. The ABNO controller, built with the Netphony Open Source ABNO suite [17], runs in the same virtual machine as the pPCE. A workflow is implemented that defines the interactions with the pPCE and can be triggered either from the Planning tool or from the Network Management System. A web server with the Graphical User Interface is also deployed to facilitate the operation of the network.

E. Planning tool

Upon the reception of a bundle of connections to be served, the PLATON planning tool solves an optimization problem so as to minimize the resources used for the whole bundle. To that end, the planning tool explores a fixed number of solutions by randomly sorting the bundle set. For each individual connection request, the planning tool solves the Routing Spectrum and Modulation Format Assignment (RSMA) problem (an extension of the RSA problem, where the modulation format is as well assigned)[18]. The RSMA problem can be stated as follows:

Given:

- a graph G(N, E) representing the network topology. The set N includes the set of optical nodes. The set of optical links E connecting nodes in N, where each link e is of a given length.
- the availability of every frequency slice (slice width denoted as Δ_s) in the optical spectrum of every link in E;
- the set *T*(*n*) of SBVT installed in every node *n* in *N*. The characteristics of the SBVTs are known, including available capacity and sub-carrier modules;
- a set F of modulation formats supported by the SBVTs, where m(f) represents the spectrum efficiency of f. F is

ordered by m(f);

• a connection request to be served defined by the tuple <*src*, *dst*, *bw*>, where *src* and *dst* are source and destination nodes in *N*, and *bw* is the requested bitrate.

Output: the SBVTs and the RSMA for the request.

Objective: minimize the used resources.

Table I presents the pseudocode of the proposed algorithm to solve the RSMA problem. A set of shortest paths are computed between end nodes selecting one of the set SBVTs with enough available capacity and sub-carrier modules (line 1 in Table I); each path includes its physical route k (sequence of hops), and the width of the largest continuous slot in that route, n. Each path is afterwards checked to verify the width of largest slot available (line 4). Next, the best modulation format is selected from set F provided that the reach works for the length of the route (lines 6-9). The set of paths satisfying the previous constraints, if any, is sorted first by the length of its route and the best path is selected (line 12). A slot of the proper width is selected (line 13) and the computed lightpath is eventually returned (line 14).

TABLE I RMSA ALGORITHM

IN: (G(N, E), <src, bw="" dst,=""> OUT: <k, c,="" f=""></k,></src,>
1:	$Q = \{ \langle k, n_k \rangle \} \leftarrow \text{kSP}(G, \langle src, T(src, bw), dst, T(dst, bw) \rangle)$
2:	$Q' \leftarrow \emptyset$
3:	for each q in Q do
4:	if <i>q</i> . <i>n</i> < <i>minSlotWidth</i> then continue
5:	$q.f \leftarrow 0$
6:	for each f in F do
7:	if $len(q,k) \le len(f)$ AND width $(bw, m(f)) \le q.n$ then
8:	$q.f \leftarrow f$
9:	break
10:	if $q.f \neq 0$ then $Q' \leftarrow Q' \cup \{q\}$
11:	if $Q' = \emptyset$ then return \emptyset
12:	$\operatorname{sort}(Q', < q.k , \operatorname{ASC>}); q \leftarrow \operatorname{first}(Q')$
13:	$q.c \leftarrow \text{selectSlot}(k, \text{width}(bw, m(f)))$
14:	return q

IV. EXPERIMENTAL EVALUATION

To demonstrate the architecture, an end-to-end connection of 107Gb/s, with one segment in domain 102, and a second segment with a cross-vendor connection between S-BVTs in domain 101, is setup.

A. Control Plane Evaluation

A Wireshark capture of the PCEP messages flow of the computation and instantiation is shown in Fig. 7 with a total set-up time of 6.12 seconds. In the experiment, the data plane configuration took considerable amount of the set-up time. From the perspective of control plane, the set-up time is in the order of tens/hundreds of milliseconds. Looking at individual

5	*REF*	SDN_CONTROLLER	SDN_CONTROLLER	Path	Computation Request (PCReq)	
Itatio	0.002892	TID_PARENT_PCE	172.16.102.161	Path	Computation Request (PCReq)	
2	0.003051	TID_PARENT_PCE	CNIT_CHILD_PCE	Path	Computation Request (PCReq)	
a	0.067978	172.16.102.161	TID_PARENT_PCE	Path	Computation Reply (PCRep)	
5	0.119092	CNIT_CHILD_PCE	TID_PARENT_PCE	Path	Computation Reply (PCRep)	
3	0.122653	SDN_CONTROLLER	SDN_CONTROLLER	Path	Computation Reply (PCRep)	
mi	0.153279	SDN_CONTROLLER	SDN_CONTROLLER	Path	Computation LSP Initiate (PCInitiate	à
ŝ	0.160785	TID_PARENT_PCE	CNIT_CHILD_PCE	Path	Computation LSP Initiate (PCInitiate	3
2	0.161159	TID_PARENT_PCE	172.16.102.161	Path	Computation LSP Initiate (PCInitiate)
3	2.245301	CNIT_CHILD_PCE	TID_PARENT_PCE	Path	Computation LSP State Report (PCRpt)	2
8	6.119321	172.16.102.161	TID_PARENT_PCE	Path	Computation LSP State Report (PCRpt)	
5	6.123022	SDN_CONTROLLER	SDN_CONTROLLER	Path	Computation LSP State Report (PCRpt)	ė,

Fig. 7: Wireshark Capture

components, the transceiver setting time was relatively fast (~15% of the entire time), but the configuration of the WSS and EXC considerable contributed to ~60-65% of the setting time. In addition, the current ODL middleware for WSS/EXC contributed to 15-25% of the total configuration time. In the trace, the set up time per domain is measured, 2.29 seconds in domain 101 and 5.96 seconds in domain 102. Therefore, the set-up time is constrained by state-of-the-art data plane technologies and the delay introduced by the control plane is acceptable for operational scenarios.

B. ALU-BRISTOL Optical Performance

The Flexi-grid domain (102) has been characterized in terms of optical performances for both SBVTs and off-line processing from Alcatel-Lucent (ALU) and University of Bristol (UNIVBRIS). Fig. 8 shows the resulting BER vs. OSNR curves. For ALU→UNIVBRIS, ALU's transmitter is used as well as the off-line processing of UNIVBRIS at the receiver. Conversely, for UNIVBRIS→ALU, the SBVT transmitter of UNIVBRIS is used with the DSP design of ALU. As shown in Fig. 8, at the HD-FEC limit, a penalty as low as 1dB is observed in the BER curves, while the maximum penalty is observed below 2dB for the highest OSNR. This performance confirms an achievable interoperability in between different elastic SBVTs and different DSPs. Fig. 8 also includes DP-QPSK constellations for the back-to-back and after transmission through the two AoD nodes and 175km of standard single-mode fiber (SSMF). Even after optical transmission of a signal at total bitrate of 107Gb/s and configuration of Flexi-grid WSS with 37.5GHz filtering window, the constellation remains clear for both polarization states, showing the interoperability between the ALU SBVT transmitter and UNIVBRIS SBVT receiver. The switching time has also been measured in domain (102). At the restful server level, the time between the http request and the http reply, including complete configuration of all elements below the server, has been measured at 0.9s for the SBVT + OTN matrix, while it is ~4s for the AoD nodes.

C. Multi-vendor Flexi-grid Interoperability Performance

Fig.9 right reports the transmission performance of the single-vendor (CNIT_{TX} \rightarrow CNIT_{RX} - SV) and cross-vendor (CNIT_{TX} \rightarrow COR_{RX} - CV), where COR stands for Coriant. Both solutions are deployed in the (101) domain at the maximum capacity (i.e., 1 Tb/s per super-channel). The aforementioned pre-FEC BER threshold of 3.37×10^{-2} will be considered only for the SV scenario (i.e., a proprietary and more powerful SD-FEC can be adopted). In both cases we used blind-DSP algorithms [19], because an interoperability scenario would not allow the usage of algorithms that require knowledge of the link [20] or of training sequences [21].

In case we opt for the cross-vendor (CV) solution, we can rely only on already standardized FEC. For example, the ITU standard G975.1, appendix I.7 hard-decision super-FECstd (HD-FECstd) with 20% could use a pre-FEC BER threshold = 1×10^{-2} . This value could increase, in case new SD-FEC become standard, up to a reasonable pre-FEC BER threshold = 2×10^{-2} [22]. Based on the pre-FEC thresholds, we can draw the following conclusions concerning the experiment carried out by employing the test-bed depicted in Fig. 4. In case we assume the usage of already standardized HD-FEC^{std} a transmission up to ~750 km in case of SV-transmission is guaranteed. This halves once we operate in cross-vendor-mode. Though, the system performance has been significantly reduced, such a scenario would still guarantee the error-free transmission, with CV-transmission, over the majority of European links. Moreover, if we could adopt a standardized FEC (SD-FEC^{std}), the reach would approach 1100 km for SV case, and ~600 km for CV-transmission. Finally, in case of proprietary FEC, the transmission distances would be ~1800 km [23], and once again about half in case of CV transmission. These values are summarized in Table II.

	Reach [km]					
Case	HD-FEC ^{std}	SD-FEC ^{std}	SD-FEC th			
SV	750	1100	1800			
CV	300	600	900			

In addition to the results of the conducted experiment, we display in Fig. 9 also the ones obtained within a second experiment that has been reported in [19][24]. In this previous analysis, the vendor configuration was mirrored by having $(COR_{TX} \rightarrow COR_{RX} - SV)$ and cross-vendor $(COR_{TX} \rightarrow$ $CNIT_{RX}$ - CV). In [23][24] the channel configuration was slightly different (32 GBd and 38 GHz channel spacing). The performance of this experiment is reported by the curves with filled markers in Fig. 9 and in this comparison, it is clearly visible that the two single vendor scenarios and two crossvendor-ones, between them, achieve similar performance. From a DSP perspective, the degraded performance of CV could be associated to the lack of knowledge of system, such as for example TX / RX I/Q skew compensation. Such comparison confirms that the CV-transmission, with standard HD-FEC, can reach ~300km, and that therefore if longer distances are needed, a solution concerning standardization on SD-FEC must be agreed within the ITU panels.

V. CONCLUSIONS

The paper has demonstrated a fully end-to-end interoperable EON network at control and data plane levels. The interoperability of several implementations of S-BVTs, with Hard-decision and Soft-decision FECs, is evaluated. The control architecture is able to configure the SBVTs so the multi-vendor transmission reach is failure-free up to 300 km with current FEC standards.

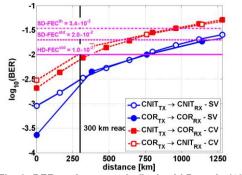


Fig. 9: BER performance in flexi-grid Domain (101)

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