# Fixed Priority Scheduling of Periodic Tasks with Varying Execution Priority

Michael Gonzalez Harbour Departmento de Electronica Universidad de Cantabria 39005 - Santander, Spain Mark H. Klein Software Engineering Institute Carnegie Mellon University Pittsburgh, PA 15213 John P. Lehoczky Department of Statistics Carnegie Mellon University Pittsburgh, PA 15213

# Abstract<sup>1</sup>

This paper considers the problem of fixed priority scheduling of periodic tasks where each task's execution priority may vary. Periodic tasks are decomposed into serially executed subtasks, where each subtask is characterized by an execution time and a fixed priority, and is permitted to have a deadline. A method for determining the schedulability of each task is presented along with its theoretical underpinnings. This method can be used to analyze the schedulability of complex task sets which involve interrupts, certain synchronization protocols, nonpreemptible sections and, in general, any mechanism that contributes to a complex priority structure. The method is illustrated with a realistic example.

## **1** Introduction

A theoretical treatment of fixed priority scheduling first appeared in 1973 when Liu and Layland [7] introduced the notion of *rate monotonic priorities* for periodic tasks. They proved the optimality of a rate monotonic priority assignment (for fixed priority scheduling) for the case where task deadlines are coincident with the end of a task's period. They also derived a sufficient condition for the schedulability of task sets that use a rate monotonic priority assignment. Leung and Whitehead [6] later showed the optimality of a *deadline monotonic* priority assignment for the case where periodic tasks have deadlines that are at or before the end of their periods.

Fixed priority scheduling theory has received renewed consideration over the last five years. This collection of results is proving to be very useful and is gaining popularity as a basis for reasoning about the timing behavior of real-time systems. These results are summarized by Sha, Klein, and Goodenough [14], and Sha and Goodenough [11]. Aperiodic task scheduling has been treated in [15], synchronization requirements treated in [8, 9, 13], and mode change requirements treated in [10]. In addition, hardware scheduling support has been treated in [4, 12], implications for Ada scheduling rules discussed in [2], and schedulability analysis of input/output paradigms discussed in [3].

Naturally, one of the assumptions of fixed priority scheduling is that a task executes at a constant priority during the course of a single period. Yet there are many occasions where this premise is violated. Even in cases where one strives to use a fixed priority assignment for periodic tasks, the task dispatching mechanism may violate this premise since periodic tasks are generally initiated by clock interrupts. Borger, Klein, and Veltre [1] present a schedulability analysis that considers the effects of task dispatching. The basic inheritance protocols also change execution priority as necessary. Sha and Goodenough [11] discuss a mechanism for emulating the priority ceiling protocol by setting the priority of a server task to be one level higher than the priority of all of the server's clients. This represents another instance of varying priorities. It is not uncommon for operating systems to ensure internal consistency by disabling interrupts for short periods of time, effectively creating small intervals of nonpreemptibility, which need to be considered when analyzing the timing behavior of a

<sup>&</sup>lt;sup>1</sup>Michael Gonzalez is currently on sabbatical as a resident affiliate at the Software Engineering Institute. His research is sponsored in part by the Direccion General de Investigacion Científica y Tecnica of the Spanish Government.

Mark Klein's research at the Software Engineering Institute is sponsored by the U.S. Department of Defense.

John Lehoczky's research is sponsored in part by the Office of Naval Research under contract number N0014-84-K-0734, in part by the Naval Ocean Systems Center under contract N66001-87-C-0155, and in part by the Federal Systems Division of IBM Corporation under University Agreement Y-278067.

system. Finally, there are many systems that have been developed where a single logical thread of execution comprises a sequence of processing steps that are implemented as a set of tasks that are serially executed at varying priority levels. In these cases task priority may be based upon a deadline or based upon semantic importance. For all of these reasons a formal framework for reasoning about the timing behavior in the context of varying execution priority is needed. This paper presents such a formal framework.

This paper is organized as follows. The remainder of this section presents the framework for the scheduling problem and discusses a difficulty that arises when task priorities vary. Section 2 presents an algorithm and schedulability equations for checking task set schedulability. Section 3 introduces a simple but realistic real-time robotics application and illustrates how one uses the schedulability equations presented in Section 2. Section 4 proves the method's correctness. Section 5 offers our conclusions.

### 1.1 The Framework

We assume there are n periodic tasks denoted by  $\tau_1,...,\tau_n$ . Each periodic task  $\tau_i$  has a total computation requirement (C<sub>i</sub>), a period (T<sub>i</sub>), and a deadline (D<sub>i</sub>). The computation requirement must be completed by the deadline or a timing fault occurs. In this paper, we assume that each periodic task may be composed of distinct subtasks, each of which may have its own timing requirement. Thus,  $\tau_i$  consists of subtasks  $\tau_{i1},...,\tau_{im(i)}$ . Each of these subtasks is characterized by a set of parameters. In particular,  $\tau_{ij}$  is characterized by (C<sub>ij</sub>, D<sub>ij</sub>, P<sub>ij</sub>) where:

•  $C_{ii}$  = worst-case computation requirement of  $\tau_{ii}$ ,

•  $D_{ij}$  = deadline of  $\tau_{ij}$  relative to the arrival time of task  $\tau_{ij}$ , taking 0 to be this arrival time,

•  $P_{ij}$  = fixed priority level of  $\tau_{ij}$ .

We assume that  $0 \le D_{i1} \le ... \le D_{im(i)} = D_i$ , and we allow either  $D_{im(i)} \le T_i$ , or  $D_{im(i)} > T_i$ . The sum of the execution times associated with each subtask of task  $\tau_i$  equals  $C_i$ . Each activation of a periodic task generates an instance of task execution called a *job* of that task.

Using this framework, we wish to derive a set of equations by which we can determine if all of the timing requirements of all of these tasks will be met under all possible phasings. We make the following assumptions concerning the execution behavior of any single task:

**Assumption 1:** Subtasks executing at a given priority level can be preempted by any subtask of higher priority.

Assumption 2: Tasks do not suspend themselves at any instant between their activation and their completion.

Assumption 3: The  $(k+1)^{st}$  job of  $\tau_i$  will not execute until the  $k^{th}$  job of  $\tau_i$  has been completed. Furthermore, any subtask  $\tau_{ii}$  is not ready for execution until subtasks  $\tau_{ir}$ ,  $1 \le r < j$  have been completed.

Assumption 4: The time required to perform task scheduling, context swapping, and other overhead is ignored.

There are situations, especially involving interrupts, in which one would want to modify Assumption 3 to allow early subtasks of a later job to interrupt unfinished later subtasks of an earlier job. The analysis given in Section 4 can be modified to handle this case, but we develop the theory specifically for Assumption 3.

To determine the schedulability of a task  $\tau_i$ , we will apply a transformation to its priority structure to derive its *canonical form*.

**Definition 1:** A task is said to be in *canonical form* if it consists of consecutive subtasks that do not decrease in priority. The canonical form of a task  $\tau_i$  is another task,  $\tau'_i$ , that is obtained by applying the following algorithm where  $P'_{ii}$  denotes the priority of subtask  $\tau'_{ii}$ .

$$\begin{array}{l} P_{im(i)}' = P_{im(i)} \\ for \ l = m(i) \ down to \ 2 \\ if \ P_{il}' < P_{il-1} \ then \ P_{il-1}' = P_{il}' \\ else \ P_{il-1}' = P_{il-1} \\ end; \end{array}$$

After applying the algorithm, consecutive subtasks of the transformed task with equal priority can be combined into a single subtask, if their deadlines are the same.

We will prove in Section 4 that the completion time of a task  $\tau_i$  and the completion time of its canonical form  $\tau'_i$ are the same. The canonical form has the advantage of being simpler for the purpose of understanding the worstcase phasing and performing the schedulability analysis.

#### 1.2 A Difficulty

The possibility that a task's deadline,  $D_{im(i)}$ , exceeds its period, T<sub>i</sub>, requires one to check more than just the deadline of the first job of a particular task. As shown in [5], all deadlines in a particular time interval called a busy period must be checked. Suppose, however, that  $D_{im(i)} \leq T_i$ ,  $1 \leq i \leq n$ , so that a job of task  $\tau_i$  must be completed before the next job of  $\tau_i$  is ready (assuming task deadlines are met). Consequently, earlier jobs of  $\tau_i$ do not compete with later jobs, and it would seem that one could follow a standard Liu and Layland approach, namely constructing a worst-case phasing of all other tasks and checking that the first job of  $\tau_i$  meets its deadline. The difficulty with this reasoning is that it overlooks another way in which earlier jobs of  $\tau_i$  can influence later jobs. If an early subtask of  $\tau_i$ , say  $\tau_{ip}$ , has a relatively low priority,  $P_{ip}$ , while a later subtask,  $\tau_{ir}$ , p < r, has a relatively high priority,  $P_{ip} < P_{ir}$ , then  $\tau_{ir}$  can delay a medium priority subtask  $\tau_{kq}$ ,  $\vec{k} \neq i$  where  $P_{ip} < P_{kq} < P_{ir}$ . This delay in  $\tau_{kq}$  creates an intermediate phasing which cannot be created as an initial phasing and which can lead to

longer response times for the next job of subtask  $\tau_{\rm ip}.$  Consider the following example.

**Example**: Let n=2, where task  $\tau_1$  has one subtask given by  $C_{11} = 4$ ,  $D_{11} = 10$ , and  $T_1 = 10$ . Task  $\tau_2$  has period  $T_2 = 14$  and two subtasks characterized by  $C_{21} = 6$ ,  $D_{21} = 14$ , and  $C_{22} = 2$ ,  $D_{22} = 14$ . Suppose further that  $P_{21} < P_{11} < P_{22}$ .

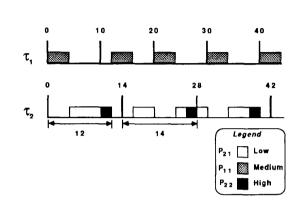


Figure 1: Considering the First Job is Not Sufficient

Under the traditional (Liu and Layland) worst-case phasing shown in Figure 1, the longest response time for  $\tau_{22}$  is given by 14 for the second job of  $\tau_2$ , whereas the first job has a response time of 12. Thus, the longest response time of this task is associated with its second job rather than the first. The cause of this phenomenon is the high priority accorded to  $\tau_{22}$  which delays the start of the second job of  $\tau_1$ . This, in turn, creates the long response times for the subtasks of  $\tau_2$ . Note that had  $\tau_i$  not been divided into two parts, but had been given its rate monotonic priority, then the task set would not be schedulable. We will return to this point in Section 4.5.

#### 1.3 Busy Period

The above example shows that one may need to check the deadlines of more than one job of a particular task. The criterion for which deadlines need to be checked is based on the concept of a *busy period*. The concept of a busy period is well known in queuing theory and was first introduced in real-time scheduling by Lehoczky [5]; however, we need to modify this concept slightly to accommodate the fact that a single task may have subtasks with different priorities. Let  $P_i = \min(P_{ij}, 1 \le j \le m(i))$  denote the minimum priority level of all of the subtasks of task  $\tau_i$ .

**Definition 2:** A  $\tau_i$ -*idle instant* is any time *t* such that all work of priority  $P_i$  or higher started before *t* and all  $\tau_i$  jobs also started before *t* have completed at or before *t*.

**Definition 3:** A  $\tau_i$ -busy period is an interval of time [A,B] such that both A and B are  $\tau_i$ -idle instants and there is no time  $t \in (A,B)$  such that t is a  $\tau_i$ -idle instant.

Intuitively, a  $\tau_i$ -busy period is a time interval during which the processor is continuously processing at priority level P<sub>i</sub> or higher. It is also "self-contained" in the sense that any job of task  $\tau_i$  that is started during the busy period is also completed during the busy period. Moreover, all work of priority level P<sub>i</sub> or higher which is ready at some time during the busy period is also finished by the end of the busy period.

#### 2 A Method for Determining Schedulability

This section describes a procedure for determining the schedulability of a task set of the general form described in the previous section. We will assume that we are analyzing a single task in the task set, and the method used can be subsequently applied to all of the other tasks.

#### 2.1 Final Deadline

First, we will focus on the task's final deadline. Our goal is to determine if task  $\tau_i$  will meet its final deadline. The general strategy is very similar to that used by Liu and Layland [7] and Lehoczky [5]. One must first find the phasing of the other tasks relative to task  $\tau_i$  that results in a critical instant for task  $\tau_i$ . A *critical instant* is a point in time such that if task  $\tau_i$  is activated at that point, its completion time will be the longest. In order to determine the critical instant phasing, we will first divide the other n-1 tasks into several groups.

**2.1.1 Task Groups:** Tasks are placed into groups based upon the priorities of their subtasks. A key criterion is the priority of the first subtask relative to the minimum priority of all subtasks of task  $\tau_i$ . For example, a task that starts with a high-priority subtask will eventually be able to preempt the subtask of task  $\tau_i$  with the minimum priority. On the other hand, a task  $\tau_i$  that starts with a lower priority subtask will never have this opportunity. Since the task groupings are relative to the priority structure of task  $\tau_i$ , the groups will vary as a function of the task being analyzed.

Recall that  $P_i = \min(P_{ij}, 1 \le j \le m(i))$  denotes the minimum priority of all of the subtasks of task  $\tau_i$ . We refer to a sequence of consecutive subtasks as a *segment*. An *H segment* comprises a sequence of consecutive subtasks, each of which has a priority equal to or greater than  $P_i$ . Note that this is a possibly pessimistic treatment of equal priority subtasks, allowing for a worst-case analysis and thus covers all scheduling policies for handling equal priorities. Similarly, an *L segment* refers to any set of consecutive subtasks, each of which has priority strictly less than  $P_i$ . In the following description of task types, a "+" denotes one or more patterns. A "0" denotes zero or one patterns. An effect due to preemption by a high prior-

ity first segment will be referred to as a preemption effect. An effect due to a high-priority segment that occurs after a low-priority segment will be referred to as a blocking effect. Figure 1 shows preemption effects of  $\tau_1$  over  $\tau_2$  at times t=0, t=12, t=20, and t=30; a blocking effect of  $\tau_2$  on  $\tau_1$  is shown at time t=10.

The five types of tasks are:

- Type 1 (H) tasks may be able to preempt task  $\tau_i$  more than once per  $\tau_i$ -busy period. Each task in this group is used to determine the worst-case completion time for task  $\tau_i$ .
- Type 2 ((HL)<sup>+</sup>) tasks are such that each high-priority segment is followed by a low-priority segment. Consequently, each task in this set can preempt task  $\tau_i$  only once per  $\tau_i$ , busy period because the low-priority segments will have to wait until the busy period is complete before they can execute. Depending on types 3 and 4, one task in this set may be used for its blocking effect rather than its preemption effect.
- Type 3 ((HL)+H) differ from the previous type in that they end with a high-priority segment. In general these tasks are treated like tasks of type 2. However, under special circumstances (described below) one task may exhibit both a preemption effect and a blocking effect.
- Type 4 ((LH)<sup>+</sup>L<sup>0</sup>) tasks are solely blocking tasks. Moreover, at most one task in this group can contribute a blocking effect.
- Type 5 (L) tasks have no effect on the completion time of task  $\tau_i$  and thus can be ignored.

2.1.2 Schedulability Equations: In this section a stepby-step procedure is described for determining if tasks can meet their final deadlines. The procedure entails methodically identifying blocking effects and preemption effects, and constructing equations that account for how these effects contribute to the completion time of task  $\tau_i$ .

We will assume throughout this section that task  $\tau_i$  has been transformed to canonical form and that adjacent subtasks of equal priority have been compressed into a single subtask. This results in a task where each subtask has a priority strictly higher than its predecessor. The rest of the tasks remain unchanged.

The procedure determines the completion time of the first subtask of the transformed canonical form. It then iteratively determines the completion of the (j+1)<sup>st</sup> subtask as a function of the j<sup>th</sup> subtask until the completion time of the final subtask has been determined. This is performed for every job in the  $\tau_i$ -busy period.

Some notation is needed prior to discussing the procedure. Let MP<sub>ii</sub> denote the set of tasks that can have more than one preemptive effect relative to the priority of the subtask  $\tau_{ii}$ . Recall that  $P_p = min(P_{pj}, 1 \le j \le m(p))$ .

$$MP_{ij} = \left\{ \tau_p \mid p \neq i \land P_p \ge P_{ij} \right\}$$

For example, since  $\tau_i$  is assumed to be in canonical form, MP<sub>i1</sub> is simply the set of type 1 tasks for  $\tau_i$ . Since the priority of the second subtask of the canonical form is greater than the first, not all of the tasks that are multiply preemptive (i.e., can preempt more than once during a task  $\tau_i$ -busy period) relative to  $\tau_{i1}$  will continue to be multiply preemptive during the execution of the second subtask. Consider the example in Figure 1. Task 1 is multiply preemptive until task 2 enters its second subtask; then task 1 can no longer preempt.

Let  $SP_{ii}$  be the set of singly preemptive tasks relative to  $\tau_{ij}$ . Furthermore, given a task  $\tau_p \in SP_{ij}$ , let h(p,i,j)denote the number of subtasks that comprise the initial H segment of task  $\tau_p$ , p≠i, relative to subtask  $\tau_{ii}$ , where

$$\begin{split} h(p,i,j) &= \left( \begin{array}{cc} h \mid p \neq i \land (P_{p1},\dots,P_{ph} \geq P_{ij}) \\ \land (P_{ph+1} < P_{ij}) \end{array} \right) \end{split}$$

The execution time associated with the leading H segment of one of these tasks is denoted as:

$$C_p^{h(p,i,j)} = \sum_{k=1}^{h(p,i,j)} C_{pk}$$

In the context of a task  $\tau_p$  and a subtask  $\tau_{ij}$ , h will serve as notational shorthand for h(p,i,j) and  $C_p^h$  for  $C_p^{h(p,i,j)}$ . The stepwise procedure for determining if task  $\tau_i$  can

meet its final deadline follows.

Step 1: Find the worst-case phasing for the other n-1 tasks.

In this step tasks other than  $\tau_i$  are placed into the groups defined in the previous section, MP<sub>i1</sub> and SP<sub>i1</sub> are determined, and then the blocking term, B<sub>i</sub>, is identified.

- 1. Let MP<sub>i1</sub> be the set of type 1 tasks for  $\tau_i$  and set SP<sub>i1</sub> to the union of type 2 and type 3 tasks.
- 2. Determine the longest H segment of the type 4 tasks. Denote the length of this as B.
- 3. For each type 2 and 3 task, denote the length of the longest inner blocking H segment (if any) as U, the final blocking H segment (if any) as V, and the initial H segment as W. If there is no inner blocking segment then set U=0. If there is no final blocking segment then set V=0.

Calculate max(U-W-B', V-B') for each task and choose the task with the largest value (let  $\boldsymbol{\tau}_m$  be this task). If this value is negative then  $B_{i}=B'$ . If this value is positive then a new blocking term must be computed. We must determine if the blocking term will be from the inner segment or the final segment of task  $\tau_m$ . If U-W > V, then set  $B_i=U$  and remove task  $\tau_m$  from  $SP_{i1}$ ; otherwise set  $B_i=V$  and  $\tau_m$  remains in SP<sub>i1</sub>.

<u>Step 2</u>: Determine how many jobs of task  $\tau_i$  must be checked.

1. The length of the task- $\tau_i$  busy period is:

$$L_{i} = \min(t > 0 | B_{i} + \sum_{\tau_{p} \in MP_{i1}} \lceil t/T_{p} \rceil C_{p} + \sum_{\tau_{p} \in SP_{i1}} C_{p}^{h} + \lceil t/T_{i} \rceil C_{i} = t)$$

The length of the  $\tau_i$ -busy period is determined by considering processing contributed by:

- the blocking term,
- the multiply preemptive tasks (relative to the first subtask),
- the singly preemptive tasks (relative to the first subtask), and
- complete jobs of  $\tau_i$ .

The idea is to look for the minimum time t, such that all work of priority  $P_i$  or higher initiated in the interval [0,t] is completed at time t. Notice that multiply preemptive tasks and  $\tau_i$  may contribute processing more than once during the  $\tau_i$ -busy period, and the blocking term and singly preemptive tasks contribute exactly once during this busy period.

2. The number of jobs of  $\tau_i$  in the busy period is:

$$N_i = \left[ L_i / T_i \right]$$

<u>Step 3</u>: Check the completion time of each of the  $N_i$  jobs in the  $\tau_i$ -busy period.

1. The completion time of the first subtask of the  $k^{th}$  job of task  $\tau_i$  is represented by  $E_{i1}(k)$ .

$$E_{i1}(k) = \min\left(t > 0 \mid B_i + \sum_{\tau_p \in MP_{i1}} \lceil t/T_p \rceil C_p + \sum_{\tau_p \in SP_{i1}} C_p^h + (k-1)C_i + C_{i1} = t\right)$$

To understand this equation, consider as an example the first subtask of the first job in canonical form. Under worst-case phasing, the completion of this subtask is impacted by the blocking term, all higher priority processing that is initiated at the same instant and the execution time of the subtask itself. The right side of the equation represents the first point in time t, where all of the processing initiated by multiply preemptive and single preemptive tasks in the interval [0,t], the processing associated with the blocking term, and the processing associated with the subtask itself has completed.

2. Given the completion time of the first subtask of the  $k^{th}$  job, it is possible to calculate the completion of the second subtask of the  $k^{th}$  job. The first step in this calculation is to insert the correct elements into the set  $SP_{i2}$ . Insert into  $SP_{i2}$  those tasks that were multiply preemptive relative to subtask  $\tau_{i1}$ , but due to the higher priority of  $\tau_{i2}$ , are singly preemptive relative to  $\tau_{i2}$ .

$$\begin{split} SP_{i2} &= \left\{ \left. \tau_{p} \right| \tau_{p} \in (MP_{i1} - MP_{i2}) \land \\ &\left( \left. \exists l \right| (P_{p1}, \dots, P_{pl} \geq P_{i2}) \land (P_{pl+1} < P_{i2}) \right) \right\} \end{split}$$

 $SP_{12}$  is constructed by first selecting those tasks from  $MP_{11}$  that may have become singly preemptive. This is accomplished via the set subtraction in the equation. Secondly, one must ensure that each of the selected tasks is actually a singly preemptive task relative to  $\tau_{12}$ . This is accomplished by making sure that there exists a leading segment that has a priority greater or equal to  $P_{12}$ . Notice that tasks in  $SP_{11}$  are not included in  $SP_{12}$ . This is because these tasks only preemption has already been accounted for in the calculation of  $E_{11}$ .

3. Using the completion time of the first subtask and sets  $SP_{i2}$  and  $MP_{i2}$ , the completion time of the second segment is:

$$\begin{split} E_{i2}(k) &= \min\left(t > 0 \mid E_{i1}(k) + \\ &\sum_{\tau_p \in MP_{i2}} \left[ \left\lceil t/T_p \right\rceil - \left\lceil E_{i1}(k)/T_p \right\rceil \right] C_p + \\ &\sum_{\tau_p \in SP_{i2}} \min\left(1, \left\lceil t/T_p \right\rceil - \left\lceil E_{i1}(k)/T_p \right\rceil \right] \right) C_p^h + C_{i2} = t \end{split}$$

The equation uses the completion time of the first subtask (in canonical form) as the starting point for calculating the completion time of the second subtask. The first summation represents the multiply preemptive processing initiated by tasks after the completion of the first subtask. The second summation represents the singly preemptive processing initiated by tasks after the completion of the first subtask. The "min" function within the second summation ensures that singly preemptive tasks can have at most one preemption effect on the remainder of the job. The execution time of the second subtask is then added in.

4. In general, given the completion time of the  $j^{th}$  subtask of the  $k^{th}$  job, it is possible to calculate the completion of the  $(j+1)^{st}$  subtask of the  $k^{th}$  job.

Once again we must first insert the proper elements into the set  $SP_{ii+1}$ .

$$\begin{split} SP'_{ij+1} &= \left\{ \left. \tau_p \right| \tau_p \in SP_{ij} \land \\ \left( \left\lceil E_{ij}(k)/T_p \right\rceil - \left\lceil E_{ij-1}(k)/T_p \right\rceil \right) = 0 \land \\ \left( \left. \exists l \right| \left( P_{p1}, \dots, P_{pl} \geq P_{ij+1} \right) \land \left( P_{pl+1} < P_{ij+1} \right) \right) \right\} \end{split}$$

 $SP'_{ij+1}$  is the set of singly preemptive tasks relative to  $P_{ij}$  that have not yet exhibited their singly preemptive effect and are also singly preemptive relative to  $P_{ij+1}$ , and

$$\begin{split} SP_{ij+1}^{\prime\prime} &= \left\{ \left. \tau_p \right| \tau_p \in (MP_{ij} - MP_{ij+1}) \land \\ \left( \left. \exists l \right| \left( P_{p1}, \dots, P_{pl} \geq P_{ij+1} \right) \land \left( P_{pl+1} < P_{ij+1} \right) \right. \right\} \end{split}$$

 $SP''_{ij+1}$  is the set of multiply preemptive tasks relative to  $P_{ij}$  that are singly preemptive relative to  $P_{ij+1}$ . The set  $SP_{ii+1}$  is the union of the above two sets.

$$SP_{ij+1} = SP'_{ij+1} \cup SP''_{ij+1}$$

The calculation for  $E_{ii+1}(k)$  is:

$$\begin{split} E_{ij+1}(k) &= \min\{t > 0 \mid E_{ij}(k) + \\ &\sum_{\tau_p \in MP_{ij+1}} \left[ \left\lceil t/T_p \right\rceil - \left\lceil E_{ij}(k)/T_p \right\rceil \right] C_p + \\ &\sum_{\tau_p \in SP_{ij+1}} \min\{1, \left[ \left\lceil t/T_p \right\rceil - \left\lceil E_{ij}(k)/T_p \right\rceil \right] \right] C_p^h + C_{ij+1} = t \end{split}$$

All jobs of task  $\tau_i$  will meet their deadlines if the following condition is satisfied.

$$max((k-1)T_i + D_i - E_{in(i)}(k)) \ge 0 \text{ for } k \le N_i$$

### 2.2 Deadlines for Other Subtasks

The analysis of the other subtasks is very similar (and in some cases identical) to the analysis of the final subtask. The only difference is in the analysis of the first job. Assume that subtask  $\tau_{ij}$  is to be analyzed, where  $j \neq m(i)$ . Let  $P_i(j) = \min(P_{ik}, 1 \le k \le j)$ . Special analysis of the first job of  $\tau_{ij}$  is necessary only if  $P_i(j) > P_i$ .

If the special analysis is needed, then truncate task  $\tau_i$ after subtask  $\tau_{ij}$  and use the algorithm for converting a task to canonical form on this truncated task. Determine task groupings for the truncated task and apply the stepwise method from the previous section. The schedulability test for the first job of subtask  $\tau_{ij}$  is:  $D_{ij} - E_{ij}(1) \ge 0$ 

# 3 Applying the Method to an Example

## 3.1 Problem Description

We will use an example developed from a real-time robotics application to illustrate the utility of the theory developed in this paper and how to perform a schedulability analysis using the method described in the previous section. This example is derived from a real robot system that measures the shape of pipes inside a nuclear reactor, by moving around them and using a distance sensor. The task set corresponding to this system has been simplified to reflect only the important activities relevant to our analysis, and the numbers used are not exact, although they approximate the real magnitudes. With no loss of generality, we will consider all tasks to be periodic, by using a worst-case arrival assumption for those tasks whose nature is essentially aperiodic, namely that those tasks arrive at their maximum expected rates.

The system, which has five tasks, is represented in Figure 2. A sequence of tasks that execute serially at varying priorities are considered a single task with multiple subtasks, for our analysis. For example, task  $\tau_1$  is considered as a single task, but is in fact composed of two

system tasks, an interrupt service routine (ISR) and Servo Control, where Servo Control executes only as a consequence of being signaled by the ISR. The five tasks are:

- Robot control. Task  $\tau_1$  has to control the robot's servomotors and has two subtasks, that have two different deadlines. The corresponding activities are: reading the inputs from the servo sensors and performing the control action for moving the robot.
- Measurement subsystem. Tasks  $\tau_2$  and  $\tau_3$  constitute the measurement subsystem, and synchronize with each other:  $\tau_2$  reads the distance sensors and does some data preprocessing, while  $\tau_3$  does some more processing and sends the results to a remote system.
- System command. Task  $\tau_4$  is in charge of receiving and interpreting commands arriving from the remote system, while  $\tau_5$  has to process and execute these commands. Both tasks synchronize with each other, and  $\tau_5$ also has to update some control variables that affect the operation of the rest of the tasks.

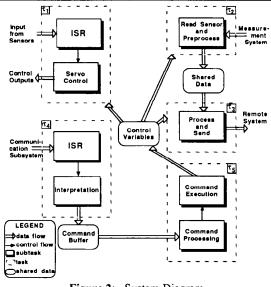


Figure 2: System Diagram

Hardware interrupts, synchronization, and existence of different deadlines lead to a task structure in which each task has several subtasks, each characterized by having different priorities and worst-case execution times. Priority ceiling protocol emulation [11] is being used for task synchronization and is responsible for the assignment of some of the priority levels. Table 1 shows the subtasks and characteristics of each task. All time values are in milliseconds.

• The lower level priorities of each task have been assigned according to rate monotonic order, using the task periods.

- Tasks  $\tau_1$  and  $\tau_4$  start with an ISR, and therefore have the priorities of their first sections fixed by the system's hardware.
- Tasks  $\tau_2$  and  $\tau_3$  synchronize with each other in their middle and final subtasks, respectively, and execute both of these subtasks at the same elevated priority. The reason for this elevated priority level will become apparent in the analysis phase.
- Tasks  $\tau_4$  and  $\tau_5$  also synchronize with each other, and are assumed to have their final and initial subtasks, respectively, executing at the same priority.
- Task  $\tau_5$ 's final subtask must modify some control variables, and is therefore executed at high priority to prevent interference from some of the other tasks.

	Timing Requirements					Priority Structure			
	Ti	<b>C</b> <sub>i 1</sub>	$C_{i2}$	C <sub>i 3</sub>	D	<b>P</b> <sub>i 1</sub>	$P_{i2}$	Pis	Shape
$\tau_1$	40	1	5	-	40	10	7	-	
$\tau_{2}$	100	10	5	5	100	4	8	4	
$\tau_{s}$	50	8	12		50	5	8	-	
τ₄	200	10	20	3	200	9	2	3	
$\tau_5$	400	2	12	10	400	3	1	6	

 Table 1: Task Set Characteristics

Although tasks  $\tau_1$  and  $\tau_4$  start with an ISR and could potentially have a self-preemption effect and violate Assumption 3, the fact that their final deadlines are before or at the end of their periods ensures that this effect cannot happen, if task deadlines are met. Therefore, all of the analysis developed in this paper applies to these two tasks, as long as they meet their deadlines.

Each task has its final deadline at the end of its period, and task  $\tau_1$  has an additional deadline for its first subtask, due to physical constraints on the sensors:  $D_{11} = 1$ ms. The total CPU utilization is 97.5%. Our goal is to derive a worst-case phasing or critical instant for each task, to be able to analyze the worst-case response times. In this way we can determine if the timing requirements of each task, and of the required associated subtasks, can be met under all circumstances.

## 3.2 Problem Solution

Before starting the analysis of each task we will reduce it to its canonical form. The first step in the analysis will be to determine the critical instant phasing, by identifying the blocking term and the multiply preemptive and singly preemptive sets. The second step is to obtain the number of jobs that have to be checked by evaluating the length of the task's busy period. Finally, the worst-case completion time for each job will be obtained by application of the schedulability equations, and checked against the deadlines or timing requirements.

**3.2.1.** Analysis of  $\tau_1$ : The transformed canonical form of  $\tau_1$  is obtained by lowering the priority of its first subtask, and combining the resultant equal priority subtasks into a single segment with priority 7.

**Step 1.** The lowest priority level in task  $\tau'_1$  is 7. We will classify the rest of the tasks according to this priority level to determine the critical instant phasing.

- τ<sub>2</sub>: It is an LHL task (type 4), and its only contribution to the critical instant may be a blocking segment.
- $\tau_3$ : It is an LH task (also type 4). It can only contribute with a blocking segment.
- $\tau_4$ : It is an HL task (type 2), so it is classified as a singly preemptive task.
- $\tau_5$ : It is an L task (type 5) and has no effect on the critical instant.

Consequently, we have task  $\tau_4$  in the set of single preemptive tasks (SP<sub>11</sub>) with its first subtask  $\tau_{41}$  acting as the H segment, and  $B_1 = C_{32}$  as the maximum of the blocking terms.

Step 2. The length of the busy period is:

$$L_{1} = \min \left( t > 0 \mid B_{1} + \sum_{\tau_{p} \in MP_{11}} \left[ t/T_{p} \right] C_{p} + \sum_{\tau_{p} \in SP_{11}} C_{p}^{h} + \left[ t/T_{1} \right] C_{1} = t \right)$$

$$L_{1} = \min \left( t > 0 \mid 0 + C_{32} + C_{41} + \left[ t/T_{1} \right] C_{1} = t \right)$$

$$L_1 = 0 + 12 + 10 + 1 \cdot 6 = 28$$

Therefore, there is only one job,  $N_1 = \lceil 28/T_1 \rceil = 1$ , of  $\tau_1$  in the busy period. As it can be seen, when the task has only one segment of constant priority and the deadline is before or at the end of the period, there can only be a single job in the busy period if it makes its deadline. This fact can be used as a shortcut for this step of the analysis.

Step 3. As the transformed canonical form has only one segment and there is only one job in the busy period, the completion time and the busy period expressions become identical. Consequently:

$$E_1(1) = 28 \le 40 = D_1$$

Intermediate Deadline. Task  $\tau_1$  meets its final deadline, but it also has a deadline for its first subtask, which has to be checked. According to the schedulability rules,  $\tau_1$ 's critical instant is valid for all jobs of this first subtask, except for the first one. For this first job we have to create a different critical instant, according to its own priority level. In this case, the first subtask has the highest priority in the system; therefore, all the rest of the tasks can be classified as low priority (type 5), and the completion time of this first subtask will be:

$$E_{11}(1) = C_{11} = 1 \le 1 = D_{11}$$

**3.2.2 Analysis of**  $\tau_2$ : The transformed canonical form for  $\tau_2$  is a task with a single segment  $\tau'_{21}$  of priority 4.

Step 1. For task  $\tau_2$  we will classify the rest of the tasks according to its lowest priority level, which is 4.

- $\tau_1, \tau_3$ : They are H tasks (type 1) and have to be included among the multiply preemptive tasks.
- $\tau_4$ : It is an HL task (type 2), so it is classified into the singly preemptive set.
- $\tau_5$ : It is an LH task (type 4) and has a potential blocking effect on  $\tau_2$ .

Thus, we have tasks  $\tau_1$  and  $\tau_3$  in the set of multiply preemptive tasks (MP<sub>21</sub>),  $\tau_{41}$  as a singly preemptive segment, and  $B_2 = C_{53}$  as the only blocking term.

Step 2. The length of the busy period is:

$$\begin{split} L_{2} &= \min \left( t > 0 \mid B_{2} + \sum_{\tau_{p} \in MP_{21}} \lceil t/T_{p} \rceil C_{p} + \\ &\sum_{\tau_{p} \in SP_{21}} C_{p}^{h} + \lceil t/T_{2} \rceil C_{2} = t \right) \\ L_{2} &= \min \left( t > 0 \mid C_{53} + \lceil t/T_{1} \rceil C_{1} + \lceil t/T_{3} \rceil C_{3} + \\ &C_{41} + \lceil t/T_{2} \rceil C_{2} = t \right) \end{split}$$

 $L_2 = 10 + 3 \cdot 6 + 2 \cdot 20 + 10 + 1 \cdot 20 = 98$ 

Finding the minimum t > 0 that satisfies the equation above can be accomplished by supposing an initial positive but very small value for t, obtaining the ceiling functions, and adding all the terms in the left-hand side of the equality. Using this result as the new value for t, we repeat the same process until we find a value of t that is the same as in the last iteration and, therefore, satisfies the equality. If the total utilization is less than or equal to 100%, then we know that the busy period will end (and so will the algorithm). In this particular case, we find that there is only one job of  $\tau_2$  in the busy period.

Step 3. As the transformed canonical task has only one segment of constant priority, the completion time has the same expression as the length of the busy period. Therefore:

 $E_2(1) = 98 \le 100 = D_2$ 

Task  $\tau_2$  also meets its final deadline.

**3.2.3.** Analysis of  $\tau_3$ : Task  $\tau_3$  is already in canonical

form.

Step 1. Task  $\tau_3$  has its basic priority at level 5, and the classification of the rest of the tasks is:

- τ<sub>1</sub>: It is an H task (type 1) and is included among the multiply preemptive tasks.
- τ<sub>2</sub>: It is an LHL task (type 4), so its H segment has a potential blocking effect.
- $\tau_4$ : It is an HL task (type 2), so it is classified as a singly preemptive task.
- $\tau_5$ : It is an LH task (type 4) and has a potential blocking effect on  $\tau_2$ .

Thus, we have task  $\tau_1$  in the set of multiply preemptive tasks (MP<sub>31</sub>),  $\tau_{41}$  as a singly preemptive segment, and  $B_3 = C_{53}$  as the maximum blocking term.

Step 2. The length of its busy period is:

$$L_{3} = \min \left( t > 0 \mid B_{3} + \sum_{\tau_{p} \in MP_{31}} \lceil t/T_{p} \rceil C_{p} + \sum_{\tau_{p} \in SP_{31}} C_{p}^{h} + \lceil t/T_{3} \rceil C_{3} = t \right)$$

$$L_{3} = \min \left( t > 0 \mid C_{53} + \lceil t/T_{1} \rceil C_{1} + C_{41} + \lceil t/T_{3} \rceil C_{3} = t \right)$$

 $L_3 = 10 + 2 \cdot 6 + 10 + 2 \cdot 20 = 72$ 

Consequently, there are two jobs,  $N_3 = \lceil 72/T_3 \rceil = 2$ , of  $\tau_3$  in the busy period, which have to be checked.

**Step 3.** The completion time of the first segment of the first job is:

$$E_{31}(1) = \min\left(t > 0 \mid B_3 + \sum_{\tau_p \in MP_{31}} [t/T_p]C_p + \sum_{\tau_p \in SP_{31}} C_p^h + C_{31} = t\right)$$
  
$$E_{31}(1) = \min\left(t > 0 \mid C_{53} + [t/T_1]C_1 + C_{41} + C_{31} = t\right)$$

$$E_{31}(1) = 10 + 1 \cdot 6 + 10 + 8 = 34$$

For the second segment, we have to recalculate the sets of multiple and single preemptive tasks.  $MP_{32}$  will be empty, as there are no tasks with all their subtasks with priority higher than or equal to 8.  $SP_{32}$  can get elements from the tasks that were originally in  $MP_{31}$  and are not in  $MP_{32}$ , if their priority is first higher and then lower than  $P_{32}$ . Task  $\tau_1$  satisfies these conditions and therefore the analysis for this subtask is:

$$\begin{split} E_{32}(1) &= \min \left( t > 0 \mid E_{31}(1) + \\ \sum_{\tau_p \in MP_{32}} \left[ \left\lceil t/T_p \right\rceil - \left\lceil E_{31}(1)/T_p \right\rceil \right] C_p + \\ &\sum_{\tau_p \in SP_{32}} \min \left( 1, \left\lceil \left\lceil t/T_p \right\rceil - \left\lceil E_{31}(1)/T_p \right\rceil \right] \right) C_p^h + C_{32} = t \end{split}$$

$$E_{32}(1) = \min(t > 0 | E_{31}(1) + \min(1, [\lceil t/T_1 \rceil - \lceil E_{31}(1)/T_1 \rceil])C_{11} + C_{32} = t)$$

 $E_{32}(1) = 34 + 1 \cdot 1 + 12 = 47 \le 50 = D_3$ 

And now we have to check the second job, in the same way:

$$E_{31}(2) = \min \left( t > 0 \mid C_{53} + \lceil t/T_1 \rceil C_1 + C_{41} + C_3 + C_{31} = t \right)$$

$$E_{31}(2) = 10 + 2 \cdot 6 + 10 + 20 + 8 = 60$$

$$E_{32}(2) = \min(t > 0 | E_{31}(2) + \min(1, [\lceil t/T_1 \rceil - \lceil E_{31}(2)/T_1 \rceil))C_{11} + C_{32} = t)$$

$$E_{32}(2) = 60 + 0 \cdot 1 + 12 = 72 \le 50 + 50 = T_3 + D_3$$

We can see that task  $\tau_3$  meets its deadlines through all of the busy period, thanks to the high priority of its last subtask. If the priority of this subtask had not been so high, let us say it remained at 5, then the first job would have missed its deadline.

**3.2.4.** Analysis of  $\tau_4$ : The transformed canonical form for  $\tau_4$  is a task with two segments:  $\tau'_{41}$  of priority 2 and  $\tau'_{42}$  of priority 3.

Step 1. Task  $\tau_4$  has its lower priority subtask at level 2, so we will classify the rest of the tasks accordingly.

 $\tau_1, \tau_2, \tau_3$ :

They are H tasks (type 1) and their subtasks fall into the multiple preemptive set.

 $\tau_5$ : It is an HLH task (type 3), so its last segment has a potential blocking effect on  $\tau_4$ , and its first segment a one-time preemptive effect.

Thus, we have tasks  $\tau_1$ ,  $\tau_2$ , and  $\tau_3$  in the set of multiply preemptive tasks (MP<sub>41</sub>),  $\tau_{51}$  as a singly preemptive segment and  $B_4 = C_{53}$  as the only blocking term.

Step 2. The length of the busy period is:

$$L_{4} = \min(t>0 | C_{53} + \lceil t/T_{1} \rceil C_{1} + \lceil t/T_{2} \rceil C_{2} + \lceil t/T_{3} \rceil C_{3} + C_{51} + \lceil t/T_{4} \rceil C_{4} = t)$$

$$L_4 = 10 + 5 \cdot 6 + 2 \cdot 20 + 4 \cdot 20 + 2 + 1 \cdot 33 = 195$$

Consequently, there is only one job of  $\tau_4$  in the busy period.

Step 3. We will now obtain the completion time of this job, starting with the first segment of its canonical form:

$$E'_{41}(1) = \min(t>0 | C_{53} + \lceil t/T_1 \rceil C_1 + \lceil t/T_2 \rceil C_2 + \lceil t/T_3 \rceil C_3 + C_{51} + C_{41} + C_{42} = t)$$

$$E'_{41}(1) = 10 + 5 \cdot 6 + 2 \cdot 20 + 4 \cdot 20 + 2 + 10 + 20 = 192$$

For the completion time of the second segment of the canonical form, tasks  $\tau_1$ ,  $\tau_2$ , and  $\tau_3$  remain as multiple preemptive in the MP<sub>42</sub> set. The analysis is:

$$\begin{split} E_{42}'(1) &= \min\left(t > 0 \mid E_{41}'(1) + \left[ \lceil t/T_1 \rceil - \lceil E_{41}'(1)/T_1 \rceil \right] C_1 + \left[ \lceil t/T_2 \rceil - \lceil E_{41}'(1)/T_2 \rceil \right] C_2 + \left[ \lceil t/T_3 \rceil - \lceil E_{41}'(1)/T_3 \rceil \right] C_3 + C_{43} = t \end{split}$$

$$E'_{42}(1) = 192 + 0 + 0 + 0 + 3 = 195 \le 200 = D_4$$
  
Task  $\tau_{\star}$  also meets its final deadline.

**3.2.5.** Analysis of  $\tau_5$ : The transformed canonical form for  $\tau_5$  is a task with two segments:  $\tau'_{51}$  of priority 1 and  $\tau'_{52}$  of priority 6.

Step 1. Task  $\tau_5$  has its lower priority subtask at level 1, which is the lowest priority in the system, so all the rest of the tasks are of type 1.

Step 2. The length of the busy period is:

$$L_5 = \min\left(t > 0 \mid \lceil t/T_1 \rceil C_1 + \lceil t/T_2 \rceil C_2 + \lceil t/T_3 \rceil C_3 + \lceil t/T_4 \rceil C_4 + \lceil t/T_5 \rceil C_5 = t\right)$$

$$L_5 = 10 \cdot 6 + 4 \cdot 20 + 8 \cdot 20 + 2 \cdot 33 + 1 \cdot 24 = 390$$

Consequently, there is only one job of  $\tau_4$  in the busy period.

Step 3. We will now obtain the completion time of this job. The analysis of the first segment of the canonical form is:

$$E'_{51}(1) = \min(t > 0 | [t/T_1]C_1 + [t/T_2]C_2 + [t/T_3]C_3 + [t/T_4]C_4 + C_{51} + C_{52} = t)$$

$$E'_{51}(1) = 5 \cdot 6 + 2 \cdot 20 + 4 \cdot 20 + 1 \cdot 33 + 2 + 12 = 197$$

For the analysis of the second segment  $\tau_1$  remains as a multiply preemptive task,  $\tau_{41}$  is a singly preemptive segment, and the rest of the tasks have no influence. Therefore:

$$E'_{52}(1) = \min(t > 0 | E'_{51}(1) + [\lceil t/T_1 \rceil - \lceil E'_{51}(1)/T_1 \rceil]C_1 + \min(1, \lceil t/T_4 \rceil - \lceil E'_{51}(4)/T_4 \rceil])C_{41} + C_{53} = t)$$

 $E'_{52}(1) = 197 + 1 \cdot 6 + 1 \cdot 10 + 10 = 223 \le 400 = D_5$ 

Task  $\tau_5$  also meets its final deadline, so the total task set is schedulable.

#### **4** Theoretical Analysis

In this section, we present the theoretical underpinnings that support the rules for schedulability analysis that were presented and used in Sections 2 and 3.

## 4.1 Busy Period Analysis

The focus of this section is finding the longest response time for a particular subtask  $\tau_{ii}$ . Once this has been found, we check whether it is less than the subtask deadline D<sub>ii</sub>. If so, then the subtask timing requirements will be met under all task phasings. We define  $P_i$  to be  $\min(P_{ij}, 1 \le j \le m(i))$ . If we select any particular phasing of the *n* tasks and consider the resulting processor execution sequence that evolves over time, we can partition this execution sequence into intervals of time of two types:  $\tau_i$ -busy periods, as defined in Section 1, and instants of  $\tau$ -idleness or intervals of  $\tau$ -idleness, during which tasks of priority less than  $P_i$  (or no task at all) are processed. The execution of  $\tau_i$  takes place solely within the  $\tau_i$ -busy period segments. Consequently, when checking any timing requirements associated with  $\tau_i$ , attention can be restricted to  $\tau_i$ -busy periods.

The definition of a  $\tau_i$ -busy period permits two such periods to be "back-to-back." In this case, we consider the interval of lower priority processing or idleness to exist, but to be of zero length. One could require that the  $\tau_i$ -busy period have strictly positive intervals of lower priority processing (or idleness) on each side; however, this can lead to unnecessary deadline checking.

We begin our schedulability analysis by looking only at the final subtask deadlines,  $D_{im(i)}$ ,  $1 \le i \le n$ . We later discuss the deadlines for the earlier subtasks in the task set.

## 4.2 Schedulability of the Canonical Form

We next show that for purposes of checking its timing requirements,  $\tau_i$  can be reduced to its canonical form. Consider any task  $\tau_i$  that has two consecutive subtasks  $\tau_{ij}$  and  $\tau_{ij+1}$  with strictly decreasing priorities  $P_{ij} > P_{ij+1}$ . We consider a modified version of  $\tau_i$ ,  $\tau'_i$ , obtained by reducing the priority of  $\tau_{ij}$  to  $P'_{ij} = P_{ij+1}$ . We next show that the completion times of  $\tau_i$  and of the subtasks  $\tau_{ik}$ ,  $j+1 \le k \le m(i)$ , are unchanged in the modified version.

**Theorem 1:** Suppose  $\tau_i$  has two consecutive subtasks  $\tau_{ij}$  and  $\tau_{ij+1}$  of strictly decreasing priority  $P_{ij} > P_{ij+1}$ . Then for any task set phasing, the completion times of a task  $\tau_i$  and its subtasks  $\tau_{ik}$ ,  $j+1 \le k \le m(i)$  are unchanged if the priority of  $\tau_{ij}$  is reduced to  $P_{ij+1}$ , assuming all equal priority segments are executed in the same relative order.

**Proof:** Let the execution sequence for the task set be given. For any single job of  $\tau_i(\tau_i)$  let  $b_{ik}(b_{ik})$  and  $f_{ik}(f_{ik})$  denote the start time and the completion time of  $\tau_{ik}(\tau_{ik})$ . Notice that the completion time of  $\tau_{ik}(\tau_{ik})$  is the activation time  $\sigma_{ik+1}(\tau_{ik+1})$ . The activation time is the instant at which a particular task or subtask becomes ready to

execute. Preemption may cause the task to start executing at an instant later than the activation; we call this the start time. Assume that the two versions of this job of  $\tau_i$  are activated at the same time. The execution sequences will then be identical up to  $f_{ij-1}$ . If we prove  $b_{ij+1} = b'_{ij+1}$ , then the execution sequences will be identical after  $b_{ij+1}$ , and the result will follow, assuming that segments with equal priority are processed in the same order in the original and in the modified task set. For the original task set, the interval  $[f_{ij-1}, b_{ij+1}]$  consists of the execution of  $\tau_{ij}$  and other tasks of higher or equal priority than  $\tau_{ii+1}$ . At time  $b_{ii+1}$ , all work of equal or higher priority than  $\tau_{ii+1}$  will have just finished, and that subtask can begin. If we now reduce the priority of  $\tau_{ii}$  to that of  $\tau_{ii+1}$ , then exactly the same work will be done during this interval, although possibly in another order. At time  $b_{ij+1}$ , all work of equal or higher priority than  $\tau_{ii+1}$  will also have just finished, and that subtask will begin execution. Consequently, b<sub>ii+1</sub> =  $b'_{ii+1}$ , and the results hold. Moreover, since the activation time of both versions is the same at the beginning of any  $\tau_i$ -busy period, this result holds for the entire busy period.

The above theorem can be used to simplify the determination of whether a particular task meets its final deadline. If any consecutive subtasks are of strictly decreasing priority, we can reduce the priority of the first to that of the second and merge these two into a single segment. By applying this theorem to all such consecutive subtasks, we can reduce the task to *canonical form*, as defined in Section 2.

#### 4.3 Worst-Case Phasing

Recall that  $P_i = \min(P_{ij}, 1 \le j \le m(i))$  and that the execution sequence arising from any choice of task phasings will create an alternating sequence of  $\tau_i$ -busy periods and periods of less than priority level  $P_i$  execution. We select a  $\tau_i$ -busy period and seek to determine the task phasings that will create the longest response time for subtask  $\tau_{im(i)}$ .

**Theorem 2:** The longest response time for  $\tau_{in(i)}$  is found during a  $\tau_i$ -busy period initiated by  $\tau_i$ .

**Proof:** Suppose that  $\tau_i$  does not initiate a  $\tau_i$ -busy period, so there exists an interval of execution of length A > 0 prior to the first initiation of  $\tau_i$  during which subtasks of priority level  $P_i$  or higher are continuously executed. If the initiation time of  $\tau_i$  were moved back by A, then  $\tau_{im(i)}$  could not start its execution any earlier than it did with its original initiation time, because all subtasks that were initiated during the period of length A would still need to be executed before  $\tau_{im(i)}$  can begin execution. Thus, the response times for all jobs of  $\tau_{im(i)}$  would be increased by A. Thus, the situation in which  $\tau_i$  does not initiate its  $\tau_i$ -busy period may not give the worst-case response time.

According to Theorem 1, one can reduce  $\tau_i$  to its transformed canonical form. The resulting task,  $\tau'_i$ , will consist

of m(i) subtasks  $\tau'_{i1}, \ldots, \tau'_{im(i)}$  having priorities  $P_i = P'_{i1} < \ldots < P'_{im(i)}$ . The transformed canonical form is useful for reasoning about the phasing of the rest of the tasks that creates the worst-case response. This phasing will depend on the priority levels of each task, compared to the priority of the first segment of the canonical form task, resulting in the classification of tasks by type that appeared in Section 2.

Clearly, type 5 tasks (lower priority) cannot be executed during a  $\tau_i$ -busy period, and therefore cannot influence the response time of  $\tau_i$ . We now seek the phasing of the 4 remaining task types that will maximize the longest response time for  $\tau_i$  during the busy period.

**4.3.1. Type 1 Tasks (H):** The phasing of a type 1 task that will create the largest response time for any job of  $\tau_i$  in a  $\tau_i$ -busy period is to have such a task initiated at the same instant that  $\tau_i$  is initiated. To see this, suppose that the first job of such a task within the  $\tau_i$ -busy period is initiated at time I > 0. The cumulative processor demands from this task during [0,t] for every  $t \in [0,b)$  monotonically increase as I decreases towards 0. Consequently, these demands are maximized uniformly over time by choosing I = 0; this maximizes the completion time of all subtasks and all jobs of  $\tau_i$  in the busy period. Therefore, setting I = 0 for any type 1 task will lead to the longest response time of  $\tau_i$ .

4.3.2. Type 4 Tasks ((LH)<sup>+</sup>L<sup>0</sup>): All H segments in type 4 tasks are preceded by an L segment that has priority lower than P<sub>i</sub>. This means that, as tasks cannot suspend themselves, at most one of the H segments can be processed during the  $\tau_i$ -busy period. Furthermore, only one segment of one of the type 4 tasks can be processed during this entire busy period. For  $\tau_i$  to initiate its own busy period, and for a segment of a type 4 task to execute during the busy period, the type 4 task must be executing one of its high-priority segments when  $\tau_i$  is initiated. It can finish that segment, but no further processing is possible until the busy period ends. Thus, to determine the worst-case phasing for all type 4 tasks, we must consider all of the H segments of all of these tasks and select the one with the longest processing requirement. That segment should be starting just as  $\tau_i$  is initiated. The phasing of the other tasks is irrelevant. We let B' denote the length of this longest segment.

**4.3.3.** Type 2  $((HL)^+)$  and Type 3  $((HL)^+H)$  Tasks: Type 2 tasks are similar to type 4, in that only one segment of the task can be processed during the busy period. The difference is that by phasing each type 2 task to be initiated at time 0, each will contribute a processing requirement that must be finished during the busy period, in contrast to the type 4 tasks, which collectively contribute only a single processing requirement. However, one of the type 2 tasks may have a long blocking segment, so long that it contributes more processing requirements than its initial segment would contribute; we have to determine if this is the case.

Type 3 tasks are the most complicated to phase. Only part of a type 3 task can be processed during the  $\tau_i$ -busy period, but that part can be an inner H segment, the last H segment, the first H segment, or the last followed by the first H segment. It should be noticed, however, that the inner H and final H segments are blocking terms and, as tasks do not suspend themselves, at most one blocking term chosen from the type 2, 3, and 4 tasks can be executed during the busy period.

To determine which task, if any, of types 2 and 3 acts with a blocking segment, and therefore has to be phased with  $\tau_i$  at one of its inner or final H segments, we can follow the next procedure. For each type 2 and type 3 task, let W be the computational requirement of the initial H segment, U be the requirement of the longest inner H segment (if any) and V be the computation requirement of the final H segment (for type 3 tasks). If there is no inner blocking segment then set U=0, and if there is no final blocking segment then set V=0.

Assume that each type 2 or type 3 task is initiated at the same time as task  $\tau_i$ . Now consider the two other possible phasings:

- Initiating the longest inner H segment at the same time as τ<sub>i</sub> will increase τ<sub>i</sub>'s response times by U-W-B', which is the marginal gain in total computational requirement from using this inner H segment.
- 2. For type 3 tasks, initiating the final H segment at the same time as  $\tau_i$  may increase  $\tau_i$ 's response times by V-B'. For type 2 tasks this value is negative or zero (because V=0), and will be disregarded in the next steps. Note that this is a pessimistic estimate in that it assumes that the final segment serves as a blocking segment and the initial segment is a preempting segment. However, the busy period may end before the initial segment can preempt.

For each type 2 and type 3 task, we calculate the gain in latency as max[(U-W-B'),(V-B')], and we choose the task with the largest value. If this value is negative, then all of these tasks should be initiated at the same time as  $\tau_i$ , and the blocking term would be  $B_i=B'$ . If the largest value is positive, then either the inner blocking or final blocking segment should be selected. If  $U-W \ge V$ , then the inner segment is chosen, and  $B_i=U$ ; in this case the initial H segment does not contribute a preemption effect. If  $U-W \le V$  then the final segment is chosen, we set  $B_i=V$ , and this task also remains as a singly preemptive task. All the other tasks should be initiated at the same time as  $\tau_i$ .

# 4.4 Other Subtask Deadlines

The preceding analysis was carried out under the assumption that each task had only a single deadline at the end of its final subtask. We now wish to allow additional

subtask deadlines, for example  $\tau_{ii}$  having a deadline  $D_{ii}$ . Clearly, one must check the deadlines of each of these subtasks throughout the  $\tau_i$ -busy period for the worst-case phasing developed earlier. However, this is not sufficient, because it will only ensure that jobs of subtasks after the first job in the busy period will meet their worst-case timing requirement. It does not guarantee the first job, because the phasing may not be worst for the first job of the subtask. The reason for this is that the phasing developed was based on the  $\tau_i$ -busy period in which only activity of priority P<sub>i</sub> or higher is executed, P<sub>i</sub> being the minimum priority of all of the subtasks of  $\tau_i$ . However, the appropriate priority level against which to create a longest response time phasing for the first job should be based upon a possibly higher level priority,  $P_i(j) = \min(P_{ik}, 1 \le k$  $\leq j$ ). This is the same as thinking of a  $\tau_i^j$ -busy period, where task  $\tau'_{i}$  is a task composed of the first j subtasks of  $\tau_{i}$ 

**Theorem 3:** The deadline,  $D_{ij}$ , of  $\tau_{ij}$  will be met under all task phasings provided:

- 1. The deadline of the first job of  $\tau_{ij}$  is met under the worst-case phasing for a  $\tau_{i}^{j}$ -busy period, and
- 2. The deadline of all jobs of  $\tau_{ij}$  after the first are met during a  $\tau_i$ -busy period with worst-case phasing for the minimum priority level of that busy period.

**Proof:** For each subtask processed during the  $\tau_i$ -busy period, we record the minimum priority of it and all subtasks of  $\tau_i$  processed before it during this busy period. This minimum priority is defined as  $P_i(j)$  for the first job of each subtask of  $\tau_i$  and will be  $P_i$  for all subtasks of second and later jobs. The proof has two parts: finding the longest response time for jobs with minimum priority  $P_i$  and finding the longest response time for jobs with minimum priority  $P_i$ .

The case of deadlines for subtasks with minimum priority  $P_i$  follows easily from the reasoning given in Section 4.3. Specifically, type 5 tasks can be ignored, type 1 tasks should be phased to maximize preemption as should any type 2 and type 3 tasks chosen for preemption. The largest blocking term derived from type 2, 3, and 4 tasks will also be the same, because a subtask of priority level  $P_i$  prior to the subtask in question is being blocked to the maximum extent, and this maximum blocking time will also delay all subsequent subtasks of  $\tau_i$ . Consequently, one needs only to check the deadlines of all subtasks during the  $\tau_i$ -busy period using the worst-case phasing derived in Section 4.3.

Subtasks  $\tau_{ij}$  of the first job of  $\tau_i$  with  $P_i(j)$  greater than  $P_i$  must be handled separately, because their critical instant phasing can be different. One must now derive the worst-case phasing described in Section 4.3 for type 1 - 4 tasks, except the priority level  $P_i$  must be replaced by  $P_i(j)$ . Consider first the subtask  $\tau_{ik}$  with the largest k

among those for which  $P_i(k) > P_i$ . We wish to determine the longest response time for its first job. To do this, we ignore any later subtasks by considering only the task  $\tau_i^k$ , and reduce this truncated task to its canonical form. Note that the minimum priority is now  $P_{k}(k)$  and the other n-1tasks must be reclassified into the five types with respect to  $P_i(k)$ . Once this is done, the arguments of Section 4.3 can be used to derive the worst-case phasing, and the longest response time of  $\tau_{ik}$  can be computed. One now picks the subtask  $\tau_{ik'}$ , k' < k, with the next largest k' among those with  $P_i(k') > \min(P_1, \ldots, P_k)$ , truncates  $\tau_i$  there to obtain  $\tau_i^k$ , reduces it to canonical form, classifies the other tasks, and invokes the worst-case phasing to derive its worst-case response time. This process continues until all deadlines of first jobs have been checked. Thus conditions (1) and (2) are sufficient for all subtask deadlines to be met.

## 4.5 Scheduling Bounds

The example presented in Figure 1 was designed to show the added scheduling complexity that can occur when tasks are composed of subtasks that are executed at different fixed priority levels. This example also shows a scheduling benefit that can accrue from such a task structure. Using ordinary rate monotonic scheduling, a task set with two tasks:  $C_1 = 4$ ,  $T_1 = 10$  and  $C_2 = 6$ ,  $T_2 = 14$ , is schedulable, but fully utilizes the processor with total utilization of .829, essentially equal to the Liu and Layland bound of .828 for two tasks. However, the example in Section 1.2 modifies task two to have two subtasks of differing priority. This modified task set has a total utilization of .971 and is schedulable using a fixed priority algorithm. Thus, we see that we can increase the fixed priority schedulability of a task set by decomposing one or more of the tasks into subtasks that are executed at modified priority levels, and this can offer an increase in schedulability over that provided by the simple rate monotonic algorithm.

This paper is restricted to methods by which the schedulability of complex tasks can be determined for any fixed priority scheduling algorithm. The question of finding least upper bounds on processor utilization which generalize the Liu and Layland bounds will be addressed in a second paper. However, one simple and very interesting result for two tasks is that by dividing the one having the largest period into two subtasks, one with the lowest priority and one with globally highest priority, 100% schedulability can be attained. Specifically, given any two periodic tasks with  $T_1 < T_2$  and utilizations  $U_1 + U_2$ = 1, one should decompose  $\tau_2$  into two subtasks with  $C_{21}$ =  $C_2 - (T_1 - C_1)$ ,  $C_{22} = T_1 - C_1$ ; these values are always non-negative.  $\tau_1$  has intermediate priority,  $\tau_{21}$  has lowest priority and  $\tau_{22}$  has highest priority. The resulting task set is schedulable, the C's and T's are arbitrary and the task set has 100% utilization. For example, the task set

depicted in Figure 1 with C<sub>2</sub> increased to 8.4 has 100% utilization, yet it is schedulable if the second task is broken into two subtasks with C<sub>21</sub>=2.4, C<sub>22</sub>=6, and  $\tau_{22}$  is given highest priority. Ordinary rate monotonic scheduling can meet the timing requirements only if C<sub>2</sub>≤6.

# **5** Conclusions

Even when application-level tasks are assigned fixed priorities, the actual priority structure of a realistic system can be much more complex. Characteristics of the operating system and underlying hardware impact the priority structure and consequently the timing behavior of the system. For example, the task dispatching mechanism of the operating system, the interrupt architecture of the processor, synchronization protocols, and intertask communication mechanisms all contribute to the system's actual timing behavior. In order to accurately predict system behavior, these effects should be included in the schedulability equations that model the system's behavior.

This paper offers a generalized model of fixed priority scheduling that provides a theoretical framework for analyzing task sets scheduled through a fixed priority preemptive scheduler, where each task is comprised of a number of subtasks, each executing at a different priority level. For a set of tasks with a complex priority structure, we offer a method of analysis with an underlying theoretical foundation. For simple task sets, our contribution is a formalization of techniques currently being used [3]. Furthermore, the method shows that an increase in schedulability can be achieved by taking advantage of the high-priority execution of the final subtasks of a task.

Another very important highlight of this method is that it provides some simple techniques for reasoning about time in systems with varying priorities. The fact that tasks can be reduced to a canonical form simplifies analysis and allows one to easily reason about worst-case phasing. It also simplifies analysis by allowing the classification of tasks according to the priority of their first subtask. We feel that in practical problems the algorithm described in Section 2 is easily implemented and runs efficiently; however, the worst case complexity of this algorithm is an open question.

# References

1. Borger, M. W., Klein, M. H., and Veltre, R. A. "Real-Time Software Engineering in Ada: Observations and Guidelines". Software Engineering Institute Technical Review (1988).

2. Goodenough, J. B., and Sha, L. "The Priority Ceiling Protocol: A Method for Minimizing the Blocking of High Priority Ada Tasks". *Proceedings of the 2nd International Workshop on Real-Time Ada Issues* (June 1988). 3. Klein, M. H., and Ralya, T. An Analysis of Input/Output Paradigms for Real-Time Systems. Tech. Rept. CMU/SEI-90-TR-19, Software Engineering Institute, July 1990.

4. Lchoczky, J. P., and Sha, L. "Performance of Real-Time Bus Scheduling Algorithms". *ACM Performance Evaluation Review, Special Issue 14*, 1 (May, 1986).

5. Lehoczky, J.P. "Fixed Priority Scheduling of Periodic Task Sets with Arbitrary Deadline". *IEEE Real-Time System Symposium* (1990).

6. Leung, J. and Whitehead, J. "On Complexity of Fixed-Priority Scheduling of Periodic Real-Time Tasks". *Performance Evaluation 2, 237-50* (1982).

7. Liu, C.L., and Layland, J.W. "Scheduling Algorithms for Multi-Programming in a Hard Real-Time Environment". *Journal of the Association for Computing Machinery Vol.* 20, 1 (January 1973), pp. 46-61.

8. Rajkumar, R., Sha, L., and Lehoczky, J.P. "Real-Time Synchronization Protocols for Multiprocessors". *IEEE Real-Time Systems Symposium* (December 1988).

9. Rajkumar, R. "Real-Time Synchronization Protocols for Shared Memory Multi-Processors". *Proceedings of The 10th International Conference on Distributed Computing* (1990).

10. Sha, L., Rajkumar, R., Lehoczky, J. and Ramamritham K. "Mode Change Protocols for Priority-Driven Preemptive Scheduling". *The Journal of Real-Time Systems Vol. 1* (1989), pp. 243-264.

11. Sha, L. and Goodenough, J. B. "Real-Time Scheduling Theory and Ada". *IEEE Computer Vol. 23, No. 4* (April 1990).

12. Sha, L., Rajkumar, R., and Lehoczky, J. P. "Real-Time Scheduling Support in Futurebus+". *IEEE Real-Time Systems Symposium* (1990).

13. Sha, L., Rajkumar, R., and Lehoczky, J. P. "Priority Inheritance Protocols: An Approach to Real-time Synchronization". *IEEE Transactions on Computers* (Sept. 1990).

14. Sha, L., Klein, M. H., and Goodenough, J. B. Rate Monotonic Analysis for Real-Time Systems. In *Foundations of Real-Time Computing: Scheduling and Resource Management*, van Tilborg, Andre and Koob, Gary M., Ed., Kluwer Academic Publishers, 1991, pp. 129-155.

15. Sprunt, B., Sha, L., and Lehoczky, J.P. "Aperiodic Task Scheduling for Hard Real-Time Systems". *The Journal of Real-Time Systems*, 1 (1989), pp. 27-60.