Frameworks for Product Family Design and Development

Fabrice Alizon

Department of Mechanical Engineering, Bucknell University, Lewisburg, PA 17837, USA

Kiran Khadke

Department of Mechanical Engineering - Engineering Mechanics Michigan Technological University, Houghton, MI 49931. USA

Henri J. Thevenot

The Harold & Inge Marcus Department of Industrial & Manufacturing Engineering The Pennsylvania State University, University Park, PA 16802, USA

John K. Gershenson

Department of Mechanical Engineering - Engineering Mechanics Michigan Technological University, Houghton, MI 49931, USA

Tucker J. Marion

The Harold & Inge Marcus Department of Industrial & Manufacturing Engineering The Pennsylvania State University, University Park, PA 16802, USA

Steven B. Shooter

Department of Mechanical Engineering, Bucknell University, Lewisburg, PA 17837, USA

Timothy W. Simpson

The Harold & Inge Marcus Department of Industrial & Manufacturing Engineering The Pennsylvania State University, University Park, PA 16802, USA, <u>tws8@psu.edu</u>

In today's market, products must meet or exceed customers' needs while being competitively priced and developed in the shortest time possible. While product platforms address many of these requirements, they can incur additional development challenges with regards to coordination, time, and cost. Companies therefore need to use a concurrent engineering process to develop product families and product platforms efficiently; however, no concurrent engineering process models exist to support product family development. Based on concurrent engineering principles, four processes are proposed for systematic product family design using two platforming approaches — top-down and bottom-up — and two development drivers: product-driven and platform-driven. The first objective of this study is to propose a consistent product family development process for the four proposed product family design processes based on the two approaches and two drivers. Several industry examples highlight the context and illustrate the four proposed processes.