

Generalized Frequency Dependent Small Signal Model for High Frequency Analysis of AlGaIn/GaN MOS-HEMTs

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Abstract—Traditional lumped small signal equivalent circuit models of AlGaIn/GaN metal oxide semiconductor high electron mobility transistors (MOS-HEMTs) are made up of constant valued circuit elements. Such models are unable to capture the high frequency behavior (above 20 GHz) of the device. In this work, a modified small signal equivalent circuit model of AlGaIn/GaN MOS-HEMTs is presented. The key feature of the proposed model is that the values of the different circuit elements in the model are considered to be frequency dependent in nature and not constants. The frequency dependent value of each circuit element is mathematically represented using polynomial functions where the coefficients of the functions are determined via a least-square curve fitting approach. This frequency dependent attribute of the circuit element values ensures that the proposed model is very accurate at high frequencies without sacrificing the compactness of the model topology. The accuracy of the proposed model has been verified up to 50 GHz using experimentally measured Y-parameters of AlGaIn/GaN MOS-HEMTs having a different gate dielectric and gate length.

Index Terms— Curve fitting, high electron mobility transistors (HEMTs), lumped model, least squares methods, unity gain frequency, Y-parameters.

I. INTRODUCTION

AlGaIn/GaN high electron mobility transistors (HEMTs) are potential candidate devices to construct radio frequency (RF) electronic circuits for future 5G communications, defense, and space applications [1], [2]. Addition of a gate dielectric to realize AlGaIn/GaN metal

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oxide semiconductor high electron mobility transistors (MOS-HEMTs) is shown to further improve the gain and noise figure of such devices when operated at high frequency [3], [4]. AlGaIn/GaN MOS-HEMTs also offer a lower subthreshold leakage current, better stability, and reduced current-collapse compared to commonly used AlGaIn/GaN HEMTs [5]–[7]. Traditionally, for using AlGaIn/GaN HEMTs or MOS-HEMTs in RF electronic circuits, a small signal equivalent circuit (SSEC) model of the device is required. Typically, this SSEC model is generated from the measured scattering parameter (S-parameter) or Y-parameter data of the device [8]. Schematic of a conventional lumped SSEC model of an AlGaIn/GaN MOS-HEMT is shown in Fig. 1 [9]–[12]. The model consists of a current source representing the drain current and few passive elements representing different resistive and capacitive elements present between the source, gate, and drain of the AlGaIn/GaN HEMT or MOS-HEMT. The main advantage of this lumped SSEC model is that it enables the Y-parameters of the device to be expressed as compact closed-form functions of the values of the circuit elements of Fig. 1. Therefore, fast frequency sweeps of the device Y-parameters can be performed in an analytic manner at minimal computational time costs. Despite the above advantages of the lumped SSEC model of Fig. 1, one key limitation is that at high frequencies (i.e., generally beyond 20 GHz) this model is no longer very accurate. This is because the values of the capacitances and the transconductance element in the model of Fig. 1 are treated as constants where the constant values are extracted from low frequency Y-parameter data [13], [14]. As a result, the variation in these circuit element values at high frequency is neglected leading to modeling errors [13], [15], [16].

To mitigate the above accuracy issue, several new SSEC models have been developed for AlGaIn/GaN HEMTs and other transistors by introducing additional circuit elements into the conventional SSEC model to capture the high-frequency behavior [13], [14], [17]–[21]. A two current source based SSEC model of AlGaIn/GaN HEMT has also been proposed for matching the experimentally measured data [17]. However, most of these modified SSEC models of AlGaIn/GaN HEMTs are empirical in nature and do not provide a generalized mechanism to improve the model accuracy. In particular, the

works of [17]–[24] fail to identify a generalized strategy as to how to determine the values of the additional circuit elements. Finally, the modified SSEC models improve the accuracy of the Y-parameters at the cost of increasing the model size and complexity. Hence, the compact closed-form expressions of the Y-parameters of the device derived from the original SSEC model of Fig. 1 are no longer applicable and need to be derived anew for each modified model [17], [21].

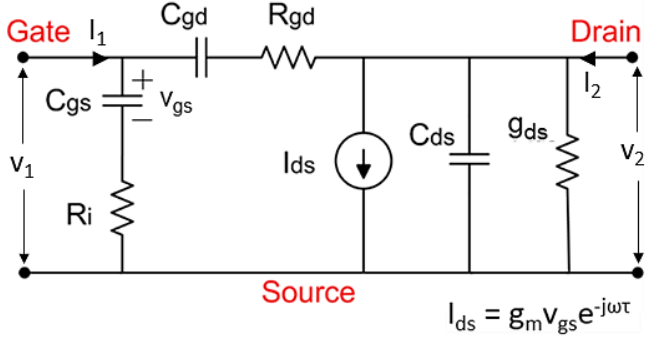


Fig. 1. Conventional lumped SSEC model of AlGaIn/GaN MOS-HEMTs. Elements of the model are R_i , R_{gd} , g_{ds} , g_m , C_{gs} , C_{ds} , C_{gd} and τ , respectively.

In this work, we present a modified SSEC model of AlGaIn/GaN MOS-HEMTs that is, by construction, significantly more accurate over the high frequency region of operation. The proposed SSEC model is similar in topology to the conventional SSEC model displayed in Fig. 1 with the key exception being that the values of all the circuit elements in the model are assumed to be frequency dependent and not constant. The frequency dependent circuit element values are expressed as polynomial functions of frequency and the coefficients of the functions are determined from broadband measured data using a least square curve-fitting technique. Thus, the coefficients can be determined offline. These coefficients are the new degrees of freedom introduced into the proposed model which can be tuned to capture the high frequency behavior of the AlGaIn/GaN MOS-HEMT device while still preserving the simplicity of the model topology of Fig. 1. Consequently, the proposed SSEC model can still use the same compact expressions obtained from Fig. 1 to represent the Y-parameters of the device – something that is not possible for the modified SSEC models reported in [13], [14], [17]–[21]. Importantly, the proposed model is not empirical in nature and represents a general numerical approach to improve the accuracy of the conventional SSEC model of Fig. 1. The proposed model is sufficiently generic in nature, and can be applied to any field effect transistor (FET) represented using the SSEC model of Fig. 1.

In this work, the proposed model is validated using measured Y-parameter data for two different gate dielectrics and gate length values for AlGaIn/GaN MOS-HEMT devices. A thorough comparison of the performance of the proposed model with respect to the conventional SSEC model of Fig. 1 is also performed in this work. This performance analysis reveals that simple 2nd order polynomial functions are sufficiently accurate to capture the frequency dependency of the circuit element values of the proposed model till 50 GHz, thereby offering far better accuracy than the conventional

SSEC model of Fig. 1 at millimeter wave frequencies.

II. REVIEW OF EXISTING LUMPED SSEC MODELS FOR ALGAN/GAN MOS-HEMTs

A. Review of Conventional SSEC Model

Traditionally, an AlGaIn/GaN MOS-HEMT or HEMT device is treated as a two port network where the gate-to-source terminal is port 1 and the drain-to-source terminal is port 2. The measured S-parameters of this 2-port device are then converted into Y-parameters as [25]:

$$\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} Y_{11}(s) & Y_{12}(s) \\ Y_{21}(s) & Y_{22}(s) \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix} \quad (1)$$

where Y_{11} , Y_{12} , Y_{21} , and Y_{22} represent the Y-parameters as functions of frequency and $s = j2\pi f$ where f is the instantaneous frequency. Now, by comparing the Y-parameters of equation (1) to the actual current/voltage quantities of the conventional SSEC model of Fig. 1, the following equations are obtained [25]:

$$\begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix} = \begin{bmatrix} \frac{sC_{gs}}{1+sC_{gs}R_i} + Y_{gd} & -Y_{gd} \\ \frac{g_m e^{-s\tau}}{1+sC_{gs}R_i} - Y_{gd} & g_{ds} + sC_{ds} + Y_{gd} \end{bmatrix} \quad (2)$$

where

$$Y_{gd} = \frac{sC_{gd}}{1+sC_{gd}R_{gd}} \quad (3)$$

From equation (2)-(3), the values of the individual circuit elements of the SSEC model of Fig. 1 can be represented as [25]:

$$C_{gd} = \frac{1}{\omega \text{Im} \left(\frac{1}{Y_{12}} \right)} \quad (4)$$

$$C_{ds} = \frac{\text{Im}(Y_{12} + Y_{22})}{\omega} \quad (5)$$

$$C_{gs} = -\frac{1}{\omega \text{Im} \left(\frac{1}{Y_{11} + Y_{12}} \right)} \quad (6)$$

$$R_i = \text{Re} \left(\frac{1}{Y_{11} + Y_{12}} \right) \quad (7)$$

$$g_{ds} = \text{Re}(Y_{12} + Y_{22}) \quad (8)$$

$$g_m = \left| \frac{(Y_{12} - Y_{21})(Y_{11} + Y_{12})}{\text{Im}(Y_{11} + Y_{12})} \right| \quad (9)$$

$$R_{gd} = -Re\left(\frac{1}{Y_{12}}\right) \quad (10)$$

$$\tau = \frac{\frac{\pi}{2} - \text{phase}(Y_{12} - Y_{21}) + \text{phase}(Y_{11} + Y_{12})}{\omega} \quad (11)$$

where $\omega = 2\pi f$, and $Re()$ and $Im()$ refers to the real part and imaginary part of the corresponding Y-parameters, respectively. From this discussion, it clear that if the measured Y-parameters of the device are known at a few frequency points, then equation (4)-(11) can be used to determine the values of the circuit elements of the SSEC model at these frequency points. For example, in [13], [14] it was demonstrated how the values of the capacitances and the transconductance element of the SSEC model can be determined from the low frequency Y-parameter data of the device. Once determined, these circuit element values can be replaced in equation (2)-(3) to predict the Y-parameters of the device at any arbitrary frequency point in the bandwidth of operation. However, it is pointed out that if the circuit element values are extracted from the low frequency region as in [13], [14], then the variation of these circuit element values with frequency, typically seen in the high frequency region, is not taken into account. Consequently, the SSEC model obtained from these circuit element values may prove to be inaccurate in the high frequency region of operation.

B. Modified SSEC Models

Several works have attempted to address the low accuracy of the conventional SSEC model of Fig. 1 in the high-frequency region of operation. For example, Huang *et al.* enhanced the accuracy of the SSEC model by adding passive components (R_L and L_{ds}) between the drain and source terminal [21]. In particular, an inductive circuit element is added to better capture the high frequency effects of the device. A similar observation is also reported by Ahsan *et al.* where conventional constant value SSEC models of AlGaIn/GaN HEMTs correctly predict the measured Y-parameters at low-frequency whereas inductive effects dominating at high frequency results in failure of the same models [13]. Adding these additional inductive circuit elements lead to greater model accuracy, but at the cost of greater model complexity. For example, the equation of Y_{22} for the modified model proposed by Huang *et al.* is given as [21]:

$$Y_{22} = \frac{\frac{1}{L_{ds}} \left(s + \frac{1}{R_{ds} C_{ds}} \right)}{s^2 + \left(\frac{R_L}{L_{ds}} + \frac{1}{R_{ds} C_{ds}} \right) s + \left(\frac{R_L}{L_{ds} R_{ds} C_{ds}} + \frac{1}{L_{ds} C_{ds}} \right)} + Y_{gd} \quad (12)$$

where separating the real and imaginary part of equation (12) becomes challenging. Thus, the modified model of [21] is unable to develop any closed-form expressions similar to equations (4)-(11) to quickly determine the value of the inductive circuit element. Rather, the value of the inductive

circuit element is identified using an empirical approach where the said value is tuned in a circuit simulator till an accurate fit of the predicted Y-parameters is achieved.

Another approach to better match the measured Y-parameters, proposed by Brady *et al.*, is to replace the resistive element between the drain and source terminals with a current source [17]. This modification introduces new phase elements associated with the g_{ds} circuit element shown in Fig. 1. The modified equation for Y_{22} thus becomes:

$$Y_{22} = g_{ds} e^{-s\tau_{ds}} + sC_{ds} + Y_{gd} \quad (13)$$

where τ_{ds} is an additional phase component added to match the phase component of the experimentally measured Y_{22} parameter. Unfortunately, this modification in the circuit will not increase the accuracy of the Y_{11} and Y_{12} parameters in the high frequency region. This is because these parameters are independent of the admittance branch existing between the source and drain terminals. Therefore, several such additional empirical modifications will have to be made to the circuit so as to accurately model all the Y-parameters simultaneously in the high frequency region.

In the work of Wu *et al.* [18], an additional series R-L network is introduced between the drain and source terminals of silicon-on-insulator (SOI) metal oxide semiconductor field effect transistors (MOSFETs). This R-L network is added to model the magnetic effect caused by channel current and resistive losses in silicon, respectively. Accordingly, the analytical equation of the Y_{22} parameter of the modified model of [18] changes to:

$$Y_{22} = \left(C_{ds} + C_{gd} - \frac{L_C}{R^2 + L_C^2 \omega^2} \right) s + \omega^2 C_{gd}^2 R_{gd} + \frac{R}{R^2 + L_C^2 \omega^2} + g_{ds} \quad (14)$$

where L_C and R are the new circuit elements added to the conventional SSEC model. However, the SSEC model of [18] only improves the low frequency (< 2 GHz) fitting of data. At frequency above 2 GHz, the circuit response is observed to be similar to that of traditionally used SSEC model.

In the work of [19], Sung *et al.* added a drain junction capacitance (C_d) and spreading resistance (R_{sub}) to capture the substrate coupling effects which dominates at higher frequency range in Si MOSFETs. An additional capacitance (C_g) between the gate to source terminals was also introduced to accommodate the gate-source overlap capacitance in MOSFETs. Unfortunately, it is pointed out that the addition of all these passive elements into the conventional SSEC model improves the model fit of all the Y-parameters except Y_{12} . Therefore, this modified model may still be inadequate for simulation and analysis of MOSFETs at high frequencies.

Another physics-based model was developed by Kwon *et al.* to consider the effect of charging current between the drain and gate terminals of a Si MOSFET [14]. Accordingly, a current source (I_I) was introduced between the drain and source terminals to capture the non-quasi static effects. However, introduction of the current source (I_I) results in a different value of C_{dg} given as:

$$C_{dg} = -\frac{\text{Im}(Y_{21})}{\omega} - g_m R_g (C_{gs} + C_{gd}) \quad (15)$$

where R_g represents the extrinsic gate resistance. Now, in this case, the analytical equation for Y_{21} becomes more complicated as it attempts to capture the higher order effects occurring due to the derivative of v_{gs} in the equation of I_1 , that is $I_1 = (C_{dg} - C_{gd}) \frac{dv_{gs}}{dt}$.

Finally, Tang *et al.* added an additional capacitor (C_{gd1}) between the drain and gate terminals of a MOSFET [20]. The addition of C_{gd1} helps to capture the high frequency coupling effects due to the overlap capacitance as well as the wire capacitance between the gate and drain terminals. Unfortunately, this means that the analytical equations of all the Y-parameters are different from that of equation (2) and a new set of closed-form equations similar to equation (4)-(11) need to be developed for this model. Furthermore, one additional zero gets added to the analytical equation of each Y-parameters in equation (2) resulting in further complications in the model equations.

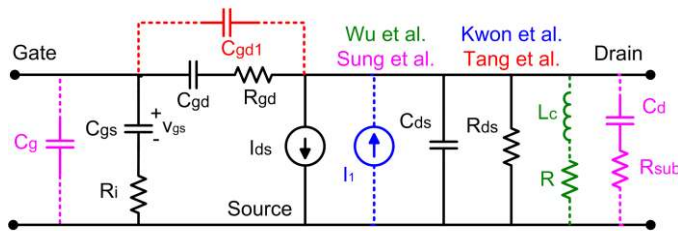


Fig. 2. Modified SSEC models proposed by Wu *et al.* [18], Sung *et al.* [19], Kwon *et al.* [14], and Tang *et al.* [20] to match the experimentally measured Y-parameters.

Thus, the above discussion shows that in order to improve the accuracy of the conventional SSEC model of Fig. 1, new circuit elements must be introduced into the model, as highlighted in Fig. 2. Unfortunately, these new circuit elements will add to the model complexity, thereby ensuring that the simple expression of the Y-parameters in equation (2) and the closed-form equations of (4)-(11) are no longer applicable. In effect, new equations and empirical approaches have to be developed in order to calculate the values of the new circuit elements introduced in the model and predict the resultant Y-parameters from the model – a highly cumbersome and time-consuming task. In order to avoid this issue, generalized numerical techniques that can significantly enhance the accuracy of the conventional SSEC model of Fig. 1 in the high frequency region without disturbing its compact model topology are required.

III. PROPOSED GENERALIZED FREQUENCY DEPENDENT LUMPED SSEC MODEL

In this work, a generalized frequency dependent lumped SSEC model is developed that can accurately predict the high frequency behavior of AlGaN/GaN MOS-HEMT devices. This SSEC model possesses the same compact topology of the

conventional SSEC model of Fig. 1. However, the values of the circuit elements making up this SSEC model are not assumed to be constant but rather considered to be frequency dependent in nature. In this work, the frequency dependency of the values of the circuit elements are represented using polynomial functions. Therefore, without loss of generality, any circuit element of the proposed SSEC model, say θ , can be represented as:

$$\theta(f) \approx \sum_{k=0}^K p_k f^k \approx p_0 + p_1 f + \dots + p_K f^K \quad (16)$$

where K is the order of the polynomial and p_k is the k -th polynomial coefficient. Now, it is assumed that the measured Y-parameters of the device are described as N sampled data points lying within the bandwidth of interest $[f_{min}, f_{max}]$ where the general i -th data point is of the form:

$$\{f_i, \mathbf{Y}(s_i)\}; s_i = 2\pi j f_i; 1 \leq i \leq N; f_{min} \leq f_i \leq f_{max} \quad (17)$$

Based on the knowledge of the sampled Y-parameter data of equation (17), the value of all the circuit elements at each data point can be calculated using the closed-form expressions of equation (4)-(11). Let the calculated value of the circuit element θ at any given i -th data point be known as $\{f_i, \theta(f_i)\}$. From this knowledge, the coefficients of equation (16) for the circuit element θ will be estimated in a least square sense by solving the following over-determined system of equations [26]:

$$\mathbf{A}\mathbf{X} = \mathbf{B}$$

$$\mathbf{A} = \begin{bmatrix} 1 & f_1 & \dots & f_1^K \\ \vdots & \ddots & \ddots & \vdots \\ 1 & f_N & \dots & f_N^K \end{bmatrix}; \mathbf{X} = \begin{bmatrix} p_0 \\ \vdots \\ p_N \end{bmatrix}; \mathbf{B} = \begin{bmatrix} \theta(f_1) \\ \vdots \\ \theta(f_N) \end{bmatrix} \quad (18)$$

where,

$$\mathbf{X} = (\mathbf{A}^T \mathbf{A})^{-1} \mathbf{A}^T \mathbf{B} \quad (19)$$

Therefore, the coefficients of equation (16) can be derived for all circuit elements in the proposed SSEC model directly from the measured Y-parameter data of equation (17). It is noted that the coefficients for equation (16) for each circuit element acts as new degrees of freedom introduced into the SSEC model. It is appreciated that these degrees of freedom were not present in the conventional SSEC model of Fig. 1. The idea is that these new degrees of freedom can be exploited to better fit the model of equation (2) to the measured Y-parameter data of equation (17) across the entire bandwidth of interest $[f_{min}, f_{max}]$ instead of only concentrating on the low frequency region of operation as done in the existing works of [13], [14], [22], [27], [28]. Therefore, in this work, the frequency dependency of the values of the circuit elements of Fig. 1, especially in the high frequency region is not neglected and hence leads to better model accuracy.

As an additional benefit, in this work, the new degrees of freedom are introduced without changing the topology of the

model of Fig. 1. This ensures that the Y-parameters of the device can be predicted at any arbitrary frequency point by using the same compact expressions of equation (2) with the only modification being that now the circuit element values in equation (2) are no longer treated as constants but rather as polynomial functions of frequency as:

$$\begin{bmatrix} Y_{11}(f) & Y_{12}(f) \\ Y_{21}(f) & Y_{22}(f) \end{bmatrix} = \begin{bmatrix} \frac{sC_{gs}(f)}{1+sC_{gs}(f)R_i(f)} + Y_{gd} & -Y_{gd} \\ \frac{g_m(f)e^{-s\tau(f)}}{1+sC_{gs}(f)R_i(f)} - Y_{gd} & g_{ds}(f) + sC_{ds}(f) + Y_{gd} \end{bmatrix};$$

$$Y_{gd} = \frac{sC_{gd}(f)}{1+sC_{gd}(f)R_{gd}(f)} \quad (20)$$

where the polynomial functions of equation (20) have already been determined using the least-square curve-fitting technique of equations (18)-(19). This the ability of the proposed frequency dependent SSEC model to directly utilize the equation (2) in order to predict the Y-parameters of the AlGaIn/GaN MOS-HEMT device represent a clear advantage over the reported works of [14], [18]–[21]. This is because in the reported works of [14], [18]–[21], additional new circuit elements are added into the SSEC model of Fig. 1. The addition of these new circuit elements complicates the model topology of Fig. 1, thereby necessitating the derivation of a new set of expressions for the Y-parameters of the device as opposed to directly using equation (2).

IV. EXPERIMENTAL DETAILS

A key aspect of this work is to use experiments to measure the Y-parameters of fabricated AlGaIn/GaN MOS-HEMT devices. It is this measured Y-parameters that is latter used in the curve fitting approach of equations (18)-(19) of the proposed frequency dependent SSEC model. In this section, the details of the experiments performed and the device structures studied in order to measure the Y-parameters is provided. The AlGaIn/GaN hetero-structures used in this work is grown on 4H-SiC substrates and procured from commercial vendors. The hetero-structure consist of a 500 nm thick undoped GaN layer and a 25 nm thick Al_{0.25}Ga_{0.75}N barrier layer. Schematic of the fabricated AlGaIn/GaN MOS-HEMTs are shown in Fig. 3. The fabrication includes a mesa isolation performed using an inductively coupled plasma reactive ion etching (ICP-RIE) system. Source/drain ohmic contacts are then formed by depositing Ti/Al/Mo/Au (15/60/35/50 nm) using e-beam evaporation system under high vacuum followed by rapid thermal annealing (RTA) at 860 °C for 30 sec in N₂ environment. Subsequently, a 10 nm thick gate dielectric deposition is done where the gate dielectric is chosen to be either SiO₂ deposited using RF sputtering or Al₂O₃ deposited using atomic layer deposition (ALD) system. Standard Ni/Au (50/150 nm) Schottky gate contacts are then deposited using e-beam evaporation system. Gate width of the AlGaIn/GaN MOS-HEMTs are kept to be 100 μm, whereas gate length (L_G) is set to two specific value of L_G = 2 μm and L_G = 1.5 μm. In effect, four different devices are fabricated for the Y-

parameter measurements – one each with SiO₂ and Al₂O₃ gate dielectric of channel length L_G = 2 μm and L_G = 1.5 μm. For each device, silicon nitride passivation dielectric (150 nm) is deposited using sputtering system. All the devices in this study has a gate to drain and gate to source distance of 6 μm and 2 μm, respectively.

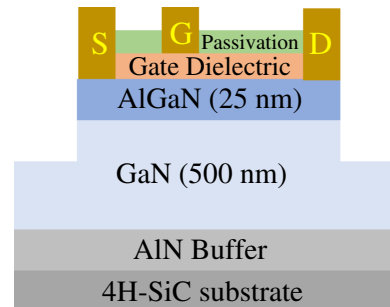


Fig. 3. Cross-sectional schematic of AlGaIn/GaN MOS-HEMTs investigated in this work.

S-parameter characterization of all the devices are carried out for a frequency range of [300 MHz, 50 GHz] (i.e., $f_{min} = 300$ MHz and $f_{max} = 50$ GHz) at drain-to-source voltage (V_{DS}) = 15 V and gate-to-source voltage (V_{GS}) is set to the value when trans-conductance is maximum measured using a network analyzer. A total of $N = 498$ sample points is used in the S-parameter characterization. Short circuit and open circuit tests were performed for the devices in order to evaluate the intrinsic S-parameters (or Y-parameters) based on open-short de-embedding method to eliminate the pad reactance and interconnect reactance [29], [30]. The S-parameters of the devices are then converted to the corresponding Y-parameters using MATLAB software with characteristic impedance of 50 Ω. Thus, the measured Y-parameters of all devices are available in the sampled data form of equation (17). Based on the Y-parameter data, the conventional SSEC model with constant circuit elements (Fig. 1) and the proposed frequency dependent SSEC model are realized. The procedure to extract constant circuit element values of the conventional SSEC model is detailed in [13], [14]. The values of individual circuit elements of conventional constant value SSEC model for all the AlGaIn/GaN MOS-HEMT structures of this work are provided in Table A1 of the Appendix section.

V. RESULTS AND DISCUSSION

A. Accuracy Comparison Between Conventional SSEC Model and Proposed Frequency Dependent SSEC Model

From the measured S-parameters of AlGaIn/GaN MOS-HEMTs, both the conventional SSEC model (Fig. 1) and the proposed frequency dependent SSEC model are developed using the methodology described in section II.A and section III, respectively. For the frequency dependent SSEC model, the individual circuit elements are represented using polynomial functions of frequency where the order K of the functions are progressively varied from the 0th order to 5th order. Note that the 0th order polynomial function

automatically refers to the conventional SSEC model of Fig. 1. For the polynomial functions of different orders, the Y-parameters of the AlGaIn/GaN MOS-HEMT devices are recovered using equation (20). Thereafter, the L2 error norm between the recovered Y-parameters of equation (20) and the measured Y-parameters of equation (17) are quantified as [31], [32]:

$$L_{\alpha\beta} = \frac{\sqrt{\sum_{u=1}^n (Y_{\alpha\beta}(data)_u - Y_{\alpha\beta}(model)_u)^2}}{N} \quad (21)$$

where $L_{\alpha\beta}$ is the L2 error norm for the entry in the (α, β) position of the Y-parameter matrix. The values of the L2 error norm calculated using equation (21) for the AlGaIn/GaN MOS-HEMTs with SiO₂ and Al₂O₃ as gate dielectrics ($L_G = 1.5 \mu\text{m}$) are shown in Fig. 4. In particular, Fig. 4 (a)-(d) illustrates the calculated L2 error norms for Y_{11} , Y_{12} , Y_{21} , and Y_{22} , respectively. It is noted from Fig. 4 that by increasing the order of the polynomial functions (K), the value of the L2 error norm decreases. Importantly, it is observed that as the order increases from $K = 0$ (i.e., the conventional SSEC model of Fig. 1) to $K = 2$, there is nearly an order of magnitude decrease in the error norm for most of the Y-parameters.

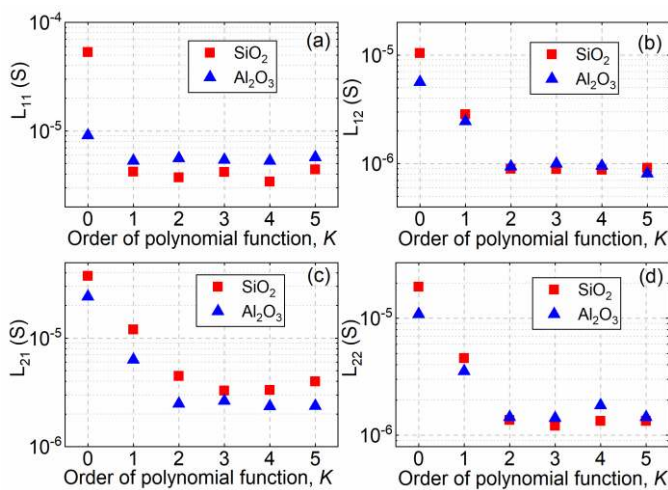


Fig. 4. L2 error norm of AlGaIn/GaN MOS-HEMTs for different gate dielectrics ($L_G = 1.5 \mu\text{m}$). (a)-(d) are L_{11} , L_{12} , L_{21} , and L_{22} , respectively for different order of polynomial functions (K).

However, increasing the order of the polynomial functions beyond $K = 2$ does not significantly improve the L2 error norm any further. This indicates a diminishing return on accuracy by increasing the order of the polynomial functions. A similar trend is also observed for AlGaIn/GaN MOS-HEMTs having SiO₂ and Al₂O₃ as gate dielectric having $L_G = 2 \mu\text{m}$, shown in Fig. A1 of the Appendix section. Thus, a 2nd order polynomial function representation of the circuit elements of the SSEC model is considered to provide a sufficiently accurate fit for the proposed frequency dependent SSEC element model. The observed values of the L2 error norm for the 0th order polynomials (i.e., the conventional SSEC model of Fig. 1) and the 2nd order polynomials (i.e., the

proposed frequency dependent SSEC model) for all the AlGaIn/GaN MOS-HEMTs used in this study is recorded in Table I. From the data of Table I, it is clear that the proposed model is significantly more accurate than the conventional SSEC model (Fig. 1) having constant element values.

TABLE I
L2 ERROR NORM FOR Y-PARAMETERS OF ALGAIN/GAN MOS-HEMTs WITH DIFFERENT GATE DIELECTRIC AND GATE LENGTH

Gate dielectric and L_G of AlGaIn/GaN MOS-HEMT	Order of the polynomial (K)	L_{11}	L_{12}	L_{21}	L_{22}
Dielectric: SiO ₂ $L_G = 1.5 \mu\text{m}$	0	52.9	10.4	37.3	18.5
	2	3.73	8.92	4.46	1.53
Dielectric: SiO ₂ $L_G = 2 \mu\text{m}$	0	25.9	15.2	18.9	20.5
	2	2.66	1.14	3.68	2.77
Dielectric: Al ₂ O ₃ $L_G = 1.5 \mu\text{m}$	0	9.08	5.6	24	10.8
	2	5.6	0.93	2.47	1.42
Dielectric: Al ₂ O ₃ $L_G = 2 \mu\text{m}$	0	63	10.9	6.65	20.4
	2	3.81	2.34	4.4	3.33

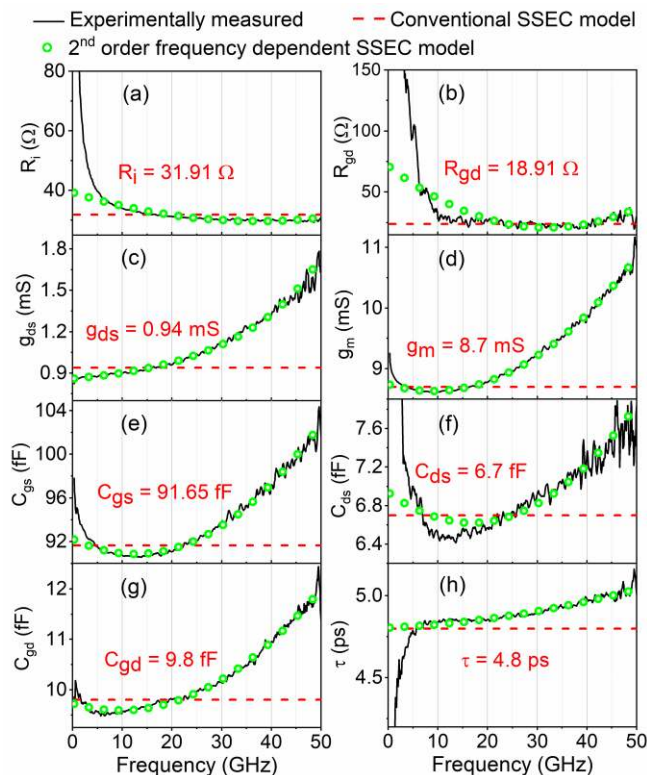


Fig. 5. Variations of the SSEC model circuit elements with frequency for AlGaIn/GaN MOSHEMT having SiO₂ as gate dielectric ($L_G = 1.5 \mu\text{m}$); (a)-(h) shows the variations in R_i , R_{gd} , g_{ds} , g_m , C_{gs} , C_{ds} , C_{gd} and τ , respectively.

The large L2 error norm observed for the conventional SSEC model (Fig. 1) in Table I is attributed to the inherent variation in the lumped circuit element values at high frequencies, as reported by other authors (see section II.B). Figure 5 (a)-(h) clearly illustrates this inherent variation in the

values of the circuit elements R_{ij} , R_{gd} , g_{ds} , g_m , C_{gs} , C_{ds} , C_{gd} and τ , respectively, as calculated from the experimentally measured Y-parameter data for the AlGaIn/GaN MOS-HEMT having SiO₂ as gate dielectric and $L_G = 1.5 \mu\text{m}$. Moreover, the constant circuit element values derived from the Y-parameters using equations (4)-(11) for the conventional SSEC model (Fig. 1) and the proposed frequency dependent SSEC model using 2nd order polynomials are also displayed in Fig. 5. From the comparison of Fig. 5, it is clear that all the lumped circuit element values vary with frequency of measurement. Thus, assuming constant values of these lumped circuit elements, as in the conventional SSEC model of Fig. 1, leads to significant deviation from the corresponding experimentally measured values. Especially at frequency beyond ~20 GHz, device transconductances (g_{ds} and g_m) and device capacitances (C_{gs} , C_{ds} and C_{gd}) are observed to be strong functions of frequency, as shown in Fig. 5 (c)-(g) respectively. Therefore, considering these circuit element values to be constant in the SSEC model of Fig. 1 is expected to result in a large L2 error norm when compared with the experimentally measured Y-parameters.

On contrary, considering the circuit element values to be functions of frequency is necessary to better fit the experimentally measured data to the SSEC model, especially beyond 20 GHz. As observed in Fig. 5, considering all the circuit element to be 2nd order function of frequency promises sufficient accuracy, as expected from Fig. 4. A similar trend is also observed for all AlGaIn/GaN MOS-HEMTs investigated in this work; corresponding graphs for AlGaIn/GaN MOS-HEMT having Al₂O₃ gate dielectric and $L_G = 1.5 \mu\text{m}$ is shown in Fig. A2 of the Appendix section. Thus, a lower error between the experimentally observed data and SSEC model is obtained by considering all the circuit elements to be 2nd order polynomial functions of frequency. A higher order polynomial function representation for the lumped circuit elements may be necessary for AlGaIn/GaN MOS-HEMTs with shorter channel lengths and higher frequency of measurements. At this point, further studies are necessary to determine exact reasons behind the non-linear change in lumped circuit element values with an increase in frequency of measurement.

Next, the comparison of the Y-parameters predicted by the conventional SSEC model (Fig. 1) and the proposed frequency dependent SSEC model using 2nd order polynomials for AlGaIn/GaN MOS-HEMT with SiO₂ gate dielectric ($L_G = 1.5 \mu\text{m}$) up to 50 GHz is shown in Fig. 6. Experimentally measured Y-parameter data is also included in Fig. 6 and had a bias condition of $V_{GS} = -5 \text{ V}$ and $V_{DS} = 15 \text{ V}$. The corresponding graphs for AlGaIn/GaN MOS-HEMT with Al₂O₃ gate dielectric ($L_G = 1.5 \mu\text{m}$) is shown in Fig. A3 of the Appendix section. Interestingly, the conventional SSEC model of Fig. 1 is observed to closely match the phase of experimentally measured Y-parameters. However, this comes at the cost of a significant deviation in magnitude of Y-parameters beyond ~20 GHz – something that is not seen using the proposed frequency dependent SSEC model. Similar results are also observed between the experimentally measured data and the predicted data for AlGaIn/GaN MOS-HEMT with Al₂O₃ as gate dielectric and varying channel lengths (images

shown in the Appendix section). Therefore, Fig. 6 clearly demonstrates the improved accuracy provided by the frequency dependent SSEC model over the conventional SSEC model of Fig. 1 at millimeter wave frequencies.

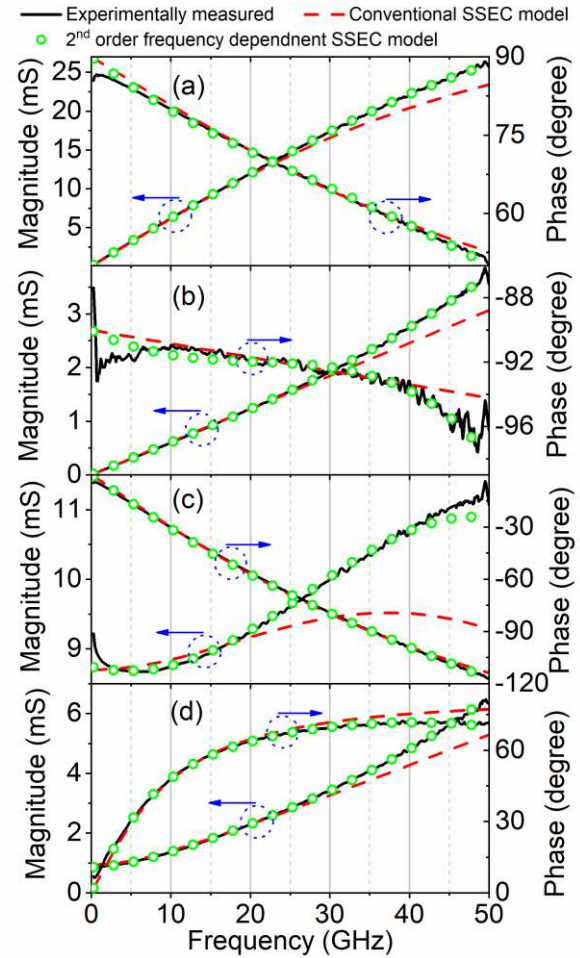


Fig. 6. Y-parameters of AlGaIn/GaN MOS-HEMT with SiO₂ as gate dielectric ($L_G = 1.5 \mu\text{m}$); (a)-(d) shows Y_{11} , Y_{12} , Y_{21} and Y_{22} , respectively.

B. Gain Analysis for Conventional SSEC Model and Frequency Dependent SSEC Model

In Section III-A, the improvement in accuracy provided by the proposed frequency dependent SSEC model over the conventional SSEC model of Fig. 1 for AlGaIn/GaN MOS-HEMT devices has been verified. This improvement in accuracy translates to improvement in estimating the unity gain frequency (f_i) and maximum frequency of oscillation (f_{max}) of the devices. Traditionally, f_i and f_{max} are derived from the current gain (H_{21}) and unilateral gain (U) given as [33]:

$$H_{21} = \frac{-2S_{21}}{(1 - S_{11})(1 + S_{22}) + S_{12}S_{21}} \quad (22)$$

$$U = \frac{\left| \frac{S_{21}}{S_{12}} - 1 \right|^2}{2\chi \left| \frac{S_{21}}{S_{12}} \right| - 2 \operatorname{Re} \left(\frac{S_{21}}{S_{12}} \right)} \quad (23)$$

where S_{11} , S_{12} , S_{21} , and S_{22} represents the S-parameters of the AlGaIn/GaN MOS-HEMT, and

$$\chi = \frac{1 - |S_{11}|^2 - |S_{22}|^2 + |S_{11}S_{22} - S_{12}S_{21}|^2}{2|S_{12}S_{21}|} \quad (24)$$

. The x-intercept of $20\log(|H_{21}|)$ vs frequency plot and $20\log(|U|)$ vs frequency plot yields the values of f_i and f_{max} , respectively. The S-parameters predicted by the conventional SSEC model of Fig. 1 is derived from the predicted Y-parameters obtained from equation (2). Similarly, the S-parameters predicted by the frequency dependent SSEC model is derived from the predicted Y-parameters obtained from equation (20).

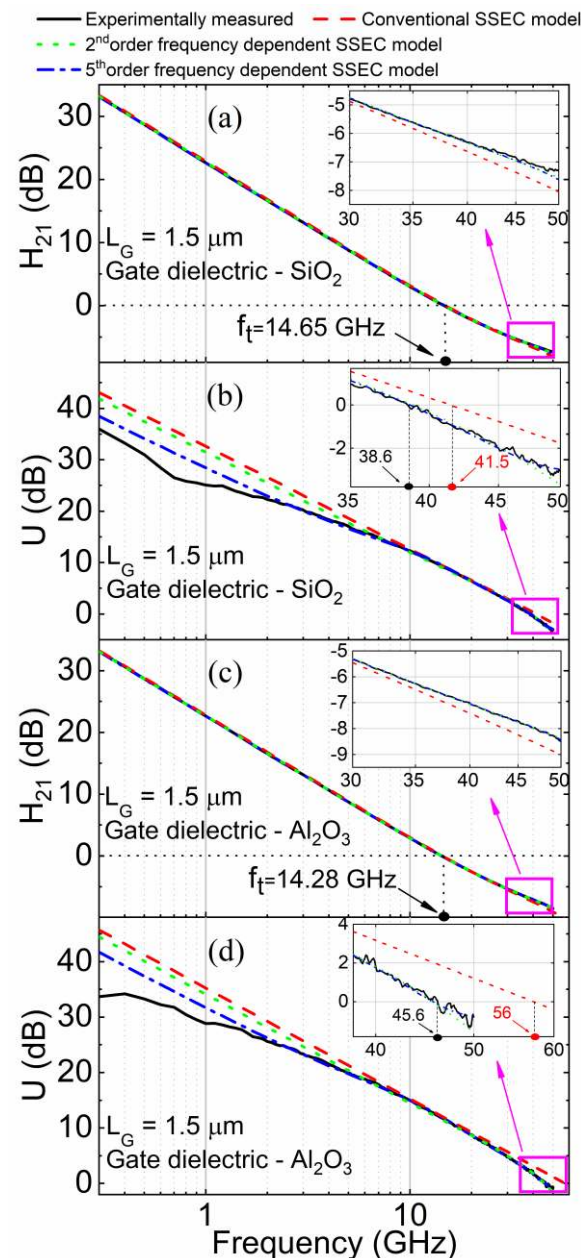


Fig. 7. Gain plots for AlGaIn/GaN MOS-HEMTs: (a) and (c) show the variations in H_{21} ; and (b) and (d) show the variations in U with frequency.

Next, the quantities H_{21} and U are calculated from the experimentally measured data, the conventional SSEC model of Fig. 1, and the proposed frequency dependent SSEC model for the AlGaIn/GaN MOS-HEMT devices (SiO₂ and Al₂O₃ gate dielectric, $L_G = 1.5 \mu\text{m}$) are displayed in Fig. 7. For the frequency range of 300 MHz to ~ 10 GHz, the slope of $20\log(|H_{21}|)$ vs frequency plot is ~ 20 dB indicating negligible frequency dispersion, as observed in Fig. 7 (a) and (c). However, for frequency above 10 GHz, the values of the corresponding slope increases indicating a high frequency capacitance dispersion. This increased slope is far better captured by the proposed frequency dependent SSEC model than the conventional SSEC model of Fig. 1 as can be observed in the insets of Fig. 7 (a) and Fig. 7 (c). Recent reports also indicate a similar failure of constant value lumped element based SSEC models to fit the experimentally measured H_{21} values in AlGaIn/GaN HEMTs having high f_i and f_{max} values [34]. Furthermore, the values of f_{max} (which lie beyond f_i in the higher frequency range) is largely over-estimated by the conventional SSEC model of Fig. 1 for all the AlGaIn/GaN MOS-HEMTs under investigation, corresponding graphs for AlGaIn/GaN MOS-HEMTs with SiO₂ and Al₂O₃ gate dielectric ($L_G = 1.5 \mu\text{m}$) are shown in Fig. 7 (b) and (d), respectively. On contrary, the proposed frequency dependent SSEC model shows very good agreement with the experimentally measured data when estimating f_{max} .

TABLE II
VALUES OF f_t AND f_{max} CALCULATED FOR ALGAN/GAN MOS-HEMT HAVING DIFFERENT GATE DIELECTRIC AND GATE LENGTH

Devices	Y-parameters	f_t (GHz)	f_{max} (GHz)
Dielectric: SiO ₂ $L_G = 1.5 \mu\text{m}$	Experimentally measured	14.65	38.6
	Conventional SSEC model	14.9	41.5
	Frequency dependent SSEC model ($K=2$)	14.63	40
Dielectric: SiO ₂ $L_G = 2 \mu\text{m}$	Experimentally measured	9.76	29.6
	Conventional SSEC model	9.94	32.4
	Frequency dependent SSEC model ($K=2$)	9.75	29.35
Dielectric: Al ₂ O ₃ $L_G = 1.5 \mu\text{m}$	Experimentally measured	14.28	45.6
	Conventional SSEC model	14.5	56
	Frequency dependent SSEC model ($K=2$)	14.33	45
Dielectric: Al ₂ O ₃ $L_G = 2 \mu\text{m}$	Experimentally measured	10.01	40.4
	Conventional SSEC model	10	45.96
	Frequency dependent SSEC model ($K=2$)	10	41.5

The ability of the proposed frequency dependent SSEC model to outperform the conventional SSEC model of Fig. 1 is also evident from the calculated values of f_i and f_{max} as listed in Table II. From Table II, it is seen that the error in the value of f_{max} estimated by the conventional SSEC model of Fig. 1 is as high as $\sim 18.6\%$ for AlGaIn/GaN MOS-HEMT having

Al₂O₃ dielectric and L_G = 1.5 μm. Such a large error in estimation is not acceptable for high frequency application. On contrary, the values of f_i and f_{max} estimated by the proposed frequency dependent SSEC model using a 2nd order polynomial function closely matches the experimentally measured f_i and f_{max} values. Increasing the order of the polynomial function better fits the experimentally measured values at lower frequency as well as higher frequency, as shown in Fig. 7. Note that the experimentally measured S-parameters in this work is up to 50 GHz; a higher order polynomial function may be necessary if the f_i and/or f_{max} values are further extended beyond this 50 GHz range. Thus, representing all the SSEC model elements of AlGaIn/GaN MOS-HEMTs as a function of frequency is necessary to correctly fit the experimentally measured S-parameters and better estimate f_i and f_{max} values.

C. Discussion

The results shown in section V.A and V.B indicate that the frequency dependent SSEC model proposed in this work is a generic model that can be applied to any electronic device (such as MOSFETs, multi-finger HEMTs or fin-FETs [35], [36], MOS-HEMTs, etc.) whose SSEC resembles the topology shown in Fig. 1. The end user is required to find the maximum order of the polynomial functions representing the SSEC model circuit elements that best fits the experimentally measured Y-parameters. Typically, this order is small as seen from these examples. In contrast, if the Y-parameters are directly expressed as polynomials functions of frequency and the coefficients are calculated using equation (18), then a much larger order would be required. Moreover, the resultant matrix A equivalent to equation (18) will be highly ill-conditioned. Consequently, calculating the inverse of $A^T A$ will be highly inaccurate, and in many cases, downright impossible [37], [38]. Thus, directly representing the Y-parameters in terms of polynomial function is not a valid alternative to the proposed model.

It is noted that the dependency of C_{gs} or C_{gd} values with respect to frequency is obtained from experimentally measured Y-parameters. This dependency is due to both dispersive effects as well as the effect of residual parasitics that may not have been completely removed during de-embedding. For the chosen examples of this work, this dependency seems linear in the high frequency region. However, such dependency may not necessarily remain linear as we further increase the frequency or vary the AlGaIn/GaN MOS-HEMT device geometry. For other circuit elements, especially resistive/conductive circuit elements, dielectric losses and residual capacitive/inductive parasitics are responsible for the variation of their values with respect to frequency. Now, it is observed from Fig. 5 that different circuit elements show different levels and trajectories of variation with respect to frequency. Thus, to capture this different level of variation and trajectories, different orders of polynomials can also be adopted for different circuit elements.

Furthermore, as observed in Fig. 6, both the conventional

SSEC model and frequency dependent SSEC model fail to capture some of the Y-parameter variations at broadband frequency range, especially in the low frequency region. This indicates that the conventional SSEC model of Fig. 1 may require minor modifications such as addition of inductive and/or capacitive elements to better capture the AlGaIn/GaN MOS-HEMT behavior over broadband frequency range. However, such modification invalidates the expression of the Y-parameters in equation (2) and the closed-form equations of equation (4)-(11). This work is dedicated to correctly predict the Y-parameters using the conventional SSEC model topology of Fig. 1. Hence any modification to the conventional SSEC model of Fig. 1 is avoided in this work.

VI. CONCLUSION

Traditionally used lumped small signal equivalent circuit models for AlGaIn/GaN metal oxide semiconductor high electron mobility transistors (MOS-HEMTs) implicitly assume the values of the circuit elements to be constant. Thus, these models fail to capture the variability in the circuit element values at high frequencies resulting in low model accuracy. Therefore, in this work, a modified lumped small signal equivalent circuit model is proposed where the value of the circuit elements making up the model are treated as frequency dependent polynomial functions. This frequency dependent lumped element small signal equivalent circuit model can better capture the experimentally measured Y-parameters of AlGaIn/GaN MOS-HEMTs, especially in the high frequency region (i.e., greater than 20 GHz). Moreover, in this work, a least-squares based curve-fitting approach has been developed to offline estimate the coefficients of the polynomials from the experimentally measured Y-parameter data. The proposed model is sufficiently generic and can be applied to different devices such as MOSFETs, HEMTs, and MOS-HEMTs by varying the order of the polynomial function appropriately.

APPENDIX

Appendix section consist of Table A1, Fig. A1, Fig. A2 and Fig. A3. Table A1 lists the values of all circuit elements derived for the conventional SSEC model for AlGaIn/GaN MOS-HEMTs having different gate dielectrics and L_G.

TABLE A1
CONVENTIONAL SSEC MODEL ELEMENTS VALUES EXTRACTED FROM EXPERIMENTALLY MEASURED DATA FOR ALGAIN/GAN MOS-HEMTS

Intrinsic elements	SiO ₂		Al ₂ O ₃	
	L _G =1.5 μm	L _G =2 μm	L _G =1.5 μm	L _G =2 μm
C _{gs} (fF)	91.65	111.6	135.6	140
C _{gd} (fF)	9.80	10	9.66	8
C _{ds} (fF)	6.7	5.95	6.8	5
g _m (mS)	8.7	7.2	12.5	9
g _{ds} (mS)	0.94	0.75	0.76	0.5
R _i (Ω)	31.91	34.4	21.63	24
R _{dg} (Ω)	18.91	18.19	2	4
τ (ps)	4.8	6.5	4.8	6.2

Figure A1 highlights the values of the L2 error norm calculated using equation (21) for the AlGaIn/GaN MOS-HEMTs with SiO₂ and Al₂O₃ as gate dielectrics ($L_G = 2 \mu\text{m}$).

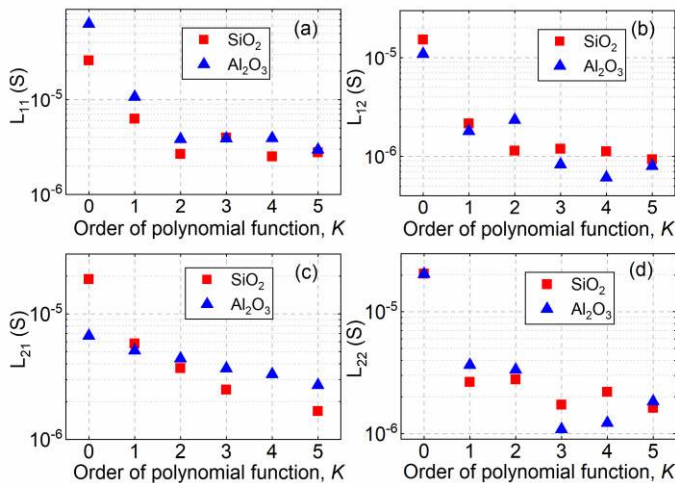


Fig. A1. L2 error norm of AlGaIn/GaN MOS-HEMTs for different gate dielectrics ($L_G = 2 \mu\text{m}$). (a)-(d) are L_{11} , L_{12} , L_{21} and L_{22} , respectively for different order of polynomial functions (K).

Similarly, Fig. A2 represents the variations in the values of circuit elements of the conventional SSEC model with frequency for AlGaIn/GaN MOS-HEMT having Al₂O₃ as gate dielectric ($L_G = 1.5 \mu\text{m}$).

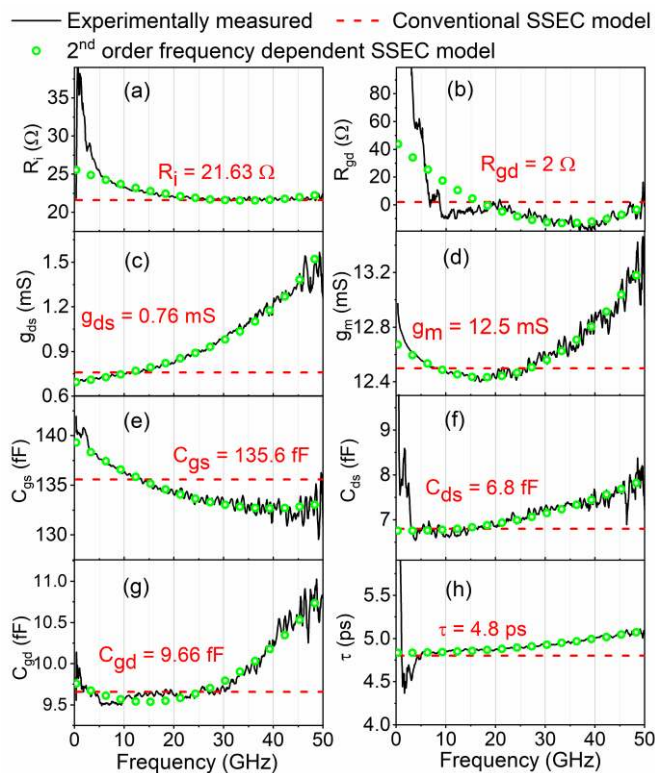


Fig. A2. Variations of the SSEC model circuit elements with frequency for AlGaIn/GaN MOS-HEMTs ($L_G = 1.5 \mu\text{m}$) having Al₂O₃ as gate dielectric and; (a)-(h) shows the variations in R_i , R_{gd} , g_{ds} , g_m , C_{gs} , C_{ds} , C_{gd} and τ , respectively.

Finally, Fig. A3 illustrates the comparison of the Y-

parameters predicted by the conventional SSEC model (Fig. 1) and the proposed frequency dependent SSEC model using 2nd order polynomials for AlGaIn/GaN MOS-HEMT with Al₂O₃ gate dielectric ($L_G = 1.5 \mu\text{m}$) up to 50 GHz.

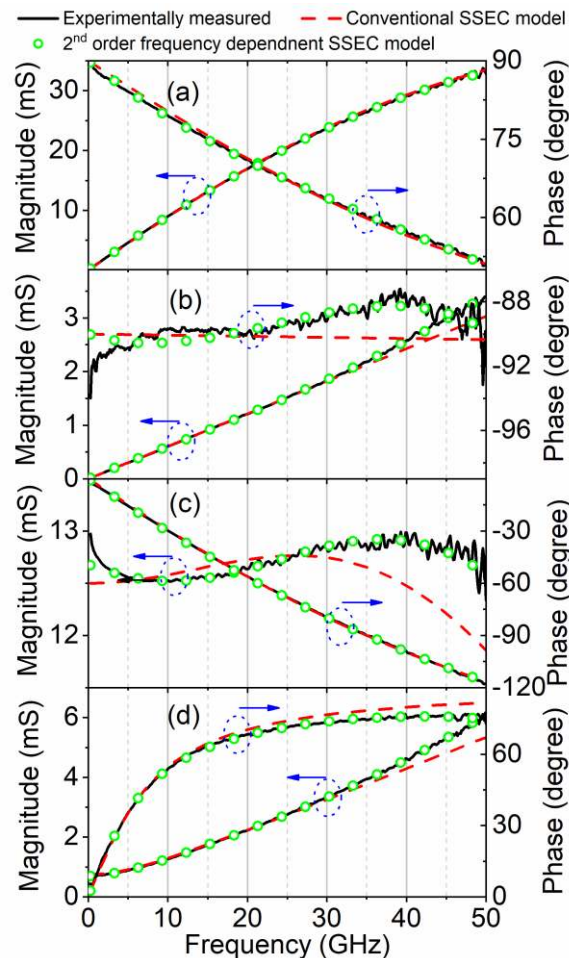


Fig. A3. Y-parameters of AlGaIn/GaN MOS-HEMT with Al₂O₃ as gate dielectric ($L_G = 1.5 \mu\text{m}$); (a)-(d) shows Y_{11} , Y_{12} , Y_{21} and Y_{22} , respectively.

REFERENCES

- [1] R. Sun, J. Lai, W. Chen, and B. Zhang, "GaN Power Integration for High Frequency and High Efficiency Power Applications: A Review," *IEEE Access*, vol. 8, pp. 15529–15542, 2020, doi: 10.1109/ACCESS.2020.2967027.
- [2] J. J. Komiak, "GaN HEMT: Dominant Force in High-Frequency Solid-State Power Amplifiers," *IEEE Microwave Magazine*, vol. 16, no. 3, pp. 97–105, Apr. 2015, doi: 10.1109/MMM.2014.2385303.
- [3] V. Sandeep, J. C. Pravin, A. R. Babu, and P. Prajoun, "Impact of AlInN Back-Barrier Over AlGaIn/GaN MOS-HEMT With HfO₂ Dielectric Using Cubic Spline Interpolation Technique," *IEEE Transactions on Electron Devices*, vol. 67, no. 9, pp. 3558–3563, Sep. 2020, doi: 10.1109/TED.2020.3010710.
- [4] C. Lee, Y. Chiou, and C. Lee, "AlGaIn/GaN MOS-HEMTs With Gate ZnO Dielectric Layer," *IEEE Electron Device Letters*, vol. 31, no. 11, pp. 1220–1223, Nov. 2010, doi: 10.1109/LED.2010.2066543.
- [5] K. Geng, D. Chen, Q. Zhou, and H. Wang, "AlGaIn/GaN MIS-HEMT with PECVD SiN_x, SiON, SiO₂ as Gate Dielectric and Passivation Layer," *Electronics*, vol. 7, no. 12, Art. no. 12, Dec. 2018, doi: 10.3390/electronics7120416.
- [6] E. Miyazaki, Y. Goda, S. Kishimoto, and T. Mizutani, "Comparative study of AlGaIn/GaN metal-oxide-semiconductor heterostructure field-effect transistors with Al₂O₃ and HfO₂ gate oxide," *Solid-State*

- Electronics*, vol. 62, no. 1, pp. 152–155, Aug. 2011, doi: 10.1016/j.sse.2011.04.017.
- [7] Y. C. Chang *et al.*, “Atomic-layer-deposited Al₂O₃ and HfO₂ on GaN: A comparative study on interfaces and electrical characteristics,” *Microelectronic Engineering*, vol. 88, no. 7, pp. 1207–1210, Jul. 2011, doi: 10.1016/j.mee.2011.03.098.
- [8] T. J. Brazil, “Simulating circuits and devices,” *IEEE Microwave Magazine*, vol. 4, no. 1, pp. 42–50, Mar. 2003, doi: 10.1109/MMW.2003.1188235.
- [9] Y. Karisan, C. Caglayan, G. C. Trichopoulos, and K. Sertel, “Lumped-Element Equivalent-Circuit Modeling of Millimeter-Wave HEMT Parasitics Through Full-Wave Electromagnetic Analysis,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 64, no. 5, pp. 1419–1430, May 2016, doi: 10.1109/TMTT.2016.2549520.
- [10] G. Crupi *et al.*, “High-Frequency Extraction of the Extrinsic Capacitances for GaN HEMT Technology,” *IEEE Microwave and Wireless Components Letters*, vol. 21, no. 8, pp. 445–447, Aug. 2011, doi: 10.1109/LMWC.2011.2160525.
- [11] G. Crupi, A. Raffo, G. Avolio, D. M. M.-P. Schreurs, G. Vannini, and A. Caddemi, “Temperature Influence on GaN HEMT Equivalent Circuit,” *IEEE Microwave and Wireless Components Letters*, vol. 26, no. 10, pp. 813–815, Oct. 2016, doi: 10.1109/LMWC.2016.2601487.
- [12] C. Pavageau *et al.*, “A 7-dB 43-GHz CMOS Distributed Amplifier on High-Resistivity SOI Substrates,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 56, no. 3, pp. 587–598, Mar. 2008, doi: 10.1109/TMTT.2008.916930.
- [13] S. A. Ahsan, S. Ghosh, S. Khandelwal, and Y. S. Chauhan, “Physics-Based Multi-Bias RF Large-Signal GaN HEMT Modeling and Parameter Extraction Flow,” *IEEE Journal of the Electron Devices Society*, vol. 5, no. 5, pp. 310–319, Sep. 2017, doi: 10.1109/JEDS.2017.2724839.
- [14] Ickjin Kwon, Minkyu Je, Kwiro Lee, and Hyungcheol Shin, “A simple and analytical parameter-extraction method of a microwave MOSFET,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 50, no. 6, pp. 1503–1509, Jun. 2002, doi: 10.1109/TMTT.2002.1006411.
- [15] V. Kilchytyska *et al.*, “Frequency Variation of the Small-Signal Output Conductance of Decanometer MOSFETs Due to Substrate Crosstalk,” *IEEE Electron Device Letters*, vol. 28, no. 5, pp. 419–421, May 2007, doi: 10.1109/LED.2007.895374.
- [16] R. Valentin *et al.*, “RF Small-Signal Analysis of Schottky-Barrier p-MOSFET,” *IEEE Transactions on Electron Devices*, vol. 55, no. 5, pp. 1192–1202, May 2008, doi: 10.1109/TED.2008.919382.
- [17] R. G. Brady, C. H. Oxley, and T. J. Brazil, “An Improved Small-Signal Parameter-Extraction Algorithm for GaN HEMT Devices,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 56, no. 7, pp. 1535–1544, Jul. 2008, doi: 10.1109/TMTT.2008.925212.
- [18] Y. Wu, Q. Wang, J. Liu, C. Zhao, H. Tang, and K. Kang, “An Improved Small-Signal Equivalent Circuit Model Considering Channel Current Magnetic Effect,” *IEEE Microwave and Wireless Components Letters*, vol. 28, no. 9, pp. 804–806, Sep. 2018, doi: 10.1109/LMWC.2018.2850895.
- [19] R. Sung, P. Bendix, and M. B. Das, “Extraction of high-frequency equivalent circuit parameters of submicron gate-length MOSFET’s,” *IEEE Transactions on Electron Devices*, vol. 45, no. 8, pp. 1769–1775, Aug. 1998, doi: 10.1109/16.704377.
- [20] Y. Tang, L. Zhang, and Y. Wang, “Accurate small signal modeling and extraction of silicon MOSFET for RF IC application,” *Solid-State Electronics*, vol. 54, no. 11, pp. 1312–1318, Nov. 2010, doi: 10.1016/j.sse.2010.06.025.
- [21] F. Y. Huang, X. S. Tang, Z. N. Wei, L. H. Zhang, and N. Jiang, “An Improved Small-Signal Equivalent Circuit for GaN High-Electron Mobility Transistors,” *IEEE Electron Device Letters*, vol. 37, no. 11, pp. 1399–1402, Nov. 2016, doi: 10.1109/LED.2016.2609462.
- [22] A. Jarndal and G. Kompa, “A new small-signal modeling approach applied to GaN devices,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 53, no. 11, pp. 3440–3448, Nov. 2005, doi: 10.1109/TMTT.2005.857332.
- [23] J. R. Shealy, J. Wang, and R. Brown, “Methodology for Small-Signal Model Extraction of AlGaN HEMTs,” *IEEE Transactions on Electron Devices*, vol. 55, no. 7, pp. 1603–1613, Jul. 2008, doi: 10.1109/TED.2008.925335.
- [24] W.- Tang, C.- Wang, and Y.- Hsin, “A New Extraction Technique for the Complete Small-Signal Equivalent-Circuit Model of InGaP/GaAs HBT Including Base Contact Impedance and AC Current Crowding Effect,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 54, no. 10, pp. 3641–3647, Oct. 2006, doi: 10.1109/TMTT.2006.882411.
- [25] N. Rorsman, M. Garcia, C. Karlsson, and H. Zirath, “Accurate small-signal modeling of HFET’s for millimeter-wave applications,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 44, no. 3, pp. 432–437, Mar. 1996, doi: 10.1109/22.486152.
- [26] Da-Zheng Feng, Zheng Bao, and Li-Cheng Jiao, “Total least mean squares algorithm,” *IEEE Transactions on Signal Processing*, vol. 46, no. 8, pp. 2122–2130, Aug. 1998, doi: 10.1109/78.705421.
- [27] Q. Fan, J. H. Leach, and H. Morkoc, “Small Signal Equivalent Circuit Modeling for AlGaN/GaN HFET: Hybrid Extraction Method for Determining Circuit Elements of AlGaN/GaN HFET,” *Proceedings of the IEEE*, vol. 98, no. 7, pp. 1140–1150, Jul. 2010, doi: 10.1109/JPROC.2010.2044630.
- [28] Y. Cao, W. Zhang, J. Fu, Q. Wang, L. Liu, and A. Guo, “A Complete Small-Signal MOSFET Model and Parameter Extraction Technique for Millimeter Wave Applications,” *IEEE Journal of the Electron Devices Society*, vol. 7, pp. 398–403, 2019, doi: 10.1109/JEDS.2019.2900202.
- [29] L. F. Tiemeijer, R. J. Havens, A. B. M. Jansman, and Y. Boutement, “Comparison of the ‘pad-open-short’ and ‘open-short-load’ deembedding techniques for accurate on-wafer RF characterization of high-quality passives,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 53, no. 2, pp. 723–729, Feb. 2005, doi: 10.1109/TMTT.2004.840621.
- [30] J. Luo, L. Zhang, and Y. Wang, “A Distributed De-Embedding Solution for CMOS mm-Wave On-Wafer Measurements Based-on Double Open-Short Technique,” *IEEE Microwave and Wireless Components Letters*, vol. 23, no. 12, pp. 686–688, Dec. 2013, doi: 10.1109/LMWC.2013.2284773.
- [31] Y. Liu, Q. Gao, X. Gao, and L. Shao, “ \mathcal{L}_2 -Norm Discriminant Manifold Learning,” *IEEE Access*, vol. 6, pp. 40723–40734, 2018, doi: 10.1109/ACCESS.2018.2859299.
- [32] R. Wang, J. Han, B. F. Cockburn, and D. G. Elliott, “Stochastic Circuit Design and Performance Evaluation of Vector Quantization for Different Error Measures,” *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 24, no. 10, pp. 3169–3183, Oct. 2016, doi: 10.1109/TVLSI.2016.2535313.
- [33] D. M. Pozer, *Microwave Engineering*, 4th Edition. John Wiley & Sons Ltd., 2011.
- [34] K. Shinohara *et al.*, “Scaling of GaN HEMTs and Schottky Diodes for Submillimeter-Wave MMIC Applications,” *IEEE Transactions on Electron Devices*, vol. 60, no. 10, pp. 2982–2996, Oct. 2013, doi: 10.1109/TED.2013.2268160.
- [35] G. Crupi, A. Raffo, G. Avolio, D. M. M.-P. Schreurs, G. Vannini, and A. Caddemi, “Temperature Influence on GaN HEMT Equivalent Circuit,” *IEEE Microwave and Wireless Components Letters*, vol. 26, no. 10, pp. 813–815, Oct. 2016, doi: 10.1109/LMWC.2016.2601487.
- [36] G. Crupi, D. Schreurs, B. Parvais, A. Caddemi, A. Mercha, and S. Decoutere, “Scalable and multibias high frequency modeling of multi-finger FETs,” *Solid-State Electronics*, vol. 50, no. 11, pp. 1780–1786, Nov. 2006, doi: 10.1016/j.sse.2006.09.006.
- [37] W. T. Beyene and J. Schutt-Aine, “Accurate frequency-domain modeling and efficient circuit simulation of high-speed packaging interconnects,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 45, no. 10, pp. 1941–1947, Oct. 1997, doi: 10.1109/22.641798.
- [38] W. T. Beyene and J. E. Schutt-Aine, “Efficient transient simulation of high-speed interconnects characterized by sampled data,” *IEEE Transactions on Components, Packaging, and Manufacturing Technology: Part B*, vol. 21, no. 1, pp. 105–114, Feb. 1998, doi: 10.1109/96.659513.