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*Published in:*  
Proceedings of EPE '13-ECCE Europe

*Publication date:*  
2013

[Link back to DTU Orbit](#)

*Citation (APA):*  
Thummala, P., Zhang, Z., & Andersen, M. A. E. (2013). High Voltage Bi-directional Flyback Converter for Capacitive Actuator. In *Proceedings of EPE '13-ECCE Europe* IEEE.

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# High Voltage Bi-directional Flyback Converter for Capacitive Actuator

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## Keywords

«High voltage power converters», «Switched-mode power supply », «Energy efficiency», «Actuator», «Electroactive materials».

## Abstract

This paper presents a high voltage DC-DC converter topology for bi-directional energy transfer between a low voltage DC source and a high voltage capacitive load. The topology is a bi-directional flyback converter with variable switching frequency control during the charge mode, and constant switching frequency control during the discharge mode. The converter is capable of charging the capacitive load from 24 V DC source to 2.5 kV, and discharges it to 0 V. The flyback converter has been analyzed in detail during both charge and discharge modes, by considering all the parasitic elements in the converter, including the most dominating parameters of the high voltage transformer viz., self-capacitance and leakage inductance. The specific capacitive load for this converter is a dielectric electro active polymer (DEAP) actuator, which can be used as an effective replacement for conventional actuators in a number of applications. In this paper, the discharging energy efficiency definition is introduced. The proposed converter has been experimentally tested with the film capacitive load and the DEAP actuator, and the experimental results are shown together with the efficiency measurements.

## Introduction

High voltage power converters are widely used in medical, airborne and space applications with voltage range from 5 kV to 100 kV and with a power level of few tens of watts to several hundred kilowatts. A number of applications require a capacitive load to be charged to several thousands of volts. Such applications include pulsed lasers, pulsed sonar equipment, photo flash systems, electric fences, and plasma research, which require high voltage DC power supplies to efficiently charge a large capacitive load to high voltage. Design of efficient high voltage power supplies is very vital in terms of selection of converter configuration, switching frequency, and control strategy. All these vital aspects are very closely related to the high voltage transformer used in the power converter.

The focus of our research is to develop high voltage DC power supplies for DEAP actuators, which are a special type of capacitive actuators made with dielectric electro active material, and require high voltage (~2.5 kV) at relatively low current, to fully actuate them. The dielectric electro active polymer is a thin silicon elastomer film (~20-80  $\mu\text{m}$ ) sandwiched between two metallic electrodes. Due to requirement of very high electric field strength of  $>40 \text{ V}/\mu\text{m}$ , the material needs high operating voltage to completely elongate the actuator [1]. DEAP technology has a wide potential in actuator, energy harvesting and sensor applications [2], [3] due to the material unique properties such as light weight, very low electrical power consumption, flexible nature, low noise and higher performance than competing technologies. DEAP actuators have some interesting applications in linear incremental motors, loud speakers, in-line heating valves, and wind turbine flaps, etc. [4], [5].

High voltage switch-mode power supplies for charging the capacitive loads have been implemented in [4]-[8]. In most of the papers the high output voltage was discharged by using a resistive load [7], an active discharge circuit [8], or with the high voltage probes (4 kV, 50 M $\Omega$ , <6 pF) [4], [5]. With these discharging methods, the reactive energy stored in the load capacitance is lost. If the output energy stored in the capacitive load is transferred back to the source, the lifetime of the source can be increased for battery powered applications. The energy stored in the load can be transferred to the source by discharging the capacitive load using a bi-directional DC-DC converter. The bi-directional flyback converters proposed in [9]-[11] perform the synchronous bi-directional operation to transfer the power in both directions. The operation and control of bi-directional flyback converter for charging and discharging a DEAP actuator is different from that of [9]-[11]. For this application, a new bi-directional flyback converter has been proposed and implemented to transfer the energy in both directions. It is to be noted that bi-directional in this application means either the energy flows from the source to the load or vice versa, but not at the same time.

**Table I: Specifications of the proposed bi-directional flyback converter**

Parameter	Target	Achieved
Input voltage ( $V_{in}$ )	24 V	10-24 V
Output voltage ( $V_{out}$ )	0-2500 V	0-2500 V
Peak charging efficiency ( $\eta_{cpk}$ )	~ 90 %	~ 85 %
Peak discharging efficiency ( $\eta_{dpk}$ )	~ 80 %	~ 79 %
117 nF capacitive load charging time to charge from 0 V to 2.5 kV ( $t_{ch1}$ )	20 ms	21 ms
DEAP actuator charging time to charge from 0 V to 2.5 kV ( $t_{ch2}$ )	20 ms	22 ms
117 nF capacitive load discharging time to discharge from 2.5 kV to 0 V ( $t_{dch1}$ )	20 ms	16 ms
DEAP actuator discharging time to discharge from 2.5 kV to 0 V ( $t_{dch2}$ )	20 ms	17 ms
DEAP actuator capacitance ( $C_{DEAP}$ )	NA	117 nF
Charging switching frequency ( $f_{swe}$ )	NA	~ 10-60 kHz
Discharging switching frequency ( $f_{swd}$ )	NA	~ 25.6 kHz
Primary peak current during charging process ( $I_{ppk\ ch}$ ) / discharging process ( $I_{ppk\ dch}$ )	NA	4.24 A / 5.3 A
Stored energy in the capacitive load @ 2.5 kV	NA	0.37 J [Equivalent to 16 W charging power during 21 ms, and 22 W discharging power during 16 ms]

This paper focuses on the accurate analysis, design, component selection, and implementation of the 0-2.5 kV, low power bi-directional flyback converter. This paper illustrates a hard switched flyback converter topology that is capable of operating with reasonably good charging and discharging energy efficiencies over a wide operating output voltage range. The specifications of the bi-directional flyback converter are provided in Table I.

## Converter design and analysis

In this section the high voltage bi-directional DC-DC converter, shown in Fig. 1 is discussed. High voltage unidirectional flyback converter for a normal resistive load is analyzed in [12] without considering all parasitic elements in the flyback converter. The converter operation during the charge and the discharge modes including all parasitic elements is analyzed in detail in this paper. Loss modeling of the proposed converter has been performed in [13] using the proposed accurate analysis. The approximated model of the flyback transformer from [14] either referred to primary or secondary is used in this paper for the analysis in each stage of charging and discharging processes (Fig. 3).

## Topology

Flyback converter topology had been widely used because of its relatively simple structure and better performance for single or multiple output applications. It can save the cost and volume compared with the other converters. Flyback converter is a well-suited topology for low power ( $< 200$  W) and high output voltage applications. Some high voltage applications of flyback converter are in cathode ray tube (CRT) for televisions, monitors and in xenon flash lamps for exciting the xenon gas.

The power stage of Fig. 1a can be divided into three parts: primary stage, high voltage flyback transformer and secondary stage. The primary stage consists of a DC voltage source, input capacitor  $C_{in}$ , and low voltage MOSFET  $M_1$ . The flyback transformer with  $1:n$  turns ratio is provided with an air gap, to store the energy in the magnetizing winding when the switch  $M_1$  is turned ON. In the transformer model shown in Fig. 1b,  $L_{mp}$ ,  $C_p$ ,  $C_{int}$ ,  $C_s$ ,  $L_{lkp}$ ,  $L_{lks}$ ,  $R_p$ , and  $R_s$  represent the primary magnetizing inductance, primary self-capacitance, interwinding capacitance, secondary self-capacitance, primary and secondary leakage inductances, primary DC resistance, and secondary DC resistances, respectively. The secondary stage consists of high voltage MOSFET  $M_2$ , high voltage diode  $D_2$ , which is required as the body diode of  $M_2$  has very high reverse recovery time ( $t_{rr}=2.8 \mu s$ ), another high voltage diode  $D_b$  added in series with the drain of  $M_2$ , to block the body diode of  $M_2$  during the discharging process, and a high voltage capacitive load  $C_{out}$ .

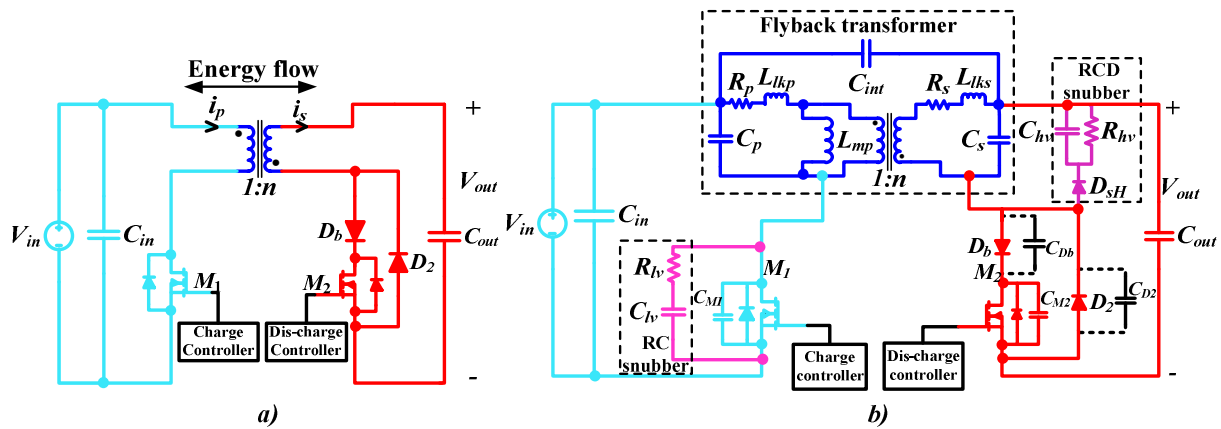


Fig. 1: Circuit configuration of the high voltage bi-directional flyback converter (a); with equivalent transformer model, and RC snubber on low voltage side and RCD snubber on high voltage side (b).

If a snubber circuit is not used in the flyback converter, then the stored energy in the leakage inductance is dissipated in the MOSFET resulting in large voltage spikes across it. In this capacitor charging application, on the primary side only a RC snubber has been used which can damp the high frequency oscillations in the drain voltage waveform, when the switch is OFF. The RCD snubber on the low voltage side can be skipped by either using an overrated low voltage MOSFET  $M_1$  or by reducing the transformer leakage inductance. The stray capacitance of the transformer contributes to capacitive switching loss. We are investigating how to reduce both the leakage inductance and stray capacitance of the flyback transformer, and to make a trade-off between them for this high voltage capacitor charging application [13]. On the high voltage side a RCD snubber has been used to protect the high voltage MOSFET  $M_2$  from the voltage spikes due to the secondary leakage inductance during discharging. As the maximum drain voltage of  $M_2$  used in the converter is 4 kV, and with 24 V as input voltage and  $n=25$  as the turns ratio, the maximum drain voltage of  $M_2$  excluding the voltage increase due to secondary leakage inductance, at 2.5 kV output voltage is 3.1 kV. If the leakage inductance of the flyback transformer secondary is considered, the voltage seen by the drain of  $M_2$  will be higher

than by a factor of  $I_{spk} \cdot \left( \sqrt{\frac{C_{sec2}}{L_{lks}} - \left( \frac{R_s}{2L_{lks}} \right)^2} C_{sec2}^2} \right)^{-1}$  (see (12)). Even though a passive snubber (RC

or RCD) protects the MOSFET, it results in high power loss. To eliminate this power loss, active snubber circuits can be used at the cost of extra components. The circuit configuration of the practical

flyback converter with the transformer equivalent model and the low and high voltage snubber circuits is shown in Fig. 1b.

### Converter analysis during charging process

A soft-switched flyback converter is analyzed in [6] for high voltage capacitor charging application. While charging the capacitive load the proposed converter operates in the boundary mode (boundary between CCM and DCM), with peak current control using LT3751 [15]. The MOSFET  $M_1$  is turned OFF when the primary current reaches the peak current  $I_{ppk}$ . The waveforms of the converter during the charge operation are shown in Fig. 2a. In general, self-capacitance of the transformer low voltage winding ( $C_p$ ) is very small compared to that of the high voltage winding, and hence can be neglected. One complete switching period  $T_s$ , during the charge mode can be divided into four stages. These stages operate continuously until the desired output voltage is reached. The first stage will begin after the transformer primary has finished storing the energy or when the primary MOSFET is turned OFF.

#### Stage 1 [ $t_0 < t \leq t_1$ ]

During this stage the MOSFET  $M_1$  is OFF and the high voltage diode  $D_2$  is blocked. The equivalent parasitic capacitance referred to the primary side ( $C_{pri}$ ) when both the switch  $M_1$  and the diode  $D_2$  are OFF, is the parallel combination of the output capacitance of  $M_1$ , the transformer secondary self-capacitance referred to the primary side, and the equivalent reflected parasitic capacitance of high voltage diodes ( $M_2$  is OFF during the charging process), and is given by

$$C_{pri} = \left[ C_{M_1} + \left( C_s + C_{D_2} + \frac{C_{D_b} C_{M_2}}{C_{D_b} + C_{M_2}} \right) n^2 \right] \quad (1)$$

The magnetizing current  $i_p$  charges the equivalent parasitic capacitance  $C_{pri} = C_{pri}$  in a resonant manner. The current through the magnetizing inductance  $L_{mp}$  is ( $i_{C_{pri}}(t) = -i_p(t)$ )

$$i_p(t) = -C_{pri} e^{-\alpha_{0C}(t-t_0)} \left[ (K_{0C} \beta_{0C} - V_{in} \alpha_{0C}) \cos(\beta_{0C}(t-t_0)) - (K_{0C} \alpha_{0C} + V_{in} \beta_{0C}) \sin(\beta_{0C}(t-t_0)) \right] \quad (2)$$

The voltage across the transformer primary winding is

$$V_{C_{pri}}(t) = e^{-\alpha_{0C}(t-t_0)} \left[ V_{in} \cos(\beta_{0C}(t-t_0)) + K_{0C} \sin(\beta_{0C}(t-t_0)) \right]; V_{C_{pri}}(t_0) = V_{in}; V_{C_{pri}}(t_1) = \frac{-(V_{out}(t_0) + V_{HVd})}{n} \quad (3)$$

$$\alpha_{0C} = \frac{R_p}{2L_{mp}}; \beta_{0C} = \sqrt{\left( \frac{1}{L_{mp} C_{pri}} - \alpha_{0C}^2 \right)}; K_{0C} = \frac{1}{\beta_{0C}} \left( V_{in} \alpha_{0C} - \frac{I_{ppk}}{C_{pri}} \right); i_p(t_0) = i_p(t_1) = i_{M_1}(t_0) = I_{ppk}; i_{M_1}(t_1) = 0$$

#### Stage 2 [ $t_1 < t \leq t_2$ ]

During this stage the voltage across the primary winding of the transformer is clamped to the reflected voltage of  $-(V_{out}(t_0) + V_{HVd})/n$ . The diode in the secondary side is turned-ON and the magnetizing current flows to the secondary side, and delivers the energy stored in  $L_{mp}$  to the output capacitor  $C_{out}$ .

The primary leakage and secondary currents, and voltage across drain of  $M_1$ , and the output voltage are

$$i_{lkp}(t) = I_{ppk} e^{-\alpha_{2C}(t-t_1)} \left[ \cos(\beta_{2C}(t-t_1)) - \frac{\alpha_{2C}}{\beta_{2C}} \sin(\beta_{2C}(t-t_1)) \right]; V_{M_1}(t) = V_{in} + \frac{(V_{out}(t_1) + V_{HVd})}{n} + \frac{I_{ppk}}{\beta_{2C} C_{M_1}} e^{-\alpha_{2C}(t-t_1)} \sin(\beta_{2C}(t-t_1))$$

$$i_s(t) = C_{out} e^{-\alpha_{1C}(t-t_1)} \left[ (K_{1C} \beta_{1C} - (V_{out}(t_1) + V_{HVd}) \alpha_{1C}) \cos(\beta_{1C}(t-t_1)) - (K_{1C} \alpha_{1C} + (V_{out}(t_1) + V_{HVd}) \beta_{1C}) \sin(\beta_{1C}(t-t_1)) \right] + \frac{i_{lkp}(t)}{n}$$

$$V_{out}(t) = \left\{ e^{-\alpha_{1C}(t-t_1)} \left[ (V_{out}(t_1) + V_{HVd}) \cos(\beta_{1C}(t-t_1)) + K_{1C} \sin(\beta_{1C}(t-t_1)) \right] - V_{HVd} \right\}; \alpha_{1C} = \frac{R_s + r_{HVd}}{2(L_{ms} + L_{lks})}; \alpha_{2C} = \frac{R_p}{2L_{lkp}} \quad (4)$$

$$\beta_{1C} = \sqrt{\left( \frac{1}{(L_{ms} + L_{lks}) C_{out}} - \alpha_{1C}^2 \right)}; \beta_{2C} = \sqrt{\left( \frac{1}{L_{lkp} C_{M_1}} - \alpha_{2C}^2 \right)}; K_{1C} = \frac{1}{\beta_{1C}} \left( (V_{out}(t_1) + V_{HVd}) \alpha_{1C} + \frac{I_{spk}}{C_{out}} \right)$$

where  $V_{HVd}$  and  $r_{HVd}$  are the voltage drop and resistance of the high voltage diode ( $D_2$  or  $D_b$ ) respectively. This stage ends when the secondary current falls to 0 A, and  $C_{pri2} = C_{M_1}$  (Fig. 3).

### Stage 3 [ $t_2 < t \leq t_3$ ]

This stage occurs after the energy stored in  $L_{mp}$  is completely transferred to the output capacitor  $C_{out}$ . In this stage the output capacitance of  $M_1$  resonates with the magnetizing inductance  $L_{mp}$  by bringing the voltage across  $M_1$  close to 0 V. In this stage, the equivalent reflected capacitance  $C_{pri3} = C_{pri1} = C_{pri}$  discharges through  $M_1$  and  $i_{C_{pri3}}(t) = -i_p(t)$ .

The current through the magnetizing inductance and voltage across the primary winding are

$$\begin{aligned} i_p(t) &= e^{-\alpha_{0C}(t-t_2)} \left( \frac{-(V_{out}(t_2) + V_{HVd})}{n} \right) \left[ \frac{1}{L_{mp}\beta_{0C}} \sin(\beta_{0C}(t-t_2)) \right] \\ V_{C_{pri3}}(t) &= e^{-\alpha_{0C}(t-t_2)} \left( \frac{-(V_{out}(t_2) + V_{HVd})}{n} \right) \left[ \cos(\beta_{0C}(t-t_2)) + \frac{\alpha_{0C}}{\beta_{0C}} \sin(\beta_{0C}(t-t_2)) \right]; V_{C_{pri3}}(t_3) = V_{in} \end{aligned} \quad (5)$$

The voltage across MOSFET  $M_1$  is

$$V_{M_1}(t) = V_{in} + V_{C_{pri3}}(t); V_{M_1}(t_3) = 0 \quad (6)$$

By solving (5) and (6), and using  $\alpha_{0C}$  and  $\beta_{0C}$  from (3)

$$\sin(\beta_{0C}(t_3 - t_2)) = G \cdot \frac{\beta_{0C}}{w_{0C}^2} \left[ \alpha_{0C} + \sqrt{G^2 - \beta_{0C}^2} \right]; G = \frac{V_{in} \cdot n}{(V_{out}(t_2) + V_{HVd}) e^{-\alpha_{0C}(t_3 - t_2)}}; w_{0C} = \sqrt{\frac{1}{L_{mp} C_{pri3}}} \quad (7)$$

The peak negative amplitude of the equivalent capacitor  $C_{pri3}$  current at the time  $t_3$  is

$$|i_{C_{pri3}}(t_3)| = \frac{V_{in}}{L_{mp}} \left[ \frac{R_p C_{pri3}}{2} + \frac{1}{w_{0C}} \sqrt{G^2 - \frac{\beta_{0C}^2}{w_{0C}^2}} \right] \quad (8)$$

### Stage 4 [ $t_3 < t \leq T_s$ ]

Stage 4 begins when  $M_1$  is turned-ON by the controller under ZVS conditions. The current through the MOSFET  $M_1$  flows in the negative direction (Fig. 2a) through its body diode for a short time, and then it continue to flow through  $M_1$ . The currents through  $C_{pri4}$ , transformer primary and MOSFET  $M_1$  are

$$\begin{aligned} i_{C_{pri4}}(t) &= C_{pri4} e^{-\alpha_{3C}(t-t_3)} \left[ \frac{i_{C_{pri3}}(t_3)}{C_{pri4}} \cos(\beta_{3C}(t-t_3)) - (K_{3C}\alpha_{3C} + (V_{C_{pri}}(t_3) - V_{in})\beta_{3C}) \sin(\beta_{3C}(t-t_3)) \right] \\ i_p(t) &= \left[ \frac{V_{in}}{R_p + r_{dson1}} + \left( i_p(t_3) - \frac{V_{in}}{R_p + r_{dson1}} \right) e^{-\frac{R_p + r_{dson1}}{L_{mp} + L_{lkp}}(t-t_3)} \right] - \frac{L_{lkp}}{L_{mp}} i_{C_{pri4}}(t); i_{M_1}(t) = i_p(t) + i_{C_{pri4}}(t) \\ \alpha_{3C} &= \frac{r_{dson1}}{2L_{lkp}}; \beta_{3C} = \sqrt{\left( \frac{1}{L_{lkp} C_{pri4}} - \alpha_{3C}^2 \right)}; K_{3C} = \frac{1}{\beta_{3C}} \left( (V_{C_{pri3}}(t_3) - V_{in})\alpha_{3C} + \frac{i_{C_{pri3}}(t_3)}{C_{pri4}} \right); C_{pri4} = n^2 \cdot (C_s + C_{M_2} + C_{D_2}) \end{aligned} \quad (9)$$

### Converter analysis during discharging process

The same controller LT3751 [15] is used in the proposed bi-directional flyback converter for discharging the capacitive load. During discharging process, the converter operates in the discontinuous conduction mode (DCM) with constant switching frequency. The converter waveforms during the discharging operation are shown in Fig. 2b. The capacitive load is discharged when the high voltage MOSFET  $M_2$  is turned ON, and transfer the energy stored in the secondary magnetizing winding  $L_{ms}$  to the source, through the body diode of  $M_1$ , when  $M_2$  is OFF. As the output voltage discharges, the ON-

time of the high voltage MOSFET  $M_2$  increases, and OFF-time is constant with a value of  $L_{mp} i_{ppk\_dch} / V_{in}$ .

At the end of the discharging process  $M_2$  is completely turned ON. The switching period  $T_s$  during discharging operation can be divided into 5 stages. The Stage 1 begins once the secondary magnetizing inductance  $L_{ms}$  of the transformer has finished storing the energy.

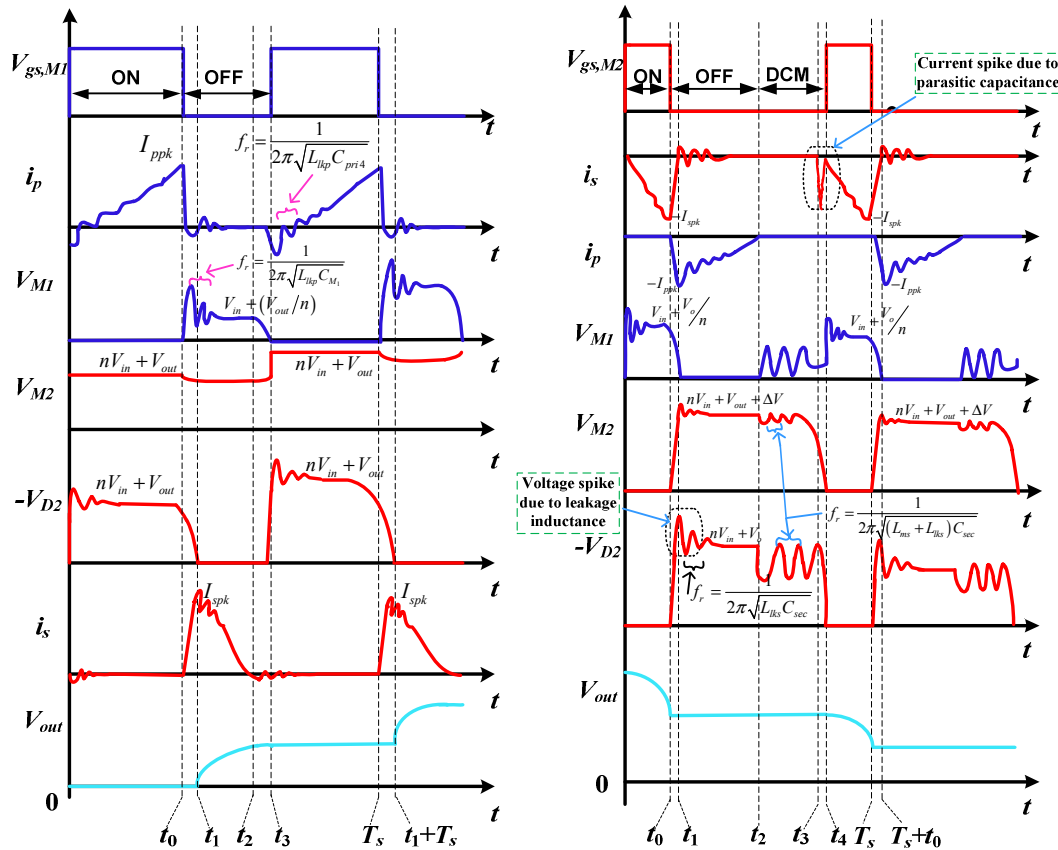


Fig. 2: Charging (a) and discharging (b) waveforms of the proposed converter.

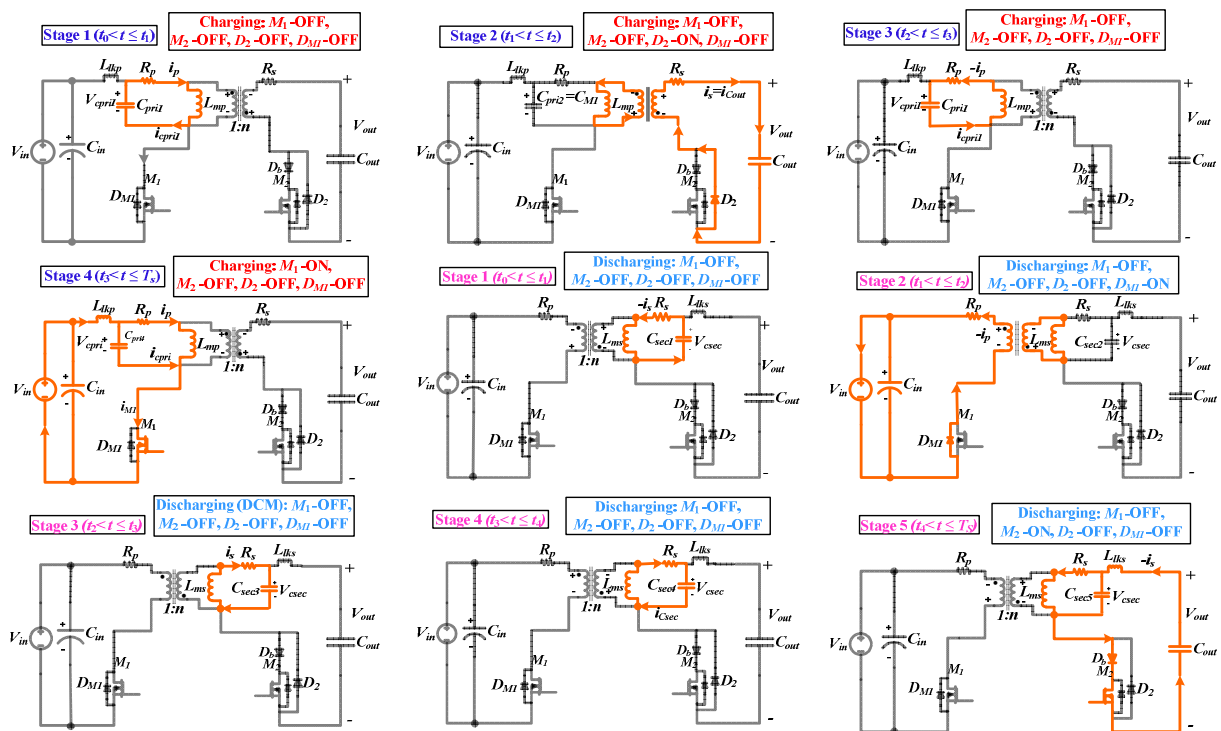


Fig. 3: All charging and discharging stages in one respective charging and discharging switching cycle

**Stage 1 [ $t_0 < t \leq t_1$ ]**

The high voltage MOSFET  $M_2$  is turned OFF at time  $t=t_0$ . The secondary magnetizing inductance ( $L_{ms}$ ) in series with the secondary leakage inductance ( $L_{lks}$ ) resonates with the equivalent parasitic capacitance referred to the secondary side  $C_{sec}$ .

$$C_{sec} = C_{M_2} + C_s + C_{D_2} + \frac{C_{D_b} C_{M_2}}{C_{D_b} + C_{M_2}} \quad (10)$$

The voltage across the transformer secondary winding and peak secondary current are

$$\begin{aligned} V_{C_{sec}}(t) &= e^{-\alpha_{0D}(t-t_0)} \left[ V_{out}(t_0) \cos(\beta_{0D}(t-t_0)) + K_{0D} \sin(\beta_{0D}(t-t_0)) \right]; V_{C_{sec}}(t_1) = -nV_{in} \\ \alpha_{0D} &= \frac{R_s}{2L_{ms}}; \beta_{0D} = \sqrt{\left( \frac{1}{L_{ms} C_{sec}} - \alpha_{0D}^2 \right)}; K_{0D} = \frac{1}{\beta_{0D}} \left( V_{out}(t_0) \alpha_{0D} + \frac{I_{spk}}{C_{sec}} \right) \\ I_{spk} &= \sqrt{I_{spk\_design}^2 + (C_{sec}/L_{ms}) V_{out}^2} \end{aligned} \quad (11)$$

where  $I_{spk}$  is the actual secondary peak current,  $I_{spk\_design}$  is the design value of the secondary peak current or  $M_2$  turn OFF current. If the energy stored in the equivalent parasitic capacitance  $C_{sec}$  ( $0.5 \cdot C_{sec} V_{out}^2$ ), is higher than the energy stored in the secondary winding inductance  $L_{ms}$  ( $0.5 \cdot L_{ms} I_{spk\_des}^2$ ), then the secondary peak current will be greater than the  $M_2$  turn OFF current.

**Stage 2 [ $t_1 < t \leq t_2$ ]**

During this stage the voltage across the secondary winding of the transformer is clamped to  $-nV_{in}$ . The body diode of  $M_1$  in the primary side conducts and the magnetizing current flows to the primary side, in the reverse direction delivering the output energy to the source. The stage ends when the primary current falls to 0 A.

The secondary leakage and primary currents, and voltage across diode  $D_2$  are

$$\begin{aligned} i_{lks}(t) &= I_{spk} e^{-\alpha_{1D}(t-t_1)} \left[ \cos(\beta_{1D}(t-t_1)) - \frac{\alpha_{1D}}{\beta_{1D}} \sin(\beta_{1D}(t-t_1)) \right]; \alpha_{1D} = \frac{R_s}{2L_{lks}} \\ V_{D_2}(t) &= nV_{in} + V_{out}(t_1) + \frac{I_{spk}}{\beta_{1D} C_{sec2}} e^{-\alpha_{1D}(t-t_1)} \sin(\beta_{1D}(t-t_1)); \beta_{1D} = \sqrt{\left( \frac{1}{L_{lks} C_{sec2}} - \alpha_{1D}^2 \right)} \\ i_p(t) &= \left[ \frac{V_{in} + V_{Lvd}}{R_p} \left( 1 - e^{-\frac{R_p}{L_{mp} + L_{lp}}(t-t_1)} \right) \right] - n \cdot i_{lks}(t); V_{out}(t_1) = V_{out}(t_0); C_{sec2} = C_{M_2} + C_s + C_{D_2} \end{aligned} \quad (12)$$

**Stage 3 [ $t_2 < t \leq t_3$ ]**

This stage occurs when the primary current becomes zero. In this stage the converter operates in DCM. The secondary inductance ( $L_{ms}$ ) resonates with the equivalent secondary parasitic capacitance  $C_{sec3}=C_{sec}$ . The voltage  $V_{Lvd}$  in (13) is the voltage drop of the body diode of  $M_1$ .

The voltages across  $C_{sec3}$  and the drain of  $M_2$ , and the current through  $C_{sec3}$  are

$$\begin{aligned} V_{C_{sec}}(t) &= -\left[ n(V_{in} + V_{Lvd}) \right] \cdot e^{-\alpha_{2D}(t-t_2)} \left[ \cos(\beta_{2D}(t-t_2)) + \frac{\alpha_{2D}}{\beta_{2D}} \sin(\beta_{2D}(t-t_2)) \right]; i_{C_{sec}}(t) = C_{sec3} \frac{dV_{C_{sec}}(t)}{dt} \\ V_{M_2}(t) &= V_{out}(t_1) + V_{C_{sec}}(t) - V_{D_b}(t); V_{M_1} = V_{in} + \left( \frac{V_{C_{sec}}(t)}{n} \right); \alpha_{2D} = \alpha_{0D}; \beta_{2D} = \sqrt{\left( \frac{1}{L_{ms} C_{sec3}} \right)^2 - \alpha_{2D}^2} \end{aligned} \quad (13)$$

**Stage 4 [ $t_3 < t \leq t_4$ ]**

In this stage the equivalent parasitic capacitance  $C_{sec4}=C_{sec}$  resonates with the secondary magnetizing inductance  $L_{ms}$  by bringing the voltage across  $M_2$  to 0 V.



The current through the equivalent parasitic capacitance  $C_{sec4}$  is given by

$$i_{C_{sec}}(t) = -e^{-\alpha_{0D}(t-t_3)} \left[ \frac{V_{C_{sec}}(t_3)}{L_{ms}\beta_{0D}} \sin(\beta_{0D}(t-t_3)) \right] \quad (14)$$

### Stage 5 [ $t_4 < t \leq T_s$ ]

This stage begins when  $M_2$  is turned-ON by the controller. The current not only flows through the magnetizing inductance  $L_{ms}$ , but also through the equivalent parasitic capacitance  $C_{sec5} = C_s + C_{D_2}$ .

The currents through  $C_{sec5}$ ,  $L_{ms}$  and the MOSFET  $M_2$ , and the output voltage are

$$\begin{aligned} i_{C_{sec}}(t) &= -C_{pri5} e^{-\alpha_{5D}(t-t_4)} \left[ (K_{5D}\beta_{5D} - V_{out}(t_1)\alpha_{5D}) \cos(\beta_{5D}(t-t_4)) - (K_{5D}\alpha_{5D} + V_{out}(t_1)\beta_{5D}) \sin(\beta_{5D}(t-t_4)) \right] \\ V_{out}(t) &= e^{-\alpha_{4D}(t-t_4)} \left[ (V_{out}(t_1) - V_{HVd}) \cos(\beta_{4D}(t-t_4)) + K_{4D} \sin(\beta_{4D}(t-t_4)) \right] + V_{HVd} \\ i_s(t) &= \left[ -C_{out} \frac{dV_{out}(t)}{dt} - \frac{L_{lks}}{L_{ms}} i_{C_{sec}}(t) \right]; i_{M_2}(t) = i_s(t) + i_{C_{sec}}(t); V_{out}(t_4) = V_{out}(t_3) = V_{out}(t_2) = V_{out}(t_1) \\ \alpha_{4D} &= \frac{r_{ds(on)2} + R_S + r_{HVd}}{2(L_{ms} + L_{lks})}; \beta_{4D} = \sqrt{\left( \frac{1}{(L_{ms} + L_{lks})C_{out}} - \alpha_{4D}^2 \right)}; K_{4D} = \frac{1}{\beta_{4D}} \left( (V_{out}(t_1) - V_{HVd})\alpha_{4D} - \frac{i_{C_{sec}}(t_4)}{C_{out}} \right) \\ \alpha_{5D} &= \alpha_{1D}; \beta_{5D} = \sqrt{\left( \frac{1}{L_{lks}C_{sec5}} - \alpha_{5D}^2 \right)}; K_{5D} = \frac{1}{\beta_{5D}} \left( V_{out}(t_1)\alpha_{5D} - \frac{i_{C_{sec}}(t_4)}{C_{sec5}} \right) \end{aligned} \quad (15)$$

These stages operate continuously until the output voltage is discharged to  $I_{spk\_dch} \sqrt{L_{ms}/C_{out}}$ , which is the minimum voltage required to store the energy in  $L_{ms}$  during the discharging process. At this point the MOSFET  $M_2$  is completely ON, since the secondary current becomes zero, and  $L_{ms}$  resonates with  $C_{out}$ .

## Controller description, component selection and transformer design

Possible control techniques for the capacitor charging flyback converter have been discussed in [16]. For achieving 2.5 kV bi-directional operation, the capacitor charger controller from Linear Technology [15] has been chosen as charge and discharge controllers.

### Variable frequency during charging and Constant frequency during discharging

The primary inductance store the energy with constant ON-time at low output voltages, and with variable ON-time at high output voltages. The discharging time of  $M_1$  decreases with the increase in the output voltage, which increases the switching frequency during the charging process. During discharging process, the control IC needs to sense the output voltage to operate in the boundary mode. But the controller LT3751 is not designed for wide input voltage variations (2.5 kV to 0 V is too wide operating range). So, we operated the IC in the start-up protection mode without giving the output voltage feedback to it, and the IC is operated with a constant switching frequency of  $\sim 25.6$  kHz.

### Component selection and Transformer design

The components used for the implementation of the bi-directional flyback converter are given Table 2. The flyback transformer is designed with the ETD 29 core and the measured parameters of the transformer are provided in Table 3. The transformer should be designed without saturating the magnetic core during both the charging as well as the discharging processes.

## Experimental results

The experimental results of the bi-directional flyback converter are shown in Figs. 4 and 5. Fig. 4 shows the zoomed waveforms during charging and discharging operations respectively, and Fig. 5 shows the waveforms of the continuous charging and discharging operation of the flyback converter for

the film capacitive load and the DEAP actuator respectively. The charging energy efficiency is defined as, the ratio of energy stored in the capacitive load, to the energy input to the converter during charging process [17]. The discharging energy efficiency is defined as, the ratio of energy recovered back to the source, to the energy input during the discharging process (which is the energy stored in the capacitive load). The charging  $\eta_{charge}$  and discharging  $\eta_{discharge}$  energy efficiency expressions are given below.

$$\eta_{charge} = \frac{0.5 \cdot C_{out} V_{out}^2}{\int_0^{T_{charge}} V_{in} i_{in\_charge} dt} \quad (17)$$

$$\eta_{discharge} = \frac{\int_{T_{charge}+T_{delay}}^{T_{discharge}} V_{in} i_{in\_discharge} dt}{0.5 \cdot C_{out} V_{out}^2} \quad (18)$$

where  $T_{charge}$  and  $T_{discharge}$  are the charging and discharging times of the capacitive load respectively ( $T_{delay}$  is a delay between the charging and discharging processes). The input supply current during charging is  $i_{in\_charge}$  and the recovered input current during discharging is  $i_{in\_discharge}$ . The energy efficiency curves are given in Fig. 6.

**Table II: Components used in the converter**

Component	Name
Low voltage / High voltage MOSFET	STB50NF25 [250 V, 45 A, 55 m $\Omega$ ] / IXTV03N400S [4 kV, 300 mA, 290 $\Omega$ ]
High voltage diode	SXF6525 [5 kV, 150 mA, 70 ns ( $t_{rr}$ )]

**Table III: Flyback transformer parameters**

Parameter	Value
Primary ( $L_{mp}$ ) / Secondary magnetizing inductance ( $L_{ms}$ )	44 $\mu$ H / 28 mH
Flyback transformer core / Material	ETD 29 / N87
Primary turns ( $N_p$ ) / Turns ratio ( $n$ )	10 / 25
Leakage inductance of transformer primary ( $L_{lkp}$ ) / secondary winding ( $L_{lks}$ )	930 nH / 647 $\mu$ H
Secondary winding ( $C_s$ ) / Inter-winding stray capacitance ( $C_{int}$ )	26 pF / 26.2 pF
DC resistance of primary ( $R_p$ ) / secondary winding ( $R_s$ )	60 m $\Omega$ / 7.6 $\Omega$

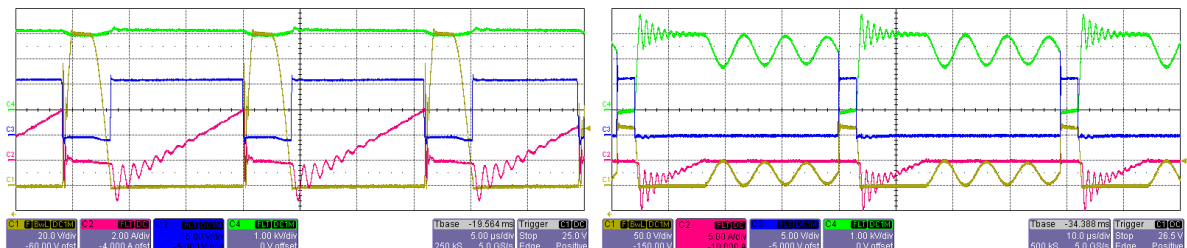


Fig. 4: Zoomed waveforms during charging operation (left)- CH1:  $V_{drainM1}$ ; CH2:  $i_p$ ; CH3:  $V_{GateM1}$ ; CH4:  $V_{drainM2}$  and discharge operation (right)- CH1:  $V_{drainM1}$ ; CH2:  $i_p$ ; CH3:  $V_{GateM2}$ ; CH4:  $V_{D2}$

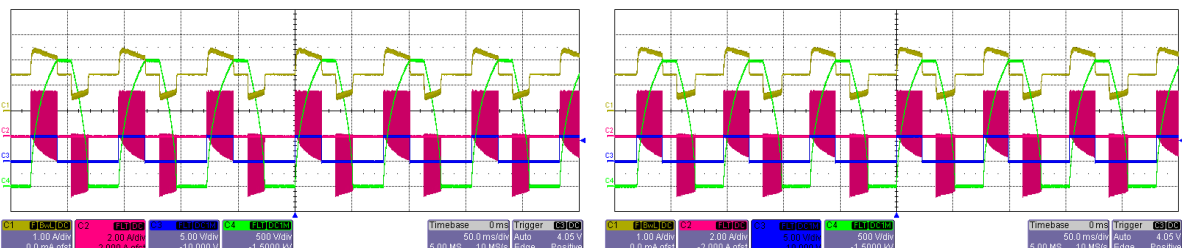


Fig. 5: Continuous bi-directional operation of the flyback converter at 2.5 kV output voltage with film capacitive load (left), and with DEAP actuator (right) - CH1:  $i_{in}$ ; CH2:  $i_p$ ; CH3:  $V_{charge1}$ ; CH4:  $V_{out}$

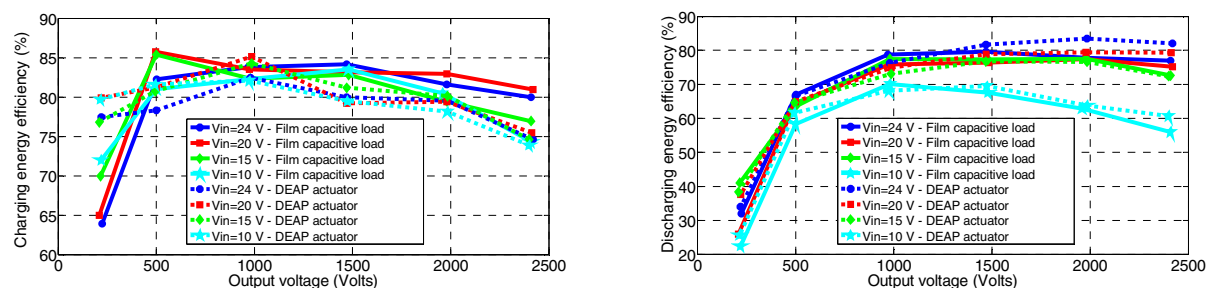


Fig. 6: Charging energy efficiency (left) and discharging energy efficiency curves (right)

## Conclusion

The bi-directional flyback converter was successfully implemented for high voltage capacitor charging application. The converter was operating with 80-85% charging and 70-80% discharging energy efficiencies. Careful design of the flyback transformer is required for successful implementation of high voltage bi-directional flyback converter, without damaging the high voltage 4 kV MOSFET.

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