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Improved control configuration of PWM rectifiers based on neuro-fuzzy controller

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Abstract

It is well-known that rectifiers are used widely in many applications required AC/DC transformation. With technological advances, many studies are performed for AC/DC converters and many control methods are proposed in order to improve the performance of these rectifiers in recent years. Pulse width modulation (PWM) based rectifiers are one of the most popular rectifier types. PWM rectifiers have lower input current harmonics and higher power factor compared to classical diode and thyristor rectifiers. In this study, neuro-fuzzy controller (NFC) which has robust, nonlinear structure and do not require the mathematical model of the system to be controlled has been proposed for PWM rectifiers. Three NFCs are used in control scheme of proposed PWM rectifier in order to control the dq-axis currents and DC voltage of PWM rectifier. Moreover, simulation studies are carried out to demonstrate the performance of the proposed control scheme at MATLAB/Simulink environment in terms of rise time, settling time, overshoot, power factor, total harmonic distortion and power quality.

Keywords: PWM rectifiers, Neuro-fuzzy controller, Total harmonic distortion, Power quality

Background

With advances in power electronics and microprocessors, power electronics technology has widely used for many applications. Nowadays, AC/DC converters, which are also known as rectifiers, are used in adjustable speed drives, uninterruptible power supply systems, photovoltaic systems, battery energy storage systems, DC motor drives and communication systems (Singh et al. 2004; Blasko and Kaura 1997). AC to DC conversion has been performed by an uncontrollable diode rectifier or a phase controlled thyristor rectifier. Although these rectifiers have high reliability, simple structure and low cost, they have many disadvantages such as low power factor, high THD and unidirectional power flow. Moreover, these rectifiers that produce high harmonic currents are actually a harmonic source and have caused harmonic problems (Dannehl et al. 2009; Sekkeli et al. 2015). To solve these problems, new standards have been introduced by a number of countries and international organizations to limit harmonics formed in the current drawn from main supply by rectifiers. During the past 20 years, the interest in AC/DC rectifiers has been growing by day by day due to the increasing concern about the harmonic pollution in the power systems. Thanks to the rapid development of technology, new rectifier type for

three-phase AC/DC conversion have been developed such as PWM rectifier (Wu et al. 1991). PWM rectifiers used for AC-DC conversion have many advantages like controllable DC voltage, fast dynamic response, controllable reactive power, unity power factor, low harmonic distortion and bidirectional power flow (Bouafia et al. 2010a, b). Generally, the control techniques of PWM rectifiers can be classified into two types: Voltage oriented control (VOC) and direct power control (DPC). VOC based on internal current control loops became very popular method. Another control method is called as DPC which has not internal current loops and PWM blocks (Malinowski et al. 2001; Malinowski and Kazmierkowski 2003; Monfared et al. 2010). The main goal of these control techniques is to eliminate the current harmonics and to regulate the DC bus voltage. In the control of PWM based rectifiers, DC bus voltage and dq-axis currents are generally controlled by proportional-integral (PI) controllers due to their simple structure. PI controllers need linear mathematical model of system. Moreover, it is known that PI controllers have many disadvantages such as slow response, large overshoots and oscillations (Cortes et al. 2008; Blasko and Kaura 1997). To cope with these problems, many control methods have been proposed by many academics and researchers, namely fuzzy logic controllers (FLC), robust H_∞ controller, linear quadratic regulator (LQR), sliding mode control (SMC) and predictive control (PC). These intelligent controllers have used for many industrial applications and rectifier systems to obtain a good performance in both transient and steady state from PWM rectifier (Antoniewicz and Kazmierkowski 2006; Yu et al. 2010; Zhao et al. 2011; Jiabing et al. 2011; Bouafia and Krim 2008; Djerioui et al. 2014). NFC that has nonlinear, robust structure and based on FLC whose functions are realized by ANN is one of these intelligent controllers (Zadeh 1965; Jang et al. 1997; Mohagheghi et al. 2007). In this paper, the robust and nonlinear control strategy based NFC controllers are proposed for DC bus voltage and dq-axis currents control of PWM rectifier in order to achieve a good dynamic response. NFC controllers designed for DC voltage and dq-axis currents have two inputs, single output and six layers. This paper is organized as follows: Power circuit and mathematical model of PWM rectifier is given in first section. The determination of electrical parameters in PWM rectifier and design of PI controller are presented in second section. The description of the NFC and its training algorithm are explained in third section. The simulation results related to proposed controller are comprehensively presented in fourth section. The final section provides the conclusions of this study.

Mathematical model of PWM rectifier

The three-phase PWM rectifiers are widely used in a wide diversity of applications in recent years. These rectifiers have many advantages such as bi-directional power flow, low harmonic distortion of line current, unity power factor, control of DC bus voltage (Blasko and Kaura 1997; Kazmierkowski et al. 2002).

The structure of three-phase PWM rectifier is as shown in Fig. 1. The source phase voltages are expressed as:

$$V_a = V_m \sin \theta \quad (1)$$

$$V_b = V_m \sin(\theta - 2\pi/3) \quad (2)$$

$$V_c = V_m \sin(\theta + 2\pi/3) \quad (3)$$

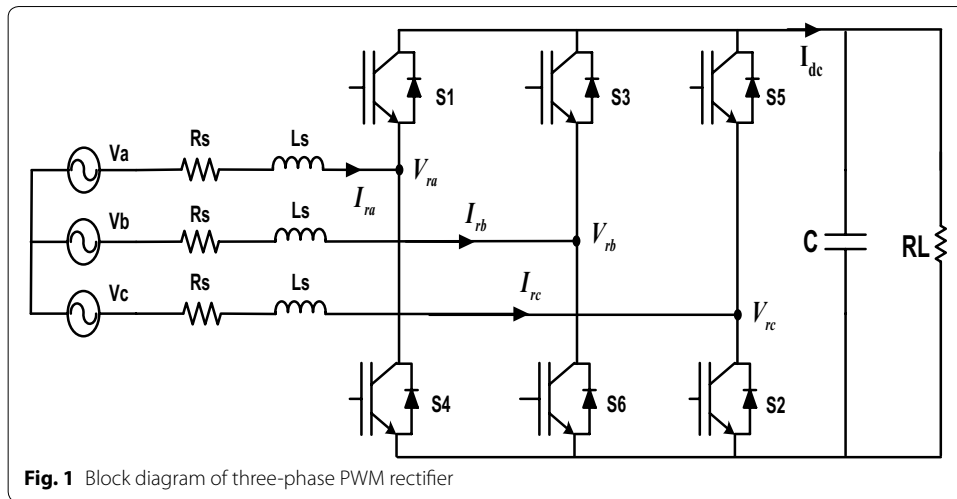


Fig. 1 Block diagram of three-phase PWM rectifier

The mathematical model of PWM rectifier in abc frame can be expressed as: (Blasko and Kaura 1997).

$$\begin{bmatrix} L_s \frac{dI_{ra}}{dt} \\ L_s \frac{dI_{rb}}{dt} \\ L_s \frac{dI_{rc}}{dt} \\ L_s \frac{dI_L}{dt} \end{bmatrix} = \begin{bmatrix} -R_s & 0 & 0 & 0 \\ 0 & -R_s & 0 & 0 \\ 0 & 0 & -R_s & 0 \\ S_a & S_b & S_c & -1 \end{bmatrix} \begin{bmatrix} I_{ra} \\ I_{rb} \\ I_{rc} \\ I_L \end{bmatrix} + \begin{bmatrix} V_a - V_{ra} \\ V_b - V_{rb} \\ V_c - V_{rc} \\ 0 \end{bmatrix} \tag{4}$$

$$\begin{bmatrix} V_{ra} \\ V_{rb} \\ V_{rc} \end{bmatrix} = \begin{bmatrix} 2/3 & -1/3 & -1/3 \\ -1/3 & 2/3 & -1/3 \\ -1/3 & -1/3 & 2/3 \end{bmatrix} \begin{bmatrix} S_a \\ S_b \\ S_c \end{bmatrix} V_{dc} \tag{5}$$

where, L_s and R_s are grid inductance and resistance, respectively. I_a , I_b and I_c are grid phase currents; V_{ra} , V_{rb} and V_{rc} are the rectifier input voltages. V_{ra} , V_{rb} and V_{rc} voltages can be found by opening and closing in accordance with the switching elements in the structure of rectifier to obtain the DC link voltage. Where, S_a , S_b and S_c show switching functions. These functions get 0, if the switch is off; if it is on, then they are 1. Clarke’s matrix in α - β frame can be described as following (Blasko and Kaura 1997):

$$T = \begin{bmatrix} 1 & -1/2 & -1/2 \\ 0 & \sqrt{3}/2 & -\sqrt{3}/2 \end{bmatrix} \tag{6}$$

According to Clarke’s transformation, the dynamic model of PWM rectifier can be defined as:

$$\begin{bmatrix} V_\alpha \\ V_\beta \end{bmatrix} = L_s \begin{bmatrix} \frac{dI_\alpha}{dt} \\ \frac{dI_\beta}{dt} \end{bmatrix} + \begin{bmatrix} R_s & 0 \\ 0 & R_s \end{bmatrix} \begin{bmatrix} I_\alpha \\ I_\beta \end{bmatrix} + \begin{bmatrix} V_{r\alpha} \\ V_{r\beta} \end{bmatrix} \tag{7}$$

If Park’s transformation is applied to rectifier system then following equation can be derived:

$$\begin{bmatrix} V_d \\ V_q \end{bmatrix} = L_s \begin{bmatrix} \frac{dI_d}{dt} \\ \frac{dI_q}{dt} \end{bmatrix} + \begin{bmatrix} R_s & -\omega L_s \\ \omega L_s & R_s \end{bmatrix} \begin{bmatrix} I_d \\ I_q \end{bmatrix} + \begin{bmatrix} V_{rd} \\ V_{rq} \end{bmatrix} \tag{8}$$

Angle value (θ) required for above transformations can be found with PLL (phase locked loop) in MATLAB/Simulink or it can be obtain by using abc– $\alpha\beta$ transformation.

Determination of electrical parameters used in PWM rectifier and design of PI controller

Input inductor must be designed very carefully in order to obtain good performance from rectifier. Low inductance value causes the increase in current ripple and the performance of rectifier depends on impedance of the grid. The high inductance value reduces current ripple but it limits the operating range of the rectifier (Wang and Yin 2008). Consequently, the maximum inductance value can be determined as follows:

$$\frac{(2U_{dc} - 3U_m)U_m T_s}{2U_{dc} \Delta I_{max}} \leq L \leq \frac{2U_{dc}}{3I_m \omega} \tag{9}$$

where, U_m is the phase voltage amplitude of the grid, T_s is the switching period, I_m is the amplitude of the grid current, ω is the angular frequency, and ΔI_{max} is the allowed maximal ripple of the grid current. The determination of the value of C is very important because C has key role in fixed the DC voltage. Also, the value of C should be as small as possible in order to provide fast tracking of the reference DC voltage. The value of C can be found as the following equations:

$$C \leq \frac{t_r^*}{R_L \ln \left(6 - \frac{8.27U_m}{U_{dc}} \right)} \tag{10}$$

$$C > \frac{1}{2R_L \Delta U_{dc}^*} \tag{11}$$

where, $\Delta U_{dc} = (U_{dc} - U_{dcmin})/U_{dc}$, t_r^* is the rising time for the output voltage and R_L is the output load resistance. The choice of U_{dc} must meet the load requirement and the grid current control requirement. Neglecting the high-order harmonics, the limits of the DC voltage can be determined by the following equations:

$$U_{dc} \geq \begin{cases} 2\sqrt{2}U_{1abc} & \text{(For SPWM)} \\ \sqrt{6}U_{1abc} & \text{(For SVPWM)} \end{cases} \tag{12}$$

$$\begin{aligned} \text{SPWM: } U_{dc} &\geq 2\sqrt{2}[(U_{1abc} + RI_{1abc})^2 + (\omega LI_{1abc})^2]^{1/2} \\ \text{SVPWM: } U_{dc} &\geq \sqrt{6}[(U_{1abc} + RI_{1abc})^2 + (\omega LI_{1abc})^2]^{1/2} \end{aligned} \tag{13}$$

where I_{1abc} is the rms value of the grid currents and R is the resistance of the filter reactor and U_{1abc} is the rms value of the grid voltages (Wang and Yin 2008). Based on Eqs. (9)–(13), the parameters used in simulation study are given in Table 2.

The dq-axis currents must be controlled independently in the control of three-phase PWM rectifiers. The dq-axis currents are usually controlled by PI controllers with fixed parameters. A mathematical model of the system to be controlled has been needed to obtain K_p and K_i parameters of PI controller (Blasko and Kaura 1997). The reduced block diagram is formed for the control of dq-axis currents given in Fig. 2. The gain values of PI controller may be easily determined from this reduced block diagram.

In this reduced block diagram; T_s is sampling and filter delay, K_p is proportional gain of PI controller, T_i is PI controller integral time constant, T_{PWM} is time constant of the PWM block, K_{PWM} is rectifier gain K_{RL} is gain of system, T_{RL} is the system time constant. The smallest time constants in the reduced block diagram are grouped together to form a single block as a single time constant (Blasko and Kaura 1997).

$$T_{ei} = T_s + T_{PWM}/2 \tag{14}$$

The integral time constant of the PI controller is obtained by the following equation according to the dominant pole of the system.

$$T_i = T_{RL} \tag{15}$$

Closed loop transfer function of the system can be obtained by using the reduced block diagram as below:

$$H_{ci} = \frac{1}{s^2 \frac{T_{RL} T_{ei}}{K_p K_{PWM} K_{RL}} + s \frac{T_{RL}}{K_p K_{PWM} K_{RL} + 1}} \tag{16}$$

To ensure 5 % overshoot, if the damping ratio is selected as $(\xi) = \sqrt{2}/2$, it can be obtained by the following equation:

$$\xi^2 = \frac{T_{RL}}{4K_p K_{PWM} K_{RL}} \tag{17}$$

If the damping ratio is written in Eq. 16, Eq. 18 is obtained:

$$K_p = \frac{T_{RL}}{2T_{ei} K_{PWM} K_{RL}} \tag{18}$$

The first-order transfer function can be obtained by neglecting the s^2 term because of the very small product the term $T_{RL} \cdot T_{ei}$.

$$H_{ci} = \frac{1}{1 + sT_{et}} \tag{19}$$

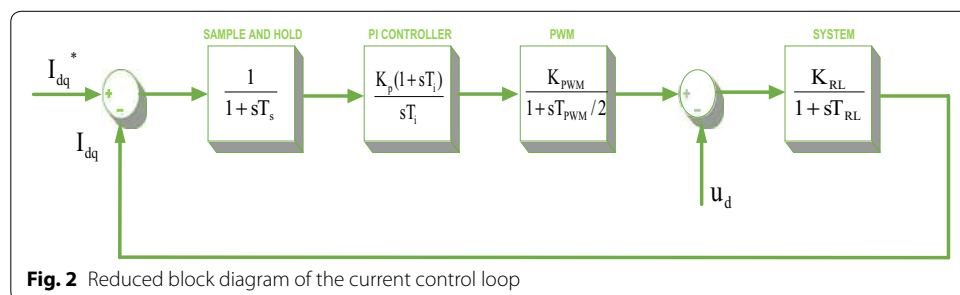


Fig. 2 Reduced block diagram of the current control loop

where, $T_{et} = 4T_{ei}\zeta^2$, $\xi = \sqrt{2}/2$, $T_{et} = 2T_{ei}$. The performance of the PI controller designed for the control of dq-axis currents is examined by using the reduced block diagram of the control loop (Blasko and Kaura 1997). As it is shown in Fig. 3, the time constant of the PI controller is selected as $T_i = 10T_{ei}$ and $T_i = T_{RL}$. Also, the responses of PI controllers are shown in Fig. 3.

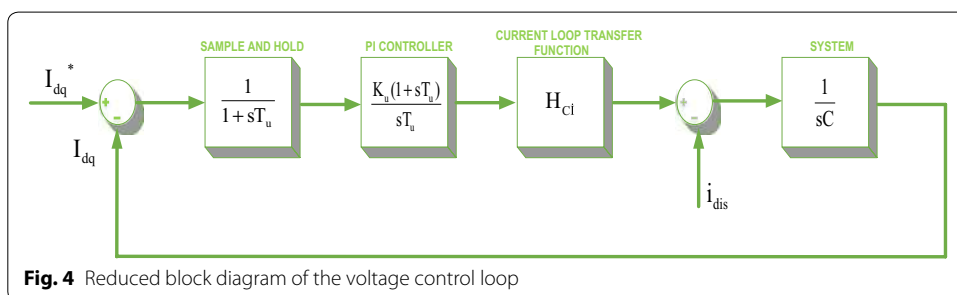
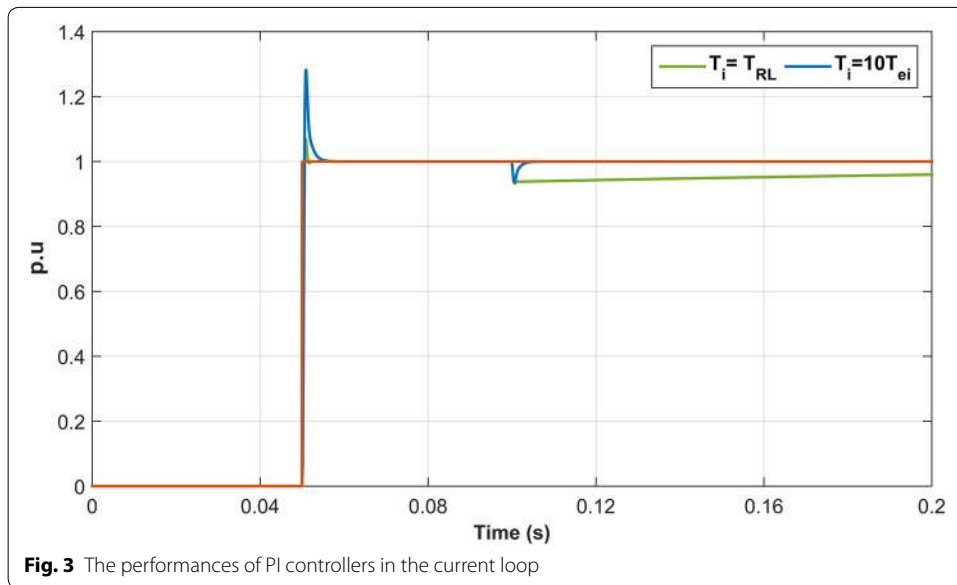
As shown in Fig. 4, reduced block diagram is designed as in the current loop in order to control the DC voltage of PWM rectifier. In this reduced block diagram; T_{du} is the sampling and filter delay, K_u is PI controller proportional gain, T_u is PI controller integral time constant, H_{ci} is the transfer function obtained from the current loop, C is the value of the DC link capacitor.

As in the current loop, the smallest time constant of the voltage loop can be expressed as a single time constant.

$$T_{eu} = T_{du} + T_{ei} \tag{20}$$

Gain parameters of PI controller used in control of DC voltage can be found by symmetric optimum method. The open-loop transfer function can be obtained as follows:

$$H_{cu} = \frac{K_u(1 + sT_u)}{sT_u(1 + sT_{eu})sC} \tag{21}$$



In the symmetrical optimum method, open loop transfer function is approximately symmetrical at the crossover frequency (ω_c). The relationship between ω_c and phase margin is given in Eq. (22).

$$\omega_c = \frac{1}{aT_{eu}} \quad \text{and} \quad a = \frac{1 + \cos \phi}{\sin \phi} \tag{22}$$

The proportional gain of the PI controller can be found with the following equation for $a = \sqrt{\frac{T_u}{T_{eu}}}$ and $a > 1$.

$$K_u = \frac{C}{aT_{eu}} \tag{23}$$

The performance of the designed PI controller for control the DC voltage of PWM rectifier is observed using a reduced block diagram of the DC voltage control loop as shown Fig. 5.

Design of proposed controller for PWM rectifier

In recent years, NFCs which are successfully applied to many industrial applications, are based on the execution of the functions of fuzzy controller (FC) by ANN constructively (Jang et al. 1997; Zadeh 1965; Dandil and Gokbulut 2005). NFCs have non-linear structure and do not require the mathematical model of the system to be controlled. Therefore the NFCs are used widespread in the systems that are uncertain, non-linear and have parameter changes (Tuncer and Dandil 2008; Liu et al. 2003). Fuzzy rules of sugeno type FC are defined as below.

$$R^j : \text{ If } X_1, A_1^j \dots X_n, A_n^j \text{ then } y = f_j = a_0^j + a_1^j X_1 + a_2^j X_2 + a_n^j X_n \tag{24}$$

Here, X_i is the input variable, y is the output variable, linguistic variables of prerequisites with $A_{i\mu}^j(x_i)$ membership function and the $a_i^j \in R$ are the coefficients of linear $f_i = (x_1, x_2, \dots, x_n)$ function. Structure of NFCs which used in control algorithms is shown in Fig. 6. As seen in the Fig. 6, NFCs have two inputs, one output and six layers. Five membership functions were chosen for each input. As inputs, error ($e = I_{dq}^*(k) - I_{dq}(k)$)

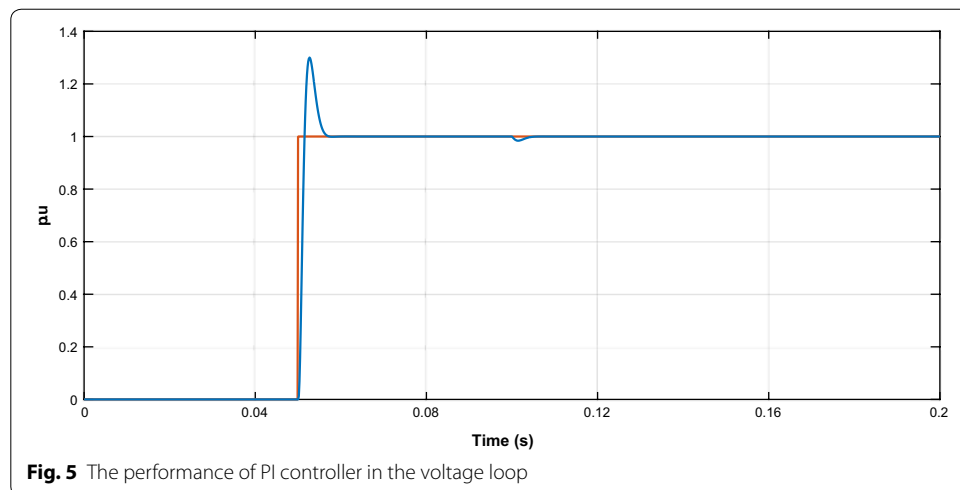


Fig. 5 The performance of PI controller in the voltage loop

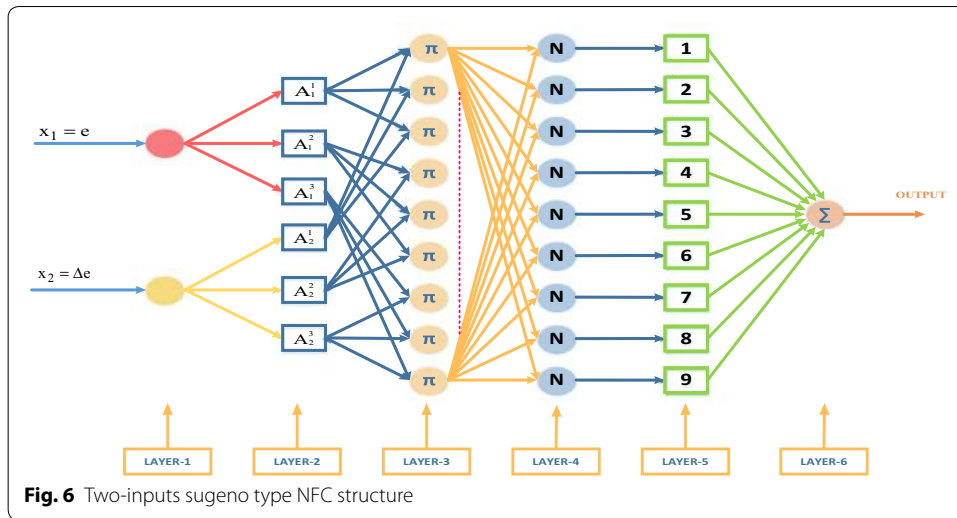


Fig. 6 Two-inputs sugeno type NFC structure

and $e = V_{dc}^*(k) - V_{dc}(k)$ and the change of error ($\Delta e = e(k) - e(k - 1)$) are chosen for NFC that used in control method. ΔV_{dq} and I_d are obtained from the output of NFCs. At the control method, two units of NFC that shown in Fig. 6 were used totally which one is for control of DC bus voltage and the other one is for control of dq-axis currents.

In the other side, the errors and changes of reactive and active currents are given to NFC as input where NFC is used for dq-axis currents control. The amount of change of voltage that must be generated by rectifier in both dq-axis components are obtained from the outputs of NFCs that used in dq-axis current controls. Also, an external integrator that prevents integral wrapping is used for eliminate the steady state errors and limit the outputs of designed NFCs. Each neuron in the NFC's first layer has linear activation function and transfers the input variables to its outputs. The input variables of NFC were taken as shown in Eqs. (25) and (26). The input variables of NFC are determined in the first layer. Input variables are the error and the change of error.

$$e(k)_1 = V_{dc}^* - V_{dc} \text{ and } e(k)_2 = I_{dq}^* - I_{dq} \tag{25}$$

$$\Delta e(k) = e(k) - e(k - 1) \tag{26}$$

Membership functions are performed in the second layer where membership function is replaced by the activation function of each artificial neuro cell. Five membership functions are determined for the error and the change of error. The output of this layer is obtained as follows:

$$net_j^2 = -\frac{(x_i - m_{ij})^2}{2(\sigma_{ij})^2}, y_j^2 = \exp\left(\text{net}_j^2\right) \tag{27}$$

σ_{ij} and m_{ij} , which are also input parameters, here represent the parameters of membership functions to be adapted. X_i represents the input of i th cell of 2nd layer. Similar to

fuzzy logic controller, the third layer of NFC consists of rule base and fuzzy rules are determined in this layer.

$$net_k^3 = \prod_j w_{jk}^3 x_j^3, y_k^3 = net_k^3 \tag{28}$$

X_j^3 represents the input of j th cell of third layer. The output of the system defined by using central clarification for Mamdani fuzzy logic:

$$net_0^4 = \sum_k w_{k0}^4 y_k^3, y_0^4 = \frac{net_0^4}{\sum_k y_k^3} \tag{29}$$

4th layer is called normalization layer where the accuracy of fuzzy rules are calculated. 5th layer is called as firing size of a rule. The firing degree of normalized rules are multiplied by linear f function in this layer. In order to update input and output parameters by using analog teaching method with back propagation algorithm, the squared error (E) which minimizes tracking error (e) is determined as follows (Jang et al. 1997; Dandil and Gokbulut 2005):

$$E = \frac{1}{2} e^2 \tag{30}$$

The performance index for the parameters of membership functions in PWM rectifier can be derived as follows:

$$\frac{\partial E}{\partial w_{k0}} = -e \cdot \text{sgn} \left(\frac{\Delta i_{dq}}{\Delta y_0^4} \right) \frac{1}{\sum_k y_k^3} w_{k0}^4 \frac{x_i - m_{ij}}{(\sigma_{ij})^2} y_j^2 \tag{31}$$

$$\frac{\partial E}{\partial w_{k0}} = -e \cdot \text{sgn} \left(\frac{\Delta V_{dc}}{\Delta y_0^4} \right) \frac{1}{\sum_k y_k^3} w_{k0}^4 \frac{x_i - m_{ij}}{(\sigma_{ij})^2} y_j^2 \tag{32}$$

$$\frac{\partial E}{\partial \sigma_{ij}} = -e \cdot \text{sgn} \left(\frac{\Delta i_{dq}}{\Delta y_0^4} \right) \frac{1}{\sum_k y_k^3} w_{k0}^4 \frac{(x_i - m_{ij})^2}{(\sigma_{ij})^3} y_j^2 \tag{33}$$

$$\frac{\partial E}{\partial \sigma_{ij}} = -e \cdot \text{sgn} \left(\frac{\Delta V_{dc}}{\Delta y_0^4} \right) \frac{1}{\sum_k y_k^3} w_{k0}^4 \frac{(x_i - m_{ij})^2}{(\sigma_{ij})^3} y_j^2 \tag{34}$$

Matlab model of the NFCs used in the simulation study is shown in Fig. 7. As shown in Fig. 7, inputs of NFCs were selected as error and change in error. Five membership functions are used for each input. Membership functions were constructed to represent the input and output values.

Figure 8 shows the fuzzy sets and corresponding bell membership function description of each signal for inputs and output. The fuzzy membership functions consist of five fuzzy sets: NB, NS, ZE, PS, PB as shown in Fig. 8.

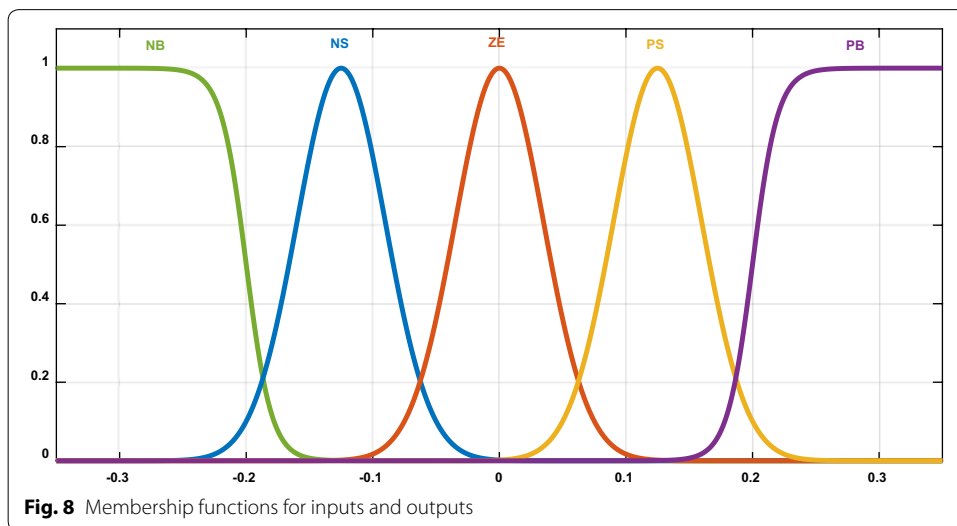
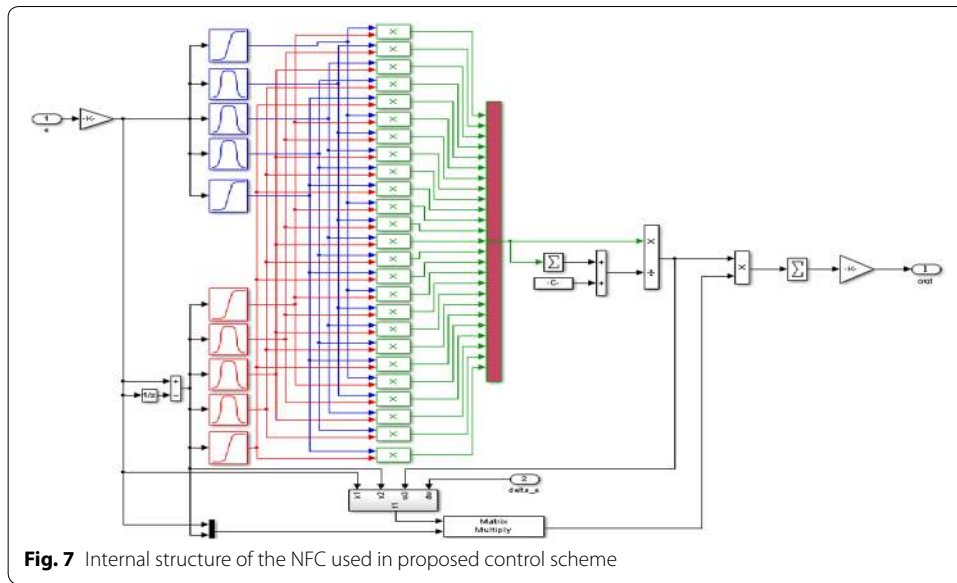


Table 1 shows the corresponding rule table for NFCs. The top row and left column of the matrix indicate the fuzzy sets of the variables e and Δe respectively, and the output variable Δu are shown in the body of the matrix numerically. There may be $5 \times 5 = 25$ possible rules in the matrix.

In the proposed NFC structure, precondition parameters of membership layer have been trained in the simulation model. During the simulation studies, output parameters have been trained using back-propagation learning algorithm. These parameters are adapted until the desired performance is reached.

Table 1 5 × 5 Rule table for fuzzy inference system

Δu	Δe				
	NB	N	Z	P	PB
e					
NB	NB	NB	NB	N	Z
N	NB	N	N	N	Z
Z	NB	N	Z	P	PB
PS	Z	P	P	P	PB
PM	Z	P	PB	PB	PB

Simulation results

In this section, it has been carried out a number of simulation studies in order to test the dynamic performance of proposed PWM rectifier system. The proposed control scheme used in PWM rectifier system is shown in Fig. 9a. As shown in this, DC bus voltage

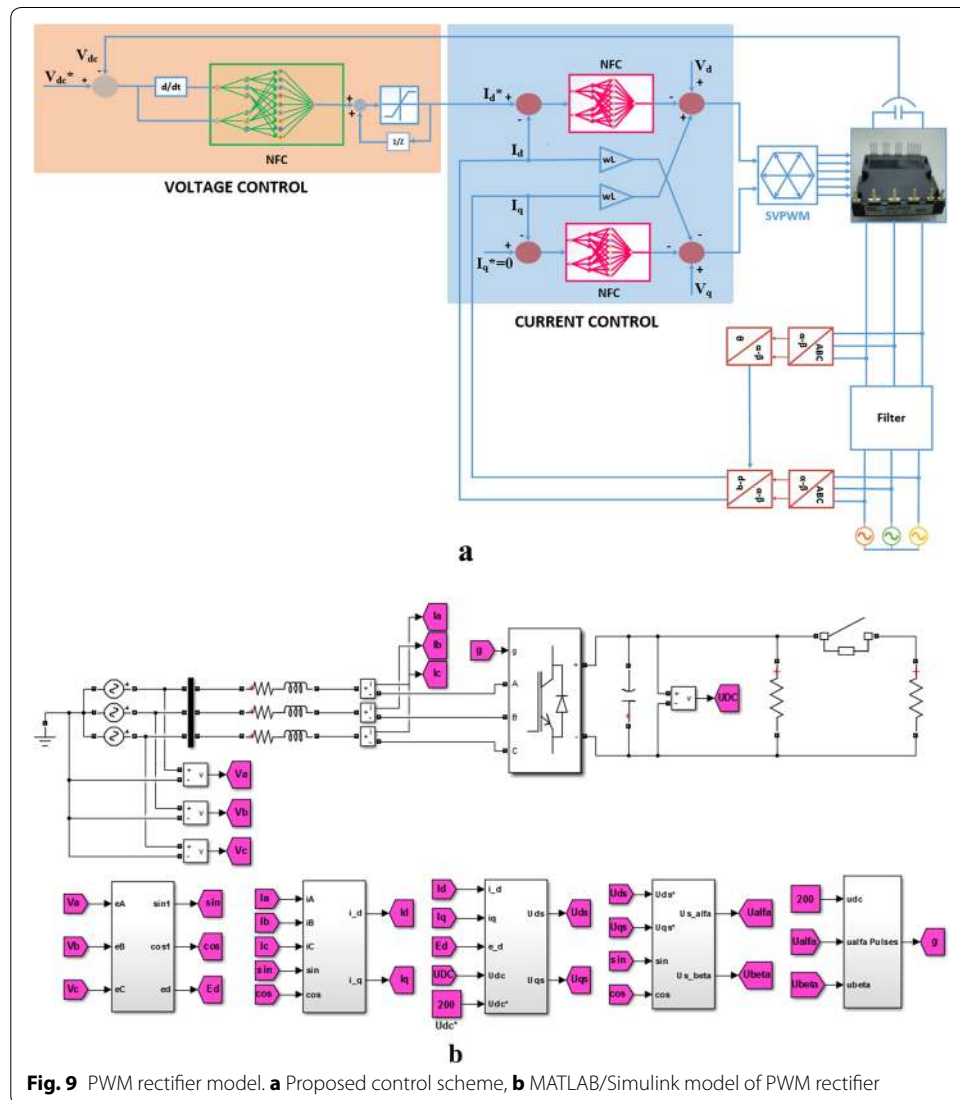


Fig. 9 PWM rectifier model. **a** Proposed control scheme, **b** MATLAB/Simulink model of PWM rectifier

and dq-axis currents of PWM rectifier are controlled by using NFC controllers. Also, the simulation model has designed in MATLAB/Simulink environment as shown in Fig. 9b. The transformation blocks used in the simulation model is also given in Fig. 9b.

The electrical and control parameters of PWM rectifier used in the simulation study are given in Table 2.

The first test is realized under steady state operation as shown in Fig. 10. Reference DC voltage of PWM rectifier is set to 200 V with unity power factor. It is seen from the Fig. 10a that the PI controller reaches to reference DC voltage after 101 ms with overshoot (nearly

Table 2 Electrical parameters of PWM rectifier

Parameters	Value
Input grid voltage	60 V _{rms} /50 Hz
Source resistance	100 mΩ
Source inductance	3.5 mH
DC bus capacitor	3.3 mF
Load resistance	20 Ω/10 Ω
Sampling time	50 μs
Switching frequency	5 kHz

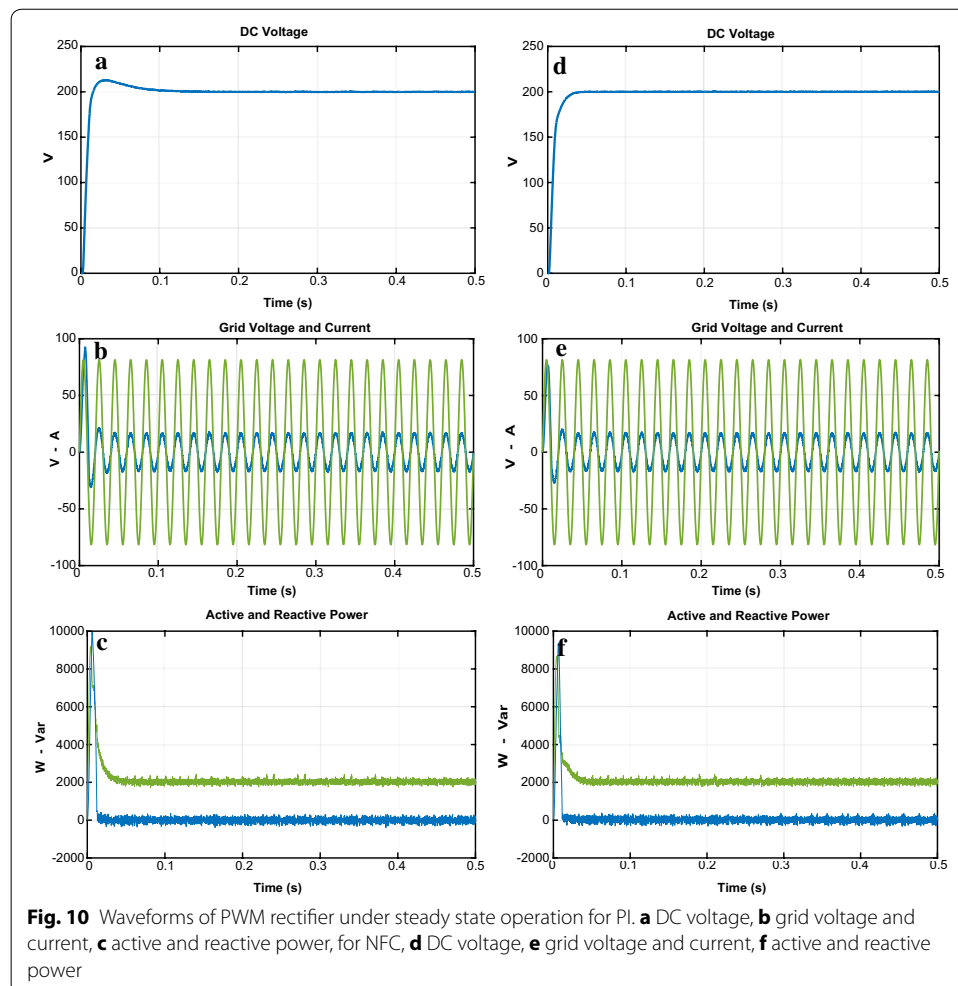
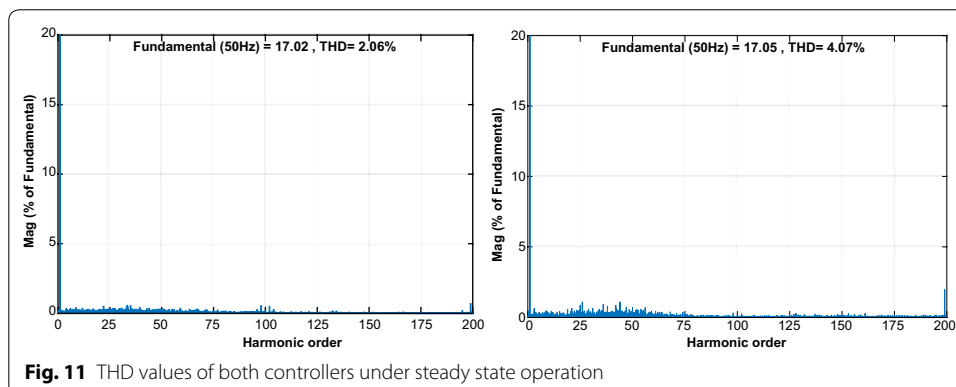


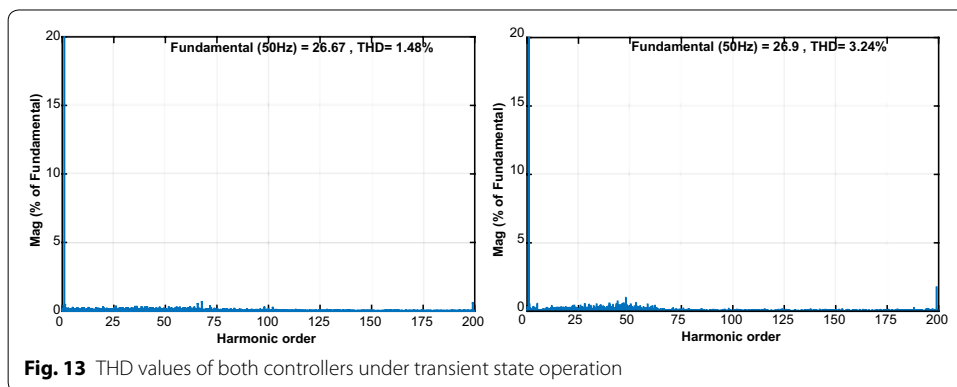
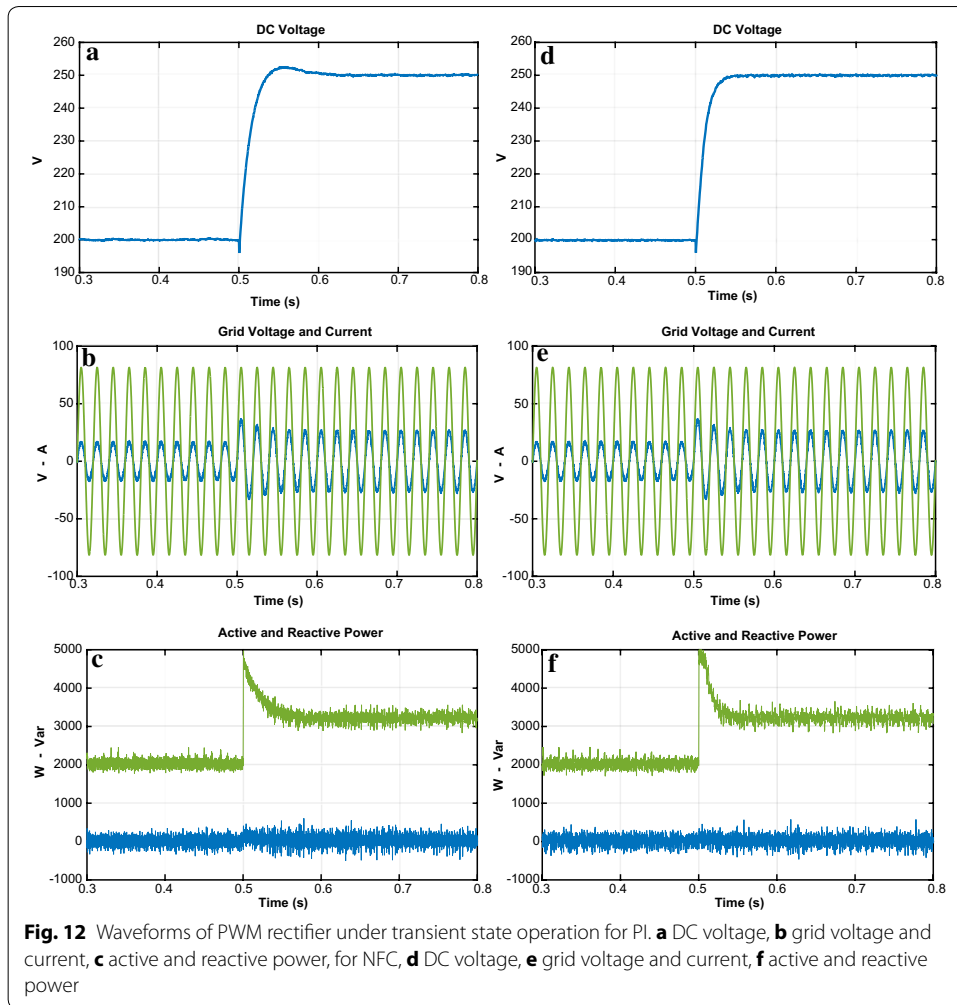
Fig. 10 Waveforms of PWM rectifier under steady state operation for PI. **a** DC voltage, **b** grid voltage and current, **c** active and reactive power, for NFC, **d** DC voltage, **e** grid voltage and current, **f** active and reactive power

7.15 %). Moreover, the input currents have nearly sinusoidal waveforms and in phase with grid voltage thus a unity power factor is nearly 0.99 and THD value is 4.07 %. As it can be seen from the Fig. 10d, the proposed controller reaches to reference DC voltage after 32 ms without overshoot. Also, the proposed control scheme has power factor of 1 and THD of 2.06 %. It is noticed that supply current in phase with grid voltage and so unity power factor is obtained. The waveforms related to active and reactive power are shown in Fig. 10c–f. It can be seen that active power close to new reference value and reactive power is zero because of the unity power factor operation. It can be seen that the performance of NFC is much better than PI controller in terms of settling time, maximum peak overshoot. Moreover, as shown in Fig. 11, THD levels of grid current for PI and controller and NFC under steady state operation are 4.07 and 2.06 %, respectively. These THD levels are smaller than specific limit that is mentioned IEEE 519-1992 standard.

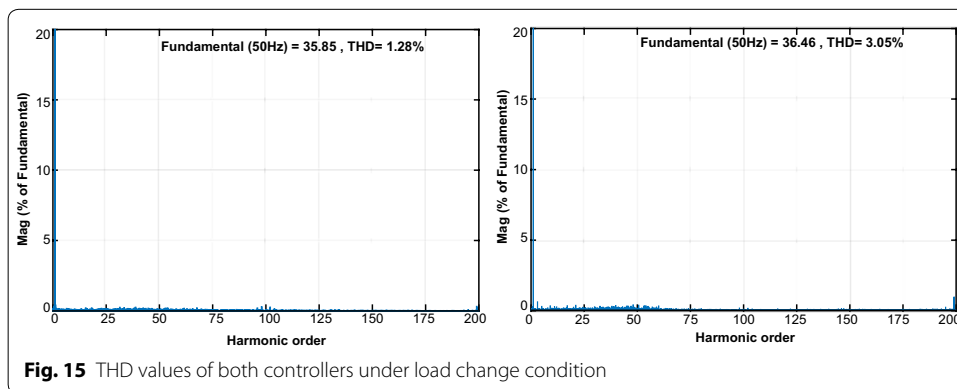
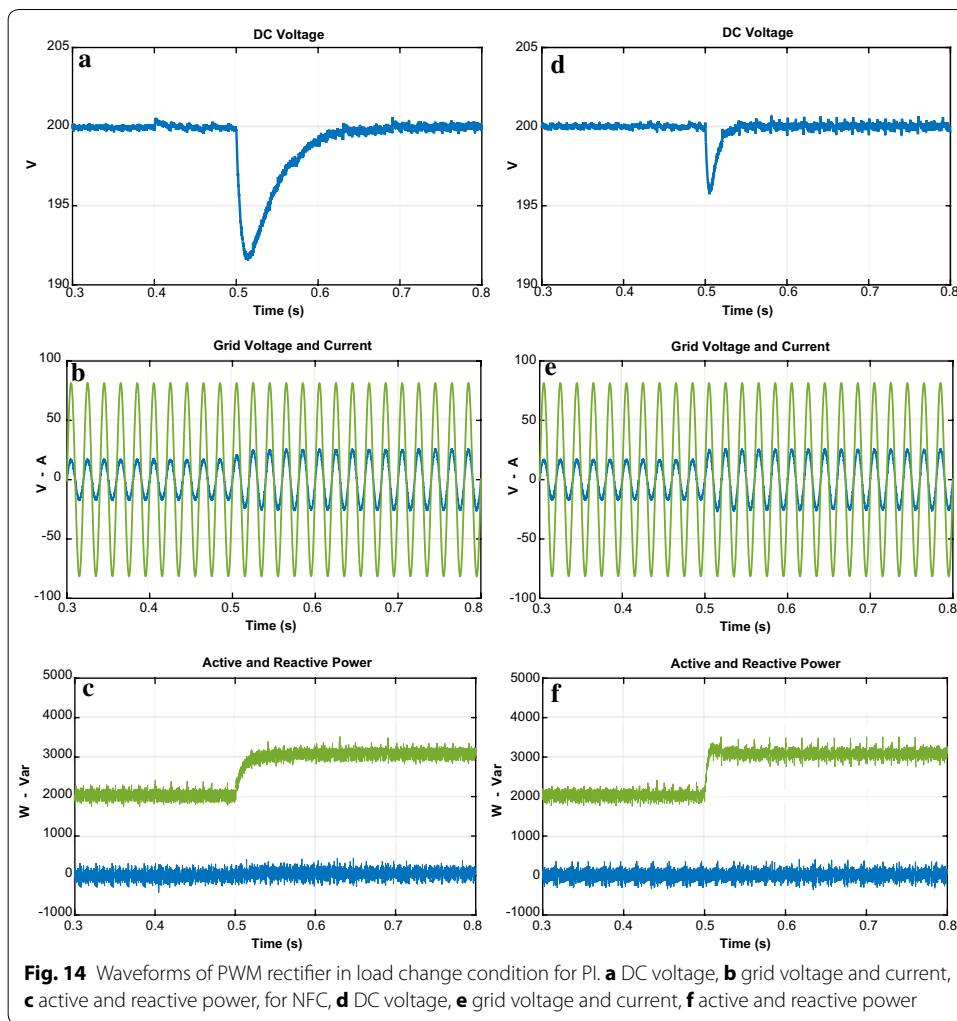
The second test is realized in order to demonstrate the dynamic performance of proposed control scheme and PI controller under step response. Figure 12 shows that the reference DC voltage steps up to 200 V from 250 V at 0.5 s. When applied the step DC voltage, PI controller response reaches the reference DC voltage at 0.6 s with overshoot while the proposed controller follows the reference DC voltage after 0.0358 s without overshoot and steady state error. The line currents given in Fig. 12b–e have clearly sinusoidal waveforms and in phase with grid voltage. Due to its good performance, the output active power of proposed controller reaches to its desired value faster than PI controller. Moreover, the reactive power response of proposed controller is rapidly regulated to zero. Figure 12 is clearly specified that reactive power control and active power is performed more effective with proposed controller. As shown in Fig. 13, THD levels of grid current for PI controller and NFC under transient state operation are 3.24 % and 1.48 %, respectively.

The third test is realized in order to indicate the performance of both controllers against the load change. The DC bus voltage responses with load at 0.3 s are given in Fig. 14a–d. When the load is applied, there is sudden dip in DC voltage. The DC voltage response of proposed controller falls from 200 to 196.5 V and it takes 0.021 s to reach the reference DC voltage whereas the DC voltage response of PI controller dips from 200 to 191 V and reaches after 0.125 s. Moreover, the reactive power responses of both controllers are shown in Fig. 14c–f. It can be seen from these figures that reactive power is zero despite the load change.

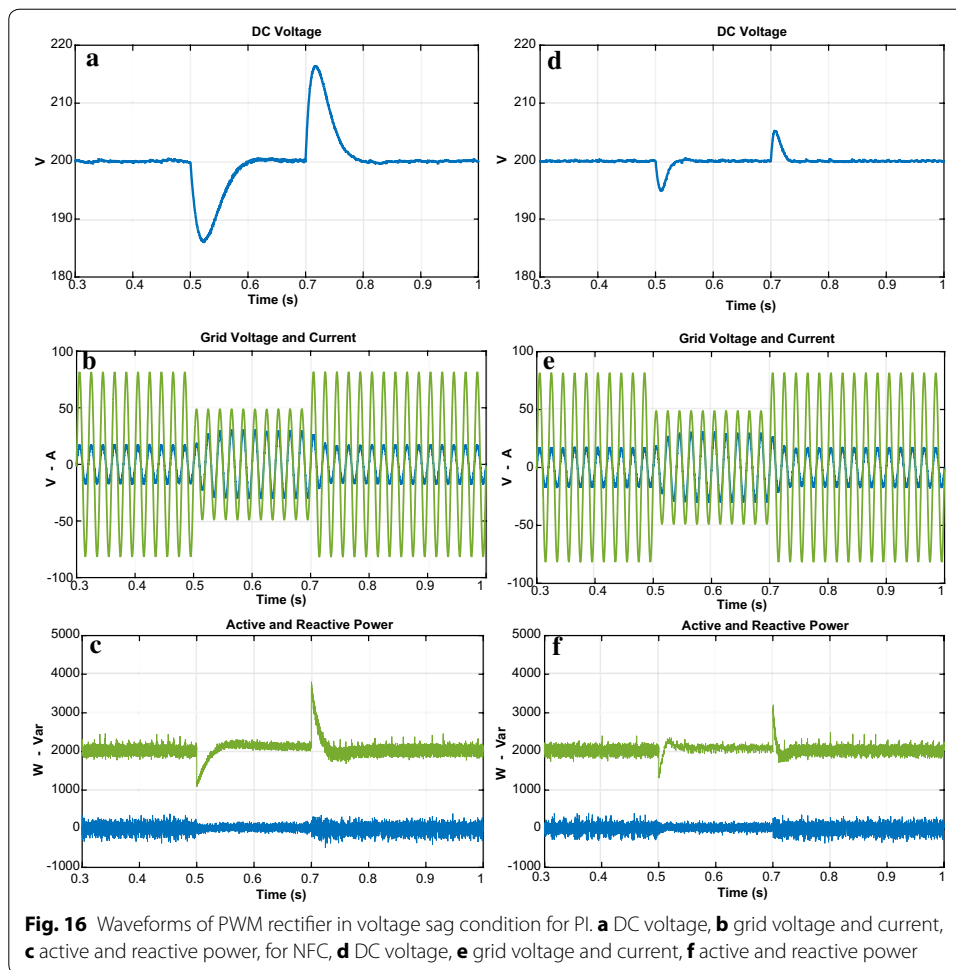




As shown in Fig. 15, THD values of both controllers under load change condition were analysed in order to better verify the success of the proposed controller in terms of THD and power quality. THD values of both controllers under load change condition are 1.28 % and 3.05 %, respectively. The study demonstrates that the proposed control scheme based PWM rectifier improves THD and power quality.

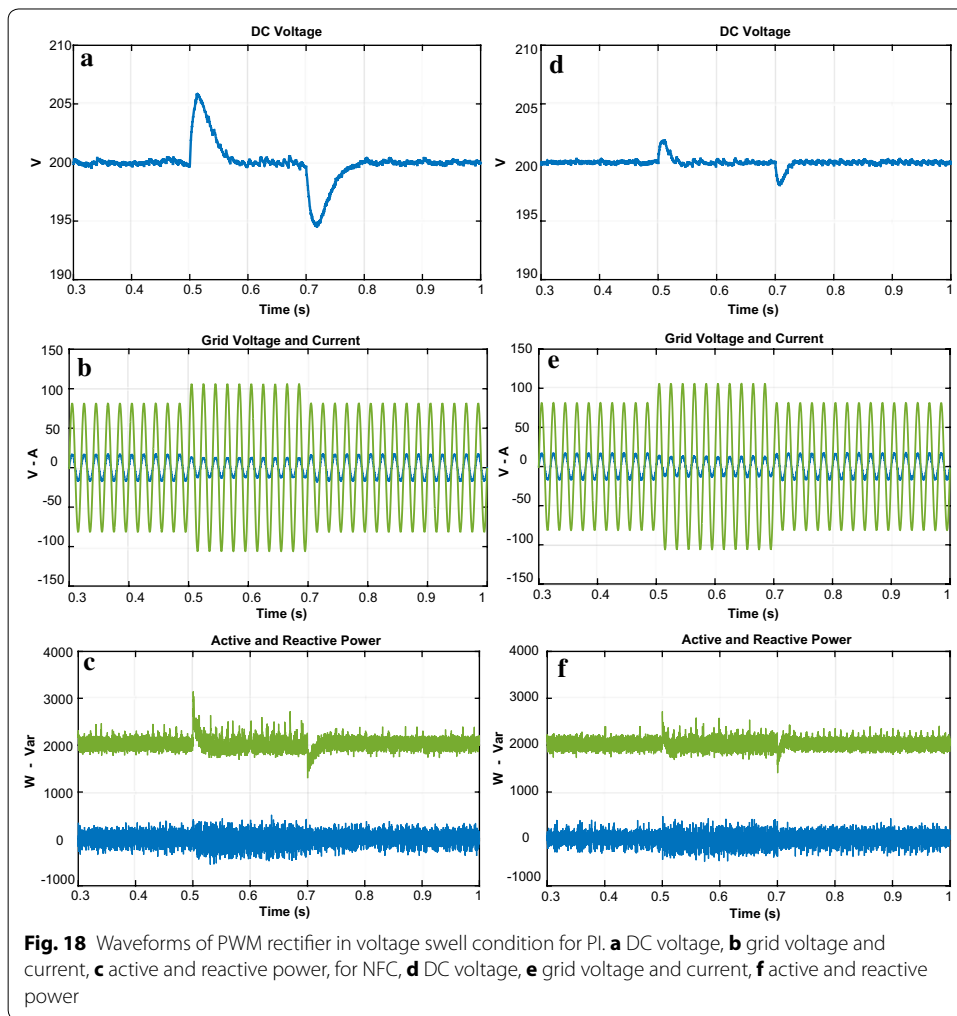
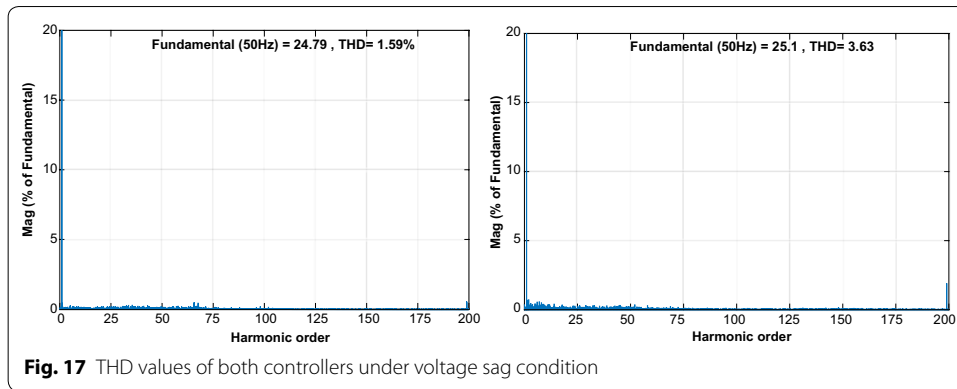


The fourth test is carried out to demonstrate the response of both controllers under voltage sag condition as shown in Fig. 16. In this test, the magnitude of grid voltage changes from 100 to 70 % at 0.5 s, and then changes back from 70 to 100 % at 0.7 s. When

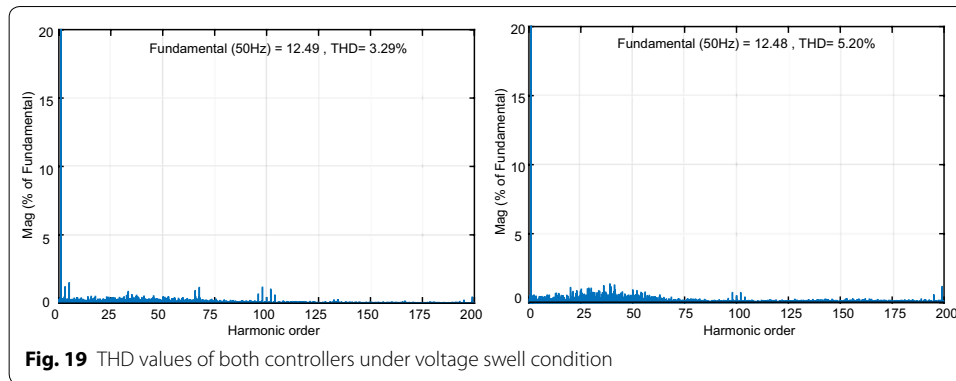


the voltage sag happens at $t = 0.5$ s, the enlarged DC voltage obtained from PI controller falls nearly 187 V and reaches reference DC voltage at 0.8 s whereas proposed controller falls 198 V and reaches reference DC voltage at 0.73 s. As clearly seen in Fig. 16d, although voltage sag at input stage is occurred, DC bus voltage response obtained from proposed controller is more durable than PI controller. Active power is nearly constant and reactive power is zero because of good performance of controllers. The line currents given in Fig. 16b–e have clearly sinusoidal waveforms and in phase with grid voltage. As shown in Fig. 17, THD values of both controllers under voltage sag condition were analysed and THD levels of NFC and PI controller are 1.59 % and 3.63 %, respectively.

The last test is realized in order to indicate the effectiveness of both controllers against voltage swell condition. Figure 18 shows the simulation results when the grid voltages occur 30 % of three-phase voltage swells at $t = 0.5$ to 0.7 s. According to Fig. 18a–d, when the voltage swell occurs, NFC controller rises 201.56 V and reaches to reference DC bus voltage at 0.72 s whereas DC voltage obtained from PI controller rises 206 V and reaches reference DC voltage at 0.8 s. Figure 18c–f depict that after change in the grid voltages, active power is rapidly regulated at the desired value without steady-state error. NFC has much better reference tracking after voltage swell have happened. Also, the line currents in given Fig. 18b–e have clearly sinusoidal waveforms and in phase with grid



voltage. Total harmonic distortion levels of of the both controllers under voltage swell condition are shown in Fig. 19. As clearly seen in Fig. 19, the proposed control scheme based PWM rectifier system has better performance than PI controller in terms of THD.



Conclusion

In this paper, a robust control scheme has proposed and developed using neuro-fuzzy controller for PWM rectifier. The emphasis was on the analysis, design and implementation of the proposed control scheme in MATLAB/Simulink environment. First, parameters of PI controller, which needs the mathematical model of the system to be controlled, are designed according to reduced block diagram. Neuro-fuzzy controller structure for control of DC voltage and dq-axis currents of PWM rectifier has developed via MATLAB/Simulink blocks. Designed control scheme is applied to the PWM rectifier and neuro-fuzzy structure is trained until obtaining the desired results. After designing of both controllers, simulation tests are realized for both controllers at the same conditions. According to simulation results, proposed control scheme gives more superior performance than PI controller with respect to rise time, settling time, overshoot, THD and PF in all test conditions. Moreover, proposed control scheme provides the desired reactive power exact and fast within own rated power limits even in the whole operating conditions.

Authors' contributions

MS provide the basic idea of the research and supervise. HA and FK researched the background literature, mathematical model of PWM rectifier and neuro fuzzy controller. HA, CY and AG modelled three phase PWM rectifier, organized and drafting of the manuscript. All authors read and approved the final manuscript.

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Competing interests

The authors declare that they have no competing interests.

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