# Improvement of power supply EMC by chaos

J.H.B. Deane and D.C. Hamill
Department of Electronic and Electrical Engineering,
University of Surrey,
Guildford GU2 5XH,
United Kingdom

### Abstract

We demonstrate the feasibility of using chaos to depress the spectral peaks of the interference from a switched mode power supply. A boost converter is used to show experimentally that the spectrum of the input current is spread, its peaks are reduced and EMC is improved, compared to the case when the circuit is operating periodically.

## 1. Introduction

Switched mode power supplies generate electromagnetic interference, generally consisting of the switching frequency and many harmonics. Such interference poses significant EMC problems, particularly in the face of new regulations. Although it may be possible to reduce the interference at source, it is generally necessary to employ filtering to reduce conducted interference and screening to reduce radiated interference.

However, since the regulations set an upper bound on the spectral content, various methods of amelioration have been proposed based on spread spectrum techniques. These include periodic or pseudo-random modulation of pulse width or switching frequency: see [1]–[3] and references therein. Implementing any of the above requires extra circuitry, adding to size and cost.

In conventional power supplies, all the waveforms are periodic and hence most of the energy is concentrated in narrow spikes at the clock frequency and its harmonics. With modulation, however, the energy is spread over a wider range of frequencies. The advantage is that the height of the spikes is now reduced, thereby making it easier to meet the EMC regulations.

Switched mode power supplies are now known to be capable of chaotic behaviour (*i.e.* deterministic, aperiodic behaviour that exhibits broadband spectral characteristics) [4], [5]. We therefore propose a novel technique: by deliberately designing a power supply to work in the chaotic region the spectrum of interference can be spread without extra circuitry.

In order to demonstrate the viability of the technique, we concentrate on the boost converter, operating at a low switching frequency (2.5kHz). The boost converter is particularly relevant, not only because it is relatively easy to model its chaotic behaviour [4], but also because it is widely used in power factor correction schemes [6]; these in turn are becoming mandatory in order to meet harmonic regulations.

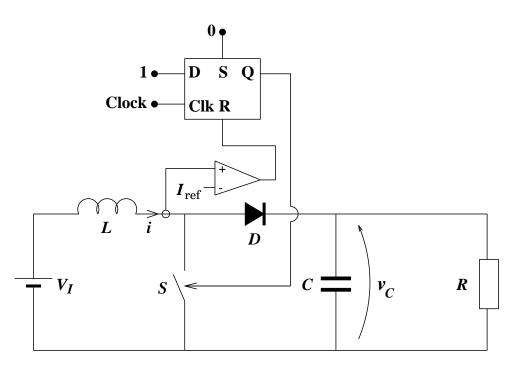


Figure 1: The boost converter studied in this paper.

# 2. Experimental results

A peak current controlled boost converter circuit is described in [4], and is shown in figure 1. This was built with  $R = 250\Omega$ , L = 100 mH,  $C = 220 \mu \text{F}$ ,  $V_I = 10 \text{V}$ , reference current  $I_{\text{ref}} = 0.5 \text{A}$  and clock frequency  $f_s = 2.5 \text{kHz}$ . With the feedback loop connected the circuit behaved chaotically — that is, the input current i(t) was aperiodic — and the output voltage was approximately 29V with a small chaotic ripple. A spectrum of i(t) was measured using an audio frequency spectrum analyser. The feedback loop was then disconnected and a pulse generator used instead to drive switch S periodically with a duty factor chosen so that the output voltage was again 29V. The current spectrum was again measured. The two spectra obtained are shown superimposed in figure 2.

The peaks in the spectrum occur at multiples of  $f_s$  and the table below shows the amount by which chaos reduces them.

Frequency	$f_s$	$2f_s$	$3f_s$	$4f_s$	$5f_s$	$6f_s$	$7f_s$	$8f_s$	$9f_s$	$10f_s$
Reduction (dB)	7	11	7	0	7	6	-2	5	8	4

$11f_s$	$12f_s$	$13f_s$	$14f_s$	$15f_s$	$16f_s$	$17f_s$	$18f_s$	$19f_s$	$20f_s$
-5	4	5	0	0	5	3	0	2	4

Although some peaks are greatly reduced (e.g. by 11dB at  $2f_s$ ), others are emphasised (e.g. by 5dB at  $11f_s$ ). The average reduction of 3.6dB represents a useful improvement, and this is without any attempt at optimisation.

Figure 2: Experimental spectra from the circuit of figure 1. The sweep rate was 5 sec/kHz and the resolution bandwidth was 100Hz. The comb-like spectrum was obtained for periodic behaviour and the broadband spectrum for chaos.

### 3. Conclusion

A useful spreading of the spectrum of the input current can be obtained simply by letting a boost converter operate chaotically. Despite the fact that no attempt was made to optimise the effect, the chaos-induced decrease in the spectral peaks is comparable with that obtained by 'artificially' spreading the spectrum, the latter requiring extra circuitry. In [2] a decrease of more than 10dB is claimed and in [3], up to 9dB.

Although the measurements have been done at low frequency, the results should scale to higher frequencies. Moreover, since the switching process also causes radiated interference, chaos is expected to reduce it by a similar amount.

Further work is needed to determine the best mode of chaotic operation and to apply the technique to practical power supplies.

## References

- [1] A.M. Stanković, G.C. Verghese and D.J. Perreault, Analysis and synthesis of randomised modulation schemes for power converters, **IEEE Transactions on Power Electronics**, vol. 10, no. 6, pp 680–693 (1995)
- [2] F. Lin and D.Y. Chen, Reduction of power supply EMI emission by switching frequency modulation, IEEE Transactions on Power Electronics, vol. 9, no. 1, pp 132–137 (1994)
- [3] D.A. Stone, B. Chambers and D. Howe, Easing EMC problems in switched mode power converters by random modulation of the PWM carrier frequency, Proceedings of the Applied Power Electronics Conference, vol. 1, pp 327–332 (San Jose, California, March 1996)

- [4] J.H.B. Deane, Chaos in a current-mode controlled boost dc-dc converter, **IEEE Transactions** on Circuits and Systems, vol. 39 no. 8, pp 680–683 (August 1992)
- [5] D.C. Hamill, J.H.B. Deane and D.J. Jefferies, *Modelling of chaotic DC-DC converters by iterated non-linear mappings*, **IEEE Transactions on Power Electronics**, vol. PE-7, no. 1, pp 25–36 (January 1992)
- [6] R. Redl, Power factor correction in single-phase switching-mode power supplies an overview, International Journal of Electronics, vol. 77, no. 5, pp 552–582 (1994)