Inferring Specifications to Detect Errors in Code

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Abstract

A new static program analysis method for checking structural properties of code is proposed. The user need only provide a property to check; no further annotations are required. An initial abstraction of the code is computed that over-approximates the effect of function calls. This abstraction is then iteratively refined in response to spurious counterexamples. The refinement involves inferring a context-dependent specification for each function call, so that only as much information about a function is used as is necessary to analyze its caller. When the algorithm terminates, the remaining counterexample is guaranteed not to be spurious, but because the program and its heap are finitized, absence of a counterexample does not constitute proof.

1. Introduction

Software model checkers typically work by extracting a state machine from the code. Procedure calls are treated as control constructs; the abstraction boundaries that they represent are not usually exploited in the subsequent analysis. This is odd, since the modularization of the code into procedures was presumably chosen in order to make reasoning easier.

More traditional program verification approaches, in contrast, made extensive use of the program structure in structuring the analysis. Each procedure would be checked against its specification, using specifications of the called procedures as surrogates for their code. If these approaches could be automated, we might have the best of both worlds: a fully automatic analysis that exploits the modularity of the code.

Moreover, software model checking techniques usually address only temporal properties; the traditional approaches accommodate arbitrary properties that relate the values of the program state before and after execution of a procedure.

Some tools have been developed that aim at exploiting the structure of the program. ESC/Java[9], for example, extracts verification conditions from a procedure, and presents them for proof (or refutation) to a specially tailored theorem prover. The tool has been applied successfully to substantial programs, but it suffers from an obstacle that severely limits its applicability in practice. It turns out that the burden of writing specifications for the called procedures is considerable. Moreover, to analyze a procedure at the root of a large tree, every procedure in the tree must be annotated. Jalloy[15], a counterexample detector for Java programs, suffers from the same problem, and although it can inline called procedures, such inlining does not scale.

This paper proposes a strategy to overcome this obstacle. A procedure-based analysis is performed that requires specifications of called procedures, but the specifications are inferred from the code rather than being provided by the user. Of course, determining the perfect specification that summarizes the exact behaviour of a procedure is not feasible. For this application, however, it is sufficient to summarize only those aspects of the behaviour that are relevant in the context of the calling procedure. Our inference scheme exploits this. In fact, the inferred specifications are sensitive not only to the calling context, but also to the property being checked. As a result, a very rough specification is sometimes sufficient, because even though it barely captures the behaviour of the called procedure, it nevertheless captures enough to verify the caller. By starting with the roughest specification first, and refining it as needed, our scheme ensures that no more inference work is done than is necessary.

The fundamental idea underlying the strategy is a familiar one: counterexample-guided refinement of an abstraction[3]. The key steps are as follows: (1) the analysis is applied to an abstraction of the code; (2) if no counterexample is found, the analysis terminates and has successfully verified the code (against the

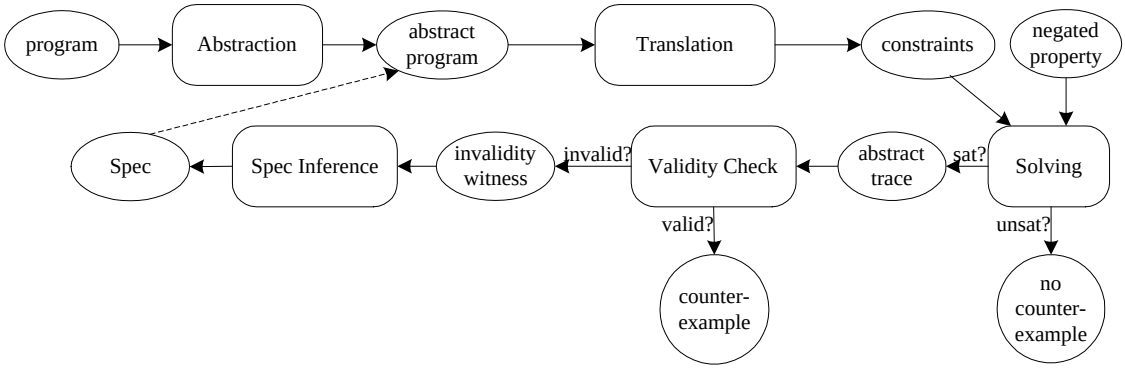

Figure 1. An Overview of the Framework

given property); (3) if a counterexample is found, it is checked for validity; (4) if the counterexample is valid, a fault has been discovered and the analysis terminates; otherwise (5) a more refined abstraction is computed, and the process is repeated.

This general scheme has been applied in a number of different contexts[1, 12, 8, 7]. Our approach differs from all of these in that the abstraction and its subsequent refinements follow the abstraction boundaries of the code itself. To our knowledge, all previous applications of this idea to software analysis involve refinement of predicate abstractions[10]. Our approach, in contrast, refines the specifications used to represent the behaviour of called procedures.

This paper describes a framework for counterexample-guided refinement of procedure specifications. It assumes an underlying analysis in which counterexamples are found by solving constraints extracted from the code and specification. The framework itself is not dependent on any particular properties of the logic used, although we use the Alloy modelling language as the logic, and a SAT solver as the constraint solver. In order to handle undecidable properties of data structures, the program is finitized (by bounding the number of loop unrollings, as in ESC/Java and Jalloy), and the space of possible heaps is finitized too (by limiting the heap's size). Consequently, although counterexamples are guaranteed not to be spurious, their absence does not constitute proof of correctness. The framework, however, does not depend on these compromises, and seems to hold promise for application in other contexts, such as the method recently proposed by Flanagan[7].

The rest of the paper is organized as follows: Section 2 gives an overview of our framework. Section 3 describes the basic structures. Section 4 gives the analysis steps. Section 5 describes our implementation. Section 6 describes our experiments. Section 7 gives related work, and Section 8 concludes the paper.

2. Overview

Our analysis is focused on checking code against a property given as a (partial) specification of a procedure selected by the user. We particularly target *structural properties*, i.e., properties that constrain the configuration of the heap after the execution of a procedure. The property can be expressed in any language that can be converted to a set of logical constraints.

Figure 1 shows our analysis framework. It consists of the following phases:

Abstraction: We construct an initial *abstract program* from the given program by replacing all procedure calls in the analyzed procedure with some approximate *specifications*. This abstraction is an overapproximation of the original code: all feasible executions of the original code are feasible in the abstract program, but not vice versa.

Translation: The abstract program is translated to a set of constraints that constitute a logical formula. The translation preserves the semantics of the code: any execution in the abstract code corresponds to a solution satisfying the generated formula and vice versa. Our method is independent of exactly how this translation is done, as long as it is semantics-preserving.

Solving: The formula generated in the previous phase is conjoined with the negation of the user-provided assertion. It is then given to a constraint solver to find a satisfying solution, called an *abstract trace*. An abstract trace denotes an execution in the abstract program that violates the assertion. If no solution is found, the assertion holds in the abstract program and thus in the original (finitized) program.

Validity Check: An abstract trace suggests a behavior for each eliminated procedure by assigning values to its inputs and outputs. The validity of each suggested behavior is checked in the original program, again using a constraint solver. If a behavior is valid,

the solution found in this phase is used to concretize the abstract trace. If the behaviors of all procedure calls are valid, the trace is a feasible counterexample and is returned. However, if the inputs and outputs assigned to a procedure denote an invalid behavior, they represent an *invalidity witness*.

Specification Inference: A more precise specification is inferred for the procedure corresponding to the found invalidity witness. We use a constraint solver capable of generating proofs to construct a proof of invalidity for the witness. A specification that rules out the given invalid behavior is then extracted from the proof. Subsequently, the abstract program is refined by conjoining this new specification with the old one, and the process starts over.

2.1. Motivating Example

The example given in Figure 2 illustrates our method. The `intersect` function is selected for analysis. It takes two lists of integers and returns a list of the elements that appear in both of them. The given property asserts that if either one of the input lists is empty, the returned list is also empty.

For simplicity, let us assume that the finitized code (not shown here) unwinds the loops only once. Figure 3.a shows the initial specifications computed for each called function. A specification is a logical formula whose elementary subformulas update variables and fields. The keyword `$return` denotes the returned value of a function. A question mark denotes a nondeterministic computation that yields any value of the appropriate type arbitrarily. For example, `$return` ← `?` in the specification of `contains` says that any `boolean` value may be returned; `?.val` ← `?` says that the value of the `val` field for any `List` object may be set to any integer. Any variable or field not assigned a value in a specification has the same value before and after the execution of the function.

The initial abstract program is presented in Figure 3.b. The line numbers shown in this figure correspond to the line numbers given in Figure 2. The specifications shown in Figure 3.a are inlined at the call points of their corresponding functions.

This abstract program and the negation of the given property are then translated to a set of constraints. The abstract trace shown in Figure 4 is found by a constraint solver as a counterexample. Each line in this figure consists of a line number, a program state and a statement from the abstract program that is executed. The line numbers correspond to the line numbers in Figure 3 and show the control flow of the execution. The program state at each line shows the values bound to the variables before the execution of the statement in

```
class List {
  int val;
  List next;

  List(int v) {
      val = v;
      next = null;
  }
  /** assert:
     (l1 = null) || (l2 = null) => $return = null **/
  static List intersect(List l1, List l2) {
0:  List res = null;
1:  while (l1 != null) {
2:    boolean cnt = contains(l2, l1.val);
3:    if (cnt)
4:      if (res == null)
5:        res = new List(l1.val);
6:      else
7:        res.add(l1.val);
8:    l1 = l1.next;
9:  }
10: return res;
  }
  void add(int v) {
      List c = new List(v);
      List l = this;
      while (l.next != null)
        l = l.next;
      l.next = c;
  }
  static boolean contains(List l, int v) {
      while (l != null) {
        if (l.val == v) return true;
        l = l.next;
      } return false;
  }}
```

Figure 2. Example

that line. Because of space limitations, the unchanged values are not repeated.

In this trace, the `contains` function and the `List` constructor are called. The values assigned to the nondeterministic computations are such that the `contains` function (Line 2) returns `true` for the inputs `int0` (`l1.val`) and a `null` list (`l2`). Furthermore, the `List` constructor (Line 5) assigns `int1` instead of `int0` (`l1.val`) to `res.val`. Both of these behaviors are invalid. The behavior assigned to the `contains` function is analyzed first, by checking the original code of this function against the values given to its inputs and outputs. Since the behavior is invalid, it is marked as an invalidity witness and no further validity checking is performed at this stage.

A constraint solver is then used to generate a proof

```
List.List(v):[$return.val <- ? && $return.next <- ?]
List.add(v):[?.val <- ? && ?.next <- ?]
List.contains(l, v):[$return <- ?]

                    (a)
class List {
  int val;
  List next;
static List intersect(List l1, List l2) {
0: List res = null;
1: if (l1 != null) {
2:   [boolean cnt <-?] //contains(l2, l1.val)
3:   if (cnt)
4:     if (res == null)
5:      [res.val <-? && res.next <-?]//List(l1.val)
6:     else
7:      [?.val <-? && ?.next <-?] //res.add(l1.val)
8:   l1 = l1.next;
9: } <assert l1 == null>
10:return res;
}}
                    (b)
```

Figure 3. (a) Initial Specification of the Called Functions. (b)Initial Abstract Program

for the invalidity of the inputs and outputs assigned to the `contains` function. The following specification is then extracted from the proof.

```
List.contains(l, v): [l <- l2 && v <- l1.val &&
  (l = null => $return <- false) &&
  (l != null => ($return <- ? && l <- ?))]
```

The generated specification only includes those parts of the `contains` function that are relevant to the found counterexample; the rest of the function is still abstracted. This new specification is inlined at the call point of the `contains` function and the process starts over. In this example, the analysis of the new abstract program generates no counterexamples. Thus, the process terminates and the property has been validated.

3. Basic Structures

3.1. Abstract Program

Syntax. An *abstract program* is constructed to check the correctness of a procedure selected by the user, called the *initial procedure*. Our framework can be applied to any programming language that supports procedure declarations and can be translated to logical constraints. We use a subset of Java syntax shown in Figure 5 to illustrate our approach.

```
 0: [l1.val = int0, l1.next = null,
      l2 = null] res = null;
 2: [res = null, ...] cnt <- ?
 5: [cnt = true, ...] res.val <- ? && res.next <- ?
 8: [res.val = int1, res.next = null,..] l1 = l1.next;
10: [l1 = null, ...] $return = res;
```

Figure 4. Abstract Trace for the Example

An abstract program consists of a set of class declarations that may contain procedure definitions. A procedure is defined by a name and a sequence of formal parameters and may return a value. A statement can be a local variable definition, an assignment, a branch, a return statement or a procedure call. It is assumed that expressions are free of side effects. Branch conditions are boolean predicates represented by *pred*. A procedure call in an abstract program is a triple consisting of a flag, the name and actual parameters of the procedure, and a specification. A flag can be either *transparent* or *opaque*. A transparent flag means that the body of the called procedure will be inlined in the calling context during the analysis. An opaque flag, on the other hand, means that the provided specification will be used for the called procedure during the analysis.

Since the analysis is done on a finitized program, there are no loops in our language. All loops and recursive calls in the original program are assumed to be unwound some fixed number of times before the abstraction.

Semantics. We use *program points* to denote the control points in a program. A program point corresponding to a procedure call is called a *call point*. The set of program points and call points of a program are represented by Π and Ψ respectively ($\Psi \subseteq \Pi$). Furthermore, we define s_π to denote the statement from the original program that corresponds to a program point π. Thus, for a call point ψ, s_ψ denotes the procedure called corresponding to ψ.

A *program state* σ is defined at each program point π as a mapping from variables accessible at π to some values. The set of all possible states of a program is denoted by Σ.

Each program statement can be viewed as a transition in the state of the program and thus, represented by a set of pairs of program states, i.e. $s \subseteq \Sigma \times \Sigma$. That is, a pair of program states (σ, σ') is included in the set defining a statement s if and only if executing s in the state σ can result in the state σ'.

Transformations. Two transformations are defined on abstract programs: *close* and *open*. Given

```
program ::= classDecl*
classDecl ::= class class{fieldDecl* procDecl*}
fieldDecl ::= class field;
procDecl ::= class proc(paramDecl*){stmt}
paramDecl ::= class var,
stmt ::= class var | var = new class()
      |  var = expr | expr.field = expr
      |  if pred stmt else stmt
      |  return expr | procCall
      |  stmt; stmt
procCall ::= flag proc(expr*) <spec>
flag ::= transparent | opaque
```

Figure 5. Syntax for Abstract Program

```
spec ::= var <- expr | var <- ? | ?.field <- ? |
         var = expr | !spec | spec && spec |
         spec || spec | spec => spec
```

Figure 6. Syntax for Specification

an abstract program, the close transformation changes the flags of all procedure calls to opaque, i.e. abstracts all procedure calls. The open transformation, in contrast, takes an abstract program and a call point and changes the flag at that call point to transparent, i.e. inlines one procedure call.

3.2. Specification

A *specification* describes the behavior of a procedure in a calling context either exactly or over-approximately. As shown in Figure 6, a specification is a logical formula. The $\leftarrow$ sign denotes an assignment whereas $=$ is an equality predicate. `var` $\leftarrow$ `expr` changes the value of the variable `var` to the expression `expr`. A question mark in an assignment denotes a nondeterministic value. It can be replaced with any computation of the appropriate type. Thus, `var` $\leftarrow$ `?` allows the value of `var` to change arbitrarily whereas `?.field` $\leftarrow$ `?` allows an arbitrary change in the value of the given field in any object of the appropriate type. Furthermore, the logical operators negation, conjunction, disjunction, and implication are respectively denoted by `!`, `&&`, `||`, and `=>`. Any variable or field not assigned a value in the specification of a procedure is assumed to have the same value before and after the procedure call.

3.3. Abstract Trace

An *abstract trace* denotes an execution of an abstract program represented by a sequence of pairs of program points and program states, i.e. $\overrightarrow{\Pi \times \Sigma}$. Two consecutive pairs (π, σ) and (π', σ') in a trace t mean that $s_{\pi'}$ is executed immediately after s_π, and that σ and σ' are the program states before the execution of s_π and $s_{\pi'}$ respectively. It should be noted that the program state of the first pair denotes the initial state of the program which defines an initial heap configuration. The program state of the last pair represents the final state of the program. The program point of the last pair is a dummy point indicating the end of the program.

In an abstract trace t, the state of the program at a point π is denoted by $state_t(\pi)$. Furthermore, for a program state σ, $succ_t(\sigma)$ gives the program state immediately following σ in t. (The final program state does not have a successor.) A trace t is *valid* if and only if at each program point π included in t, the transition of the program state is consistent with the semantics of the statement corresponding to π as expressed in the original program. That is,

t `is` *valid* $\iff \forall(\pi, \sigma) \in t, (\sigma, succ_t(\sigma)) \in s_\pi$

3.4. Invalidity Witness

An *invalidity witness* is a triple of a program point and two program states (π, σ, σ') where the state transition from σ to σ' is not consistent with the semantics of the original statement corresponding to π, (i.e. $(\sigma, \sigma') \notin s_\pi$).

4. Basic Computations

4.1. Abstraction

During the abstraction phase, initial specifications are computed for all procedure calls. Initial specifications could allow any arbitrary behavior for the procedures. However, starting with more precise specifications can result in fewer refinements.

The initial specification we compute for a procedure aims at preserving its *frame conditions*, i.e. any variable or field not mutated by the procedure is not mutated in the specification. However, not all frame conditions can be computed statically. For example, if a program uses dynamic dispatching so that different procedure bodies are bound to a single procedure call in different executions, computing exact frame conditions statically is impossible. Consequently, we compute conservative specifications: any memory location that *may* be changed by a procedure call is allowed to change.

Figure 7 gives our abstraction rules. For a procedure p called at a program point ψ, the *global set* G_ψ denotes the set of all objects accessible both in p (the callee) and at ψ (the caller). Any change made by p to an object in G_ψ is visible to its caller.

δ[var = new class] = var $\leftarrow$? (if var $\in$ G)
δ[var = expr] = var $\leftarrow$? (if var $\in$ G)
δ[expr.field = expr] = ?.field $\leftarrow$?
δ[if pred s else s'] = δ[s] && δ[s']
δ[return expr] = $return $\leftarrow$?
δ[proc(expr*)] =δ[stmt] (where proc(var*){stmt})
δ[s; s] = δ[s] && δ[s']

Figure 7. Abstraction Rules

The abstraction function δ constructs a conservative specification for a procedure call based on its global set. As shown in Figure 7, any modification by a procedure to an object in its global set is reflected as a nondeterministic assignment in the procedure's specification. In order to take care of possible aliasing in the original program, modifications to a field f of an object of type T causes nondeterministic values to be assigned to the field f of all objects of type T. The δ function is not applied to expressions since they are assumed to have no side effects.

In order to abstract a procedure p, i.e. to determine what memory locations it may mutate, all of its callees should be abstracted first. Thus, procedures should be abstracted in a certain order. We compute the order by constructing the call graph g of the initial procedure. Since the program is finitized, g is an acyclic directed graph (DAG). Therefore, we can compute a topological sort[5] for g that is an ordering l over all procedures so that all callees of a procedure p precede p in l. The procedures are therefore abstracted in the order they appear in l.

After computing initial specifications, an abstract program is generated from the original program by annotating all procedure calls with their computed specifications. The close transformation is then applied to the generated program to make all procedure calls abstracted.

4.2. Validity Check

A counterexample found in an abstract program is an abstract trace that should be checked for validity in the original program. Since the only abstracted statements are procedure calls, the only state transitions that may be invalid in an abstract trace are those corresponding to call points. As our abstraction is based on the procedure call hierarchy of the code, the check for validity is also done hierarchically. A procedure q called within a procedure p is checked for validity only after the validity of the state transition assigned by the abstract trace to p has been validated.

Figure 8 shows how the validity of an abstract trace t is checked. The `cpSet` function takes an abstract trace

```
procedure validityCheck(t:AbsTrace):Witness{
 callpoints = cpSet(t);
 forall ψ ∈ callpoints {
   p = s_ψ;
   pre = state_t(ψ);
   post = succ_t(pre);
    d̂ = toConstraint(pre) ∧ toConstraint(post);
   p̂ = toConstraint(p);
   solution = solve(p̂ ∧ d̂);
    if (solution) {
     t.concretize(ψ, solution);
     callpoints = callpoint ∪ cpSet(solution)
   }else return (ψ, pre, post);
 } return null;
}
```

Figure 8. Validity Check Routine

and returns all of its call points as a set. For each call point ψ in t, the open transformation is applied to the abstract program to get the body of the procedure p called at ψ. It should be noted that all procedures called within p are still abstracted due to the semantics of the open transformation.

Variables *pre* and *post* denote the states of the program before and after the procedure p is called in the trace t. These states are translated to sets of constraints constituting logical formulas. The conjunction of these formulas that encodes the data is represented by $\hat{d}$. The procedure p is also translated into logical constraints denoted by $\hat{p}$. This translation is semantics-preserving. However, since the callees of p are over-approximated, the generated formula is an over-approximation of p.

A constraint solver is then used to find a satisfying solution for $\hat{p} \wedge \hat{d}$, i.e. to determine whether executing p in the assigned pre-state might result in the assigned post-state. A solution denotes a trace t' in p validating the assigned state transition. The abstract trace t is then concretized at the call point ψ by inlining t'. However, t' may introduce new call points corresponding to the procedures called in p. Since these call points are abstract, the validity of their state transitions in t' will be checked in the next iterations of the loop in the validity check routine. Although the exact order in which call points are checked may affect the performance of the analysis, it does not affect the correctness of the method as long as all call points are checked.

If no satisfying solution exists for the formula $\hat{p} \wedge \hat{d}$ at a call point ψ, it means that executing the procedure p in the pre-state can not result in the given post-state. In this case, the triple of $(\psi, pre, post)$ is returned by the routine as an invalidity witness. However, if the

```
procedure SpecInference(ψ: CallPoint,
                        σ, σ′:ProgState): Spec {
 p = s_ψ;
 d̂ = toConstraint(σ) ∧ toConstraint(σ′);
 p̂ = toConstraint(p);
 ĉ = invalidityProof(p̂ ∧ d̂);
 return (conj(set(ĉ) − set(d̂)));
}
```

Figure 9. Spec Inference Routine

state transitions corresponding to all call points are valid, no invalidity witness is returned and the trace t is a feasible counterexample.

4.3. Specification Inference

Given an invalidity witness (ψ, σ, σ'), a more precise specification is generated for the procedure p called at ψ that rules out the given invalid state transition. Figure 9 shows the specification inference routine. The pre-state σ and the post-state σ' of p are translated into logical formulas whose conjunction is denoted by $\hat{d}$. The body of p is also translated into a semantics-preserving logical formula $\hat{p}$.

Since the formula $\hat{p} \wedge \hat{d}$ is unsatisfiable, a constraint solver capable of generating proofs is used to find a proof of invalidity, namely $\hat{c}$. The proof of invalidity for a formula f gives a subset of the constraints in f that are inconsistent. Thus, $\hat{c}$ encodes the reason that the given state transition is not valid. However, $\hat{c}$ can not be used as a specification because it is unsatisfiable; using it in the analysis causes any assertion to be vacuously true. In order to get the specification, we need to extract a valid tautology from $\hat{c}$.

The translation of a program state or a procedure to constraints, i.e. the `toConstraint` function, generates a formula that is a conjunction of a set of constraints. The operator $set(f)$ breaks any such formula f into its constituting set of constraints. That is, if $f = c_1 \wedge c_2 \wedge \ldots \wedge c_n$, then $set(f) = \{c_1, c_2, \ldots, c_n\}$. The operator $conj(C)$ does the reverse, i.e. takes a set of constraints and returns their conjunction as a logical formula. Thus, if $C = \{c_1, c_2, \ldots, c_n\}$, then $conj(C) = c_1 \wedge c_2 \wedge \ldots \wedge c_n$.

Since $\hat{c}$ is an invalidity proof for $\hat{p} \wedge \hat{d}$, by definition, $set(\hat{c}) \subseteq set(\hat{p} \wedge \hat{d})$ and $\hat{c}$ is unsatisfiable. However, $\hat{p}$ is satisfiable because any execution of the procedure p is a solution to $\hat{p}$. Furthermore, $\hat{d}$ is also satisfiable because its constraints are all disjoint, i.e. each one defines the value of one variable. Therefore, $\hat{c}$ must include constraints of both $\hat{p}$ and $\hat{d}$. That is, $\hat{c} = \hat{q} \wedge \hat{r}$ where $set(\hat{q}) \subseteq set(\hat{p})$ and $set(\hat{r}) \subseteq set(\hat{d})$.

The subformula $\hat{q}$ denotes a sequence of statements in p that shows the values defined in $\hat{r}$ do not indicate a valid state transition. Since $set(\hat{q}) \subseteq set(\hat{p})$, $\hat{q}$ is satisfiable and is used as a specification. It can be extracted by comparing $\hat{c}$ against $\hat{d}$, i.e.

$\hat{q} = conj(set(\hat{q} \wedge \hat{r}) - set(\hat{d})) = conj(set(\hat{c}) - set(\hat{d}))$

The subformula $\hat{q}$ rules out the given invalid state transition and is returned by the specification inference routine to be merged with the old specification of p. The specification generated in this way is *context-dependent*, i.e. it only encodes those parts of p that are relevant to the found counterexample. The rest of the procedure is still abstracted and, therefore, new frame conditions are computed for it.

5. Implementation

In this section we explain our particular instantiation of the proposed framework.

Inputs: We assume that the input program is in Java. It is automatically finitized by unrolling the loops and bounding the depth of recursive calls to some certain number provided by the user. Our specification language is Alloy[14] which is a first order relational logic that provides transitive closure operators, making it well suited for expressing structural properties.

Abstraction: In this phase, an Alloy specification is inferred for each procedure call. Since Alloy is a declarative language with no mutations, variables and fields are renamed whenever their values are updated. This technique was previously used in Jalloy[15].

Translation: In this phase, the abstract program, which is a combination of Java statements and Alloy formulas, is converted into a boolean formula. This is done in two steps: (1) The Java parts are translated into Alloy as explained in detail elsewhere[21]. In this translation, each control point in the Java program is encoded as a boolean Alloy variable. Java objects are encoded as Alloy variables and Java fields are encoded as Alloy relations. (2) The generated Alloy formula is then conjoined with the Alloy parts of the abstract program and is converted into a boolean formula using the Alloy compiler[13]. This translation is sound. However, since first order logic is undecidable, the translation is done in a finite *scope*– a user-provided finite bound on the number of objects in the heap. The translation is complete within the given scope.

Solving: The Alloy assertion provided by the user is negated and converted to a boolean formula, again using the Alloy compiler. It is then conjoined with the formula encoding the abstract program. Any SAT solver can be used as a constraint solver to find a satisfying solution for this formula. We use ZChaff[17] in our implementation. A solution is an assignment of

truth values to all boolean variables in the formula so that the whole formula becomes true. The control flow variables assigned the value *true* represent an abstract trace. Values of variables and relations in each state of the program can be inferred from the solution.

Validity Check: In order to check the validity of an abstract trace, again we use ZChaff since it is capable of generating a proof of unsatisfiability called an *unsat core*[22]. To check the validity of a state transition at a call point, the boolean values representing those states are conjoined with the translation of the body of the corresponding procedure. If the resulting formula is satisfiable, an execution of the procedure can be extracted from the solution as in the previous phase.

Spec Inference: If no solution is found during the validity check of a procedure, ZChaff generates an unsat core. The input of ZChaff is a boolean formula in conjunctive normal form (CNF). A CNF formula is a conjunction of a set of *clauses* that are disjunctions of some *literals*. An unsat core is also in CNF format. It gives an unsatisfiable subset of the clauses in the input formula. The clauses that encode program statements in the unsat core are extracted by comparing the unsat core against the CNF formula encoding the pre and post states as explained before. These clauses form a CNF formula that is then translated back to Alloy using a technique described in a previous paper[19]. The resulting Alloy formula is a new specification that is conjoined with the previous specification of the procedure to constitute a more detailed specification.

6. Experiments

We applied our method to check some properties of the code given in Figure 10. The code is inspired by our own implementation of the framework and has extensive structural manipulations.

The code defines two linked lists, `NodeList` and `EdgeList`, as subclasses of the data type `List` . Their main function is `removeAll` which removes all the elements of the given list from the receiver object. A directed `Graph` structure is defined using lists of nodes and edges. The sets of incoming and outgoing edges of each node are represented by `inEdges` and `outEdges` fields in `NodeListElem`. In the `remove` function of the `Graph` class, the given list of nodes is deleted from the graph by removing it from the `nodes` list and removing all of the edges adjacent to any of those nodes from the `edges` list.

Figure 11 shows some of the properties checked in this code, expressed in Alloy. In these properties, a primed field gives the value of the field after the function is executed whereas an unprimed one gives the

```
class ListElem {
  int id;
  ListElem next; }
class List {
  ListElem first;

  void removeAll(List l) {
    ListElem e1 = first;
    ListElem prev = null;
    while (e1 != null) {
      int id = e1.id;
      if (l != null && l.contains(id)) {
        if (prev != null)
          prev.next = e1.next;
        else
          first = e1.next;
      } else
          prev = e1;
      e1 = e1.next;
    }}
  boolean contains(int id) {
    ListElem e = first;
    while (e != null) {
      if (e.id == id)
        return true;
      e = e.next;
    } return false;
  }}
class EdgeListElem  extends ListElem {
  EdgeListElem next; }
class NodeListElem extends ListElem {
  EdgeList outEdges;
  EdgeList inEdges;
  NodeListElem next; }

class EdgeList extends List {
  EdgeListElem first; }
class NodeList extends List {
  NodeListElem first; }

public class Graph {
  EdgeList edges;
  NodeList nodes;

  void remove(NodeList nl) {
    NodeList nds = nodes;
    nds.removeAll(nl);
    NodeListElem n = nl.first;
    EdgeList el = edges;
    while (n != null) {
      EdgeList e = n.outEdges;
      el.removeAll(e);
      e = n.inEdges;
      el.removeAll(e);
      n = n.next;
    }
  }}
```

Figure 10. Graph Manipulation Code

```
/** subset: List.RemoveAll **/
this.first'.*next' in this.first.*next
/** sameEdges: Graph.remove **/
no nl.first =>
  edges.first'.*next' = edges.first.*next
/** sameNodes: Graph.remove **/
no nl.first =>
  nodes.first'.*next' = nodes.first.*next
```

Figure 11. Graph Manipulation Properties

value before the execution. The * sign in Alloy denotes the reflexive transitive closure, i.e. it gives all the values reachable by traversing the field following it zero or more times. Furthermore, `this` stands for the receiver object of a function.

The `subset` property is a specification for the `List.RemoveAll` function. The property asserts that the elements of a list after the execution of this function are a subset of its elements before the execution. In other words, the `removeAll` function does not add new objects to the receiver list. The `sameEdges` and `sameNodes` properties are assertions for the `Graph.remove` function. They claim that if the input list of nodes is empty, the graph's lists of edges and nodes do not change by executing this function.

We compare our method with a static bug detector, Jalloy[15], since it is also based on SAT solvers and targets structural properties of Java code. The translation method used in Jalloy is identical to ours. Furthermore, we tailored Jalloy to use the same SAT solver as we do, i.e. ZChaff. However, Jalloy inlines all procedure calls to avoid user-provided specifications. This comparison therefore, shows the improvements gained by the procedure abstraction idea.

Table 1 gives the results of checking the above properties. They all hold in the given code. LoopUnroll and Scope respectively show the number of times the loops are unwound and the number of objects of each type considered in the analyses. The number of variables and clauses given for Jalloy denote the size of the generated boolean formula in CNF format; the time column gives the analysis time. The number of variables and clauses for our method correspond to the largest boolean formula checked, i.e. the formula constructed after the last refinement. The time column gives the total analysis time including all refinements. The number of iterations shows how many refinements are needed to check each property.

The results show that to check the first two assertions, the initial specifications that only preserve the frame conditions are sufficient; no further refinements are needed. Jalloy spends considerable time on translating the whole code into a boolean formula although only a small portion of code is involved in each of these properties. Consequently, the formula generated by Jalloy is too large to be handled by the SAT solver. These experiments show that our method considerably improves the analysis time, even when some refinements are needed, by translating only the parts of the code that are necessary for the analysis. In this way both the translation time is reduced and a smaller boolean formula is generated that can be solved faster.

7. Related Work

Our method is inspired by previous work [15] and [20] that translate a program to a boolean formula and use a SAT solver to check a property in a finite scope. However, they inline all called procedures that are not annotated with user-provided specifications. This severely limits their scalability as our experiments indicate.

The software model checkers SLAM[1] and BLAST[12] over-approximate the code using predicate abstraction[10]. An abstraction is refined by automatically inferring new predicates. They target temporal safety properties, and in general are not capable of checking the kind of structural properties that we do. MAGIC[2] is also based on predicate abstraction, but it uses a SAT solver to verify a user-provided specification in C code. However, if the user does not provide specifications for the called procedures, MAGIC will inline all procedure calls.

ESC/Java[9] uses a theorem prover to check properties of code relying on user-provided function specifications. An extension of ESC is proposed by Flanagan[7]. His method checks code properties via translation to a constraint logic (CLP)[16] and checking the satisfiability of the generated formula. It differs from our method in that it first translates the whole code into CLP and then checks for satisfiability iteratively based on predicate abstraction. We believe that our analysis framework can be used with CLP and a proof-generating decision procedure or a theorem prover like Verifun[8].

Bandera[4] analyzes Java code by extracting a finite state model of code, using slicing, which can be mapped into several model checkers and theorem provers. Unlike our method, it supports user-provided data abstractions that may also yield false alarms.

Dynamic slicing (e.g. [23]) extracts the statements contributing to the value of a variable at some point in a given execution of a program. Our specification inference method is similar in that it extracts the statements relevant to the input and output values assigned to a procedure. However, since the execution path is not known, dynamic slicing can not be applied.

Table 1. Experiment Results

Assertion	LoopUnroll	Scope	Jalloy			Our Method			
			Variables	Clauses	Time (sec)	Variables	Clauses	Time (sec)	#iter
subset	4	4	8216	18124	15	4928	10260	9	0
	5	5	14555	34704	162	8611	19002	98	
	6	4	13554	30555	40	6702	14013	12	
	6	5	18137	43760	234	9857	21776	83	
sameEdges	3	3	27112	56241	61	3284	6589	5	0
	4	4	66566	151323	164	6187	13507	8	
	4	5	87710	214959	206	9524	23383	27	
	5	4	–	–	> 900	6807	14794	8	
	5	5	–	–	> 900	10346	25263	36	
	6	4	–	–	> 900	7499	16207	9	
sameNodes	3	3	27147	56298	44	5927	11652	7	3
	4	4	66661	151489	123	11057	23450	13	
	4	5	87803	215129	224	15682	36890	107	
	5	4	108016	246914	359	13075	27446	17	
	5	5	141087	347466	586	18549	42948	191	

Shape analysis algorithms[18] can check properties about the structure of the heap. Parametric shape analysis[18] uses a 3-valued logic to represent shape graphs and can prove properties without bounds, but it may generate false alarms. It also requires the user to specify how each statement affects each predicate of interest. Our method, in contrast, does not require any user-provided annotations and does not give spurious counterexamples. However, the absence of a counterexample does not constitute proof.

Specification extraction is itself not new. Daikon[6] and DIDUCE[11], for example, detect invariants about programs. Unlike our static specification inference method, both of these tools detect invariants dynamically, i.e. by running the code. However, we do not generate general specifications. Our specifications are context-dependent, i.e. based on the property to be checked and on the context in which procedures are called. Furthermore, our specifications are only as precise as they need to be for the verification of their callers.

8. Conclusions

In this paper we proposed a framework to statically check a user-provided property in code. We specifically target the properties that constrain the structure of the objects in the heap. The framework exploits the modular structure of the program and is based on constraint solving. We start with a rough over-approximate specification for each procedure and refine it on-demand. While our method is capable of automatically inferring context-dependent specifications for procedure calls, it can still benefit from user-provided specifications, if available, to reduce the analysis time.

We also explained our implementation of the framework. We target Java programs and use Alloy as an intermediate language to translate Java to boolean constraints. A SAT solver is used to check the property. Specification inference is based on the unsat core generated by the SAT solver. Our experiments show that procedure abstraction can considerably reduce the analysis time by analyzing only the parts of the code that are actually needed to check a property. More experiments has yet to be done to evaluate this idea in larger code.

Our current implementation checks the validity of the procedure calls in a given trace in the depth first traversal order of the call graph of the program. Other orders such as breadth first, are possible. More experiments will be done to compare these methods. Furthermore, our initial abstraction currently infers initial specifications that only preserve the frame conditions of the procedures. A more precise initial specification can reduce the number of refinements needed. Techniques to obtain such specifications will be studied in future.

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