Interfacial Effects During Thermal Cycling of Cu-Filled Through-Silicon Vias (TSV)

P. KUMAR,¹ I. DUTTA,^{1,3} and M.S. BAKIR²

1.—School of Mechanical & Materials Engineering, Washington State University, Pullman, WA 99164, USA. 2.—School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, GA 30332, USA. 3.—e-mail: idutta@wsu.edu

Large shear stresses may develop at interfaces between dissimilar materials during thermal excursions when there is a significant difference in their coefficients of thermal expansion. The shear stress may cause interfaces to slide via diffusional process, thereby accommodating the relative dimensional changes between the two materials. This phenomenon presents a significant reliability issue in three-dimensional (3-D) interconnect structures involving through-silicon vias (TSVs), which are subjected not only to continuous thermal cycling but also to large electric current densities during service. This paper reports experimental evidence of interfacial sliding between Cu and Si in Cu-filled TSVs during thermal cycling conditions, and in the presence of electric current. Two different thermal cycling conditions were used: (i) small ΔT thermal cycling (-25°C to 135°C) and (ii) large ΔT thermal cycling (25°C to 425°C). Prior to thermal cycling, a few Cu-filled TSV samples were annealed for 30 min at 425°C. Cu intruded inside Si in nonannealed samples during small ΔT thermal cycling, whereas protrusion of Cu relative to Si occurred during all other thermal excursions. Application of electric current biased the net displacement of the Cu in the direction of electron flow, leading to enhanced protrusion (or intrusion) of Cu relative to the thermal cycling only (i.e., without electric current) condition. A simple one-dimensional analytical model and associated numerical simulations are utilized to rationalize the experimental observations.

Key words: Through-silicon via, interfacial sliding, thermal cycling, 3-D packaging, electromigration

INTRODUCTION

The coefficient of thermal expansion (CTE) mismatch between the constituent materials of a multicomponent system typically results in significant interfacial shear stresses near the extremities of the composite during a thermal excursion. Examples include fiber-reinforced composites,^{1,2} thin-film interconnect lines embedded in low-*k* dielectric structures in the back-end of microelectronic devices,³⁻⁶ as well as Cu-filled through-silicon vias (TSVs).^{7–9} If the thermal excursion is conducted at a relatively slow rate, the thermally induced stresses which build up in each component can be relieved via creep and/or plasticity, and interfacial fracture may be avoided altogether.¹ Instead, interfacial shear stresses can induce diffusionally driven interfacial sliding (IS), which accommodates differential deformation of the two adjacent components without breaking interfacial bonds.^{1,10–12} Indeed, interfacial sliding has been reported to cause protrusion or intrusion of graphite fibers in an Al-based composite^{1,2} and relative displacement of the thinfilm deposits with respect to substrates or surrounding media^{3–6,13} during thermal cycling.

The kinetics of stress-driven interfacial sliding is given as follows:¹¹

⁽Received April 28, 2011; accepted July 20, 2011; published online August 10, 2011)

$$\dot{U} = rac{C\delta_{
m i}D_{
m i}\Omega}{kTh^2} \Bigg[au_{
m i} + 2\pi^3 \left(rac{h}{\lambda}
ight)^3 \sigma_{
m n} \Bigg],$$
 (1)

where *C* is a numerical constant, δ_i and D_i are the interfacial thickness and diffusivity, respectively, Ω is the atomic volume of the diffusing species, λ and h are the periodicity and roughness of the interface, respectively, k and T are the Boltzmann constant and temperature, respectively, and τ_i and σ_n are the interfacial shear stress and the far-field normal stress, respectively. The interfacial shear stress, τ_i , is the driving force for this process, which is enhanced if a far-field tensile normal interfacial stress σ_n is present (i.e., positive σ_n), or reduced if there is a compressive σ_n (negative σ_n). The effect of σ_n on sliding is finite only when τ_i is nonzero, and $\dot{U} = 0$ when $\tau_i = 0$ even if σ_n is nonzero. Since interfacial sliding is a diffusion-controlled phenomenon, it becomes significant only at high temperatures.

Another fundamental characteristic of interfacial sliding is that its kinetics may be affected by electromigration (EM), since both are diffusionally driven, particularly when the interfaces, rather than grain boundaries, provide the primary short-circuit path for the EM flux (i.e., when the grain size is relatively large).¹⁴ It was shown that, depending on the direction of electric current relative to the applied τ_i , the interfacial sliding can either increase or decrease, or even directionally reverse, relative to sliding under τ_i only.¹⁴ The following gives the kinetics of the electromigration-assisted interfacial sliding (EM-assisted IS) in the absence of far-field normal stress and for the conditions where the interfaces act as the major route for the diffusional flux:¹⁴

$$\dot{U} = \frac{8\Omega \delta_{\rm i} D_{\rm i}}{kTh^2} \tau_{\rm i} + \frac{4\delta_{\rm i} D_{\rm i}}{kTh} Z^* eE, \qquad (2)$$

where Z^* , e, and E are the effective charge number of the diffusing atom, the free charge of an electron, and the applied electric field, respectively. An important characteristic of EM-assisted IS, as revealed through Eq. (2), is that the effects of the shear stress and the electromigration on the interfacial sliding can be linearly superimposed. Hence, based on Eqs. (1) and (2), the kinetics of EM-assisted IS in the presence of far-field normal stress can be given as

$$\dot{U} = \frac{8\delta_{\rm i}D_{\rm i}\Omega}{kTh^2} \left[\tau_{\rm i} + 2\pi^3 \left(\frac{h}{\lambda}\right)^3 \sigma_{\rm n} \right] + \frac{4\delta_{\rm i}D_{\rm i}}{kTh} Z^* eE.$$
(3)

In stacked-die packages, a TSV in a given die connects to a back-end of line (BEOL) stack at one end, and Cu pillars for connecting to other dies through solder microbumps at the opposite end. The CTE mismatch between Cu $(\alpha_{Cu}\approx 17\times 10^{-6}~K^{-1})$ and Si $(\alpha_{Si}\sim 3\times 10^{-6}~K^{-1})$ is significant, and the

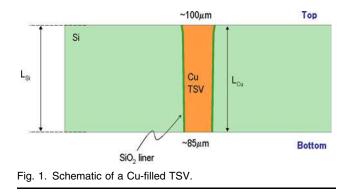
microelectronic packages continuously undergo thermal cycling, which make Cu-filled TSVs prone to interfacial sliding. Even a slight intrusion or protrusion of the TSVs would place the delicate BEOL structure and the brittle intermetallic compound (IMC)-rich microbumps in tension or compression, making them prone to distortion and fracture, particularly when adjacent TSVs protrude or intrude by different amounts depending on the direction and magnitude of current carried by each. As TSVs become finer and packages become more vertically integrated, simultaneously raising the thermal, mechanical, and electrical loads,¹⁵ these effects will become more egregious. Therefore, it is imperative to study the effects of interfacial incompatibilities on the integrity of TSVs.

To date, only one analytical study has investigated interfacial incompatibility in TSVs due to interfacial fracture, assuming that the Cu deforms elastically only.⁷ This forces the stresses in the Cu to rise to very high levels, and produces conditions suitable for interfacial fracture. In reality, these conditions will be valid during thermal shock (very rapid thermal cycling) only. During slow thermal cycling, creep/plasticity effects will limit stresses, and given sufficient time during heating, cooling, and dwell, will drive interfacial diffusional fluxes to accommodate the differential deformation between Cu and Si via interfacial sliding.^{12,14} Indeed, the importance of plastic flow in Cu vias is well known in the phenomenon known as "Cu pumping," which occurs during temperature excursions associated with BEOL processes.¹⁶ Therefore, it is necessary to incorporate creep and plasticity of Cu while analyzing the response of Cu-filled TSVs to a thermal excursion. However, there exists no report in the literature which has addressed the effect of slow thermal cycling on the integrity of TSVs, while accounting for creep, plasticity, interfacial sliding, and electromigration.

Accordingly, the goals of this paper are: (i) to study the effect of slow thermal cycling, hence allowing creep, plasticity, and interfacial sliding to occur, on the integrity of Cu-filled TSVs, and (ii) to study the effect of electric current (electromigration) on the net protrusion or intrusion of Cu relative to Si. Also, an existing model^{1,10} is modified to include the effects of electromigration and to explain the experimental trends.

EXPERIMENTAL PROCEDURES

Figure 1 shows a schematic of a Cu-filled TSV. A few such samples were annealed at 425° C for 30 min in a vacuum furnace operating at 10^{-4} Torr vacuum. Annealing was conducted to increase grain size and to relieve residual stresses in Cu, which has often been reported to be beneficial against Cu/Si interface fracture as well as for the electronic performance of devices in the vicinity of TSVs (private communication with IBM). Samples were



exposed to various numbers of cycles of two kinds of thermal cycling: (i) small ΔT thermal cycling: from -25° C to 135° C at $\sim 0.1^{\circ}$ C/s in a thermal cycling chamber, and (ii) large ΔT thermal cycling: from 25° C to 425° C at $\sim 0.02^{\circ}$ C/s in a vacuum furnace operating at 10^{-4} Torr vacuum.

Electric current, corresponding to current density of 5.22×10^4 A/cm², was passed through Cu pillars of a few of the TSV samples undergoing large ΔT thermal cycling. The electric current was passed in a direction so that the flow of electrons and the interfacial shear stress were parallel.

All samples were observed by scanning electron microscopy (SEM), before and after thermoelectrical excursions. SEM micrographs from the same place were taken from the same angle for direct comparison. A few samples, in conditions of as-fabricated, after annealing, and after three cycles of large ΔT thermal cycling, were metallographically polished to reveal the cross-sectional features, and to study the evolution in the Cu microstructure and the Cu/Si interface during thermal excursions.

MODEL

Dutta¹ presented a one-dimensional model for the axial strain response of continuous fiber composites during axial creep, accounting for interfacial sliding due to the presence of interfacial shear stresses near the end of the fiber. The model was developed for a composite system with thermoelastic fiber and thermoelastoplastic creeping matrix and was able to predict protrusion as well as intrusion of fiber (graphite) relative to matrix (an Al alloy).¹ Here, the model is slightly modified to suit current-carrying, Cu-filled TSVs by accounting for: (i) thermoelastoplastic creeping fiber (Cu), (ii) thermoelastic matrix (Si), and (iii) electromigration. The details of the steps to derive the model are available in Refs. 1,10, whereas here, an outline highlighting the important steps and the solution scheme to incorporate electromigration will be presented.

A schematic of the interfacial region, as conceptualized for the model, is shown in Fig. 2. During thermal cycling, a shear stress τ_i is generated which varies along the axial coordinate *z*. τ_i is zero at the center of Cu (*z* = 0) and maximum at the end of the

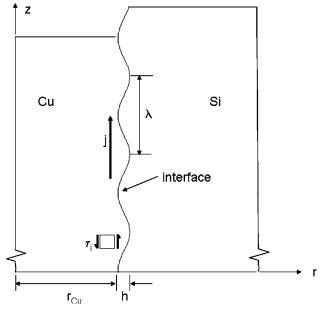


Fig. 2. Schematic of the Cu/Si interfacial region used in modeling, showing a periodic interface with width of *h* and periodicity of λ . The axial coordinate is zero at the middle of the Cu length, and the radial coordinate *r* is zero at the middle of the Cu diameter.

Cu pillar (i.e., $z = l_{Cu}/2$). Along with the τ_i , which varies with temperature (i.e., during a thermal cycle) and with z, an electric current with constant density, equal to j, is assumed to flow along the +zdirection. The following two assumptions are made for simplicity: (i) interface is the dominant route for the diffusional flux due to the electromigration, and (ii) the effect of the radial stresses, akin to σ_n , generated during thermal cycling due to the CTE mismatch between Cu and Si on interfacial sliding is negligible. Based on Eq. (2), the interfacial shear strain rate can be given as

$$\dot{\gamma}_{i} = \dot{\gamma}_{i,\tau} + \dot{\gamma}_{i,\text{EM}} = A_{i}\tau_{i} + B_{i,\text{EM}}Z^{*}eE, \qquad (4)$$

where subscripts " τ " and "EM" indicate interfacial shear stress and electromigration, respectively, and A_i and $B_{i,EM}$ are given as

$$A_{\rm i} = \frac{8\delta_{\rm i} D_{\rm i} \Omega}{kTh^3}, \qquad (5a)$$

$$B_{\rm i,EM} = \frac{4\delta_{\rm i} D_{\rm i}}{kTh^2}.$$
 (5b)

When Si and Cu are in nonisostrain condition, the stress equilibrium under a constant applied stress σ_a requires that¹⁰

$$\sigma_{\rm a} = \sigma_{\rm Cu} V_{\rm Cu} \left[\frac{1 + \bar{\epsilon}_{\rm Si}}{1 + \bar{\epsilon}_{\rm Cu}} \right] + \sigma_{\rm Si} V_{\rm Si},\tag{6}$$

where σ_{Cu} and σ_{Si} are the axial stress in Cu and Si, respectively, V_{Cu} and V_{Si} are the volume fractions of Cu and Si, respectively, and $\bar{\epsilon}_{Cu}$ and $\bar{\epsilon}_{Si}$ are the average axial strains of the Cu and Si, respectively. Strain continuity across the interface requires

that^{1,10}

$$v = u + w, \tag{7}$$

where v, u, and w are the displacements in Cu, Si, and interface, respectively. Differentiating Eq. (7) with respect to z gives

$$\frac{l_0}{2}(\bar{\varepsilon}_{\rm Cu} - \bar{\varepsilon}_{\rm Si}) = h\gamma_{\rm i,\tau},\tag{8}$$

where l_0 is the nominal gauge length (the shorter of the Cu and Si lengths), \bar{e}_{Cu} and \bar{e}_{Si} represent average axial strain values over half the gauge length (z = 0to $l_0/2$), and $\gamma_{i,\tau}$ is the interfacial shear strain at $z = l_0/2$ only due to the applied interfacial shear stress, given by¹

$$\gamma_{\mathbf{i},\tau} = \dot{\gamma}_{\mathbf{i},\tau} t = A_{\mathbf{i}} \tau_{\mathbf{i},\mathrm{at}\ z = l_0/2} t. \tag{9}$$

It is important to note that, even though the interfacial shear strain is affected by the electromigration, the interfacial sliding due to electromigration is uniform throughout the length of the Cu and hence electromigration does not explicitly cause any strain in the Cu and Si. Accordingly, the continuity relationship in presence of electromigration, as given by Eq. (8), is identical to the situation where only τ_i is present. Nevertheless, at any instance, the electromigration biases the displacement of the entire Cu pillar in one direction, meaning that, unlike the pure thermal cycling condition where the displacement of Cu relative to Si is symmetric across the center of Cu (i.e., z = 0), thermal cycling with electric current results in a net shift of Cu pillar along the direction of electron flow in addition to the symmetric effects of τ_i . This is because the value of w (Eq. (7)), which is the summation of the effects of τ_i (symmetric across z = 0) and electromigration (unidirectional, nonsymmetric), is different above and below z = 0.14 Since electromigration results in a change in the length of Cu inside Si as compared with the situation where sliding is due to τ_i only, the stress/strain state in Cu and Si changes in the next iteration. The steps taken to account for this will be explained later while discussing computer program execution steps.

Assuming that diffusional creep is the dominant creep mechanism under the given temperature and stress regimes, the strain in Cu and Si is given by^{1,10}

$$\begin{split} \bar{\varepsilon}_{\mathrm{Cu}} &= \bar{\varepsilon}_{\mathrm{Cu}}^{\mathrm{th}} + \bar{\varepsilon}_{\mathrm{Cu}}^{\mathrm{el}} + \bar{\varepsilon}_{\mathrm{Cu}}^{\mathrm{pl}} + \bar{\varepsilon}_{\mathrm{Cu}}^{\mathrm{cr}} \\ &= \int_{T_1}^{T_2} \alpha_{\mathrm{Cu}} \mathrm{d}T + \frac{\sigma_{\mathrm{Cu}}}{E_{\mathrm{Cu}}} + \left(\frac{\sigma_{\mathrm{Cu}} - \sigma_{\mathrm{Cu}}^{\mathrm{YS}}}{K_1}\right)^{1/n_1} + A_{\mathrm{diff}} \sigma_{\mathrm{Cu}} (10a) \end{split}$$

and

$$ar{arepsilon}_{\mathrm{Si}} = ar{arepsilon}_{\mathrm{Si}}^{\mathrm{th}} + ar{arepsilon}_{\mathrm{Si}}^{\mathrm{el}} = \int\limits_{T_1}^{T_2} lpha_{\mathrm{Si}} \mathrm{d}T + rac{\sigma_{\mathrm{Si}}}{E_{\mathrm{Si}}}, \qquad (10\mathrm{b})$$

where the superscripts "th," "el," "pl," and "cr" indicate thermal, elastic, plastic, and creep components, respectively, T_1 and T_2 are the lower and upper limits of temperature during a thermal excursion, α and E are the CTE and Young's modulus, respectively, K_1 and n_1 are the work-hardening coefficient and exponent, respectively, and A_{diff} is the effective coefficient for diffusional creep, given as

$$A_{\rm diff} = \frac{14\Omega}{kTd^2} \left[\left(\frac{\pi}{d}\right) \delta_{\rm gb} D_{\rm gb} + D_{\rm L} \right], \qquad (11)$$

where *d* is the Cu grain size, $\delta_{\rm gb}$ is the effective thickness of the grain boundary, $D_{\rm gb}$ is the grain boundary diffusivity, and $D_{\rm L}$ is the volume diffusivity.

Rewriting Eq. (8) in incremental form and substituting for \bar{e}_{Cu} , \bar{e}_{Si} , and $\gamma_{i,\tau}$ using Eqs. 8–10 and assuming that the externally applied axial stress on TSV during thermal cycling is equal to zero yields

$$\Delta \sigma_{\mathrm{Cu}} = - \left[\frac{(\alpha_{\mathrm{Cu}} - \alpha_{\mathrm{Si}}) \Delta T + \Delta \varepsilon_{\mathrm{Cu}}^{\mathrm{cr}} + \frac{2h}{l_0} A_{\mathrm{i}} \tau_{\mathrm{i}(z=l_0/2)} \Delta t}{\frac{V_{\mathrm{Cu}}}{E_{\mathrm{Si}} V_{\mathrm{Si}}} \left(\frac{1 + \bar{\varepsilon}_{\mathrm{Si}}}{1 + \bar{\varepsilon}_{\mathrm{Cu}}} \right) + \frac{1}{E_{\mathrm{Cu}}} + \frac{1}{K_1 n_1} \left(\frac{K_1}{\sigma_{\mathrm{Cu}} - \sigma_{\mathrm{Cu}}^{\mathrm{YS}}} \right)^{(n_1 - 1)/n_1}} \right],$$
(12)

where superscript "YS" indicates yield point.

The interfacial shear stress can be calculated as a function of z by using a modified shear lag approach after incorporating the effects of plasticity and creep in Cu, and is given by^{1,10}

$$\tau_{\rm i} = -\frac{r_{\rm Cu}}{2} B_1 \sqrt{\frac{H_1}{E_{\rm Si}}} \sinh\left(\sqrt{\frac{H_1}{E_{\rm Si}}} z\right) \quad \text{for } z < z_0, \quad (13)$$

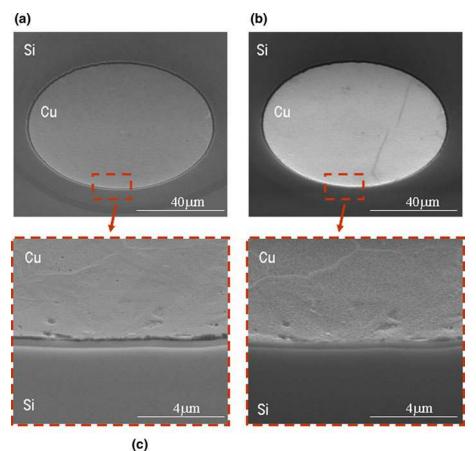
and

$$au_{
m i} = A_0 + ig(au_{
m Cu}^{
m YS} - A_0ig)igg(rac{z}{z_0}igg)^{1/B_0} \quad {
m for} \ z \ge z_0, \quad (14)$$

where r_{Cu} is the radius of Cu, and the parameters H_1 , B_1 , A_0 , and B_0 are given as follows:

$$H_1 = \frac{2}{r_{\rm Cu}(N+M)},$$
 (15a)

$$B_1 = -rac{E_{\mathrm{Si}}ar{arepsilon}_{\mathrm{Cu}}}{\left(rac{1}{E_{\mathrm{Cu}}} + A_{\mathrm{diff}}
ight)\cosh\left(rac{eta l_0}{2}
ight)},$$
(15b)



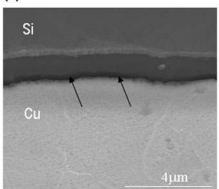


Fig. 3. The effect of small ΔT thermal cycling on nonannealed Cu-filled TSV samples: (a) as-fabricated samples, (b) after 20 cycles between -25° C and 135°C at \sim 0.1°C/s, and (c) after 20 small ΔT thermal cycles from a region of the sample showing the SiO₂ liner wall. Arrows show the vertical portion of the liner wall, where Cu has slid down.

$$A_0 = \sigma_{\rm Cu}{}^{\rm YS} \left(1 - \frac{K_1}{E_{\rm Cu}} \right), \tag{15c}$$

$$B_0 = \frac{K_1}{h}(N+M), \tag{15d}$$

where $N = (r_{\mathrm{Cu}}/G_{\mathrm{Si}}) \ln(R/r_{\mathrm{Cu}}), M = hA_{\mathrm{i}}t, R = r_{\mathrm{Cu}}/\sqrt{V_{\mathrm{Cu}}}, \text{ and } \beta = \sqrt{H_1(1/E_{\mathrm{Cu}} + A_{\mathrm{diff}})}.$

The basic solution procedure to obtain the stress/ strain state of the Cu and Si during a thermoelectrical excursion consists of: (1) calculating the initial stress state in Cu, $\sigma_{Cu,0}$, at the start of thermal cycling; (2) calculating the incremental stress change $\Delta\sigma_{Cu}$ using Eq. (12); (3) updating the stress in Cu as $\sigma_{Cu,k+1} = \sigma_{Cu,k} + \Delta\sigma_{Cu}$ (here the subscript "k" represents the kth solution step); (4) computing the Cu and Si strain components using Eq. (10); (5) updating the length of Cu above z = 0 (i.e., at z > 0) by adding the displacement due to electromigration, equal to $B_{i,EM}Z^*eE\Delta t$, to the topmost axial coordinate of Cu (i.e., $z = l_0/2$); and then (6) adjusting the length of Cu (i.e., $t_0/2$) inside Si over which τ_i and σ_{Cu} are calculated in next step. This procedure is

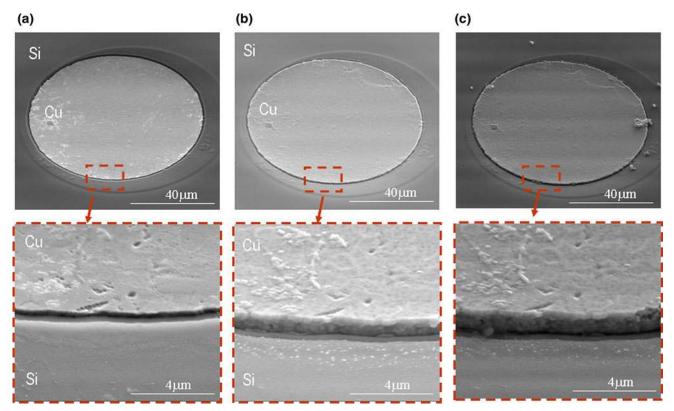


Fig. 4. The effect of annealing and small ΔT thermal cycling on Cu-filled TSV samples: (a) as fabricated, (b) after 30 min of annealing at 425°C, and (c) after 20 cycles between -25° C and 135° C.

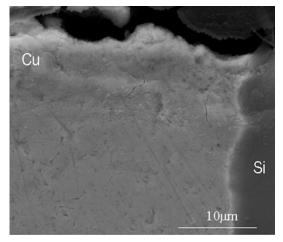


Fig. 5. Cross-sectional micrograph, showing the top edge of the Cu-filled TSVs following annealing at 425°C for 30 min.

utilized iteratively, allowing the temperature and time to change incrementally. At each solution step, the stress and strain increments are calculated and the z-coordinate of the topmost point of Cu and Si are updated. A computer program was written in MATLAB to conduct the above iterative computations.

RESULTS AND DISCUSSION

Figure 3 shows the effect of the small ΔT thermal cycling on a nonannealed sample. Comparison of high-magnification pictures from the same region clearly shows that the Cu has intruded after 20 small ΔT thermal cycles. SEM micrographs from different locations, as shown in Fig. 3c, revealed a clean SiO₂ liner wall, free from any leftover Cu, indicating interfacial fracture-free intrusion of the Cu solely due to the diffusion-assisted interfacial sliding. Intrusion of Cu (high α , thermoelastoplastic creeping component) inside Si (low α , thermoelastic component), as observed in these TSV samples during small ΔT thermal cycling, is analogous to the intrusion of graphite fibers (low α , thermoelastic component) in an Al-based composite (high α , thermoelastoplastic creeping component) during large ΔT thermal cycling.

Figure 4 shows the effect of annealing on the Cu-filled TSV samples. As shown in Fig. 4a, b, the Cu protrudes out of Si following annealing. Figure 5 shows the cross-section of the sample near the extremities of the Cu where the shear stresses generated due to CTE mismatch are maximum. Figure 5 clearly shows that the interface between Cu and Si did not fracture during annealing and the protrusion, as shown in Fig. 4, was due to

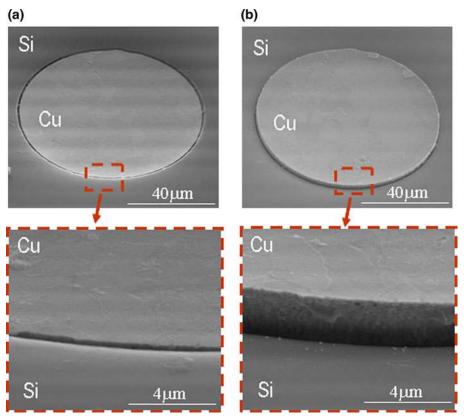


Fig. 6. Effect of large ΔT thermal cycling on nonannealed Cu-filled TSV samples: (a) as fabricated, and (b) after three cycles between 25°C and 425°C at ~0.02°C/s.

diffusion-assisted interfacial sliding. Annealing acts as one high ΔT thermal cycle with a long dwell time at the maximum temperature. As will be shown later, high ΔT thermal cycle always led to protrusion of Cu. At high temperatures, creep and/or plasticity are activated, leading to significant stress relaxation in Cu. Also, interfacial sliding rates are very high at high temperatures, leading to substantial length difference between Cu and Si. Besides protrusion of Cu, annealing also resulted in Cu grain growth and relaxation of residual stresses in Cu. Cross-sectional images of samples revealed insignificant change in the characteristics (size, number, and preferred area of distribution) of cavities or pores in the Cu due to the annealing.

Figure 4b, c shows the effect of small ΔT thermal cycling on an annealed sample. Following small ΔT thermal cycling, Cu protrudes further out relative to Si. Cu continues to protrude out relative to Si, albeit at a slower rate, even after 120 small ΔT thermal cycles. Hence, a transition from intrusion of Cu to protrusion of Cu under an identical small ΔT thermal cycling condition occurred following the annealing treatment. This transition is attributed to the reduction in the residual stresses in Cu following annealing, and a rationale for such a transition

will be presented later with the help of the previously outlined model.

As shown in Figs. 6 and 7, protrusion of Cu relative to Si was also observed in both annealed and nonannealed samples during large ΔT thermal cycling conditions. Figure 8 shows a cross-section of a sample after three large ΔT thermal cycles, near the extremities of the Cu, where the interfacial shear stresses due to CTE mismatch are maximum. As revealed by Fig. 8, even the large extent of protrusions of Cu, which were observed during high ΔT thermal cycling, did not cause fracture of Cu/Si interface, and the Cu protrusions were entirely due to the diffusion-assisted interfacial sliding at the Cu/Si interface. Protrusion of Cu relative to Si during large ΔT thermal cycling is analogous to the protrusion of graphite fibers in an Al-based composite during small ΔT thermal cycling.

Figure 9 shows the effect of the electric current on the extent of the protrusion of Cu relative to Si. In this electromigration test, the direction of electron flow was parallel to the interfacial shear stress. Comparison of Fig. 7b, c with Fig. 9b, c reveals that the electric current significantly enhanced the extent of protrusion of the Cu out of Si.

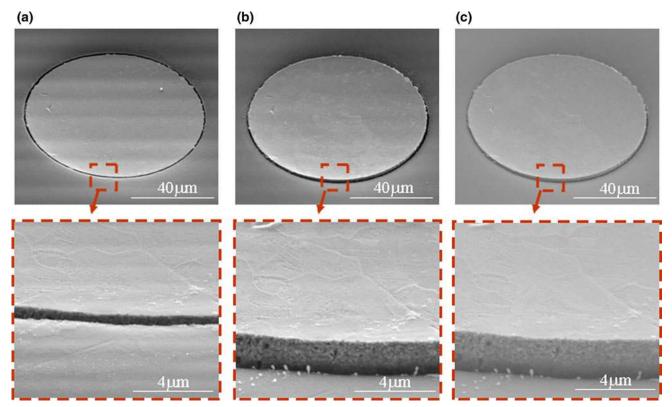


Fig. 7. Effect of large ΔT thermal cycling on an annealed Cu-filled TSV samples: (a) after annealing at 425°C for 30 min, (b) after three cycles between 25°C and 425°C at ~0.02°C/s, and (c) after six cycles between 25°C and 425°C at ~0.02°C/s.

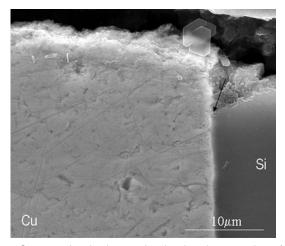


Fig. 8. Cross-sectional micrograph, showing the top edge of the Cu-filled TSVs following annealing at 425°C for 30 min, followed by three thermal cycles between 25°C and 425°C. The *arrow* shows the end Si at the Si/Cu interface.

A quantitative summary of the experimental observation is shown in Fig. 10, and is outlined as follows:

- 1. For small ΔT thermal cycling, annealing causes a transition from intrusion of Cu to protrusion of Cu relative to Si.
- 2. For large ΔT thermal cycling, Cu protrudes relative to Si irrespective of the annealing condition;

however, annealing slows down the extent of protrusion during subsequent thermal cycling.

- 3. Cu protrusion during large ΔT thermal cycling was much larger than that during small ΔT thermal cycling.
- 4. With thermal cycling, Cu continues to protrude (or intrude), although the rate of protrusion (or intrusion) slows down with number of cycles.
- 5. Protrusion or intrusion of Cu does not occur due to interfacial fracture but rather occurs due to the diffusion-assisted interfacial sliding.
- 6. Interfacial sliding due to shear stress is enhanced by electric current (or electromigration) if the electrons flow parallel to the interfacial shear stress.

In the following, a rationale for the above observations will be presented with the help of the previously outlined model. Table I lists the value of several constants used in the calculations.

Figure 11 shows the effect of the interfacial sliding on the calculated strains in Cu and Si following one thermal cycle between 250 K and 410 K, akin to the small ΔT thermal cycling condition. Figure 11a shows that, in the absence of interfacial sliding, although there is a finite residual strain after thermal cycling, both Si and Cu deform equally, hence precluding any protrusion or intrusion of Cu relative to Si. On the other hand, Cu and Si deform differently in the presence of interfacial sliding,

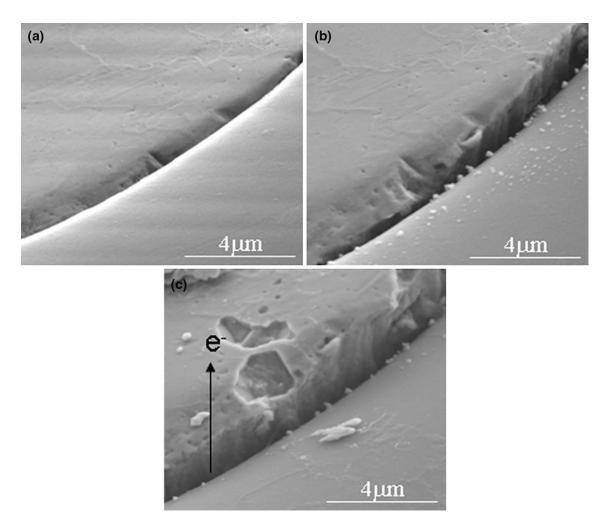


Fig. 9. Effect of large ΔT thermal cycling and EM on an annealed Cu-filled TSV samples: (a) as-fabricated, (b) after annealing at 425°C for 30 min and three cycles between 25°C and 425°C at ~0.02°C/s, and (c) after six cycles between 25°C and 425°C at ~0.02°C/s with the last three cycles with electrons flowing in upward direction (as shown by *arrow*). The current density was 5.22 × 10⁴ A/cm².

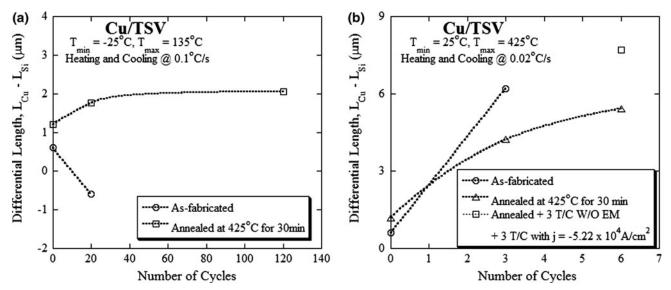


Fig. 10. Quantitative summary of the experimental observations, as shown by Figs. 3–9. (a) Small ΔT thermal cycling, and (b) large ΔT thermal cycling.

Property	Copper	Si
Melting temperature (K)	1356	N/A
Coefficient of thermal expansion, α (1/K)	$17 imes 10^{-6}$	$3 imes 10^{-6}$
Young's modulus at 300 K, E_0 (GPa)	115	130
Shear modulus at 300 K, G_0 (GPa)	42	80
Work-hardening coefficient at 300 K, $K_{1,0}$	1000	
Work-hardening exponent, <i>n</i>	0.5	
Temperature dependence of modulus calculations	$\Delta = (T_{\rm m}/G_{\rm Cu})({\rm d}G_{\rm Cu}/{\rm d}T) - 0.54$	
Temperature-dependent yield strength of Cu (MPa)	$\sigma_{ m YS}$ = 222.4 $-(T-273)$	
Temperature-dependent Young's modulus	$E_{\rm Cu} = E_{\rm Cu,0}[1 + \Delta(T - 300)/T_{\rm m}]$	
Temperature-dependent shear modulus	$G_{C_{11}} = G_{C_{11}} \left[1 + \Delta (T - 300) / T_m \right]$	
Temperature-dependent linear hardening modulus	$G_{\rm Cu} = G_{\rm Cu,0}[1 + \Delta(T - 300)/T_{\rm m}]$ 2 × 10 ⁻⁵	
Frequency factor for lattice diffusion, D_{10} (m ² /s)	$2 imes 10^{-5}$	
Activation energy for lattice diffusion, Q (kJ/mol)	197	
Pre-exponential for grain boundary diffusion, $\delta_{gb}D_{gb0}$ (m ³ /s)	$5 imes 10^{-15}$	
Activation energy for grain boundary diffusion, Q (kJ/mol)	104	
Pre-exponential for interfacial diffusion, $\delta_i D_{i0}$ (m ³ /s)	$\frac{10^{-4}}{55} \delta_{\rm gb} D_{\rm gb0}}{55}$	
Activation energy for interfacial diffusion, Q (kJ/mol)	55	
Atomic volume, Ω (m ³)	1.18×10^{-29}	
Copper grain size, $d(\mu m)$	1 or 0.1	
Effective charge density, Z^*	5	
<i>h</i> (m)	10^{-9}	
h/λ	0.1	
Electrical resistivity of copper $(n\Omega m)$	ho = 16.8 × [1 + 3.9 × 10 ⁻³ (T - 273)]	
Diameter (µm)	100	$158.1 (V_{\rm Si} = 0.6)$
Initial length (μm)	400	400

Table I. Properties and constants used in calculation

leading to a finite length difference between Cu and Si at the end of a thermal cycle. The differential length change (defined as $L_{\rm Cu} - L_{\rm Si}$) or differential strain (defined as $\varepsilon_{\rm Cu} - \varepsilon_{\rm Si}$) between Cu and Si causes protrusion or intrusion of one relative to another. Another interesting phenomenon, as revealed by Fig. 11, is that the residual differential strain or differential length (as shown in Fig. 11c) per cycle diminishes with number of cycles, implying that the rate of protrusion (or intrusion) of Cu relative to Si will slow down with number of cycles. This is consistent with the experimental observations, as reported in Fig. 10. Also, a comparison of Figs. 10a (for annealed sample*) and 11c reveals that the first-principles model adopted in this study is able to predict the differential length change with acceptable accuracy, further proving that the adopted model is able to capture the underlying physics of the problem.

Figure 12 shows the variation in the axial stress in Cu with temperature change during a small ΔT thermal cycle. As the temperature increases, Cu expands more relative to Si due to its higher CTE and hence starts to build up compressive stress. With temperature, the stress state in Cu changes from tensile to compressive, and then at high temperatures where either the compressive stress becomes large enough to cause plasticity in Cu or creep becomes significant, the stress in Cu starts to relax, forming a distinct knee in the stress profile. During cooling, Cu contracts more rapidly than Si, and the accumulation of tensile stress in Cu begins. Finally, a finite residual stress, which is different from the initial stress, is accumulated in Cu, resulting in a stress hysteresis loop. During this process, except for thermal strains, finite residual creep, plastic and elastic strains are accumulated in Cu. Unless the distinct knee in the stress (or strain) profile is formed, a finite residual strain in Cu or Si at the end of the thermal cycle is not possible. The presence of interfacial sliding allows Si and Cu to accumulate different amounts of total strain, leading to a finite differential strain.

Figure 13 shows the effect of higher initial residual stress in Cu on the residual strains in Cu and Si during a thermal cycle. Due to annealing, the residual stress in Cu reduces and hence samples with high and low initial tensile residual stresses in Cu qualify as as-fabricated (or nonannealed) and annealed samples, respectively. A comparison of Figs. 11b and 13a reveals that a sample with smaller initial tensile residual stress (akin to annealed sample) shows protrusion of Cu relative to Si whereas a sample with higher initial residual stress (akin to as-fabricated sample) shows intrusion of Cu relative to Si during the small ΔT thermal cycling condition. These predictions are consistent with the experimental observations, as shown in Figs. 3 and 4 and summarized in Fig. 10.

^{*}The as-fabricated sample, which showed intrusion of Cu relative to Si, is explained in a later section.

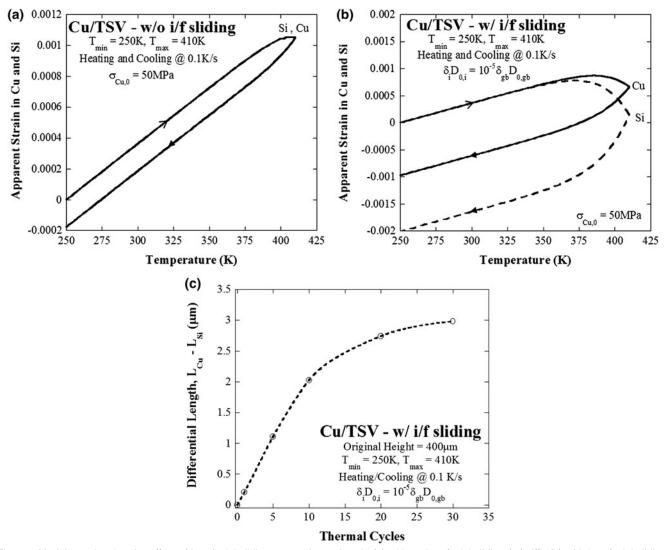


Fig. 11. Model run showing the effect of interfacial sliding over a thermal cycle (a) without interfacial sliding (w/o i/f), (b) with interfacial sliding (w/ i/f), and (c) the evolution in the differential length between Cu and Si with number of cycles.

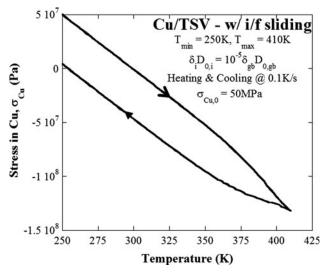


Fig. 12. Model run showing the variation in the axial stress in Cu during thermal cycling.

Figure 13b shows that the Cu will continue to intrude (or extrude) relative to Si with increasing number of thermal cycles; however, consistent with the experimental results, the rate of intrusion (or extrusion) will slow down with number of cycles.

Figure 14a shows the effect of the temperature range on the strain variation in Si and Cu during a thermal cycle. Consistent with the experimental results, the differential residual strain significantly increases as the ΔT during a thermal cycle is increased. This is attributed to the longer span of the region after the knee formation in the stress profile, which results in larger difference in the residual stress (and hence residual strain) following a thermal cycle. Figure 14b shows the effect of initial residual stress in Cu during a large ΔT thermal cycle. During a large ΔT thermal cycle, a larger initial residual tensile stress results in smaller residual strains in Cu and Si as well as a smaller

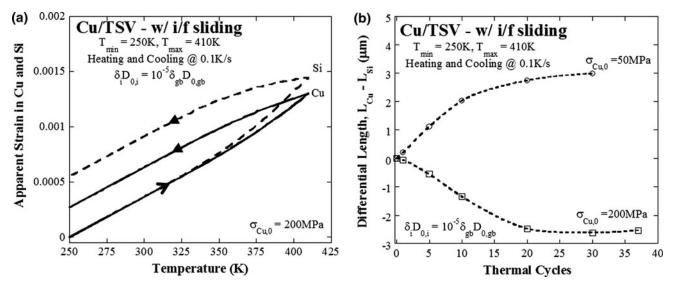
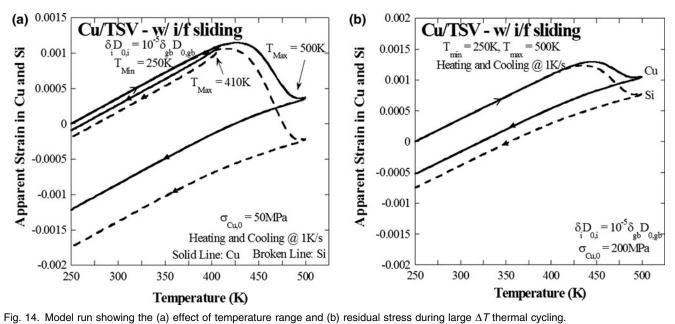


Fig. 13. (a) Model run showing the effect of high residual stress, akin to nonannealed condition, during small ΔT thermal cycling, and (b) variation in the differential height with number of cycles for low and high residual stress.



differential strain; however, no transition from protrusion of Cu to its intrusion relative to Si occurred. These two observations are consistent with the experimental results. Based on the model, an initial residual stress of ~230 MPa will cause a transition from protrusion of Cu to its intrusion for the ΔT used for the calculations shown in Fig. 14.** This stress is close to the yield strength of Cu (~240 MPa), and it is unlikely that such a transition will take place during large ΔT thermal excursions.

Figure 15 shows the effect of electromigration on the extent of protrusion or intrusion of Cu relative to Si. Consistent with the experimental results, the protrusion of Cu relative to Si is enhanced if the flow of electrons is parallel to the interfacial shear stress. On the other hand, the protrusion becomes lesser if the electrons flow in the direction opposite to the shear stress. Figure 15b shows the effect of electromigration on the evolution in the differential length of Cu and Si with number of cycles. The effect of the interfacial shear stress alone on the protrusion (or intrusion) of Cu relative to Si saturates as the number of cycles is increased; however, in the presence of electromigration, the protrusion

^{**}The value of critical initial residual stress in Cu required for a transition from protrusion of Cu to its intrusion increases with the ΔT of the thermal cycle.

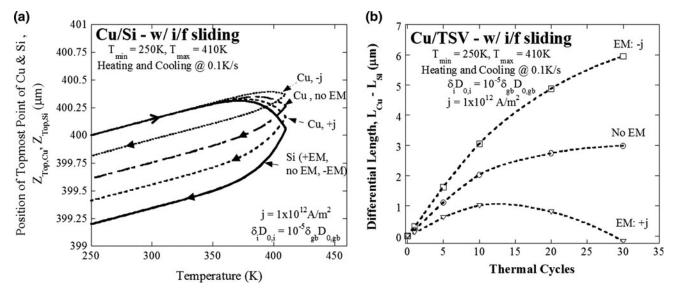
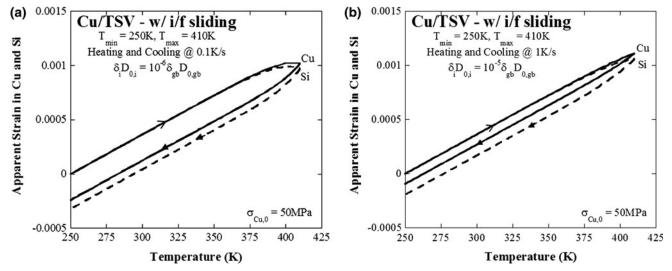
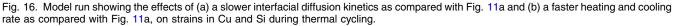


Fig. 15. Model run showing the effect of electromigration on: (a) the position of the topmost point of Cu and Si during a thermal cycle, and (b) the relative difference in the length of Cu and Si with number of cycles.





(or intrusion) of Cu relative to Si does not saturate. Once the effect of the interfacial shear stress saturates, the electromigration dominates the protrusion or intrusion of Cu relative to Si. It is also interesting to note that an initial protrusion of Cu may be "forced" to become an intrusion if electric current is passed for a sufficiently long period. Since the length of Cu and Si is not affected by the electromigration, the effect of electromigration on the stress state of Cu and Si is insignificant.

Two more important parameters, for which experimental results are not available, can be predicted using the developed model, namely (i) the effect of interfacial kinetics,[†] and (ii) the heating and cooling rate.[‡] Comparison of Fig. 11b with Fig. 16a, b reveals the effects of interfacial kinetics and the heating and cooling rate, respectively, on the residual strains in Cu and Si during a thermal cycle. With an increase in the interfacial kinetics, the residual strains in Cu and Si as well as the

[†]Depending on the electroplating technique and the liner layer, the interfacial kinetics may change.

[‡]Depending on the application of a microelectronic device, the heating and cooling rate during thermal cycling will change.

differential residual strains between Cu and Si increase. On the other hand, a slow cooling and heating rate also lead to larger residual strains in Cu and Si as well as larger differential strains. This is attributed to the larger time available for creep and interfacial sliding to occur.

CONCLUSIONS

Thermal cycling experiments on Cu-filled TSVs for two different ΔT ranges and for two different initial residual stresses in Cu were conducted. Thermal cycling caused sliding at the Cu/TSV interface, leading to protrusion or intrusion of Cu relative to Si without interfacial fracture. The rate of protrusion (or intrusion) slowed down with increasing number of cycles. Annealing caused small protrusion of Cu without causing interfacial fracture. Furthermore, annealing caused a transition from intrusion of Cu to its protrusion during small ΔT thermal cycling. However, Cu always protruded relative to Si if the thermal cycling temperature range was increased. Protrusion (or intrusion) of Cu can be increased, decreased or reversed with application of an electric current. Moreover, the differential residual strain between Cu and Si per cycle did not show any saturation with number of cycles if an electric current was constantly passed. The residual differential strain between Cu and Si per cycle increases for: (a) high temperature range, (b) slow heating and cooling rates, (c) high interfacial diffusion kinetics, and (d) low initial residual stress in Cu.

ACKNOWLEDGEMENT

This work was supported by the National Science Foundation, Division of Materials Research, under Grant # DMR-0513874.

REFERENCES

- 1. I. Dutta, Acta Mater. 48, 1055 (2000).
- I. Dutta, S. Mitra, and A.D. Wiest, *Residual Stresses in Composites*, ed. E.V. Barrera and I. Dutta (Warrendale: TMS-AIME, 1993).
- I. Dutta, C. Park, K.A. Peterson, J. Vella, and D. Pan, *IEEE Trans. CPMT* 28, 397 (2005).
- C. Park, I. Dutta, K.A. Peterson, and J. Vella, J. Electron. Mater. 32, 1059 (2003).
- I. Dutta, C. Park, and J. Vella, *Mater. Sci. Eng. A* 421, 118 (2006).
- I. Dutta, M.W. Chen, K. Peterson, and T. Shultz, J. Electron. Mater. 30, 1537 (2001).
- S. Ryu, K. Lu, X. Zhang, J. Im, P. Ho, and R. Huang, *IEEE Trans. Dev. Mater. Rel.* 11, 43 (2010).
- Y. Yang, R. Labie, F. Ling, C. Zhao, A. Radisic, J.V. Olmen, Y. Travaly, B. Verlinden, and I. De Wolf, *Microelectron. Reliab.* 50, 1636 (2010).
- C. Okoro, R. Labie, K. Vanstreels, A. Franquet, M. Gonzalez, B. Vandevelde, E. Beyne, D. Vandepitte, and B. Verlinden, J. Mater. Sci. 46, 3868 (2011).
- R. Nagarajan, I. Dutta, J.V. Funn, and M. Esmele, *Mater. Sci. Eng. A* 259, 237 (1999).
- 11. J.V. Funn and I. Dutta, Acta Mater. 47, 149 (1999).
- K.A. Peterson, I. Dutta, and M.W. Chen, Acta Mater. 51, 2831 (2003).
- 13. M.W. Chen and I. Dutta, Appl. Phys. Lett. 77, 4298 (2000).
- 14. P. Kumar and I. Dutta, Acta Mater. 59, 2096 (2011).
- N. Khan, V.S. Rao, S. Lim, H.S. We, V. Lee, X. Zhang, E.B. Liao, R. Nagarajan, T.C. Chai, V. Krispesh, and J.H. Lau, *IEEE Trans. Compon. Packag. Technol.* 33, 3 (2010).
- 16. I. De Wolf, International reliability physics symposium (Garden Groves, CA, IEEE 2010), p. 1.