

MODELING AND ANALYSIS OF SPACECRAFT POWER SYSTEMS

by

Bo Hyung Cho

Dissertation submitted to the Faculty of the Virginia
Polytechnic Institute and State University in partial
fulfillment of the requirements for the degree of

DOCTOR OF PHILOSOPHY

in

ELECTRICAL ENGINEERING

APPROVED:

F. C. Lee, Chairman

S. L. Hendricks

S. Rahman

W. H. VanLandingham

V. Vorperian

October, 1985

Blackburg, Virginia

MODELING AND ANALYSIS OF SPACECRAFT POWER SYSTEMS

by

Bo Hyung Cho

(ABSTRACT)

A comprehensive large-scale power system modeling is developed to facilitate the design and analysis of present and future spacecraft power systems. A two-port coupling method is utilized to provide a modularity in model building and analysis of the system. The modular approach allows the model to be flexible, verifiable and computationally efficient. A methodology for the system level analysis is presented with the ability to focus on the performance characteristics of an arbitrary component or subsystem. The system performance parameters are derived explicitly in terms of the two-port hybrid g-parameter representation of the component or subsystem, and impedances of its terminating subsystems. From this, the stability of the system is analytically determined and the subsystem interaction criteria is observed. Also presented is a model development from the empirical data employing the complex curve fitting technique. The technique is especially powerful for large scale system modeling and analysis where certain components and subsystems are viewed as black boxes with measurable terminal characteristics. The technique can also be used to realize a reduced order model of a complex subsystem.

The Direct Energy Transfer (DET) spacecraft power system is modeled to demonstrate the versatility of the comprehensive system model by per-

forming various DC, small-signal and large-signal analyses. Of particular interest is the analysis of the large-signal behavior of the nonlinear solar array system by employing the state-plane method. The analysis of the solar array system operation focused on the transition mode between the shunt mode and the battery discharging mode is presented. The subsystem interaction problems in the local component and global system are illustrated. A methodology for the design and trouble-shooting of a system dealing with the interaction problems using the g-parameters is described. Finally, a system level analysis of the DET system using an empirical data modeling technique is performed.

ACKNOWLEDGEMENTS

I wish to express my deepest gratitude to my advisor, Professor F. C. Lee for his continuing encouragement and guidance throughout the course of this research work. He has cultivated an intellectually stimulating environment in the Power Electronics Research Group conducive to the progress of research.

I also appreciate the support received from entire Power Electronics Research Group who made my stay a very productive and enjoyable experience.

I gratefully acknowledge Naval Research laboratory and NASA Goddard Space Flight Center who sponsored this research work.

My thanks to friends who helped me in typing and proofreading this dissertation.

I thank my wife , who has been a constant source of love and attention. Without her understanding and encouragement, this work would not have been possible.

Finally, and most importantly, I would like to thank my parents. Throughout my life their support and encouragement have never wavered. This debt is truly unpayable, and it is to them that this dissertation is dedicated.

TABLE OF CONTENTS

ABSTRACT		ii
ACKNOWLEDGEMENTS		iv
CHAPTER 1	INTRODUCTION	1
CHAPTER 2	METHODOLOGY OF MODELING AND ANALYSIS OF SPACECRAFT POWER SYSTEM	6
	2.1 Introduction	6
	2.2 Modeling approaches of large scale systems	7
	2.3 Two-port transfer function models	12
	2.4 System analysis approaches using the transfer function models	16
	2.5 Conclusions	20
CHAPTER 3	SPACECRAFT COMPONENTS MODELING AND ANALYSIS	23
	3.1 Introduction	23
	3.2 Switching regulator modeling	25
	3.3 Solar array modeling	58
	3.4 Shunt regulator modeling	68
	3.5 Cable/filter modeling	74
	3.6 Payload modeling	76
	3.7 Conclusions	77
CHAPTER 4	MODELING A DIRECT ENERGY TRANSFER SPACECRAFT POWER SYSTEM WITH EASY5	78
	4.1 Introduction	78
	4.2 Direct energy transfer power system	78
	4.3 EASY5 model generation and analysis	80
CHAPTER 5	ANALYSIS OF SOLAR ARRAY POWER SYSTEM MODES OF OPERATION	85
	5.1 Introduction	85
	5.2 Large-signal behavior of solar array operating points	87
	5.3 Analysis of the system operating near the solar array maximum power point	103
	5.4 Analysis of the solar array system in the battery discharge mode	109
	5.5 Analysis of modes of operation of the direct energy transfer power system	129
	5.6 Conclusions	134

CHAPTER 6	SUBSYSTEM INTERACTION ANALYSIS	136
6.1	Introduction	136
6.2	Load interaction analysis	137
6.3	Source interaction analysis	146
6.4	Conclusions	156
CHAPTER 7	EMPIRICAL DATA MODELING AND ANALYSIS	158
7.1	Introduction	158
7.2	Model development from frequency response data	159
7.3	Review of existing complex curve fitting algorithms	161
7.4	Empirical data modeling and analysis of power systems	165
7.5	Conclusions	
CHAPTER 8	CONCLUSIONS	174
REFERENCES		179

Chapter 1

INTRODUCTION

The ever increasing demand on spacecraft power systems for improved efficiency and reliability, smaller size and lighter weight, coupled with continuous growth in dimension and complexity of the spacecraft's payloads, has focused attention on a major deficiency - the ability to design, test, and trouble-shoot large scale power systems.

During the past several decades, numerous efforts have been made to develop new and more powerful techniques for modeling and analysis of spacecraft power components (equipments) and subsystems. However, when the components and subsystems are interconnected to form a complex system, it is quite difficult to predict the total system response even though the behavior of individual components may be well understood and documented. This is due to many undesired interactions that exist among highly nonlinear components. The need of a comprehensive power system modeling tool is, therefore, most critical since the elaborate design verification through integrated systems hardware testing is prohibitively expensive or often impossible. This is particularly true in the future space station system since, due to the large dimension and complexity, the complete system can only be assembled in space. Therefore, a comprehensive computer model that can actually predict a system's local and global behaviors is most critical for the success of future missions.

Considering the size and complexity of today's spacecraft power

processing systems, the digital computer is perhaps the only viable tool for system modeling and simulation. Two general classes of power system modeling programs appear in the literatures. These are :

- Generalized circuit and system analysis models.
- Dedicated models of specific systems.

The generalized circuit analysis models, as a class, are represented by programs such as SPICE, SCEPTRE, SYSCAP, ICAP, ASTAP and a number of others. All have similarities in that they offer a set of typical device modules which are assembled into a network of interconnecting nodes. This class of computer programs shares a common drawback, such that with an increasing number of components, the memory requirements increase dramatically. By use of dynamic memory management it may be possible to analyze a large-scale system, but only at the cost of a geometric increase in computation time. The labor involved in model-building is rather extensive and the resulting model, in general, lacks flexibility for future modifications. Furthermore, the analysis capability of these canned programs is limited and usually not effective for design aid or trouble-shooting.

Dedicated models of specific power systems have been used successfully but only on a limited basis. Typically this class of model is capable only of DC analysis. Execution speed and cost of such programs tend to be much less than that for generalized circuit and system routines [1].

Therefore, a comprehensive power system model must be capable of performing various analyses - DC, small signal and large signal transient. The model should have the following attributes :

- flexible for future modification of component models and system configuration,
- accurate to provide sufficient details about the behavior of individual components for design and troubleshooting,
- verifiable whenever equipment or subsystem experimental testing is possible,
- efficient to minimize computer core memory and computation time.

One way to develop a model with such attributes is to modularize various components of the power system. In Chapter 2, a methodology for modeling and analysis of a large scale power system is presented. A two-port subsystem coupling method is utilized and its interconnection law is defined. A system analysis technique is described that defines the total system as being a doubly terminated two-port network for an arbitrary subsystem. The system performance parameters are derived explicitly as functions of the two-port g-parameters of the subsystem (or component) and impedances of its terminating subsystems.

In Chapter 3, modeling of various components of the spacecraft power system is presented. Each component is modeled as an unterminated two-port. For each nonlinear component, the time-domain simulation model and the linearized small signal model are developed. The transfer function models of the g-parameter of individual component and the small signal performance parameters of the terminated component are derived. Using the component models developed in Chapter 3, a sample system model - the direct energy transfer (DET) spacecraft power system is developed. The system model building with the host software system and its analysis capabilities are described in Chapter 4.

Using the DET system model, various types of system level analyses are performed in Chapters 5 through 7. In Chapter 5, various modes of operation of the DET system are described. The large signal behavior of the nonlinear solar array power system is analyzed using the state-plane method. The system behavior near the solar array maximum power point is discussed.

The operation of a battery discharger circuit and the design considerations suitable for a wide range of operations (i.e., from the no-load to full load) of a feedback compensator are described. The complete system mode of operation including the transition mode between the shunt mode and the battery discharging mode is illustrated.

In Chapter 6, subsystem interaction problems are illustrated. The local and global interactions are described and a methodology for the design and trouble-shooting of a system dealing with the interaction problems is presented. One of the most attractive approaches for modeling

and analysis of complex large scale systems is the empirical data modeling technique. In chapter 7, empirical data modeling employing a complex curve fitting technique is presented. The system level analysis using this technique is performed by defining an arbitrary subsystem as a black box for which the curve fitted analytical model is realized from the empirical data. The complex curve fitting technique is extended to derive a reduced order model for a high order subsystem. Conclusions are presented in Chapter 8.

Chapter 2

METHODOLOGY OF MODELING AND ANALYSIS OF SPACECRAFT POWER SYSTEMS

2.1 Introduction

This chapter discusses a modeling and analysis methodology of large scale systems. Section 2.2 uses the modular concept that a large scale system may be defined as an interconnection of many individual subsystems. This idea is particularly attractive for spacecraft power systems consisting of many individual components since each component may be supplied by a different vendor where performance parameters and design information are documented as a stand alone equipment (component). By defining standardized interconnection laws between each component, a system can be analyzed and synthesized using the information of the individual component modules. In Section 2.3 the terminal characteristics of a singly and doubly terminated two-port subsystem are derived as transfer function models for a system linear analysis. In Section 2.4, based on the model representation derived in Section 2.3, system stability is analyzed. This section further addresses the compatibility and incompatibility of individual components in terms of their terminal characteristics and the interconnection schemes. Performance degradations and system instability as a result of undesired component interactions are illustrated. Conclusions are presented in Section 2.5.

2.2 Modeling approach of large scale systems

As discussed in Chapter 1, defining a complete spacecraft power system entirely in terms of circuit elements is not feasible. A practical approach is to consider that a large scale system can be defined by the interconnection of many smaller subsystems. The system dynamics, therefore, stem from the individual subsystems and their interconnection laws that identify the signal flows among them. Due to the complexity of the individual components and their interconnection scheme it is usually very difficult to predict the system's overall behavior. A desirable analysis or synthesis procedure for a large scale system is the separation of these two levels (component and system). One thus prefers an analysis or synthesis procedure to be based, on one hand, on "local" subsystems and, on the other hand, on the "global" interconnection laws.

In order to utilize the modular concept, each component should be modeled as an unterminated two-port network because the terminal characteristics of both source and load are unknown until the complete system is configured. The interconnection law should not only describe accurately the signal flows between the subsystems but also be standardized. There exist several kinds of two-port representations such as impedance, admittance, chain, and hybrid matrices. The impedance or admittance matrices cannot be used as a standard interconnection law because their inputs and outputs are in alternate forms. The chain matrix is not as flexible as the hybrid matrix because it only characterizes a system in uni-direction. For example, the chain matrix can describe port-1 of a two-port subsystem as a function of its load (which may be connected in

cascade in n two-port subsystems) that is connected at port-2 as shown in Eq.(2.1).

$$\begin{bmatrix} e_1^1 \\ f_1^1 \end{bmatrix} = \begin{bmatrix} C_1 \end{bmatrix} \begin{bmatrix} C_2 \end{bmatrix} \dots \begin{bmatrix} C_n \end{bmatrix} \begin{bmatrix} e_2^n \\ f_2^n \end{bmatrix} \quad (2.1)$$

where the subscripts and superscripts represent the port numbers and the j^{th} component, respectively.

However, quite often, it is necessary to analyze a two-port subsystem as functions of both its source and load. Thus, the information flow can be obtained in both directions. Fig.2.1 illustrates the interconnection law for unterminated modules. Each component, in general, receives two inputs v_1 and i_2 from the preceding and following modules, and provides two outputs i_1 and v_2 to the adjacent modules. The terminal characteristics of each component are described by the matrix equation called "hybrid g -parameters" in Eq.(2.2).

$$\begin{bmatrix} i_1 \\ v_2 \end{bmatrix} = \begin{bmatrix} g_{11} & g_{12} \\ g_{21} & g_{22} \end{bmatrix} \cdot \begin{bmatrix} v_1 \\ i_2 \end{bmatrix} \quad (2.2)$$

For modeling a two-port, one can always define the output and input variables as shown in Fig.2.1. For example, a single stage LC low-pass filter can be modeled with state equations as shown in Fig.2.2. Since these unterminated two-port components have the same interconnection law,

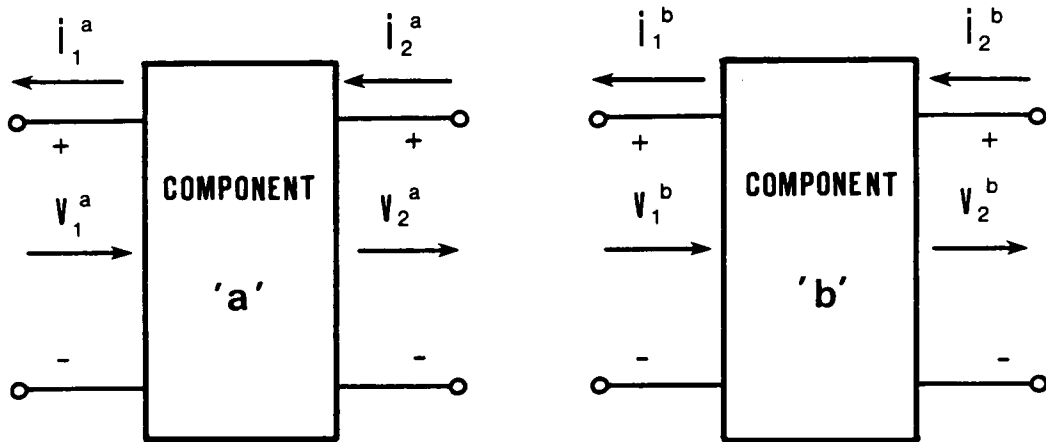
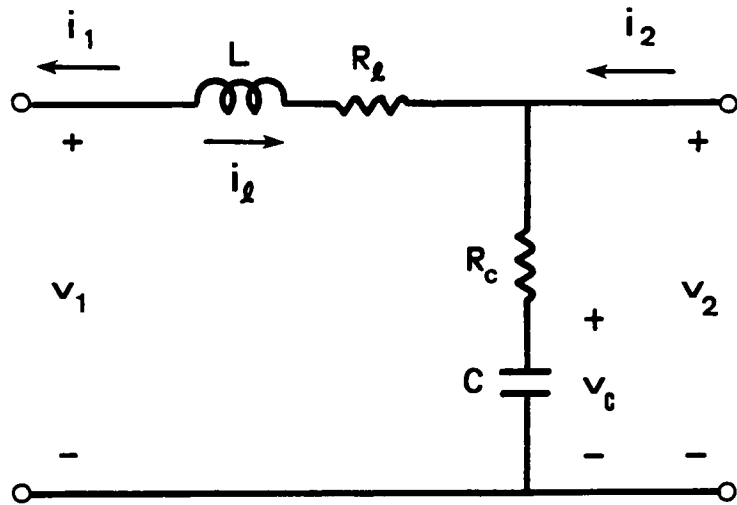


Figure 2.1 Two-port subsystem coupling using the hybrid G-matrix



$$\begin{bmatrix} i_l \\ v_c \end{bmatrix} = \begin{bmatrix} \frac{-(R_l + R_c)}{L} & -\frac{1}{L} \\ \frac{1}{C} & 0 \end{bmatrix} \begin{bmatrix} i_l \\ v_c \end{bmatrix} + \begin{bmatrix} \frac{1}{L} & -\frac{R_c}{L} \\ 0 & \frac{1}{C} \end{bmatrix} \begin{bmatrix} v_1 \\ i_2 \end{bmatrix}$$

$$\begin{bmatrix} i_1 \\ v_2 \end{bmatrix} = \begin{bmatrix} -1 & 0 \\ R_c & 0 \end{bmatrix} \begin{bmatrix} i_l \\ v_c \end{bmatrix} + \begin{bmatrix} 0 & 0 \\ 0 & R_c \end{bmatrix} \begin{bmatrix} v_1 \\ i_2 \end{bmatrix}$$

Figure 2.2 Example of an unterminated two-port modeling with state equations

they can be interconnected arbitrarily to form a system.

Having identified the two-port component coupling approach, efforts have been made to find a software system which can be adapted to multi-port component modeling and system integration. The program EASY5 developed by Boeing Computer Service has been selected as the host software system.

The EASY5 software [2] provides a modular approach to dynamic system model building and analysis. It was chosen particularly for its effective means of assembling complex component modules into a system. Predefined component modules can be stored in the EASY5 component library. The component modules can have several input and output ports and, in each port, information can flow into and out of the port. All detailed connections of signal paths between component blocks are programmed with minimal user intervention. More details about the EASY5 program will be described in Section 4.2.

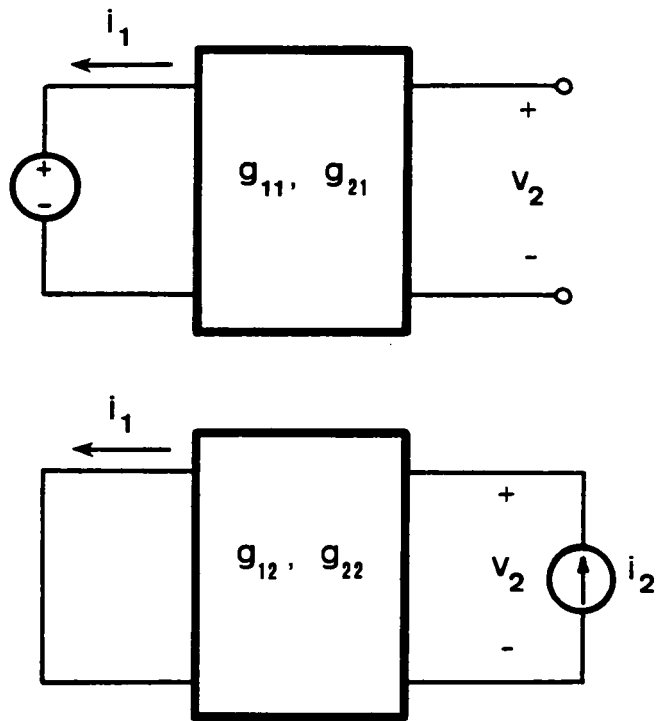
For a large scale spacecraft power system, the large signal performance evaluation is invariably limited to tactics closely identified with time-domain simulation techniques. The actual operating point trajectories or transient responses due to the large signal disturbances can be simulated by a digital computer. In this way, the existence of unstable solutions can be observed along with peak values and actual waveforms. Thus, the model development of each nonlinear component of a spacecraft power systems involves two separate models, the large signal time-domain simulation model and the small signal linearized model.

For time-domain simulation, the EASY5 numerical integration routine calculates current values of the states and output variables for each component from the input values. It then passes along the output values according to the interconnection law. There are seven different types of numerical integration routines available in EASY5. Each offers different numerical stability and computational efficiency dependent on the type of nonlinearity of a system. To examine the approximate large signal behavior of a system analytically, rather than by time-domain simulation, one may simplify the system focusing on a particular component of interest. For instance, the large signal behavior of the solar array system's operating point can be analyzed by simplifying the system to a second order nonlinear system as illustrated in Chapter 5.

For the small signal linear analysis, various types of classical linear system theories can be adapted. The methodology of large scale system analysis is described in the following sections.

2.3 Two-port transfer function models

To analyze the two-port component or subsystem in Fig.2.1, one needs to identify the G-matrix in Eq.(2.2). From Eq.(2.2), the g-parameters can be described in the form of transfer functions. As Fig.2.3 illustrates, each parameter can be identified by using an appropriate sinusoidal current or voltage excitation. It is important to notice that the g-parameters are identified in terms of measurable (observable) quantities, thus the model can be verified. This modeling technique is particularly useful when the component is best treated as a black box due



$$g_{11} = \left. \frac{i_1}{v_1} \right|_{i_2=0} \quad ; \quad \text{open circuit input admittance}$$

$$g_{21} = \left. \frac{v_2}{v_1} \right|_{i_2=0} \quad ; \quad \text{open circuit voltage transfer function}$$

$$g_{12} = \left. \frac{i_1}{i_2} \right|_{v_1=0} \quad ; \quad \text{short circuit current transfer function}$$

$$g_{22} = \left. \frac{v_2}{i_2} \right|_{v_1=0} \quad ; \quad \text{short circuit output impedance}$$

Figure 2.3 Two-port g-parameter realizations

to its complexity or unknown nature. The empirical data of the two-port g-parameters can easily be obtained, from which an analytical closed-form model is realized employing the complex curve fitting technique. The empirical data modeling approach will be discussed in Chapter 7.

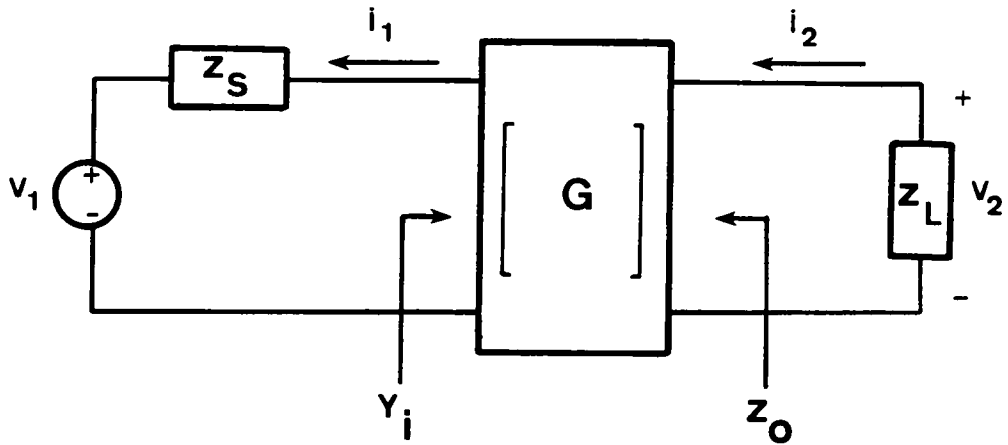
The two-port modeling approach allows one to analyze the complete system with a focus on the local behavior of a particular component or subsystem. As far as the two-port subsystem is concerned, the entire system can be considered as a doubly terminated two-port system as illustrated in Fig.2.4. The quantities Z_S and Z_L are the source and load impedances seen by the two-port subsystem. Notice that the equivalent circuit may be obtained by replacing the generator and Z_S by its Norton equivalent in the circuit. As shown in Fig.2.4 four important parameters can be realized in terms of the two port g-parameters Z_S and Z_L .

$$G_v = \frac{g_{21} \cdot Z_L}{Z_L - Z_S \cdot \Delta G + g_{11} \cdot Z_S \cdot Z_L} \quad (2.3)$$

$$G_i = \frac{g_{11} \cdot g_{22} + g_{12} \cdot g_{21} - Z_L}{g_{21}} \quad (2.4)$$

$$Y_i = \frac{g_{11} + \Delta G / Z_L}{1 + g_{22} / Z_L} \quad (2.5)$$

$$Z_o = \frac{g_{22} - \Delta G Z_S}{1 - g_{11} \cdot Z_S} \quad (2.6)$$



$$G_v = \frac{v_2}{v_1} \quad ; \text{ forward voltage transfer function}$$

$$G_i = \frac{i_1}{i_2} \quad ; \text{ reverse current transfer function}$$

$$Y_i = \frac{i_1}{v_1} \quad ; \text{ load terminated input admittance}$$

$$z_o = \frac{v_2}{i_2} \quad ; \text{ source terminated output impedance}$$

Figure 2.4 Transfer functions of the doubly terminated two-port

where

$$\Delta G = g_{11}g_{22} - g_{12}g_{21}$$

These parameters play important roles in the analysis of the system dynamics especially the interactions among the various subsystems.

2.4 System analysis approach using the transfer function models

One of the major concerns in analyzing the large scale spacecraft power system deals with undesired interactions among components and subsystems. As a system becomes more complex, many undesired interactions occur and they create a large degree of uncertainty in the system response and stability even though each component may be well-behaved and understood. Using the presently existing modeling techniques it may be possible to simulate the entire system by a digital computer. However, the results obtained from the computer simulation often do not provide physical insight and thus give very little design information for future design changes and trouble-shootings. Based on the modeling approach described in Section 2.3 the system can be analyzed with an emphasis on either the local behavior or the global behavior. The analysis allows one to trouble-shoot the interaction problem at any system level.

From Eqs.(2.3) and (2.4) the stability of the total system can be directly analyzed by investigating the poles of the transfer function G_v and G_i . However, since G_v and G_i may be of complex form, interactions among Z_S , Z_L , and the two-port subsystem may not be easily obtainable.

Instead, one can investigate the source and load interaction problems using the quantities Y_i and Z_o in Eqs.(2.5) and (2.6), as illustrated in Fig.2.5.

The line transfer functions in Fig.2.5 can be written as follows.

From Fig.2.5a,

$$H_L = \frac{v_2}{v_s} = \frac{Z_L}{Z_o + Z_L} = \frac{1}{1 + Z_o/Z_L} \equiv \frac{1}{1 + T_L} \quad (2.7)$$

and from Fig.2.5b,

$$H_S = \frac{v_1}{v_s} = \frac{1/Y_i}{1/Y_i + Z_S} = \frac{1}{1 + Z_S \cdot Y_i} \equiv \frac{1}{1 + T_S} \quad (2.8)$$

Eqs.(2.7) and (2.8) shows that the system characteristic equation is $1 + T_S = 0$ or $1 + T_L = 0$, respectively. Hence, the system is stable if the quantity T_S in Eq.(2.7) or T_L in Eq.(2.8) satisfies the Nyquist criterion that is, the system is stable if the number of counterclockwise encirclements of the point $(-1,0)$ in s -plane of the polar plot of T 's is equal to the number of the right-half plane (RHP) poles in T . This statement still may be too general unless the number of RHP poles in T_S or T_L are identified. If we assume that the elements in Eqs.(2.7) and (2.8) are all stable quantities, i.e. no RHP poles, then the Nyquist

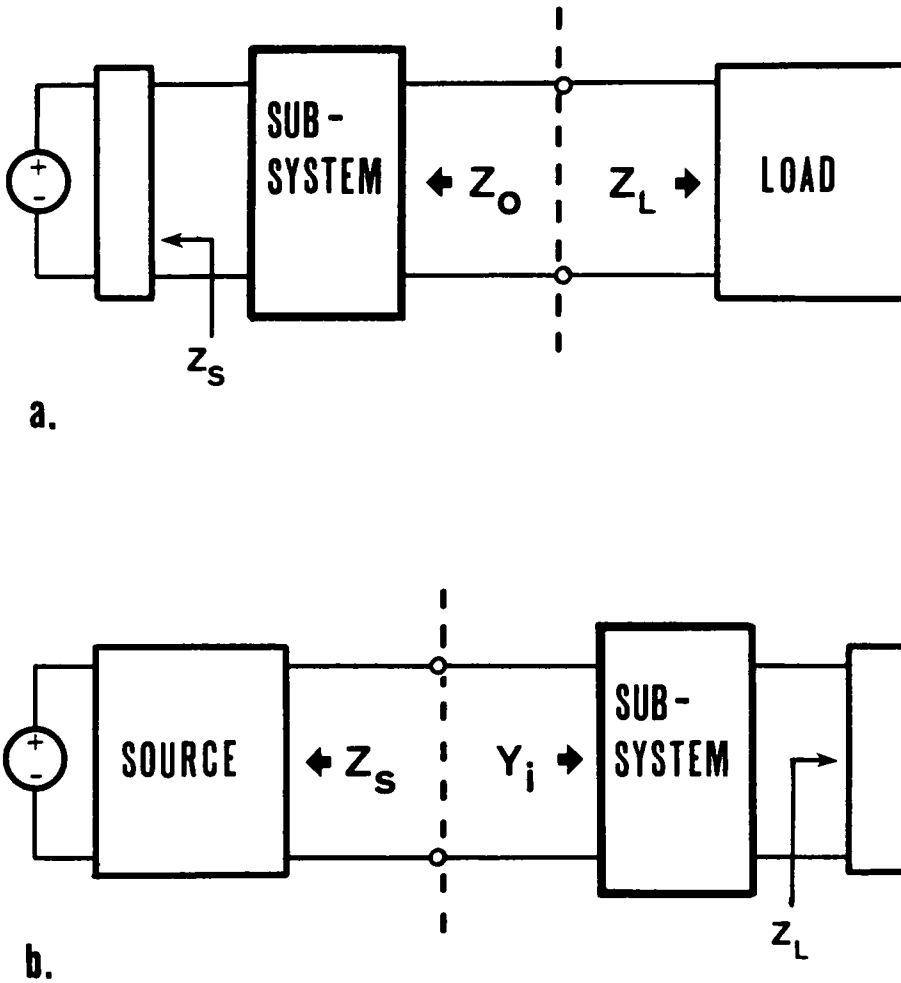


Figure 2.5 Subsystem interaction parameters

(a) load interaction (Z_o vs Z_L)

(b) source interaction (Z_s vs Z_i)

criterion can be restated as; the system is stable if the polar plot of T_S or T_L does not encircle the point $(-1, 0)$.

To analyze the interaction problems of a two-port with Z_S and Z_L , it is necessary to check the above assumption that the quantities Z_S , Z_L , Y_i and Z_o are all stable (i.e., no RHP poles). If any of these are unstable then an interaction has already occurred with an unstable quantity and the analysis should trace back until the point of the undesired interaction is found. To do this consider Fig.2.6 which elaborates the system in Fig.2.4. The quantities in Eqs.(2.3) through (2.6) of the j^{th} component can be expressed in terms of the preceding $(j-1^{\text{th}})$ and the following $(j+1^{\text{th}})$ components.

$$Z_S^j = Z_o^{j-1} \quad (2.9)$$

$$Z_L^j = 1 / Y_i^{j+1} \quad (2.10)$$

From Eqs.(2.5) and (2.6),

$$Z_o^j = \frac{g_{22}^j - \Delta G^j \cdot Z_S^j}{1 - g_{11}^j \cdot Z_S^j} = \frac{g_{22}^j - \Delta G \cdot Z_o^{j-1}}{1 - g_{11}^j \cdot Z_o^{j-1}} \quad (2.11)$$

$$Y_i^j = \frac{g_{11}^j + \Delta G^j / Z_L^j}{1 + g_{22}^j \cdot Z_L^j} = \frac{g_{11}^j + \Delta G \cdot Y_i^{j+1}}{1 + g_{22}^j \cdot Y_i^{j+1}} \quad (2.12)$$

From Eqs.(2.9) through (2.12) the stabilities of Z_S , Z_L , Y_i and Z_o for

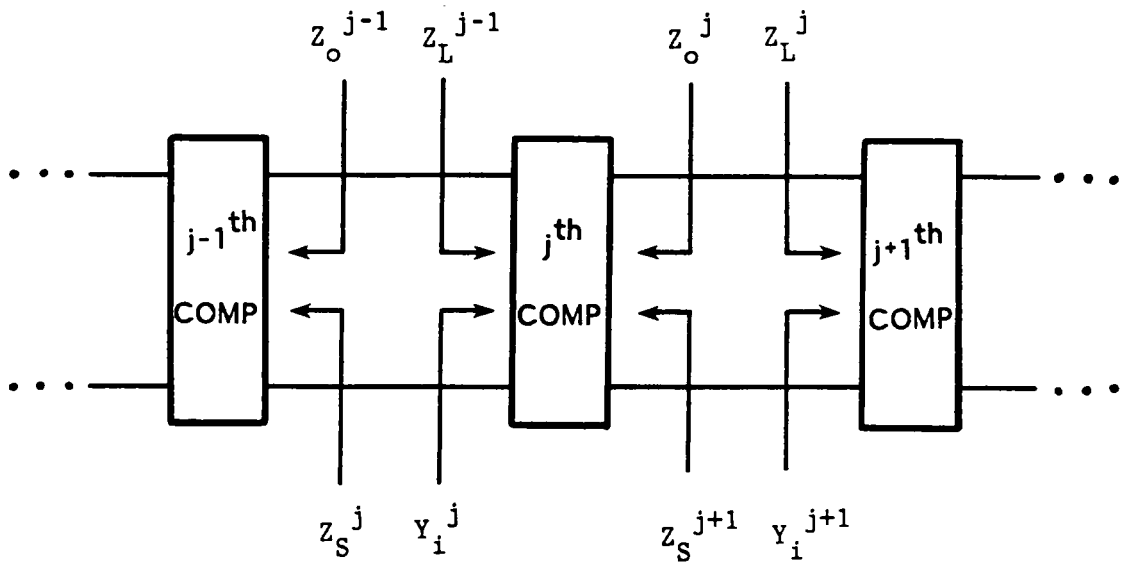


Figure 2.6 Realization of impedances for cascade components

any two-port can easily be checked and the possible undesired interaction points detected. For instance, the stability of output impedance of the j^{th} component Z_o^j can be determined from Eq.(2.11) by applying the Nyquist criterion to the product of $-g_{11}^j$ and Z_o^{j-1} . Since each unterminated component (g-parameters) is assumed to be stable and if Z_o^j is unstable while Z_o^{j-1} is stable, then the undesired interaction occurs between the j^{th} component and its source impedance Z_S^j . The same argument holds for the load interactions such that if Y_i^j is unstable while Y_i^{j+1} is stable then the undesired interaction occurs between the j^{th} component and its load impedance Z_L^j . In other words, the stability of the doubly terminated two-port component can be established as follows. First, using Eqs.(2.9) through (2.12) the stability of Z_S and Z_L must be examined then using Eqs.(2.11) and (2.12) the stability of Y_i and Z_o (singly terminated two-port) can be determined. If Y_i and Z_o are stable, which implies Z_S and Z_L are also stable, then the stability of the doubly terminated two-port system can be established by applying the Nyquist criterion to T_S and T_L in Eqs.(2.7) and (2.8).

2.5 Conclusions

In this chapter the proposed modeling and analysis techniques of large scale systems were discussed. A two-port subsystem coupling method is utilized such that a system is broken into manageable pieces. Each component is modeled as an unterminated two-port and the system is configured according to the interconnection law.

The stability of a linearized large scale system can be analyzed with a focus on either local or global behavior. The analysis not only determines the stability of the total system but also identifies the undesired interactions which occur at various component levels. Thus, the design criteria for components and their integration can be established. The modular approach allows one to pinpoint the undesired interaction point to assist in trouble-shooting. The modular approach also allows the analysis to be performed with well-defined physical quantities (i.e., impedances) which are also observable and measurable both analytically and empirically. The analysis technique is based on well-known classical linear circuit theory, two-port network theory and Nyquist theory so that any practicing engineer can utilize the technique for design, analysis and trouble-shooting a large-scale system. Various types of the system level analyses for the Direct Energy Transfer spacecraft system are performed in Chapters 5 through 7.

Chapter 3

SPACECRAFT COMPONENTS MODELING AND ANALYSIS

3.1 Introduction

Components of spacecraft power systems are inherently nonlinear. As a result, their behaviors due to large signal and small signal disturbances can be quite different. For the analysis of the small signal dynamics, such as stability, input/output impedances, and audiosusceptibility, the model can be developed by linearizing the nonlinear components about an operating point under the small signal assumptions. These small signal models are very useful since all of the well established linear system theories can be applied. Also their analytical closed forms provide useful information for designing the system. However, the small-signal models can not predict the large-signal behavior such as stability in large, initial start-up and step transients, and other nonlinear modes of operation (i.e., protection, saturation, etc.). Therefore, it is necessary to generate a small signal model and large signal model for these nonlinear components to analyze the following performance categories: dc, small signal ac, and large signal transient responses.

It is desirable that various components in the spacecraft power system be modeled with maximum flexibility, using any one of the following representations: state equations, transfer functions, empirical data, and combinations of those. One may choose one of these modeling techniques

best suited for a particular component. For instance, quite often a component can be virtually a black box due to either complexity or unknown nature, then empirical data modeling is the only resort. For a component of piecewise linear nature, such as switching converters, the state space modeling is most convenient. For a linear component, the transfer function model often suits best because it provides more design insight. Integrating these component models in different forms can be done with minimum user's intervention employing the host software system EASY5. Details for the system modeling are discussed in Chapter 4.

As described in Chapter 2, characteristics of the g-parameters for each component should be analyzed as an unterminated two-port network. The system level analysis can then be carried out based on the g-parameters of individual components. In this chapter, modeling and analysis of the following spacecraft components are discussed.

- Switching regulator - Section 3.2
- Solar array - Section 3.3
- Shunt regulator - Section 3.4
- Filter / cable - Section 3.5
- Payload - Section 3.6

3.2. Switching regulator modeling

Because of high efficiency, small size and light weight, switching regulators are widely used in spacecraft power systems. Switching regulators can be characterized, as shown in Fig.3.1, by the three basic functional blocks: power stage, analog feedback controller, and digital signal processor.

The power stage consists of energy storage elements and switches. Transfer of the input power to the load is controlled by the duty ratio of the switch (transistor). The analog feedback controller usually contains an error amplifier and a compensation network. For a single loop control it senses the output (load voltage), and for a multiple loop control it senses the output and states (i.e., load voltage, inductor current, capacitor voltage). The digital signal processor includes a ramp generator, a comparator, and latches. It takes the control voltage from the analog feedback and converts to the pulse width modulated (PWM) signal which controls the switches in the power stage switch.

Switching regulators are inherently nonlinear and discrete. The power stage has several different topological modes controlled by the switch(es) whose switching time is PWM controlled by the feedback controller. The state equations, in general, cannot be solved unless the system is linearized about an operating point. The linearized model can be used to analyze the small signal behavior about an operating point using the relatively simple linear system analysis techniques such as Bode plots, root locus, etc.

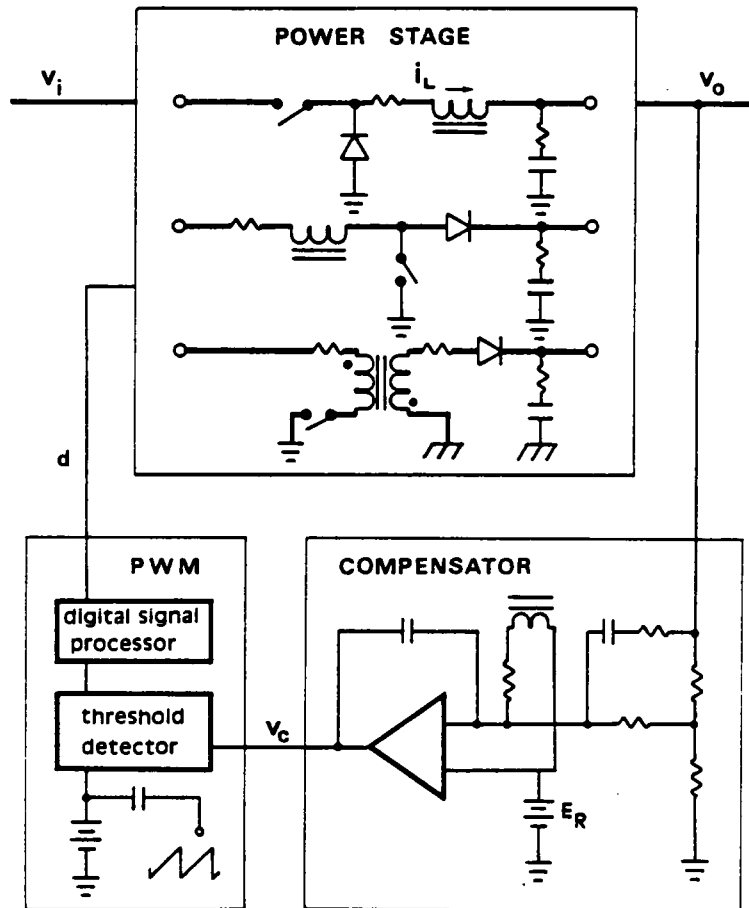


Figure 3.1 Switching regulator model

Since there exist many different types of power stage topologies and control methods, the large signal behaviors of a regulator depend largely on the type of power circuit topology and control. Thus, for maximum flexibility, it is best to develop models for each functional block as independent modules. A regulator can then be configured by collecting appropriate pre-defined modules for each functional block. Also any of these block modules can be freely replaced with alternatives, it provides maximum flexibility in building a variety of regulators. Development of each functional block are as follows.

3.2.1 Power stage modeling

Generally, three different circuit topologies exist in the course of a complete switching cycle. A converter is characterized by three sets of state equations during a switching cycle as follows

$$\begin{aligned} \dot{x} &= A_1 x + B_1 u \\ y &= C_1 x + E_1 u, \quad \text{for interval } d_{n1} T_s \end{aligned} \quad (3.1)$$

$$\begin{aligned} \dot{x} &= A_2 x + B_2 u, \\ y &= C_2 x + E_2 u, \quad \text{for interval } d_{n2} T_s \end{aligned} \quad (3.2)$$

$$\begin{aligned} \dot{x} &= A_3 x + B_3 u, \\ y &= C_3 x + E_3 u, \quad \text{for interval } d_{n3} T_s \end{aligned} \quad (3.3)$$

Here x is the state vector, u the input vector and T_s the switching period. A_i and B_i ($i=1,2,3$) are square matrices which describe the three circuit topologies and the effects of the input vector u . The switch duty ratio is represented by the fractional quantity d_{n1} , and $d_{n1} + d_{n2} + d_{n3} = 1$. These three matrix equation can be combined into one by using the three switching functions shown in Fig.3.2. With use of these switching functions, a single state equation suffices to describe the converter as in Eq.(3.4).

$$\begin{aligned} \dot{x} &= \sum_{i=1}^3 [A_i x + B_i u] d_i(t) , \\ y &= \sum_{i=1}^3 [C_i x + E_i u] d_i(t) , \quad i = 1,2,3 \end{aligned} \quad (3.4)$$

where d_i 's are either 0 or 1 depending on the specific time interval involved.

Eq.(3.4) is not only discrete but also nonlinear because $d_i(t)$ is a function of the state vector x and the input vector u . The power stage model showing in Eq.(3.4) is directly implemented with the appropriate switching function for the time-domain model. The intervals defined above include the possible inductor current discontinuous conduction mode (DCM). For the continuous conduction mode (CCM), there are only two switching intervals, $d_{n1} T_s$ and $d_{n2} T_s$. In the time-domain model, these two different modes of operation (DCM or CCM) are naturally determined depending on circuit parameter values and input and output requirements.

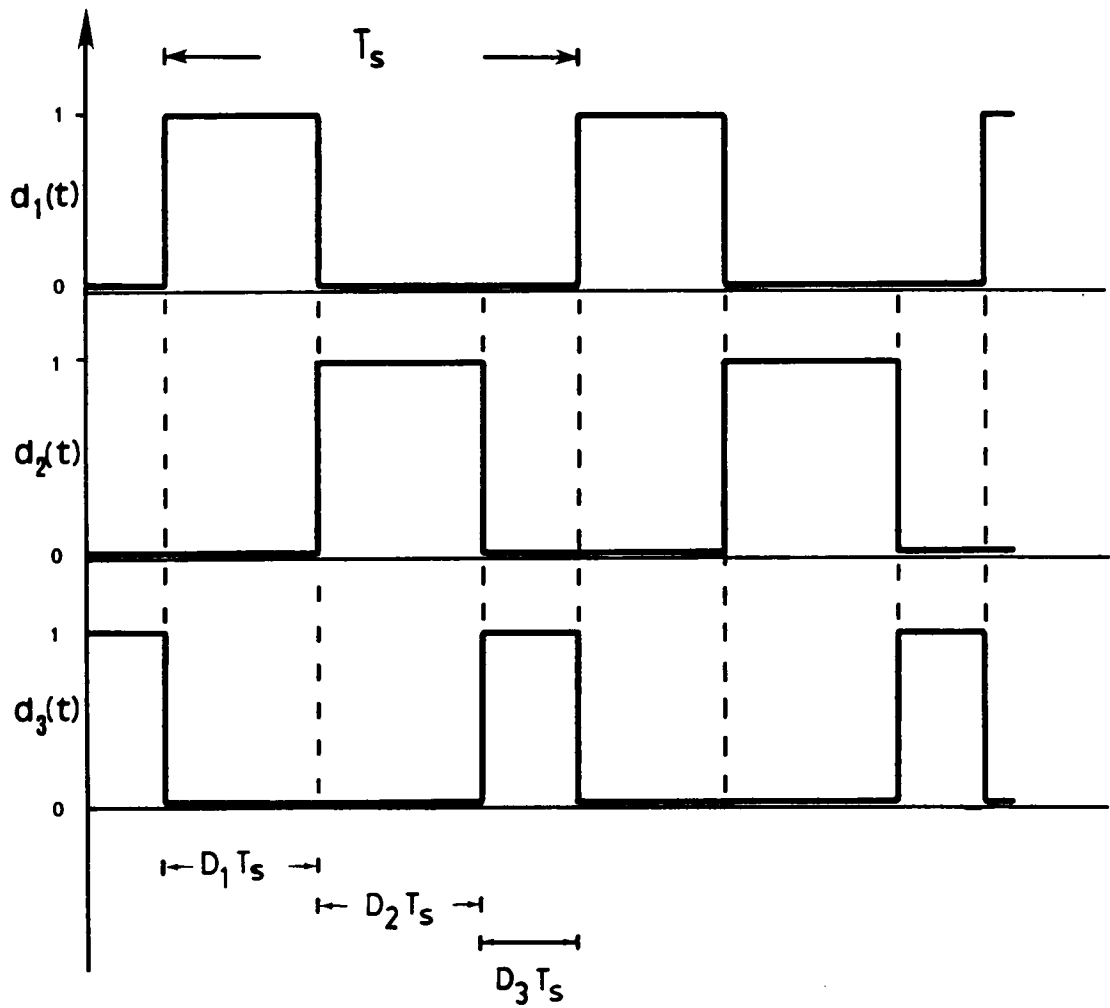


Figure 3.2 Switching functions

However, the small signal models for CCM and DCM are quite different because of the additional constraints of the state,

$$\frac{di}{dt} = 0 \quad (3.5)$$

and

$$i = f(u, y, d_{n1}, L, T_s) \quad (3.6)$$

Therefore, it is necessary to include a step to determine whether the converter is operated in CCM or DCM for the small-signal model by checking the boundary condition between these two modes. From Eq.(3.4), the small signal power stage model can be generated as follows.

Linear small signal modeling of switching converter power stage

For the small signal model, there exist various types of techniques such as state space averaging [3], discrete-averaging [4] and discrete technique [5]. Among these, the state space averaging technique is chosen for this study because it is simple and provides physical insight into the system, thus it is better suited especially for large system analysis. The small signal modeling in this section will be carried out for the case of the constant frequency controlled regulator operating in the CCM. The fundamentals of the state-space averaging are reviewed as follows.

Averaging: First the switching functions $d_i(t)$ are averaged as shown in Fig.3.3. For the CCM where $d_{n3} = 0$, let the averaged values of the

switching functions be

$$\bar{d}_1(t) = d$$

$$\bar{d}_2(t) = 1 - \bar{d}_1(t) = d' \quad (3.7)$$

Both dynamic and static equations for the two (in CCM) switched intervals in Eq.(3.4) are averaged by summing the equations for each interval $d_{ni}T_s$ multiplied by the averaged switching function $\bar{d}_i(t)$. It results in the following continuous system which represents the average effect of the circuit across the whole period T_s .

$$\dot{x} = (A_1d + A_2d')x + (B_1d + B_2d')u$$

$$y = (C_1d + C_2d')x + (E_1d + E_2d')u \quad (3.8)$$

where x and y represent the average state and output.

Perturbation : Suppose that the duty ratio changes from cycle to cycle, that is, $d = D + \hat{d}$ where D is the steady state (dc) duty ratio and \hat{d} is a superimposed (ac) variation, as shown in Fig.3.3. The notation used throughout this section is that dc or steady state values are represented by upper case letters, and perturbations by lower-case with carets. The corresponding perturbations of other quantities can also be expressed as

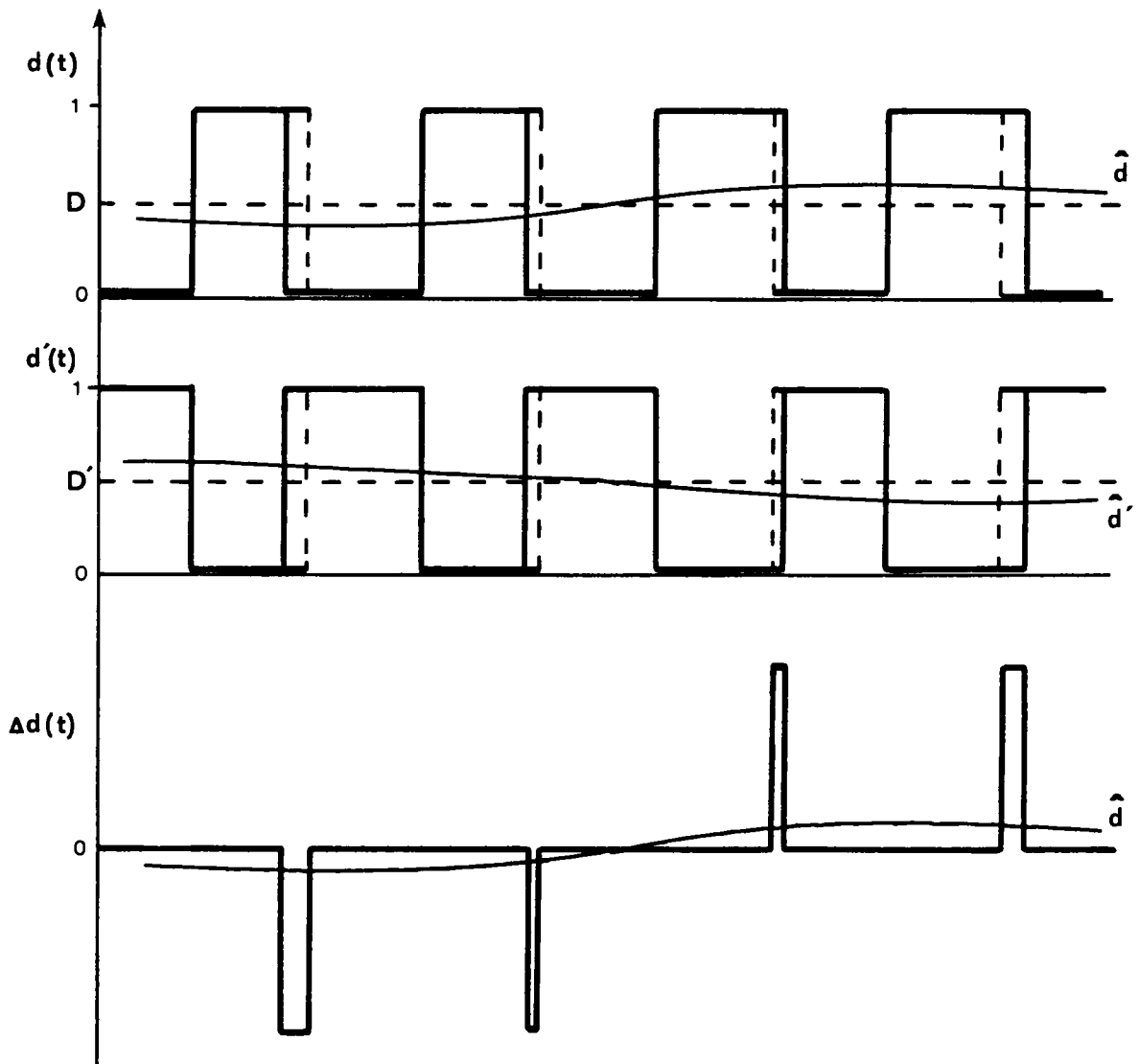


Figure 3.3 Averaging the switching functions

----- : steady state

— : perturbed small signal

$$d' = 1 - d = (1 - D) - \hat{d} = D' - \hat{d}$$

$$x = X + \hat{x}, u = U + \hat{u}, y = Y + \hat{y}, \quad (3.9)$$

Substituting these into the averaged equation (3.8), it becomes

$$\begin{aligned} \dot{\hat{x}} &= AX + BU + A\hat{x} + B\hat{u} + [(A_1 - A_2)X + (B_1 - B_2)U]\hat{d} \\ &\quad + [(A_1 - A_2)\hat{x} + (B_1 - B_2)\hat{u}]\hat{d} \\ \hat{y} &= CX + EU + C\hat{x} + E\hat{u} + [(C_1 - C_2)X + (E_1 - E_2)U]\hat{d} \\ &\quad + [(C_1 - C_2)\hat{x} + (E_1 - E_2)\hat{u}]\hat{d} \end{aligned} \quad (3.10)$$

where

$$A = DA_1 + D'A_2, \quad B = DB_1 + D'B_2$$

$$C = DC_1 + D'C_2, \quad E = DE_1 + D'E_2 \quad (3.11)$$

The perturbed state-space description is nonlinear owing to the presence of the product of the two time dependent quantities \hat{x} and \hat{d} .

Linearization: Let us now make the small signal assumption such that

$$\frac{\hat{u}}{U} \ll 1, \quad \frac{\hat{d}}{D} \ll 1, \quad \frac{\hat{x}}{X} \ll 1, \quad \text{and} \quad \frac{\hat{y}}{Y} \ll 1 \quad (3.12)$$

Using these small signal approximations, the nonlinear second order terms in Eq.(3.10) can be neglected, and the following linear time invariant system is obtained:

$$\begin{aligned}\hat{x} &= A\hat{x} + B\hat{u} + P\hat{d} \\ \hat{y} &= C\hat{x} + E\hat{u} + Q\hat{d}\end{aligned}\quad (3.13)$$

where

$$\begin{aligned}P &= (A_1 - A_2)X + (B_1 - B_2)U \\ Q &= (C_1 - C_2)X + (E_1 - E_2)U\end{aligned}\quad (3.14)$$

By collecting the dc terms from Eq.(3.10), the steady state solutions are

$$\begin{aligned}X &= -A^{-1}BU \\ Y &= (-CA^{-1}B + E)U\end{aligned}\quad (3.15)$$

The small signal solutions can be obtained by taking the Laplace transform of Eq.(3.13).

$$\hat{x} = H_{ux} \hat{u} + H_{dx} \hat{d}\quad (3.16)$$

$$\hat{y} = H_{uy} \hat{u} + H_{dy} \hat{d}\quad (3.17)$$

where

$$\left. \frac{\hat{x}}{\hat{u}} \right|_{\hat{d}=0} = H_{ux} = (sI - A)^{-1} B \quad (3.18)$$

$$\left. \frac{\hat{x}}{\hat{d}} \right|_{\hat{u}=0} = H_{dx} = (sI - A)^{-1} P \quad (3.19)$$

$$\left. \frac{\hat{y}}{\hat{u}} \right|_{\hat{d}=0} = H_{uy} = C H_{ux} + E \quad (3.20)$$

$$\left. \frac{\hat{y}}{\hat{d}} \right|_{\hat{u}=0} = H_{dy} = C H_{dx} + Q \quad (3.21)$$

Eqs.(3.16 - 3.17) describe the power stage of switching converters with unterminated input and output. The differences between the above model and the conventional models for the load terminated case are illustrated in the following example.

Example: Let us consider a boost converter, as shown in Fig.3.4. For simplicity, let us assume the switch is ideal and $R_c = 0$, $R_l = 0$. The two switched models are as shown in Fig.3.5. For choice of state vector $x^T = [i_1 \ v_2]$, the state equations become:

i) interval $d_1 T_s$

ii) interval $d_2 T_s$

$$A_1 = 0, B_1 = \begin{bmatrix} -\frac{1}{L} & 0 \\ 0 & \frac{1}{C} \end{bmatrix} \quad A_2 = \begin{bmatrix} 0 & \frac{1}{L} \\ -\frac{1}{C} & 0 \end{bmatrix}, B_2 = \begin{bmatrix} -\frac{1}{L} & 0 \\ 0 & \frac{1}{C} \end{bmatrix}$$

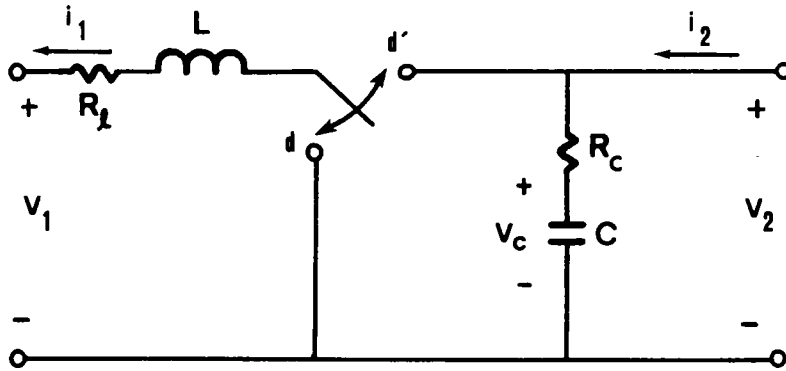
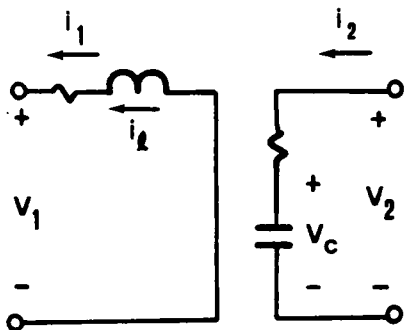


Figure 3.4 Boost converter power stage

i) interval dT_s



ii) interval $d'T_s$

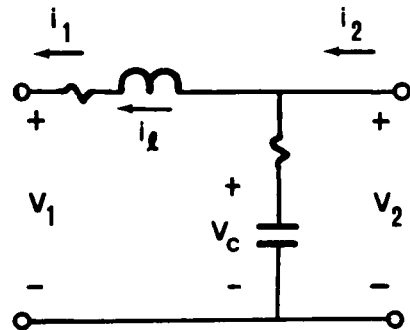


Figure 3.5 Two switched circuit models of the circuit in Fig.3.4

$$C_1 = C_2 = I, \quad E_1 = E_2 = 0 \quad (3.22)$$

(Note that, for simplicity, the sign convention of the states and outputs are purposely chosen so that the states are identical to outputs.)

From Eq.(3.11) the averaged power stage coefficient matrices are

$$A = \begin{bmatrix} 0 & \frac{D'}{L} \\ -\frac{D'}{C} & 0 \end{bmatrix}, \quad B = \begin{bmatrix} -\frac{1}{L} & 0 \\ 0 & \frac{1}{C} \end{bmatrix}, \quad C = I \quad (3.23)$$

Using Eq.(3.15), the steady state solution is

$$Y = X = \begin{bmatrix} I_1 \\ V_2 \end{bmatrix} = \frac{1}{D'} \begin{bmatrix} I_2 \\ V_1 \end{bmatrix} \quad (3.24)$$

The control coefficient matrices using Eq.(3.14) become

$$P = \begin{bmatrix} -\frac{V_1}{D'L} \\ \frac{I_2}{D'C} \end{bmatrix}, \quad Q = 0 \quad (3.25)$$

The small signal model for the power stage is obtained from Eqs.(3.18) through (3.21) and using (3.23) through (3.25) results in

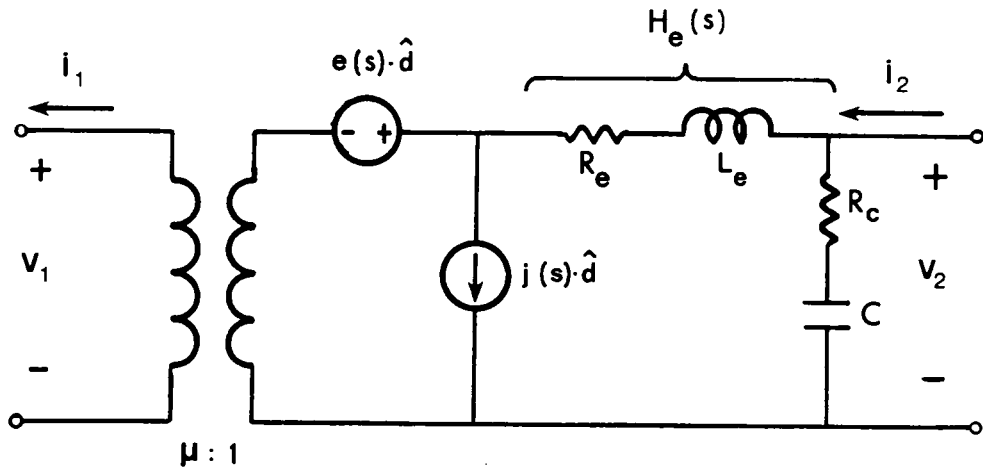
$$H_{uy} = \frac{\hat{y}}{\hat{u}} = \frac{1}{\Delta} \begin{bmatrix} -\frac{C}{D'^2} s & \frac{1}{D'} \\ \frac{1}{D'} & \frac{L}{D'^2} s \end{bmatrix} \quad (3.26)$$

$$H_{dy} = \frac{\hat{y}}{\hat{d}} = \frac{1}{\Delta} \begin{bmatrix} \frac{I_2}{D'^2} (1 - \frac{C V_1}{D' I_2} s) \\ \frac{V_1}{D'^2} (1 + \frac{L I_2}{D' V_1} s) \end{bmatrix} \quad (3.27)$$

where

$$\Delta = 1 + s^2/\omega_o^2, \quad \omega_o = D' / \sqrt{LC} \quad (3.28)$$

It is noticed that the open loop transfer functions in Eq.(3.26) and (3.27) have infinite Q (with ideal elements), which clearly implies the unterminated system. However, the control to output transfer function in Eq.(3.27) carries the dc input information, V_1 and I_2 . This is due to earlier assumption that the model describes the behavior of the regulator about a known steady state operating condition. From the state-space averaged model, the A-matrix does not carry steady state information while the control matrix P does. This may be seen more clearly in the linear equivalent canonical circuit model in Fig.3.6 which can be generated either from the state-space equations or from the circuit averaging



For the boost converter,

$$L_e = \frac{L}{D'^2}, \quad R_e = \frac{R_l}{D'^2}, \quad \mu = D'$$

$$e(s) = \frac{V_1}{D'^2} \left(1 + \frac{L}{D'} \frac{I_2}{V_1} s \right) = \frac{V_1}{D'^2} \left(1 - \frac{L}{D'^2 R_L} s \right)$$

$$j(s) = \frac{I_2}{D'} = \frac{-V_2}{D' R_L}$$

where

$$R_L = -\frac{V_2}{I_2} \quad \text{or} \quad Z_L(s) \Big|_{s \rightarrow 0}$$

Figure 3.6 Linear equivalent canonical circuit model of the switching converter power stage

technique [3]. In the circuit model the control dependent sources $e(s)$ and $j(s)$ are functions of the steady state input values while the effective filter transfer function, $H_e(s)$, is still unterminated. Hence, if a regulator is terminated with a resistive load R_L , the same R_L would appear in $e(s)$, $j(s)$ and $H_e(s)$. However, if a regulator is terminated with a frequency dependent load Z_L , only the steady state dc value of Z_L (i.e., R_L or V_2/I_2) appears in $e(s)$ and $j(s)$ while $H_e(s)$ is terminated with Z_L .

This can be shown from the load terminated open loop transfer functions. For instance, the input voltage to output voltage transfer function becomes

$$H_{vL} = \left. \frac{\hat{v}_2}{\hat{v}_1} \right|_{\hat{d}=0, Z_L} = \frac{1}{D'} \cdot \frac{1}{\left(1 + \frac{L}{D'^2 Z_L} s + \frac{s^2}{\omega_o^2} \right)} \equiv \frac{1}{D'} H_e(s) \quad (3.29)$$

and the control to output voltage transfer function is

$$H_{dL} = \left. \frac{\hat{v}_2}{\hat{d}} \right|_{\hat{u}=0, Z_L} = \frac{V_1}{D'^2} \cdot \frac{1 - \frac{L}{D'^2 R_L} s}{\left(1 + \frac{L}{D'^2 Z_L} s + \frac{s^2}{\omega_o^2} \right)} \equiv e(s) H_e(s) \quad (3.30)$$

where $R_L = -V_2 / I_2$

The analysis of the complete regulator using the above results is presented in Section 3.4.

3.2.2 Analog feedback controller

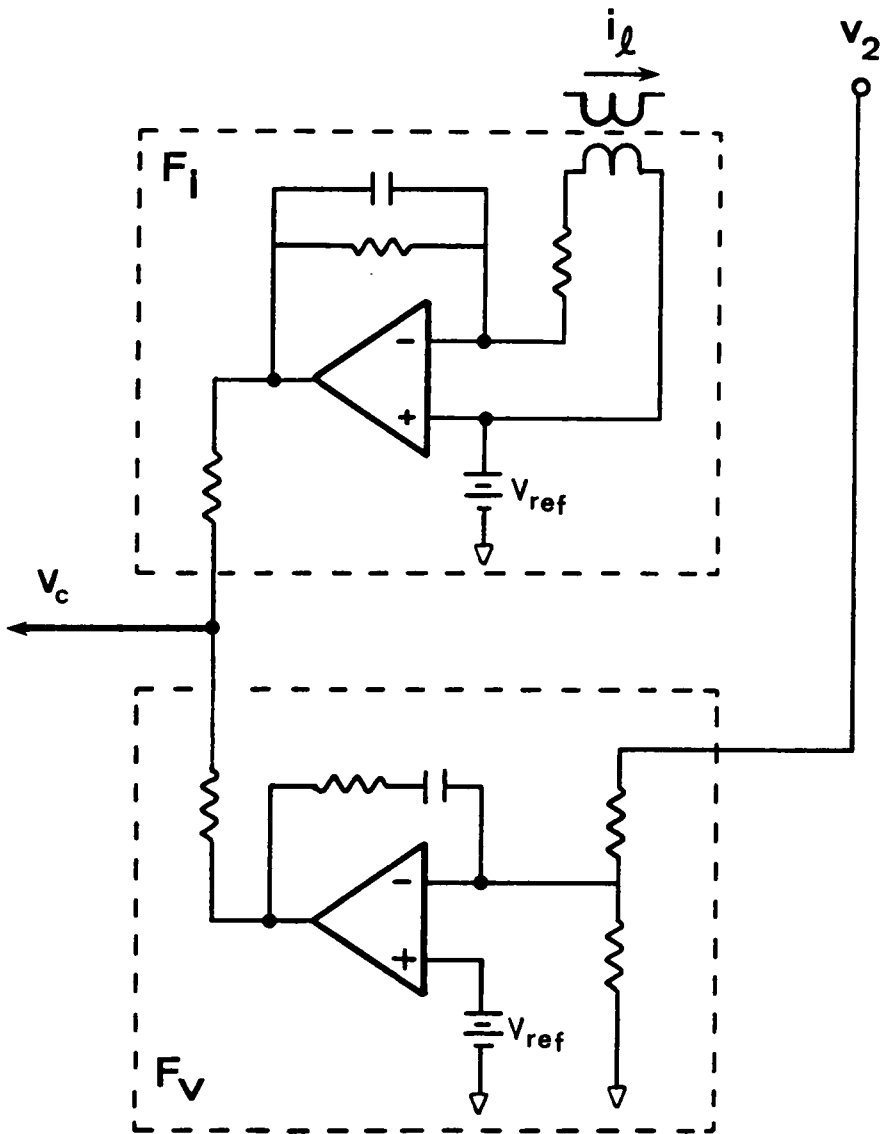
The commonly used analog feedback controller is basically linear for normal modes of operations. The controller can be modeled by either transfer functions or state equations.

The transfer function model, in Eq.(3.31) provides more design insight so that both analysis and synthesis of the controller can be done quite easily. Pole-zero compensations of the closed loop system can be directly implemented using the Bode plot technique.

$$F(s) = \frac{\omega_m (1 + s/\omega_{z1})(1 + s/\omega_{z2})}{s (1 + s/\omega_{p1})} \quad (3.31)$$

However, the second order effects of the feedback, such as the steady state switching ripple component, is lost.

Modeling with state equations can describe the circuit in more detail, and can easily include the nonlinearities such as op-amp saturation, soft starter and protection circuits. However, the circuit topology must be pre-fixed and generally, the model requires more memory space. Also, the model does not provide direct information about the location of poles and zeros to facilitate design of feedback compensator.



$$v_c = F_i i_l + F_v v_2$$

Figure 3.7 A feedback compensator (two-loop)

The choice of model depends on its objective. For design and frequency domain analysis the transfer function model suits better; and for more accurate time-domain analysis and simulation the state equation model may be the better choice. The model developed for this study includes both modeling techniques.

Fig.3.7 illustrates a typical multiple loop controller for switching regulators. The controller senses the state (inductor current) and output (output voltage). The sum of the state and output compensators (F_x and F_y) is the control voltage, v_c , as described in Eq.(3.32).

$$v_c = - (F_x x + F_y y) \quad (3.32)$$

The compensators for a regulator employing the inductor (or switch) current and output voltage sensed feedback become

$$F_x = [F_i \quad 0], \quad F_y = [0 \quad F_v] \quad (3.33)$$

3.2.3 Digital Signal Processor

The digital signal processor (DSP) converts the output control signal v_c from the analog feedback controller into discrete-time pulses to control the ON-OFF of the power switch. The DSP includes the threshold detector (comparator), the ramp function and the latch circuit.

The threshold detector compares the input control signal, v_c , and the reference signal (either dc or the ramp) to generate the trigger signal for the transistor OFF command. The latch circuit then generates the PWM signal, $d(t)$, as shown in Fig.3.2 to the power transistor. For the multiple loop control, the input control signal, v_c , is the sum of the outputs of each feedback loop. In this case v_c may include the ramp function (from the inductor current or switch current feedback), then the external ramp function can be optional, which quite often is needed to stabilize the system operating more than 50% duty ratio. Fig.3.8 illustrates this multiple loop case. For each different duty ratio control law such as constant frequency, constant T_{on} and constant T_{off} , the DSP model should be different. These different modules are stored in the component module library, as in the case of power stage models.

For the small signal model, the transfer function from the control signal v_c to the duty cycle modulation d is obtained as in Eq.(3.34) using the describing function technique.

$$d = FM \cdot v_c \quad (3.34)$$

3.2.4 Switching regulator small signal modeling and analysis

Connecting the power stage, analog feedback controller and the digital controller (Figs. 3.6, 3.7 and 3.8) together completes the small signal regulator model as shown in Fig.3.9. This unterminated regulator is ready

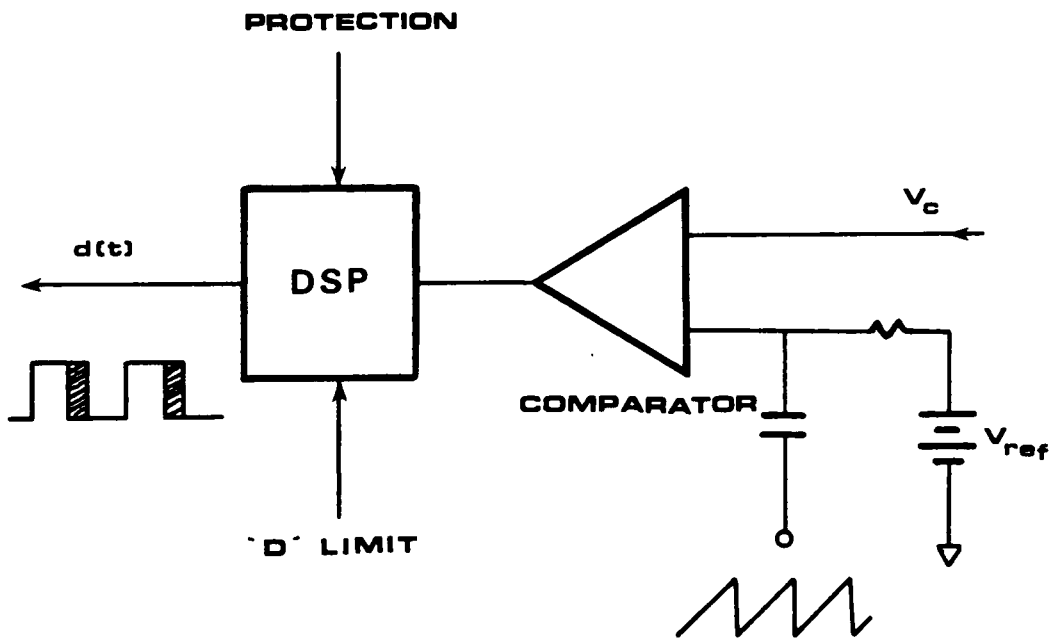


Figure 3.8 Pulse-width-modulator (PWM)

to be connected into a system. Analysis of the doubly terminated regulator can be carried out using the g-parameters of the regulator. The g-parameters can be derived either from the circuit model shown in Fig.3.9 or from the block diagram representation in Fig.3.10. The block diagram model in Fig.3.10 which uses transfer functions can describe any type of converter topology and associated controls. While the model shown in Fig.3.9 is circuit oriented, and it does provide physical insight into the system.

The closed loop transfer functions from the the input vector \hat{u} to the output vector \hat{y} , the G-matrix of the regulator, can be directly derived from the transfer function model in Fig.3.10.

$$\frac{\hat{y}}{\hat{u}} = [I + T_{eq}]^{-1} \cdot \left[H_{uy} - FM \cdot H_{dy} \cdot F_x \cdot [[I + T_x]^{-1} \cdot H_{ux}] \right] \quad (3.35)$$

where

$$T_{eq} = FM \cdot H_{dy} \cdot \left[F_y - F_x \cdot [[I + T_x]^{-1} \cdot H_{dx} \cdot F_y \cdot FM] \right] \quad (3.36)$$

$$T_x = H_{dx} \cdot F_x \cdot FM \quad (3.37)$$

Using the g-parameters in Eq.(3.35), performance parameters of the regulator operating in the system such as stability, audiosusceptibility, input and output impedances etc. can be derived as functions of impedances of its terminating subsystems.

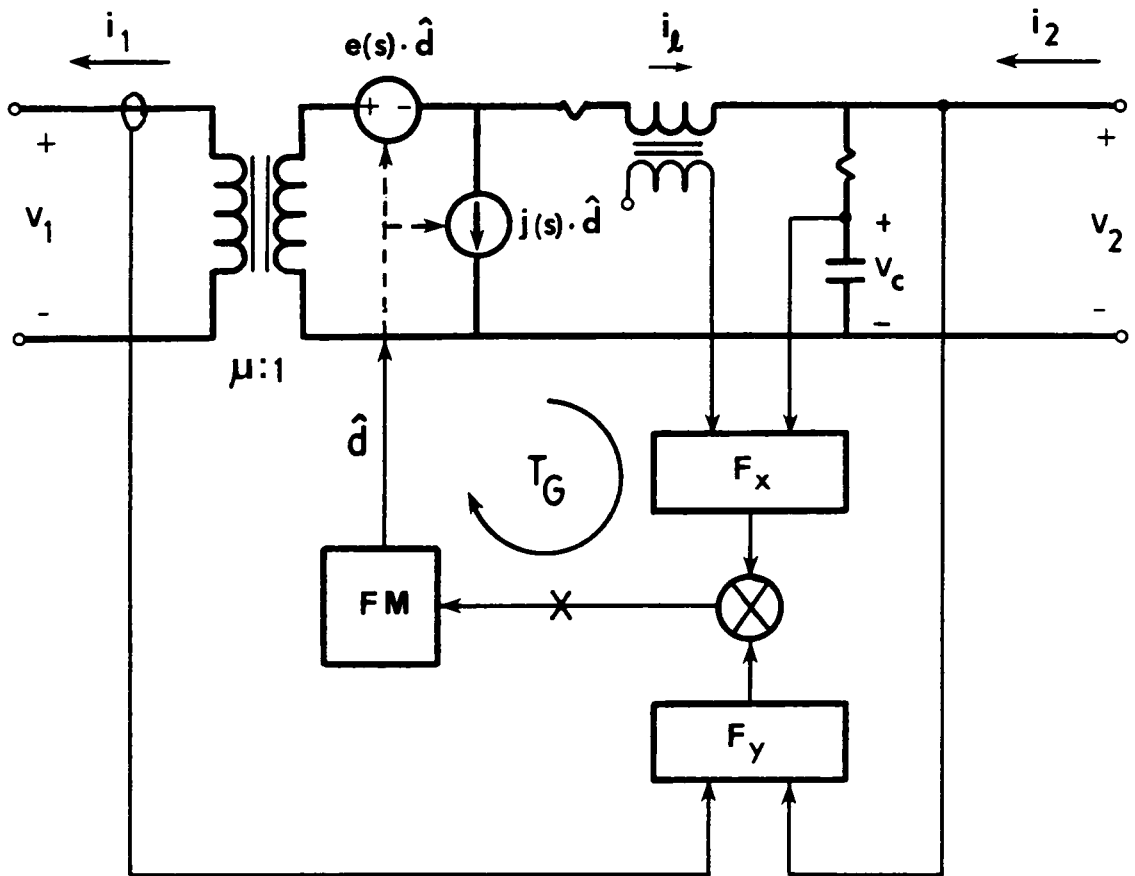


Figure 3.9 Small signal model of switching regulators

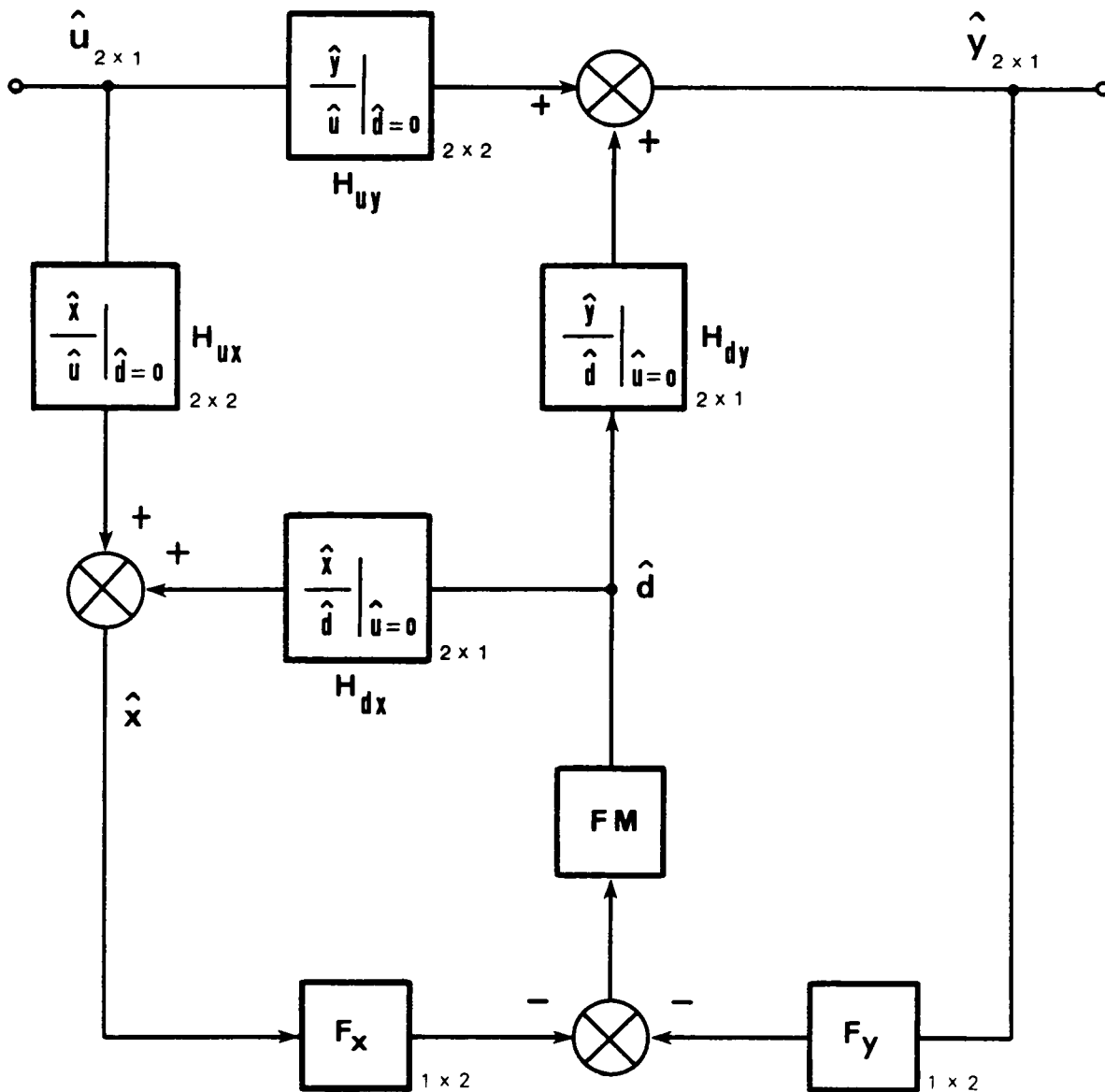


Figure 3.10 Small signal transfer function model of switching regulator

Analysis of a complete regulator is illustrated in the following example using the boost converter power stage derived in Section 3.2.1. For simplicity, a single loop (output voltage feedback) control is used.

Example: Analysis of the boost regulator using the g-parameters

For single loop output feedback control, the compensators are

$$F_x = 0, \quad F_y = [0 \quad F_v] \quad (3.38)$$

and let the PWM gain be FM.

The closed loop input to output transfer functions in Eq.(3.35) becomes

$$G = [I + FM H_{dy} F_y]^{-1} H_{uy} \quad (3.39)$$

Using the results of the power stage equations in Section 3.2.1 (Eqs.(3.26) and (3.27)), the g-parameters in Eq.(3.39) are

$$g_{11} = - \frac{1}{\Delta (1+T_G) D'^2} \left[(1+T_G) C s + \frac{FM I_2}{\Delta D'} \left(1 - C \frac{V_1}{I_2} s \right) F_v \right] \quad (3.40)$$

$$g_{12} = \frac{1}{\Delta (1+T_G) D'} \left[(1+T_G) \frac{1}{D'} - \frac{FM I_2}{\Delta D'^3} L s \left(1 - C \frac{V_1}{I_2} s \right) F_v \right] \quad (3.41)$$

$$g_{21} = \frac{1}{\Delta (1+T_G)} \frac{1}{D'} \quad (3.42)$$

$$g_{21} = \frac{1}{\Delta (1+T_G)} \frac{L}{D'^2} s \quad (3.43)$$

where

$$\Delta = 1 + s^2/\omega_o^2 \quad (3.44)$$

$$T_G = \frac{F_M V_1}{\Delta D'^2} \left(1 + \frac{L I_2}{D' V_1} s\right) F_V \quad (3.45)$$

Here, $1 / \Delta$ is the effective filter transfer function $H_e(s)$. T_G is the loop gain defined at Point X for the unterminated regulator shown in Fig.3.9. The stability of the unterminated regulator can be determined using the quantity T_G . The Bode plot technique can be used to check the Nyquist stability criterion. Using Eqs.(3.40) through (3.45), the total system can be treated as a doubly terminated two-port system focusing on the switching regulator. The system analysis can then be carried out by defining the source and load impedances seen by the regulator.

As described in Section 2.4, a doubly terminated regulator (two-port) can be analyzed in two-steps. First, by considering the load terminated regulator (one-port) with ideal source and then including the source impedance Z_S . These steps when compared to the expression for a doubly terminated two-port system in Eqs.(2.3) and (2.4), provide more physical insight into the system analysis especially for the interactions from

either the source or the load to the regulator.

Singly (load) terminated regulator:

Suppose that the regulator is terminated by a load impedance of Z_L , which is frequency dependent with a dc value of R_L , as shown in Fig.3.11.

The line transfer function from \hat{v}_1 to \hat{v}_2 is

$$G_{vL} = \frac{\hat{v}_2}{\hat{v}_1} = \frac{g_{21}}{1 + g_{22}/Z_L} \equiv \frac{g_{21}}{1 + T_L} \quad (3.46)$$

where the values of g-parameters are in Eqs.(3.40) through (3.43).

Before we analyze Eq.(3.46), let us expand this equation for comparison with the regulator terminated with resistive load R_L . Knowing the steady state operating condition (i.e., dc output power), the steady state input values can be expressed in terms of the regulated dc output voltage and the dc output power.

$$V_1 = V_2 / D', \quad I_2 = - V_2 / R_L \quad (3.47)$$

where

$$R_L \equiv V_2^2 / P_{out}$$

Expanding Eq.(3.46), the line voltage transfer function becomes

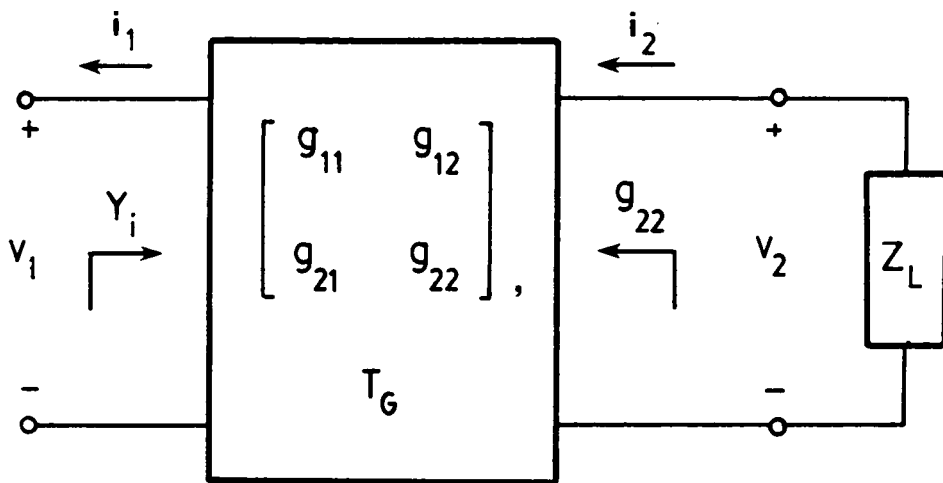


Figure 3.11 Load terminated switching regulator

$$\frac{\hat{v}_2}{\hat{v}_1} = \frac{1}{D'} \frac{1}{\left[1 + \frac{L}{D'^2 Z_L} s + \frac{s^2}{\omega_o^2} \right] + \left[\frac{V_2}{D'} \left(1 - \frac{L}{D'^2 R_L} s \right) FM F_V \right]} \quad (3.48)$$

term (1)
term (2)

It should be noted that in Eq.(3.48) the term (1) includes Z_L and the term (2) includes R_L . This again can be seen clearly from Fig.3.6 where the effective filter in the power stage model (the term (1)) is now terminated with Z_L , while the control source is a function of the dc operating condition (the term (2)).

If the load impedance, Z_L , is complex frequency dependent, it is neither easy to obtain design insight nor to analytically determine the stability of the regulator using Eq.(3.48). However, in Eq.(3.46), Z_L is factored out so that the stability of the regulator can be determined using the quantity T_L . The load interaction criteria of the regulator can also be examined from T_L . For instance, if one assumes that g_{21} or g_{22} is stable, then the Nyquist stability condition is simplified such that the regulator is stable if T_L does not encircle the point (-1,0). The condition can easily be checked using the Bode plot technique. Furthermore, under the above assumptions the sufficient condition for stability is simply $|g_{22}| < |Z_L|$ for all frequency.

Using Eq.(2.5), the input impedance $Z_i = 1 / Y_i$ of the load terminated regulator shown in Fig.3.11 can be derived as

$$Y_i = - \frac{g_{11} + \Delta G / Z_L}{1 + g_{22} / Z_L} \equiv \frac{Y_{eq}}{1 + T_L} \quad (3.49)$$

The stability of the regulator can also be determined from Eq.(3.49) using T_L . Analysis of the doubly terminated regulator taking into account of the source interactions can be carried out using Y_i . Eq.(3.49) can be expanded done as in the line transfer function in Eq.(3.48). At low frequencies where ΔG and g_{22} are vanishingly small and Y_i becomes $-g_{11}|_{s \rightarrow 0}$, the input impedance characteristic is

$$Y_i = - \frac{1}{D'^2 R_L} \quad (3.50)$$

This exhibits the negative input resistance characteristic as expected for all switching regulators.

Doubly terminated regulator:

Fig.3.12 shows the complete (doubly terminated) regulator system. The performance parameters of the system are derived using the results from the load terminated regulator as follows: first, the two line transfer functions defined in Fig.3.12 are expressed as

$$G_{s1} = \frac{\hat{v}_2}{\hat{v}_s} = \frac{Z_s}{Z_s + Z_i} = \frac{1}{1 + Z_s / Z_i} \equiv \frac{1}{1 + T_S} \quad (3.51)$$

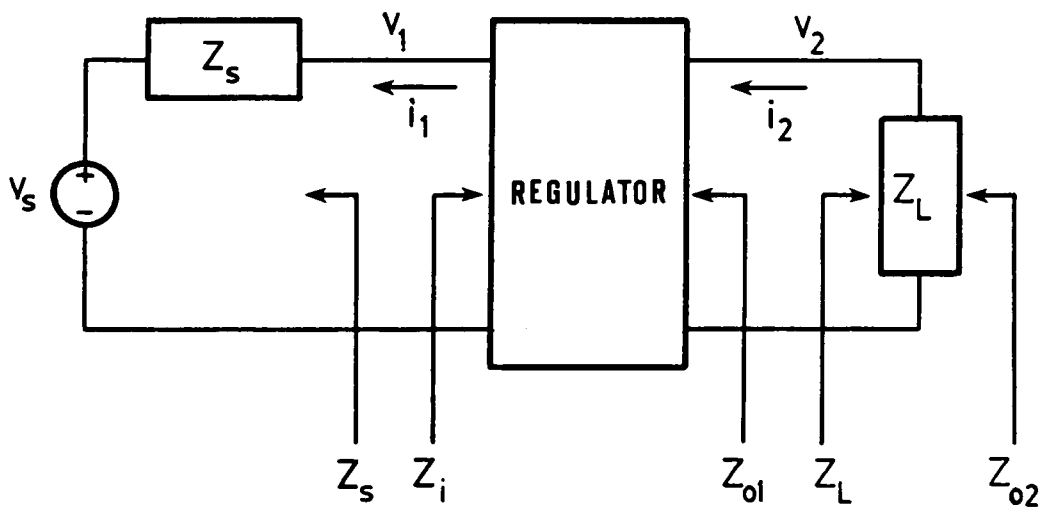


Figure 3.12 Doubly terminated switching regulator

$$G_{s2} = \frac{\hat{v}_2}{\hat{v}_s} = \frac{v_1}{v_s} \cdot \frac{v_2}{v_1} = G_{s1} \cdot G_{vL} \quad (3.52)$$

where G_{vL} is in Eq.(3.46)

From Eqs.(3.51) and (3.52), the stability of the doubly terminated regulator can be determined using T_S . If G_{vL} is stable, which implies Y_i does not have RHP poles, then the Nyquist stability condition can be checked the by Bode plots of T_S . Also, the source interactions to the regulator can be examined using T_S .

Secondly, the output impedances of the regulator can be defined as Z_{o1} and Z_{o2} as shown in the figure. Z_{o1} is used for analyzing the interactions between the regulator terminated with Z_s and the external load. Z_{o2} is used to characterize the regulator behavior due to the load disturbance, such as transient analysis due to a step load.

$$Z_{o1} = \frac{g_{22} - \Delta G}{1 - g_{11}} \frac{Z_s}{Z_s} \quad (3.53)$$

$$Z_{o2} = Z_{o1} \parallel Z_L \quad (3.54)$$

The analysis for the boost regulator example shows that, using the g-parameters, singly or doubly terminated regulator performance parameters can easily be obtained along with the interaction information between

the regulator and its source and load. The analysis can be applied to any two-port subsystem. Thus, one can focus on a particular subsystem and its interactions with the remainder of a total system. Some examples analyzing the source and load interactions of switching regulators are discussed in detail in Chapter 6.

3.3 Solar Array Modeling

Solar arrays made of silicon solar cells are the main source of electrical power for spacecraft power systems. Because the individual silicon cell used on these arrays is small and produce very little power, many solar cells are connected in series and parallel combination to provide necessary power to the spacecraft.

Several approaches have been proposed for modeling a solar array [6]. First, it is possible to model an array by an interconnection of individual solar cells (individual cell model approach). Advantages of this approach are: the parameters for the individual cell model are readily obtained using established measurement and calculation procedures, also the effects of parameter variation can be easily included. Shadowing effects as well as cell faults can easily be inserted into the array. However, the major disadvantage of this approach is that an extremely large amount of computation time is required, which might easily exceed the total computer capacity.

Another approach is to use a single cell model to simulate the entire array (macro model approach). The major advantage to this approach is its ability to minimize the computer time and memory space needed to analyze array behavior. Parameters can be obtained by incorporating the measurement/calculation of the individual cells and their interconnection scheme, or simply by making terminal measurements of the array itself. Neither of these methods takes much more effort than would be needed for the individual cell model. One disadvantage to the macro model approach

is the fact that a new set of macro model parameters needs to be developed for each different array configuration. This tends to make the model less flexible than is desired. Furthermore, shadowing and individual cell faults cannot be easily included in the macro model without, again, a redevelopment of model parameters.

An approach which uses a combination of the macro model and the individual cell model could be very advantageous. This approach retains the flexibility of the individual cell model approach while reducing the necessary computer time and capacity. Any fault that occurs in an individual cell can be simulated. Shadowing effects can be accounted for in much the same way as would be done in the individual model. Sensitivity of parameters can also be easily analyzed by varying individual cell parameters. The disadvantage of using the combination model is the need to determine at least two sets of model parameters -- one set for the group of individual cells and one set for the group of macro models.

The choice of modeling approaches depends on the particular modeling objective(s). In the present study, the macro model approach shown in Fig.3.13 is used for performing an overall system analysis. With this type of analysis it is presumed that the model of the solar array need not have great detail.

3.3.1 Solar array DC model

For this study, two types of DC models are generated. One is the analytical macro model and the other is the empirical data model. For the

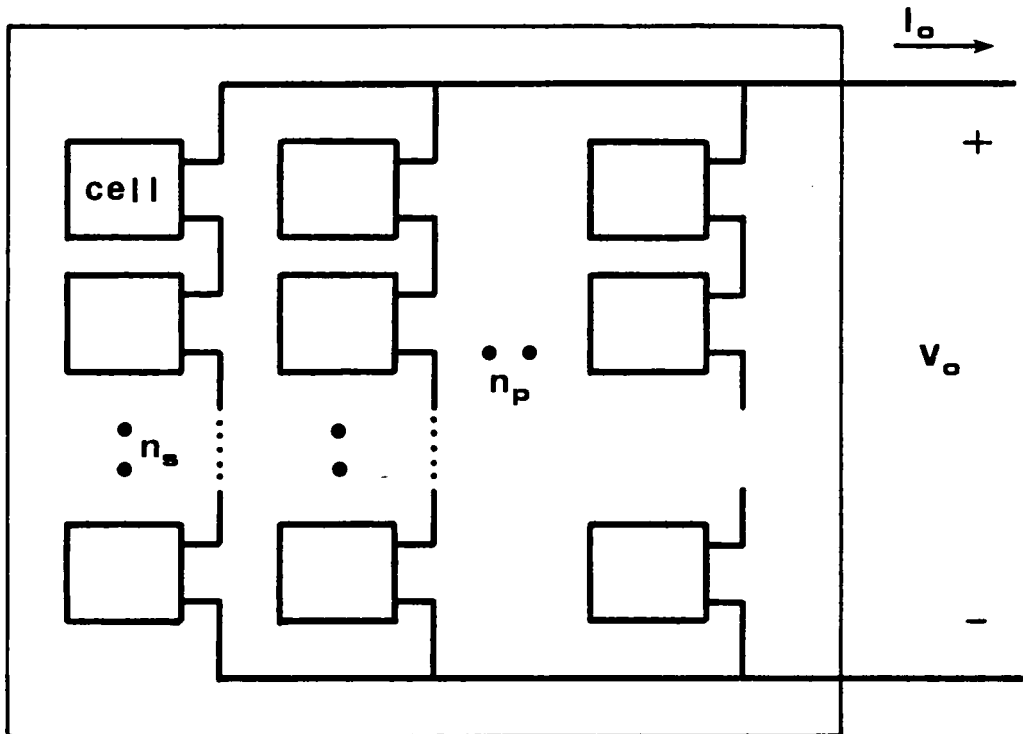


Figure 3.13 Solar array macro modeling

analytical solar array macro model, the standard solar cell DC model shown in Fig.3.14 is used [7]. It provides very good first order predictions of the DC behavior of the silicon cell. A great deal of literature is available for determining its parameter values. An analytical expression that describes the terminal characteristics of the solar cell model in Fig.3.14 is

$$I_o = I_s - I_{do} \left[\exp\left(\frac{QV_D}{KT}\right) - 1 \right] - \frac{V_D}{R_{sh}} \quad (3.55)$$

Based on the cell model, a solar array macro model is constructed by assembling solar cells in parallel and series connections as shown in Fig.3.13. In Fig.3.15, the resulting equivalent circuit model is illustrated where its elements are lumped parameters and are functions of the number of cells in series, n_s , and the number of parallel strings, n_p . The analytical expression for the terminal I-V characteristic of the solar array model in Fig.3.15 is then

$$I_o = n_p \left[I_s - I_{do} \left[\exp\left(\frac{QV_D}{KT}\right) - 1 \right] - \frac{V_D}{R_{sh}} \right] \quad (3.56)$$

where

$$V_D = \frac{V_o}{n_s} + \frac{R_s \cdot I_o}{n_p} \quad (3.57)$$

From Eqs.(3.56) and (3.57) the solar array operating conditions are set by the load power demand and shunt regulator limiting voltage. Other more elaborate analytical DC models exist, such as multiple element models

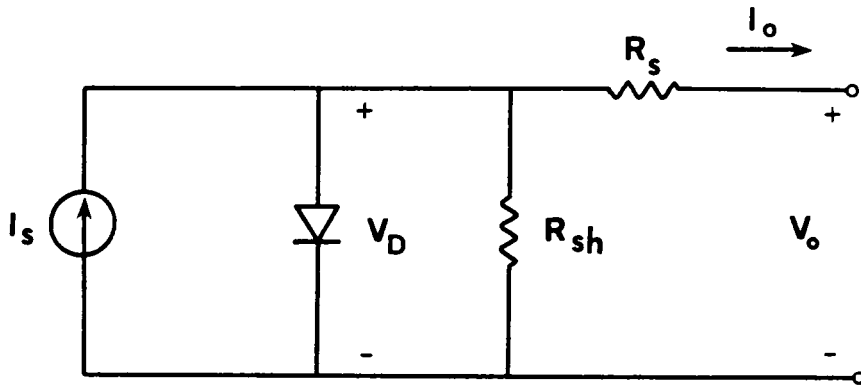


Figure 3.14 DC model of solar cell

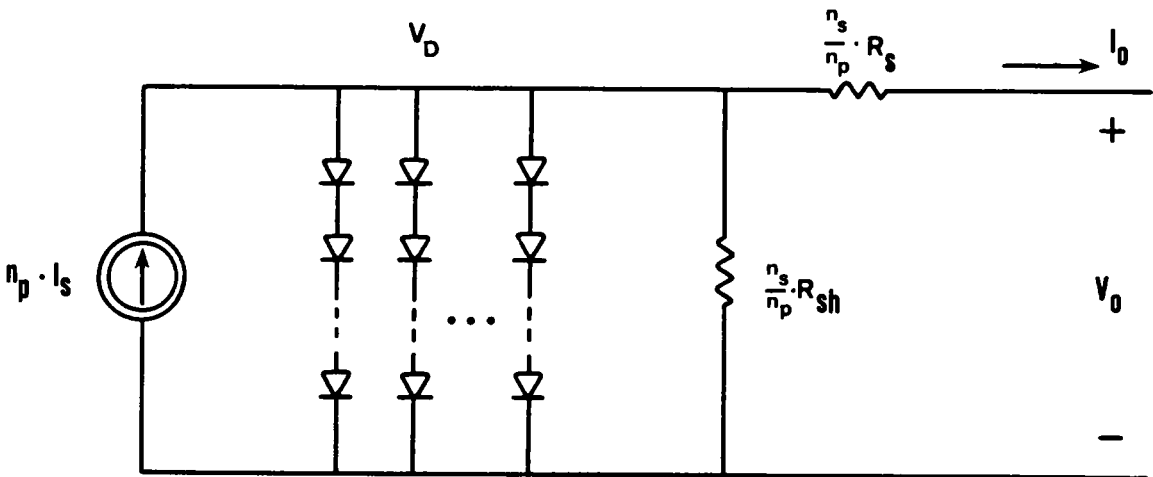


Figure 3.15 DC macro model of solar array

which include second order effects [8]. Those multiple-element models are used to model accurately the effects of distributed series resistance, complex voltage dependencies and radiation. Due to the complexities of the model it is not practical to incorporate these second order effects in the system study.

The other model generated for this study is based on the empirical data about the solar array terminal I-V characteristics. The solar array illumination level and temperature are chosen as the dependent variables. Based on four dimensional empirical data, both linear and high order interpolation techniques are employed to define the array I-V characteristic curve for any arbitrary temperature and illumination. Fig.3.16 shows the interpolated I-V characteristics based on two interpolated empirical data curves for two different illumination levels. In these data, temperature is assumed constant. Other dependent variables for the array characteristics can be added, provided their effects can be accounted for by the measurement. If the measurement data is available, this approach is simple and accurate.

The solar array dc output resistance, r_o is the tangential slope of its terminal I-V characteristic.

$$r_o = - \left. \frac{\Delta V}{\Delta I} \right|_{I_o, V_o} \quad (3.58)$$

r_o can be also analytically derived by replacing the diode in the solar array dc model in Fig.3.15 by the nonlinear diode dynamic resistance r_d .

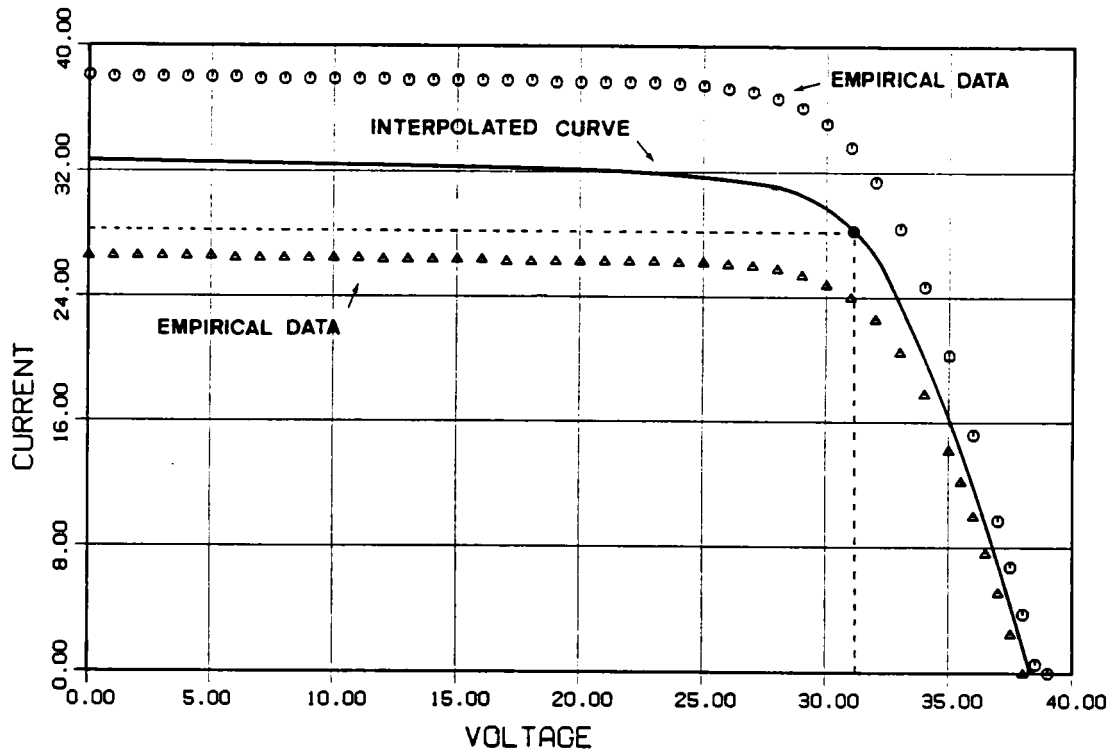


Figure 3.16 Empirical data modeling of the solar array

Differentiating the diode equation in Eq.(3.56) and substituting Eq.(3.57), the value of r_d at a particular solar array operating point is obtained

$$r_d \left| I_o, V_o = n_p I_{do} \frac{Q}{kT} \exp\left(\frac{QV_D}{kT}\right) \right| V_D = V_o/n_s + R_s \cdot I_o/n_p \quad (3.59)$$

Then the output resistance is

$$r_o = R_s + r_d \parallel R_{sh} \quad (3.60)$$

The characteristic of the solar array I-V curve is mainly determined by r_d and its value widely varies depending on the operating point.

3.3.2 Solar array small signal model

The standard small signal AC solar cell model is shown in Fig.3.17. Like the DC model, this model is simple and produces good first order results. Various techniques are given in the literatures for determination of the model parameter values. For the first order AC model, the parameters are dependent on output voltage and current, cell temperatures, cell illumination, and physical cell dimension. It is, therefore, necessary to take these dependencies into account when model parameters are calculated. It is especially important to characterize the dynamic resistance whose value varies widely with the operating point on the solar array dc I-V characteristic. With a procedure similar to the DC model

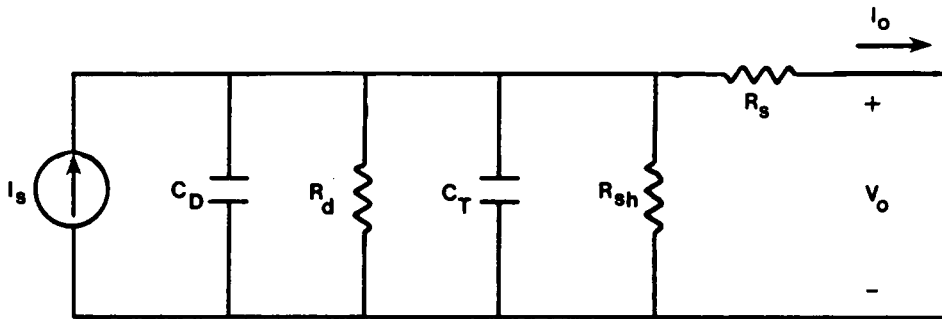


Figure 3.17 Small signal AC model of solar cell

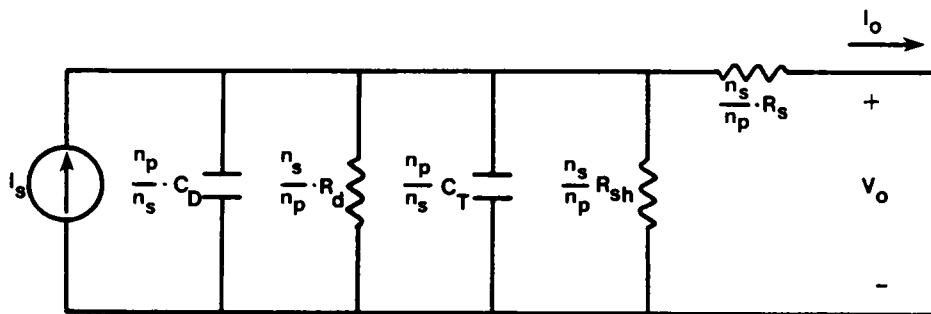


Figure 3.18 Small signal AC macro model of solar array

generation, an equivalent small signal macro model is developed as shown in Fig.3.18. For an array model, additional circuit elements should be included to incorporate the cell interconnection. These parameter values, of course, depend on the physical construction of an array. In the present model, they are described as lumped R, L, and C which are functions of the number of cells in series and parallel, n_s and n_p , respectively.

From Fig.3.18, the output impedance is

$$Z_o = R_s + R_{eq} \parallel (1 / sC_{eq}) \quad (3.61)$$

where

$$R_{eq} = r_d \parallel R_{sh}$$

$$C_{eq} = C_T + C_D$$

As mentioned in the previous section, the characteristic of the solar array output impedance, Z_o , varies widely depending on the its dc operating condition. This nonlinear output impedance coupled with a load impedance, Z_L , may result in a system instability as the dc operating point is changed. This is particularly critical when Z_L represents a switching regulator. The constant power characteristic of switching regulators coupled with the solar array I-V curve can create multiple equilibrium points. Stability of each equilibrium point depends on the dc operating region of the solar array. The detailed stability analysis of the solar array operating point is presented in Section 5.2.

3.4 Shunt regulator modeling

Shunt regulators are used to clamp the power bus voltage during periods when excess solar array power is available. There exist basically two types of shunt regulator circuits, linear type and PWM type. Modeling techniques for the PWM type have received a great deal of attention in the last two decades. A discussion of the modeling and analysis of the PWM regulator is contained in the section on Switching Regulator Modeling. The shunt regulator configurations can also be categorized by their functions, full shunt and partial shunt. In a full shunt configuration, the shunt elements are placed in parallel with the load. In a partial shunt configuration, the shunt elements only shunt part of the solar array cells. Each solar array segment is tapped by a shunt element. In this dissertation, the full shunt configuration is modeled.

Fig.3.19 illustrates the simplified block diagram of a typical shunt regulator. The basic circuit functions are as follows. A divided-down sensed bus voltage is compared to a reference voltage. The error voltage is then amplified to control the current through the shunt elements. A resistor of suitable value (depending on the maximum design current for the shunt) is used in the shunt current path to monitor the current that is flowing through the shunt elements. Before the actual shunt circuit is described, the linear analysis using the simplified circuit in Fig.3.19 will be carried out in the following section.

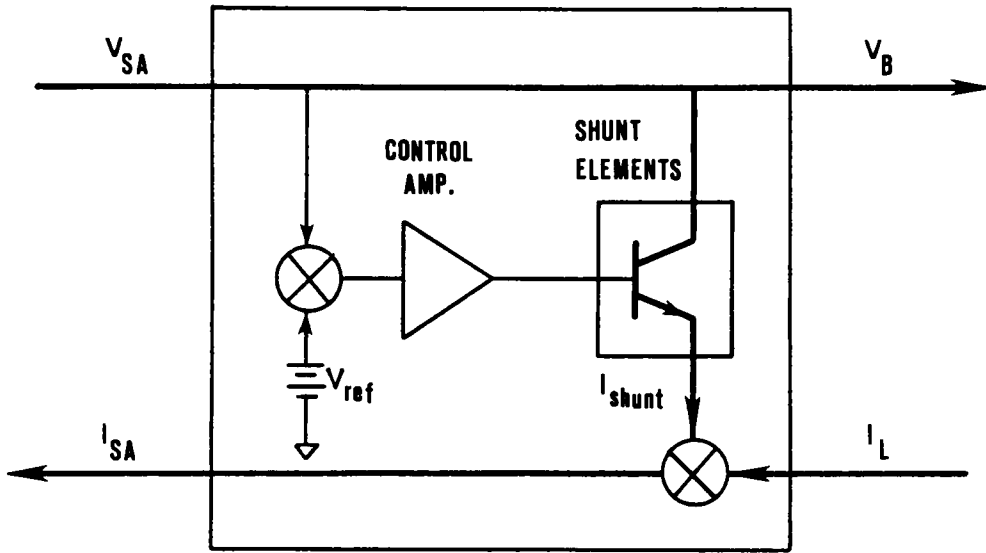


Figure 3.19 Simplified shunt regulator model

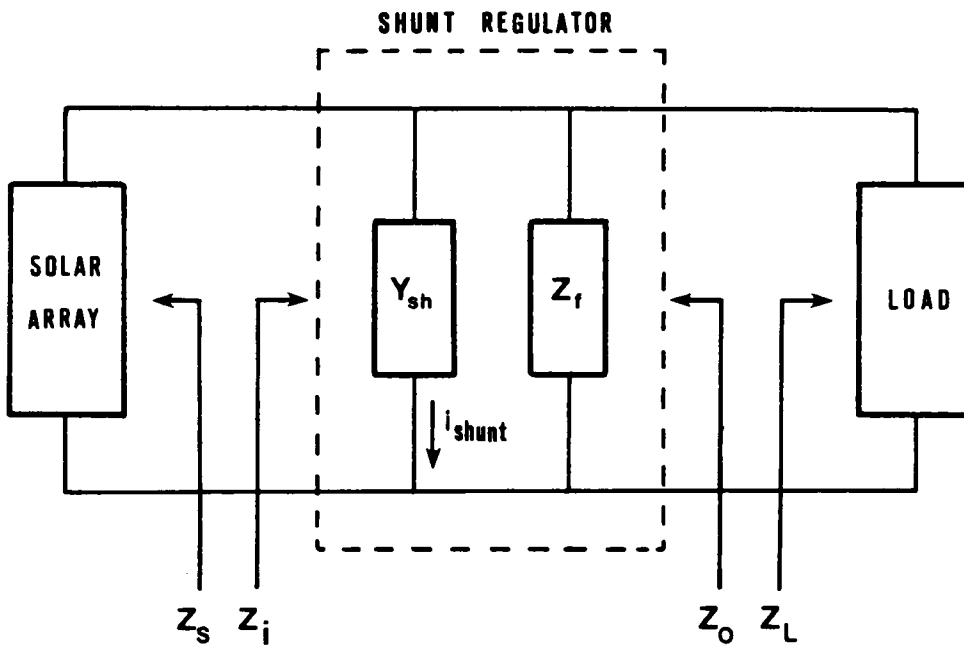


Figure 3.20 Doubly terminated shunt regulator system

3.4.1 Linear analysis of the shunt regulator

Using the quantity Y_{sh} , the transadmittance from the bus voltage to the shunt current, the impedance seen by the bus for the unterminated shunt regulator is

$$Z_B = \frac{Z_f}{1 + Z_f Y_{sh}} \quad (3.62)$$

and the loop gain of the regulator is

$$T_G = Z_f Y_{sh} \quad (3.63)$$

The performance parameters for the doubly terminated regulator shown in Fig.3.20 are derived as

$$Z_B = \frac{Z_f \parallel (Z_S \parallel Z_L)}{1 + Y_{sh} [Z_f \parallel (Z_S \parallel Z_L)]} \quad (3.64)$$

$$\begin{aligned} T &= Y_{sh} [Z_f \parallel (Z_S \parallel Z_L)] \\ &= T_G \frac{1}{1 + Z_f / (Z_S \parallel Z_L)} \end{aligned} \quad (3.65)$$

For the load (Z_L) terminated regulator, the input impedance is

$$Z_i = \frac{Z_f \parallel Z_L}{1 + Y_{sh} (Z_f \parallel Z_L)} \quad (3.66)$$

and the loop gain of the load terminated regulator can be defined as

$$T_L = Y_{sh} (Z_f \parallel Z_L) = T_G \frac{1}{1 + Z_f / Z_L} \quad (3.67)$$

For the source (Z_S) terminated regulator, the output impedance in Fig.3.20 is

$$Z_o = \frac{Z_f \parallel Z_S}{1 + Y_{sh} (Z_f \parallel Z_S)} \quad (3.68)$$

and the loop gain of the source terminated regulator is

$$T_S = Y_{sh} (Z_f \parallel Z_S) = \frac{Y_{sh} Z_S}{1 + Z_S / Z_f} \quad (3.69)$$

As described in [9], design and analysis of the unterminated shunt regulator performance parameters in Eqs.(3.62) and (3.63) are fairly straightforward. However, as Eqs.(3.64) through (3.69) show, stability of the regulator depends on its terminating impedances. Since the source impedance, Z_S , is the output impedance of the solar array, and the shunt regulator is supposed to be operated in the constant voltage region of the solar array, where the magnitude of Z_S is small, the source

interaction is not as critical as the load interaction and is quite predictable using Eq.(3.69).

The load characteristics of the power conditioning equipment create two areas of interaction concern in the stabilization of the shunt regulator feedback loop. First, the resonance of the input filters required the power conditioning equipment create a low impedance on the bus (Z_L) at the resonant frequency, which may cause an oscillation or an undesirable transient response. Secondly and more importantly, because the typical load (equipment) for shunt regulator is (a) switching regulator(s), the phase of the loop gain T_L in Eq.(3.67) can be shifted more than 180° when its magnitude is still greater than unity due to the negative input impedance characteristic of the switching regulator. This fact established the need to have the output impedance of the shunt regulator, Z_B and the load impedance, Z_L , well defined over the bandwidth of the loop gain.

3.4.2 Modeling of the shunt regulator circuit

A block diagram of an existing shunt regulator as shown in Fig.3.21 has been modeled in detail. The computer model takes into account salient dynamics and nonlinearities including op-amp feedback, op-amp saturation and darlington cut-off. All diodes, including the transistor's base-to-emitter junction, have an infinite reverse impedance. The first order model is used for transistors with their parameter values (such as current gain and base-emitter junction voltage, v_{BE}) as the user's inputs. The LC low-pass filter is added, which can be considered as the input filter

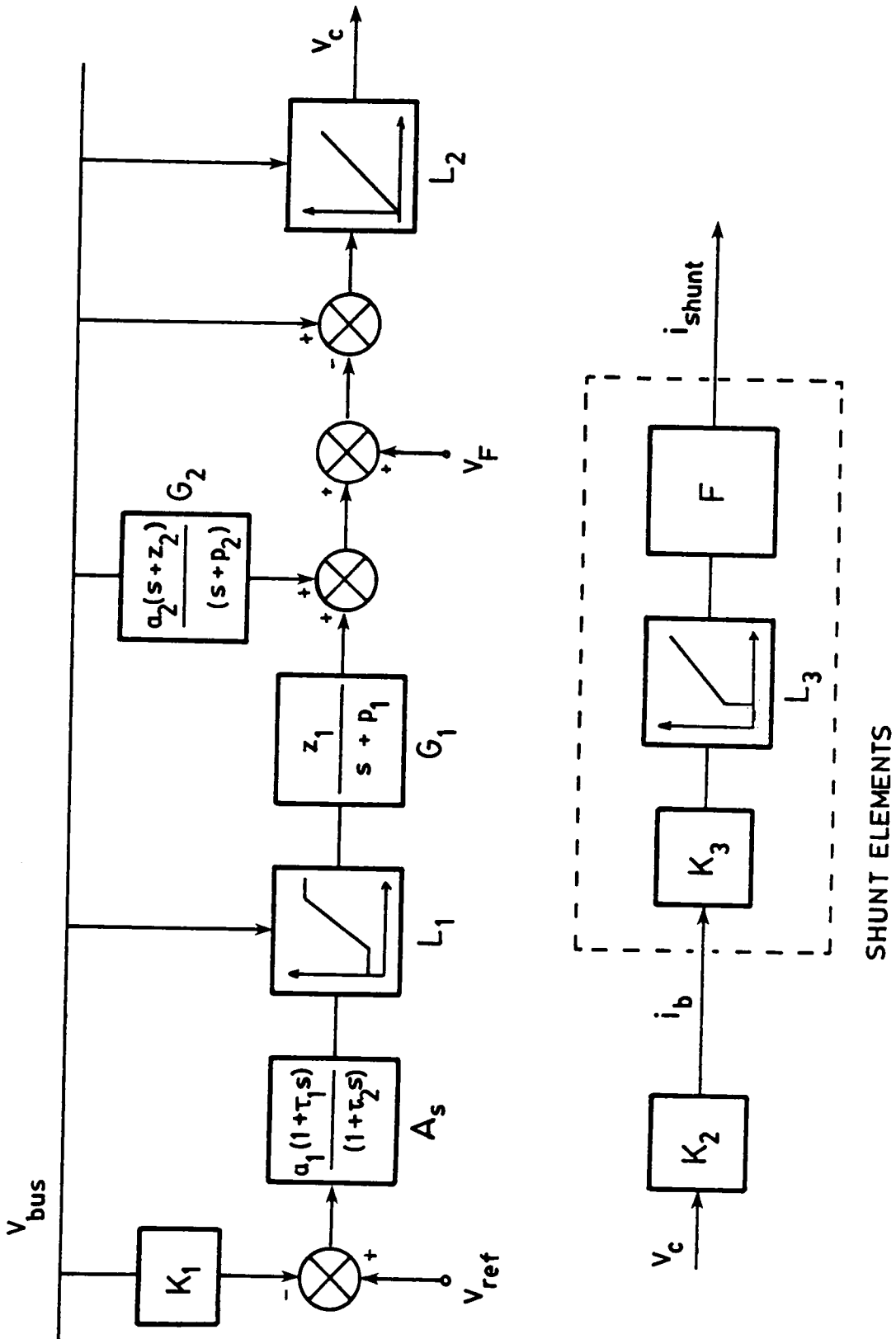


Figure 3.21 Shunt regulator circuit block diagram

and/or the cable impedance from the solar array to the shunt regulator. It also allows preservation of the standard interconnection law providing four terminal variables.

The analytical derivation of the transadmittance Y_{sh} from Fig.3.21 results

$$Y_{sh} = [K_1 A_s G_1 + G_2] K_2 K_3 F \quad (3.70)$$

where K_1 is the sensed voltage divider factor, A_s the error amplifier transfer function, G_1 and G_2 the filter transfer functions, K_2 the gain from the base voltage of the darlington transistor to the base current of the shunt elements, K_3 the 12 sections of shunt elements, and F the current gain of the shunt elements.

3.5 Filter / Cable Modeling

Spacecraft power components usually require both input and output filters to decrease andiosusceptibility and to reduce switching current ripple seen by the main bus. This is particularly important for switching converters having pulsating input or output current. Power distribution cables can also be realized in the filter model.

Fig.3.22 shows the single stage low pass filter configuration that can be used for both filter and cable models. Other multiple stage filter or n-section cable models can be constructed by cascading the model. Parameter estimation can be quite difficult for a cable model. An at-

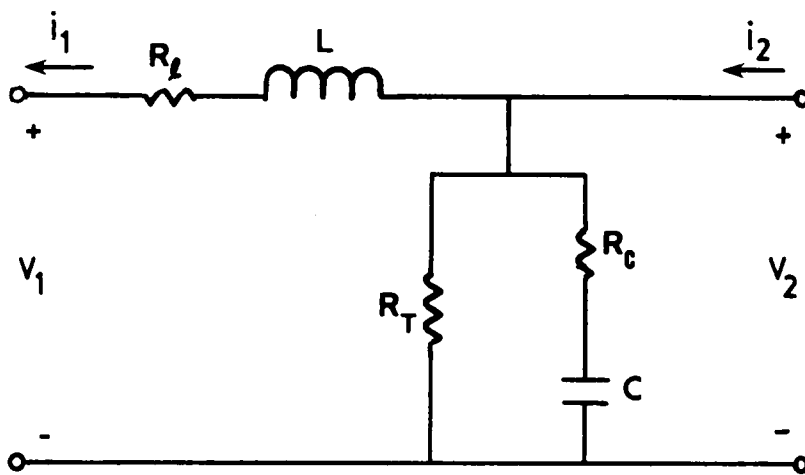


Figure 3.22 Single section of cable/filter model

attractive modeling approach using empirical data modeling of such components is discussed in Chapter 7. From Fig.3.22, the two-port g-parameters are derived as follows.

$$G = \frac{1}{Z_1 + Z_2} \begin{bmatrix} -1 & Z_L \\ Z_L & Z_1 \cdot Z_2 \end{bmatrix} \quad (3.71)$$

where

$$Z_1 = R_\ell + sL$$

$$Z_2 = R_T \parallel (R_c + 1 / sC)$$

3.6 Payload modeling

Due to the diversity of the spacecraft's payloads, flexibility of the modeling technique is very important. As described in Chapter 2, the modeling techniques can use state equations, transfer functions, empirical data, or a combination of these. For experimental data modeling, the payload can be characterized by two types of data, frequency domain and time domain. The frequency domain data can be further realized with the analytical model by using the complex curve fitting technique. Details of this approach are described in Chapter 7. The time-domain data model can be obtained in tabular form when the payload is a time varying function where the dependent variable is not directly a function of system electrical operating conditions but of some external variables such as temperature, light intensity, aging, etc.

3.7 Conclusions

In this chapter, components of spacecraft power systems are modeled. Each component is described with the two-port hybrid g-parameters and its interconnection law.

A component level analysis is performed by means of unterminated, singly terminated (one-port), and doubly terminated (two-port) component. The important performance parameters such as line transfer functions, input and output impedances and feedback loop gains for regulators are derived in analytical closed forms in which the terminating elements, Z_S and Z_L are explicitly factored out. Based on the derivations in this chapter, the system level analysis focusing on the subsystem interactions can easily be carried out as will be shown in Chapter 6.

Chapter 4

MODELING A DIRECT ENERGY TRANSFER POWER SYSTEM WITH EASY5

4.1 Introduction

Based on the component models generated in Chapter 3, a complete system can be configured by interconnecting the component models. The particular spacecraft power system under investigation is a direct energy transfer power system [10] which is described in Section 4.2. In Section 4.3, system model building with the host software EASY5 and its analysis capabilities are described.

Using the system model generated in this chapter, the capabilities of the model are demonstrated in Chapter 5 through Chapter 7, by performing various types of the system level analysis.

4.2 Direct energy transfer spacecraft power system

The system being modeled in this study is a direct energy transfer (DET) system whereby the primary power source, a solar array, is coupled through a main distribution bus directly to the spacecraft electrical loads. The various power-conditioning components, as shown in Fig. 4.1, are activated only as needed, thus requiring the system to process only the amount of power needed to maintain the bus at the specified voltage level. A system mode of operation is determined by the power system control unit. The control unit continuously monitors appropriate signals to activate the power conditioning equipment. The control unit provides

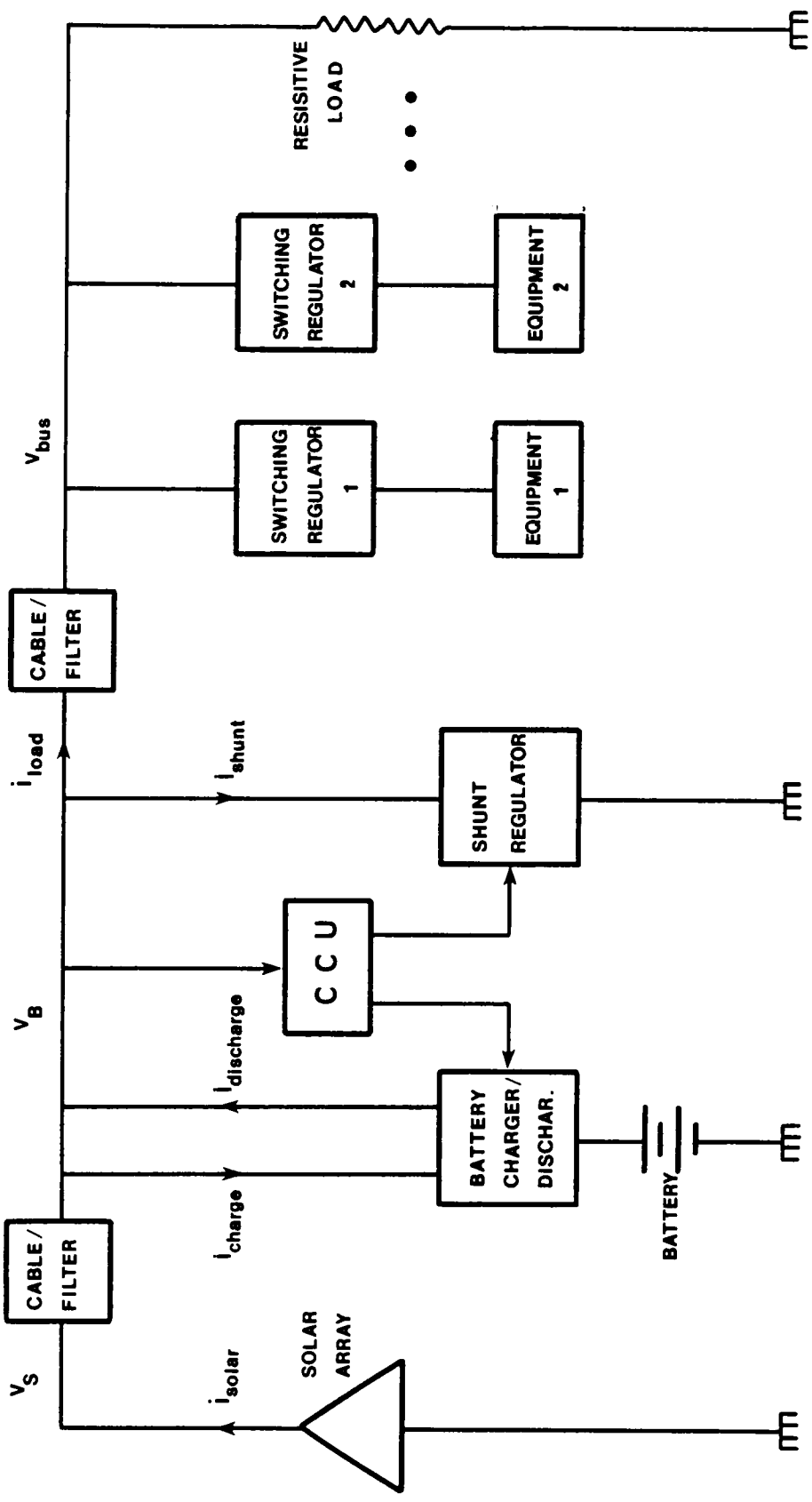


Figure 4.1 Direct Energy Transfer (DET) spacecraft power system

signals that control the disposition of the solar array, battery and shunt currents to maintain the bus voltage regulation. When the bus voltage exceeds the specified level, the control signal activates the shunt regulator, and the excessive solar array power is absorbed by the shunt elements. In this mode, the solar array also supplies the charging current to the battery if it is necessary. When the bus voltage becomes less than the specified level, the battery discharging electronics are activated and the load power is supplied by the solar array and the battery in shared mode. The battery discharging current is controlled by the bus voltage.

In this study, modeling of the battery and the battery charging electronic are omitted, thus, the battery is treated as an ideal voltage source. A boost type switching regulator is used for the battery discharger, which will be described in detail in Section 5.4.

The system modes of operation of the DET system are described in Chapter 5. Since the system is configured by interconnection of various components, complex interactions among components and subsystems may occur that can cause the total system to oscillate. In Chapter 6, various analyses are performed to demonstrate that the system can be unstable due to the undesirable interactions among the individually well behaved components.

4.3 EASY5 system model generation and analysis

Once all the necessary components are modeled, they are stored in EASY5 macro component library. As shown in Fig. 4.2, the EASY5 consists of two

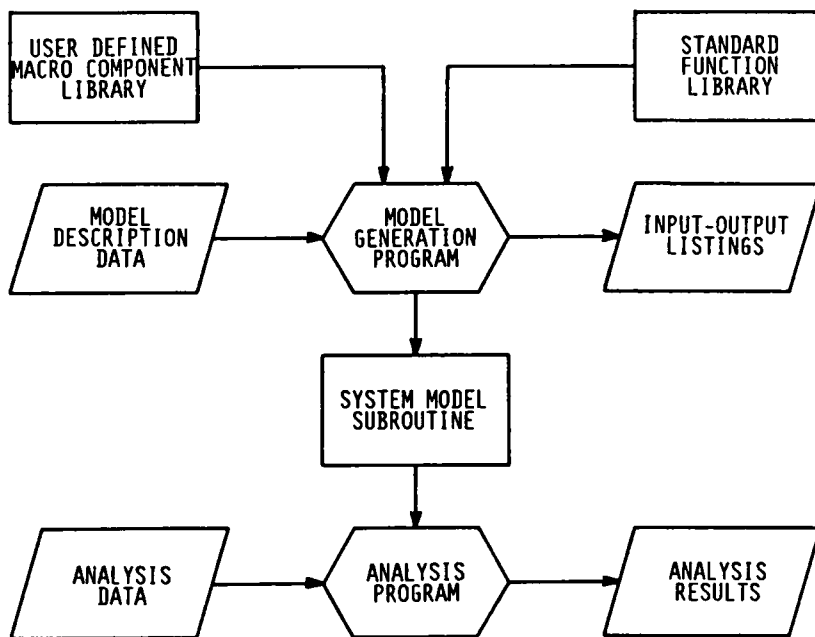


Figure 4.2 EASY5 system

programs, and a library of predefined Standard Components. Predefined Standard component models include many of the common effects found in dynamic systems such as standard analog and discrete controllers (proportional, integral, differential, etc.) transfer functions, nonlinearities (relay, saturation, hysteresis and switches), linear state equation model, etc.

Model generation: The EASY5 Model Generation program uses a block diagram type of approach for constructing various system models. All interconnections between the component models are accomplished by the Model Generation program according to the user specified Model Description Data. This is accomplished by matching the input and output port quantities of each interconnection. Table 4.1 shows the Model Description Data of the DET system. The program also produces a complete list of input data that is required by each component to complete the model description. The scalar and vector parameters and tabular data required for the analysis are included in this list.

Analysis: Once a system model has been constructed, the Analysis program performs various dynamics analyses according to the user provided Analysis Data. The Analysis Data includes parameter values, initial conditions of states, various analysis controls and analysis commands. Among the various analysis controls in EASY5, it is important to note that the INTEGRAL control allows one to perform the analysis at various levels such as system, subsystem, or component. The analysis in the component or subsystem level is done by 'freezing' the states of the subsystem of


```

*
* MODEL DESCRIPTION DATA OF THE DET SYSTEM
*
MACRO FILE NAME = DET1
*
MODEL DESCRIPTION
LOCATION = 1, SA, INPUTS = FS(I1=I2)
LOCATION = 2, FS, INPUTS = MC1(S2=I2)
LOCATION = 22, SH, INPUTS = FS(V2=V1)
LOCATION = 41, BA, INPUTS = DQ(I1=I2)
LOCATION = 43, DQ, INPUTS = BA(VG=V1), FS(V2=VC)
LOCATION = 6, FD, INPUTS = FS(V2=V1), MC2(S,2=I2)
LOCATION = 11, MC1, INPUTS = SH(IH=S1), DQ(IL=S3), FD(I1=S4)
LOCATION = 54, FS1, INPUTS = SR1(I1=I2), FD(V2=V1)
LOCATION = 55, SR1, INPUTS = FS1(V2=V1), LO1(I1=I2)
LOCATION = 56, LO1, INPUTS = SR1(V2=V1)
LOCATION = 57, FS2, INPUTS = SR2(I1=I2), FD(V2=V1)
LOCATION = 58, SR2, INPUTS = FS2(V2=V1), LO2(V2=V1)
LOCATION = 59, LO2, INPUTS = SR2(V2=V1)
LOCATION = 73, PT, INPUTS = FD(V2=V1)
LOCATION = 63, MC2, INPUTS = FS1(I1=S1), FS2(I1=S3), PT(I1=S4)
END OF MODEL
PRINT
*
*****
*
* SA : SOLAR ARRAY
* SH : SHUNT REGULATOR
* BA : BATTERY
* DQ : BATTERY DISCHARGER
* FS : SINGLE STAGE FILTER
* FD : TWO STAGE FILTER
* SR : SWITCHING REGULATOR
* LO : PAYLOAD
* PT : DIRECT LOAD
* MC : SUMMER
*
*****

```

Table 4.1 EASY5 Model Description Data of the DET system

little interest. This control feature is particularly useful when one is interested in investigating behaviors of a component or a subsystem in the presence of the entire system. A list of the analysis techniques available through the Analysis program are given as follows.

- Time history generation - nonlinear simulation
- Linear model generation with eigenvalue and eigenvector calculations
- Frequency response analysis
- Root locus calculation
- Steady state analysis
- Stability margin calculations
- Optimal controller synthesis

Chapter 5

ANALYSIS OF THE SOLAR ARRAY SYSTEM MODE OF OPERATION

5.1 Introduction

Solar arrays, as the primary power source for spacecraft, generates its output electrical power relative to the illumination level. The operating point of the solar arrays is determined by the power conditioning equipment and the load characteristic. Secondary power conditioning equipment consists of a shunt regulator, a battery bank and its charging and discharging electronics. When the output power of the solar array exceeds the load demand, a portion of the excess power is used to charge the battery bank, the secondary power source, with a portion dissipated in the shunt regulator. When the load power demand is higher than the solar array output power, the battery bank supplies the excessive load power through the battery discharging electronics. The power conditioning equipment not only balances the power of the system, but also regulates the bus voltage. Since the solar array output power varies over a wide range according to the changes in the illumination level, several modes of operation exist depending on the characteristics of the solar array, the power conditioning equipment, and the load.

As described in Section 3.3.2, the solar array output characteristic is nonlinear. This nonlinear source coupled with nonlinear load characteristics may result in multiple equilibrium points under a given operating condition. The actual equilibrium state that the system resides

on is determined by the stability nature of the equilibrium points and the past history. Thus, it is necessary to analyze the stability of the equilibrium points and to understand various modes of operation of the system.

In Section 5.2, stability of the equilibrium points of a solar array system with a nonlinear load characteristic is analyzed. The behavior of different solar array operating points are plotted as trajectories on the state plane. Stability and transient responses of the system operating near the solar array maximum power point is considered in Section 5.3.

In Section 5.4, the operation during battery discharge is analyzed including the transition mode from the shunt regulator active mode. In the direct energy transfer (DET) system, the battery discharger directly regulates the main bus voltage. Since the solar array output power varies over a wide range, the battery discharge converter operates from no-load to full load. Under light load, the converter operates in the discontinuous conduction mode where the energy storage inductor current is reduced to zero during a portion of a switching cycle. The DC and dynamic behaviors of a switching converter in the discontinuous conduction mode are quite different than that of the continuous conduction mode. Modeling of the discontinuous conduction mode employing the state space averaging is described in Section 5.4. Since the battery discharger also functions as the bus voltage regulator, the feedback compensator design for the converter in both modes of operation are also discussed.

In Section 5.5, various modes of operation of the DET system are described. Conclusions are presented in Section 5.5.

5.2 Large signal behavior of the solar array operating point

Fig.5.1 illustrates a solar array power system, in which the static load line represents a typical buck type regulator load line characteristic. The voltage V_R in the figure is the minimum solar array output voltage above which the converter is able to regulate its output voltage. When the converter is regulating, the solar array sees a constant power. Note that the converter exhibits a negative resistance characteristic in this mode. Below V_R , the converter behaves as a resistive load to the solar array.

As shown in Fig.5.1, there may exist three equilibrium points in the system. The large signal behavior of a solar array operating point depends on the stability nature of each equilibrium point. The qualitative large signal analysis can be carried out by considering a second order system shown in Fig.5.2. The solar array output and the load characteristics are replaced by the nonlinear functions and the LC low-pass filter in the model represents actual physical elements in the system such as an input filter of the switching regulator or the cable impedance between the solar array and the load.

Let us first consider the stability nature of each equilibrium point. Defining the states $x = [i_L \ v_C]^T$, the system state equations are

$$\frac{di_L}{dt} = \frac{1}{L} (v_S - v_C) = \frac{1}{L} (-f(i_L) - v_C) \quad (5.1)$$

$$\frac{dv_C}{dt} = \frac{1}{C} (i_L - i_R) = \frac{1}{C} (-f(v_C) - i_L) \quad (5.2)$$

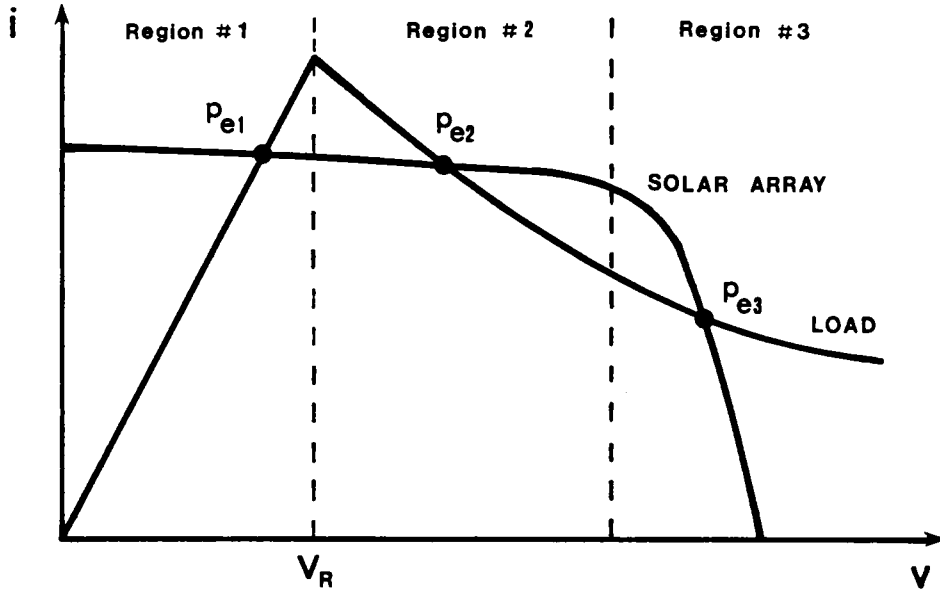


Figure 5.1 Representation of a solar array power system with a switching regulator load line

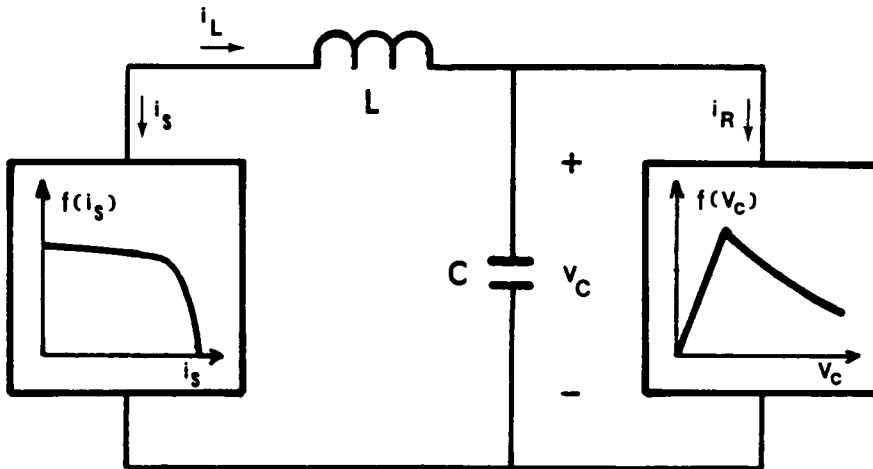


Figure 5.2 Equivalent circuit model of the system in Fig. 5.1 including LC filter

where v_S and i_R are defined in Fig.5.2.

From Eqs.(5.1) and (5.2), the equilibrium points in Fig.5.2 are

$$V_C = -f(I_L) \quad (5.3)$$

$$I_L = f(V_C) \quad (5.4)$$

The behavior of the trajectories in the neighborhood of an equilibrium point can be found by linearizing Eqs.(5.1) and (5.2) about an equilibrium point. The nature of these equilibrium points is analyzed by introducing small perturbations about the equilibrium points.

$$i_L = I_L + i_\ell \quad (5.5)$$

$$v_C = V_C + v_c \quad (5.6)$$

where I_L and V_C represent the steady state quantities, and i_ℓ and v_c are the small signal perturbations.

Substituting Eqs.(5.5) and (5.6) into (5.1) and (5.2) results in

$$\frac{di_L}{dt} = \frac{1}{L} [-f(I_L + i_\ell) - (V_C + v_c)] \quad (5.7)$$

$$\frac{dv_C}{dt} = \frac{1}{C} [-f(V_C + v_c) - (I_L + i_\ell)] \quad (5.8)$$

Expanding Eqs.(5.7) and (5.8) in a Taylor series about the equilibrium points, the equations of the first order approximation are

$$f(I_L + i_\ell) = f(I_L) + r_S i_\ell \quad (5.9)$$

where

$$r_S = \left. \frac{df(i_L)}{di_L} \right|_{I_L}$$

and similarly,

$$f(V_C + v_c) = f(V_C) + \frac{1}{r_L} v_c \quad (5.10)$$

where

$$\frac{1}{r_L} = \left. \frac{df(v_C)}{dv_C} \right|_{V_C}$$

where r_S and r_L are the incremental resistances (tangential slope) of the solar array output curve and the load line, respectively, at the equilibrium points.

Employing Eqs.(5.3) through (5.10), Eqs.(5.7) and (5.8) can be simplified as

$$\frac{di_\ell}{dv_c} = \frac{(1/L) (-r_S i_\ell - v_c)}{(1/C) (i_\ell - (1/r_L) v_c)} \quad (5.11)$$

Note that the value of r_S is the same as the output resistance at a point on the solar array I-V curve as derived in Eq.(3.60). The system eigenvalues are expressed as

$$\lambda_1, \lambda_2 = \frac{1}{2} \left[-\left(\frac{r_S}{L} + \frac{1}{r_L C} \right) + \left[\left(\frac{r_S}{L} + \frac{1}{r_L C} \right)^2 - \frac{4}{LC} \left(1 + \frac{r_S}{r_L} \right) \right]^{\frac{1}{2}} \right] \quad (5.12)$$

From Eq.(5.12), the stability of each equilibrium point can be determined for given parameter values. For the load line shown in Fig.5.1, the following results are observed.

In Region #1: $r_L > 0$

The equilibrium point, P_{e1} is stable and is either "node" (real) or "focus" (complex).

In Region #2: $r_L < 0, |r_S| < |r_L|$

λ_1 and λ_2 are both real but of opposite sign, and Point P_{e2} is a "saddle" and is unstable.

In Region #3: $r_L < 0, |r_S| < |r_L|$

The stability of Point P_{e3} depends on the parameter values.

Since the normal mode of operation should be in Region #3, the values of L and C in Fig.5.2 must be chosen to provide the stable equilibrium points in this region and to allow further analysis. However, it must be noted

that the equilibrium points in Region #1 are always stable and those in region #2 are always unstable (saddle points) regardless of the parameter values. The value of r_S for each equilibrium point can be obtained using Eq.(3.49). The value of r_L in the constant power region is

$$r_L = \left. \frac{\partial v_c}{\partial i_r} \right|_{V_C, I_R} = - \frac{P_C}{i_r^2} \bigg|_{i_r = I_R} \quad (5.13)$$

Approximate values of r_S and r_L can be found simply by drawing tangential slopes to the solar array I-V curve and load line, respectively, for each equilibrium point.

To construct a state plane trajectory the isoclines for each equilibrium point are derived using Eq.(5.11).

$$\frac{di_\ell}{dv_c} = 0 \rightarrow i_\ell = - \frac{1}{r_S} v_c \quad (5.14)$$

$$\frac{di_\ell}{dv_c} \rightarrow \infty \rightarrow i_\ell = \frac{1}{r_L} v_c \quad (5.15)$$

The results in Eqs.(5.14) and (5.15) are the tangential slopes to the source and load curve at each equilibrium point, thus the trajectories cross the solar array curve horizontally and the load line vertically. Near the equilibrium points which have real eigenvalues, the trajectories

follow the slope of the eigenvectors of the system, γ .

$$\gamma_1, \gamma_2 = \frac{L}{2} \cdot \left[-\left(\frac{r_S}{L} - \frac{1}{r_L C} \right) + \left[\left(\frac{r_S}{L} - \frac{1}{r_L C} \right)^2 - \frac{4}{LC} \right]^{\frac{1}{2}} \right] \quad (5.16)$$

Using the above results, the state plane trajectories can be sketched. Fig.5.3 illustrates the trajectories for the case where the equilibrium point P_{e3} is a stable node. As shown in the figure, the trajectories are separated by the separatrix that has the slope γ_2 at P_{e2} . The two starting points P_1 and P_2 in Fig 5.3 approach two entirely different steady states, even though the initial states differ by only a small amount.

For the solar array model used in this study the following cases are set up for the computer simulations. As mentioned above, if we assume that point P_{e3} in Region #3 is stable then there can be four possible cases:

- i) P_{e1} - node, P_{e3} - node
- ii) P_{e1} - node, P_{e3} - focus
- iii) P_{e1} - focus, P_{e3} - node
- iv) P_{e1} - focus, P_{e3} - focus

However, investigation of the cases i) and ii) are sufficient to understand the behaviors of the system. The results from simulating these

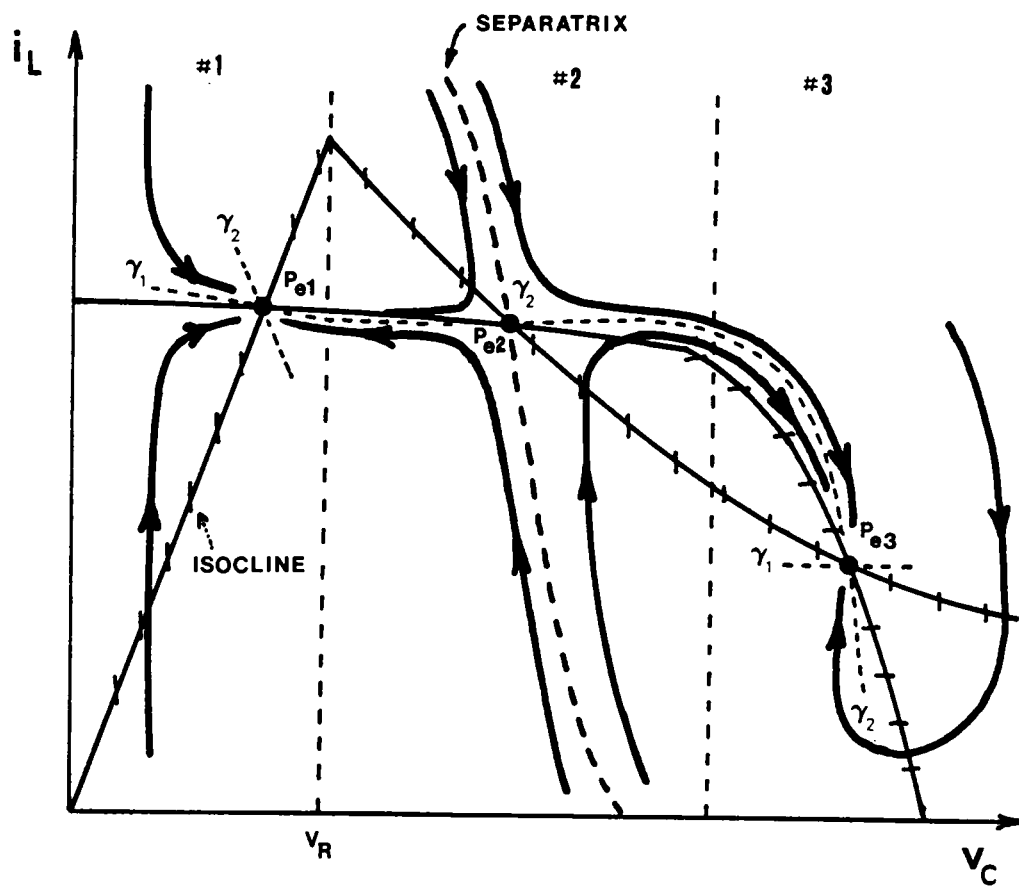


Figure 5.3 State-plane trajectories of the system in Fig.5.2

cases are shown in Fig.5.4a and 5.4b given the following system operating conditions.

The solar array short circuit current, I_{SS} , is 45.41A, the open circuit voltage, V_{OC} , is 32.0V, and the maximum power is 1050 watts. The constant power load line is set to 850 watts, and the boundary of Region #1 and #2, V_R , is 15V. The values of r_S and r_L at the equilibrium points in each region are listed in the following table.

	P_{e1}	P_{e2}	P_{e3}
I_L	45.38	45.02	30.34
V_C	12.03	17.31	28.02
r_S	64.92	37.3	0.20
r_L	0.025	-0.56	-1.47

The cases i) (P_{e3} node) and ii) (P_{e3} focus) are shown in Fig.5.4a and 5.4b. The eigenvalues and the slope of the eigenvectors for the particular values of L and C are listed in the figure. In Fig.5.4, the separatrix is clearly illustrated. It is obtained by adjusting the initial condition that is located near the line which has been estimated from information about γ_2 at P_{e2} . The trajectories from the initial points in the left-hand side of the separatrix (P_1 through P_6) converge to P_{e1} , and those in the right-hand side of the separatrix (P_7 through P_{12}) to P_{e3} .

Since in Region #1 the system bus voltage is not regulated, the desirable mode of operation should be in Region #3. This implies that there

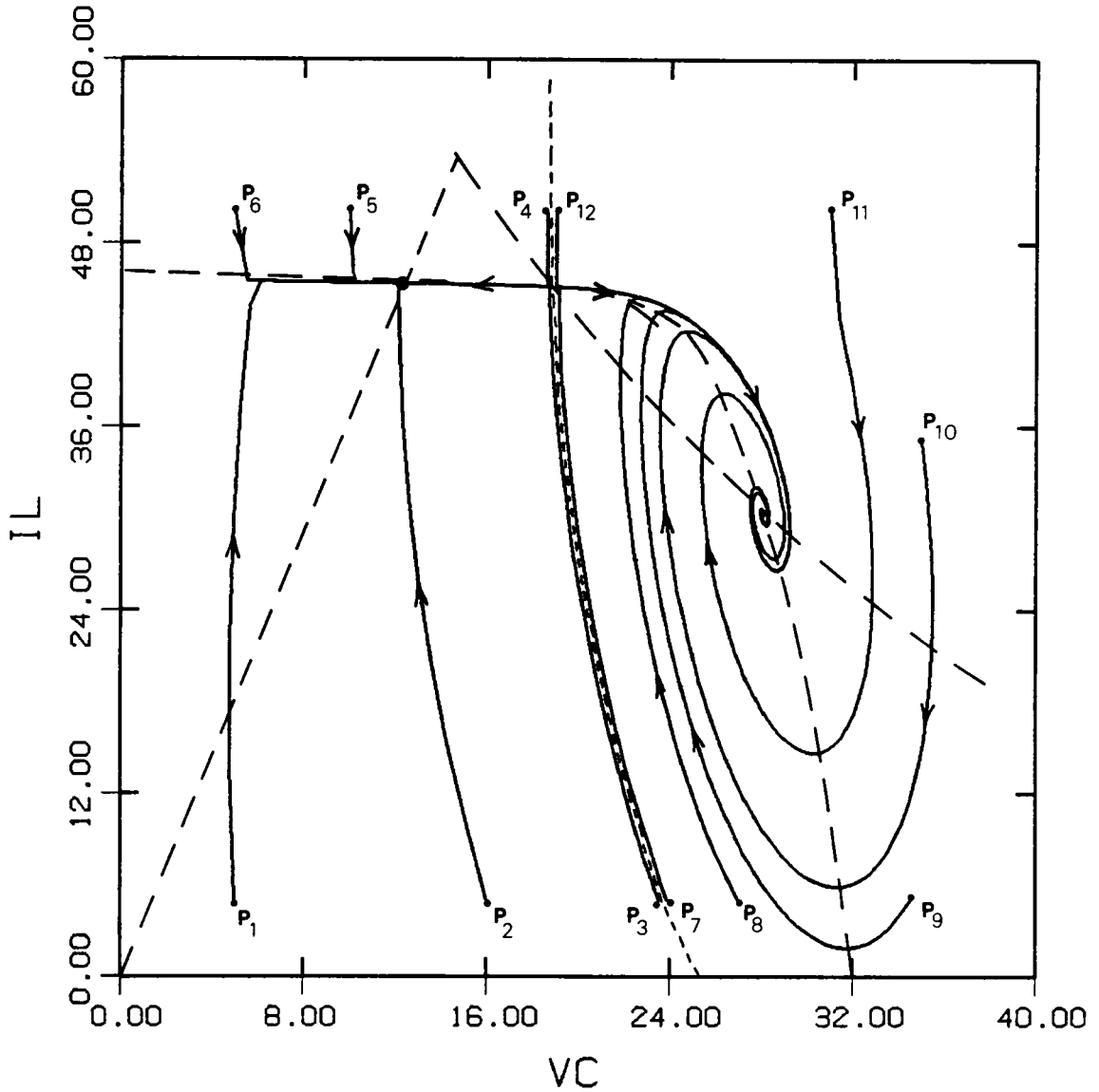


Figure 5.4b Simulation of state-plane trajectories for the case where the equilibrium point P_{e3} is 'stable focus'

($L = 30 \mu H$, $C = 500 \mu F$)

needs to limiting mechanism that prevents the operating point from settling into Region #1 or a triggering method to move it to Region #3 even if the operating point starts in Region #1. The limiting mechanism, by means of battery will be described in the following section. A triggering method is illustrated as follows.

Suppose that the operating point is at rest in Region #1. One way to move this operating point into Region #3 is to decrease the load power below the solar array curve so that the only one desired equilibrium point exists. For the load line shown in Fig. 5.5, the equilibrium points associated with Region #1 and #2 have moved outside these regions, so that they no longer exist. However, the behavior of the state trajectories can be still seen by defining virtual equilibrium points, P_{e1}' and P_{e2}' , for which the slopes of the eigenvectors are also sketched.

Fig.5.6 shows the simulation result using the previous system for the case i). In Fig.5.6a, the trajectory starts from an arbitrary initial point, P_1 and moves to P_2 for a load power of 800 watts (Curve #1). As the load power continuously decreases, the operating point moves along the solar array curve until it reaches Curve #2. Further decrease in the load power beyond this point moves the operating point from P_3 to P_4 , then to P_5 for Curve #3. Fig.5.6b shows the time history of v_c corresponding the trajectory from P_1 to P_5 . The above stability analysis for the system in Fig.5.2 can be extended to following cases.

System in the battery discharging mode: Fig.5.7 illustrates the system in the battery discharging mode of operation. As it will be

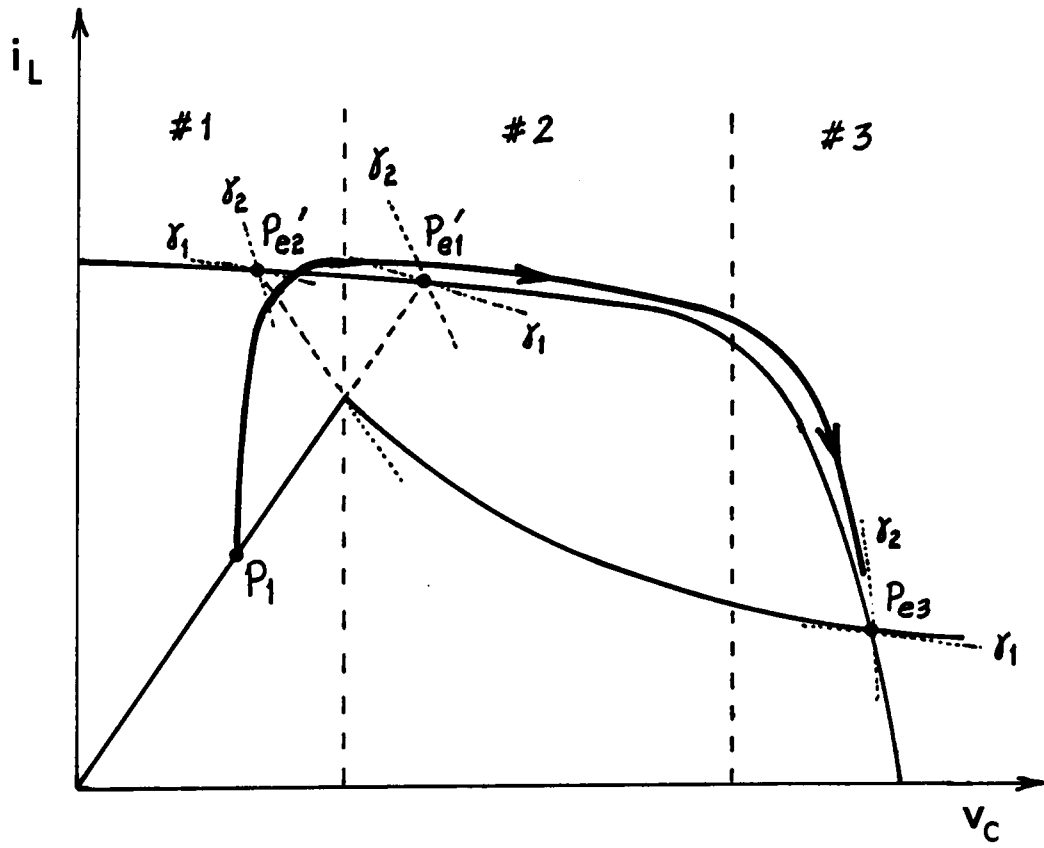


Figure 5.5 A trajectory from the initial operating point in Region #1 to Region #2

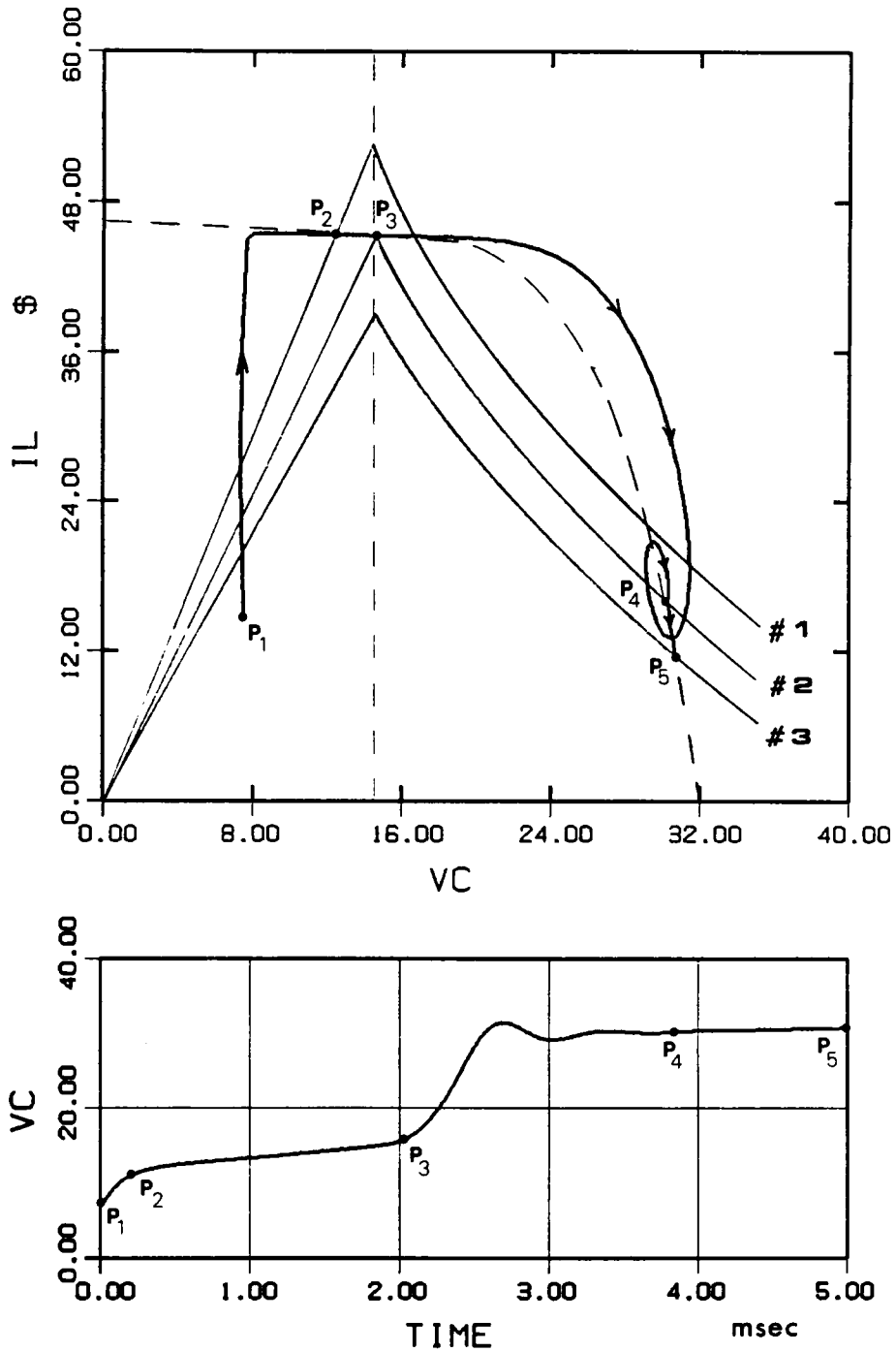


Figure 5.6 Simulation of the triggering method from the undesired stable region to the desired region
 (a) state-plane trajectory
 (b) time history of the bus voltage

described in detail in the following section, the battery discharging current can be expressed as a function of the bus voltage with an output admittance, $1/r_B$. The state equations are then,

$$\frac{di_L}{dt} = \frac{1}{L} (-f(i_L) - v_C) \quad (5.17)$$

$$\frac{dv_C}{dt} = \frac{1}{C} (i_L - \frac{1}{r_B} v_C - f(v_C)) \quad (5.18)$$

Following the same procedures as in the previous derivations, the state equations near the equilibrium point are

$$\frac{di_\ell}{dv_c} = \frac{(1/L) (-r_S i_\ell - v_c)}{(1/C) [i_\ell - [(1/r_L)+(1/r_B)] v_c]} \quad (5.19)$$

Then the eigenvalues are obtained as

$$\lambda_1, \lambda_2 = \frac{1}{2} \left[-\left(\frac{r_S}{L} + \frac{1}{r_E C} \right) + \left[\left(\frac{r_S}{L} + \frac{1}{r_E C} \right)^2 - \frac{4}{LC} \left(1 + \frac{r_S}{r_E} \right) \right]^{\frac{1}{2}} \right] \quad (5.20)$$

where

$$r_E = r_L \parallel r_B$$

Comparing Eqs.(5.12) and (5.20), the additional term, $1/r_B$, stabilizes the system, and this can be explained as the system is operating on the

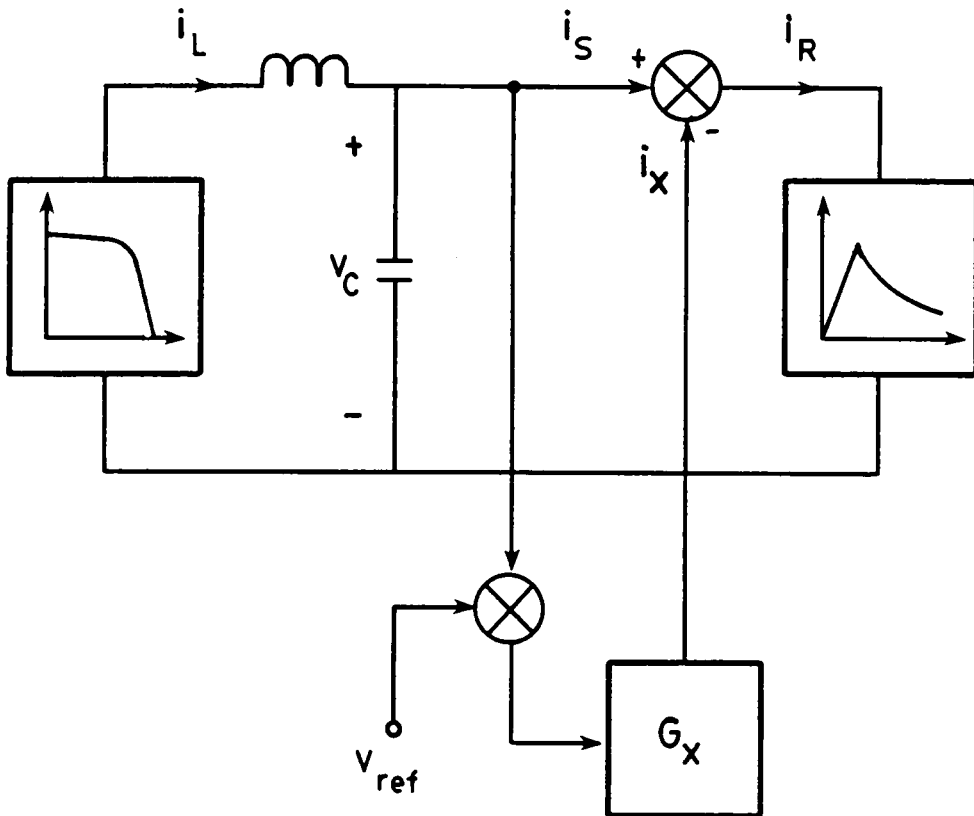


Figure 5.7 Simplified block diagram of the system in Fig.5.2 with the shunt regulator or the battery discharger

$$G_x = \begin{cases} Y_{sh} & \text{for shunt mode} \\ 1/r_B & \text{for battery discharge mode} \end{cases}$$

stiff voltage source line.

System in the shunt mode: Fig.5.7 also can be used to illustrate the system in the shunt mode of operation. In this mode of operation, Eqs.(5.17) and (5.18) are valid if the $1/r_B$ term is replaced by the shunt transadmittance, Y_{sh} . Fig.5.8 shows the state plane trajectories and the time history of the states, with and without the shunt regulator. It clearly shows the effect of the additional term described above such that the system without the shunt regulator is quite oscillatory while that with the shunt regulator has faster responses and the system appears to have real valued eigenvalues.

5.3 Analysis of the system operating near the solar array maximum power point.

To utilize the maximum available power from the solar array, several approaches were proposed in [11] and [12] to place the operating point as close to the solar array maximum power point as possible. However, near the solar array maximum power point, the dynamic resistance of the solar array is very sensitive to changes in the operating condition and, consequently, may result in system open loop instability.

To derive the solar array maximum power point, one can differentiate the constant power load line, $P_a = VI$, and set it to zero.

$$\frac{\partial P_a}{\partial V} = V \frac{\partial I}{\partial V} + I = 0 \quad (5.21)$$

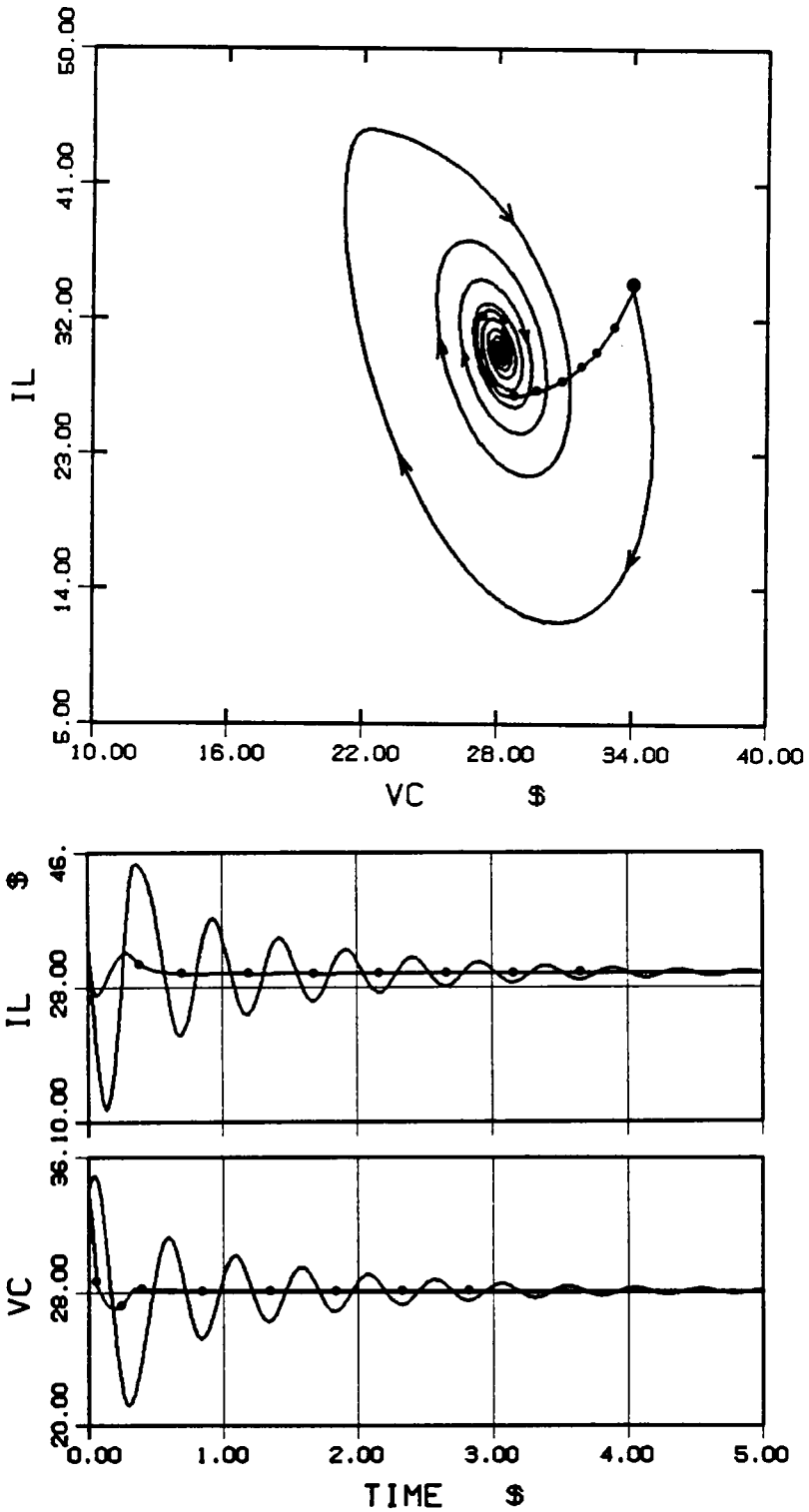


Figure 5.8 Comparison in the system response with and without the shunt

●—●—● : with shunt
 — : without shunt

$$\frac{V}{I} = - \frac{\partial V}{\partial I} \quad (5.22)$$

Eq.(5.22) states that at the maximum power point, the constant power load line, R_L , is equal to the dynamic output resistance of the solar array, R_o , as shown in Fig.5.9.

To analyze the behavior of an equilibrium point, one can use the solar array small signal model depicted by Eq.(3.5). One of the system eigenvalues at the equilibrium point is

$$\lambda = - \frac{1}{C_{eq} (1 + R_{eq} / r_L)} \quad (5.23)$$

where C_{eq} and R_{eq} are defined in Eq.(3.61).

From Eq.(5.23) , for $r_L < 0$, the system is:

stable if $|R_{eq}| < |R_L|$, and

unstable if $|R_{eq}| > |R_L|$

Here, R_{eq} represents the characteristic of the solar array output resistance r_S in Eq.(5.9).

In the stable region but near the maximum power point, M , the magnitudes of r_S and r_L are close together, and the eigenvalue is close to the origin, which implies very poor transient response and stability margin.

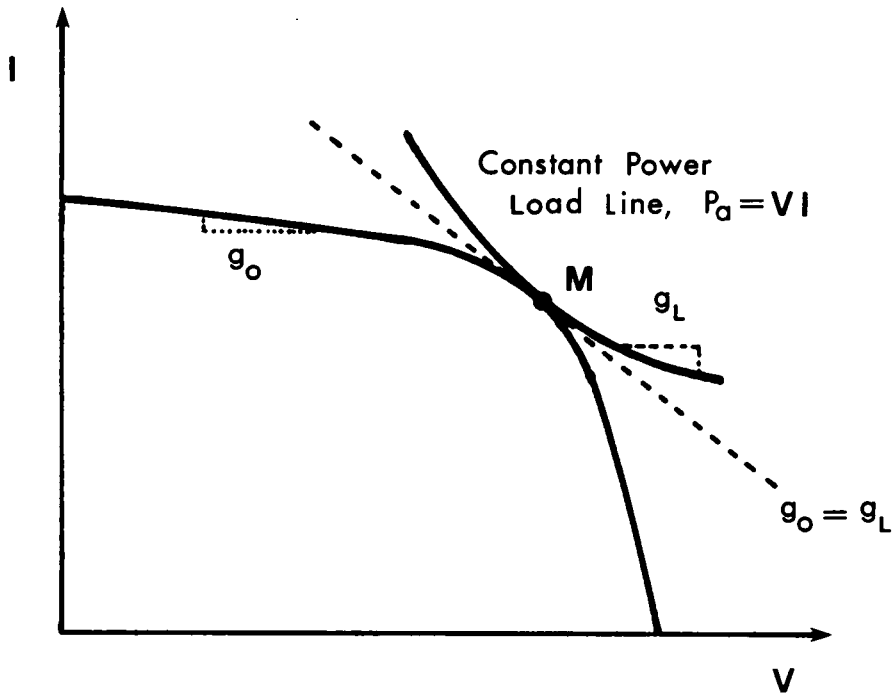


Figure 5.9 Relationships of solar array characteristic and constant power load line at the maximum power point, M

In this respect it is desirable to operate the system in a region where $|r_S| \ll |r_L|$. This can be seen from the step transient response simulation using the system model developed in Section 5.2 (Fig.5.2). Fig.10a shows the response of the step change in the load power from 800 watts to 790 watts, and Fig.5.10b from 1040 watts to 1030 watts which is close to the maximum power point (1050 watt). Therefore, to design a controller to utilize the maximum available power from the solar array, one must take into consideration of the stability of the operating point and the transient behavior of the system. One way to overcome this problem is to perhaps use the shunt regulator to dissipate some solar array power. In this mode of operation, the system can be stabilized as shown in Section 5.2.

5.4 Analysis of the solar array system in the battery discharge mode.

The basic functional requirement of the battery discharger is to regulate the bus voltage when the solar array is incapable of meeting the load demand. In this mode both the solar array and the battery function, as power sources. Fig.5.11 shows the system operating in the battery discharge mode. When the bus voltage drops below a certain magnitude, the control signal activates the battery discharge converter in order to maintain regulation of the bus voltage. The solar array then supplies the current corresponding to this regulated bus voltage.

In the DET system, when the solar array output power decreases due to the weakening of the illumination level, the battery discharger operates from no-load condition through the converter discontinuous conduction

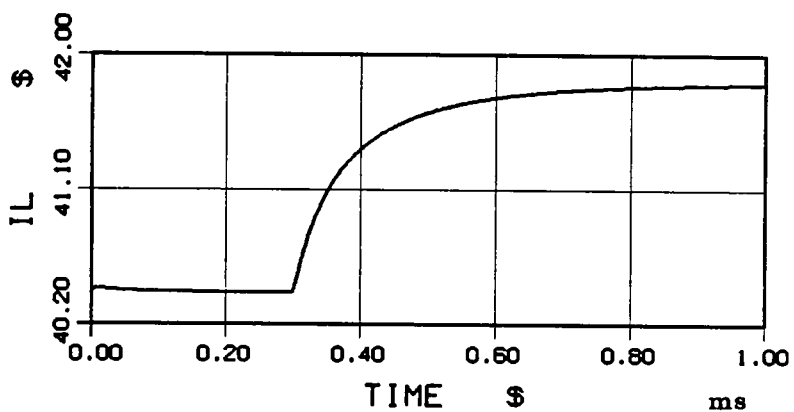
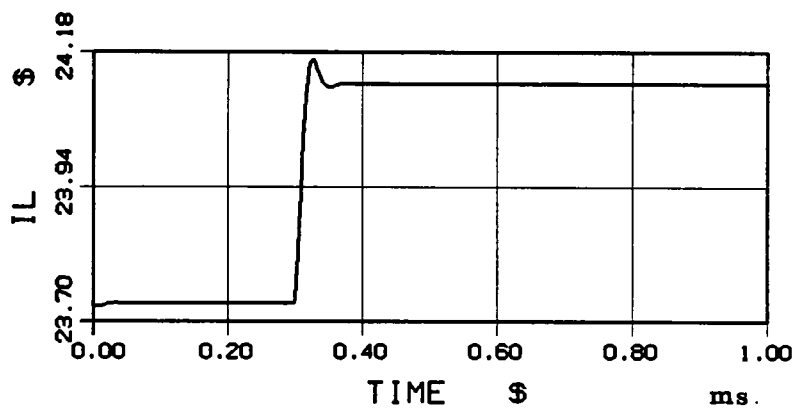


Figure 5.10 Step load transient responses

(a) 800 to 790 watt step

(b) 1040 to 1030 watt step

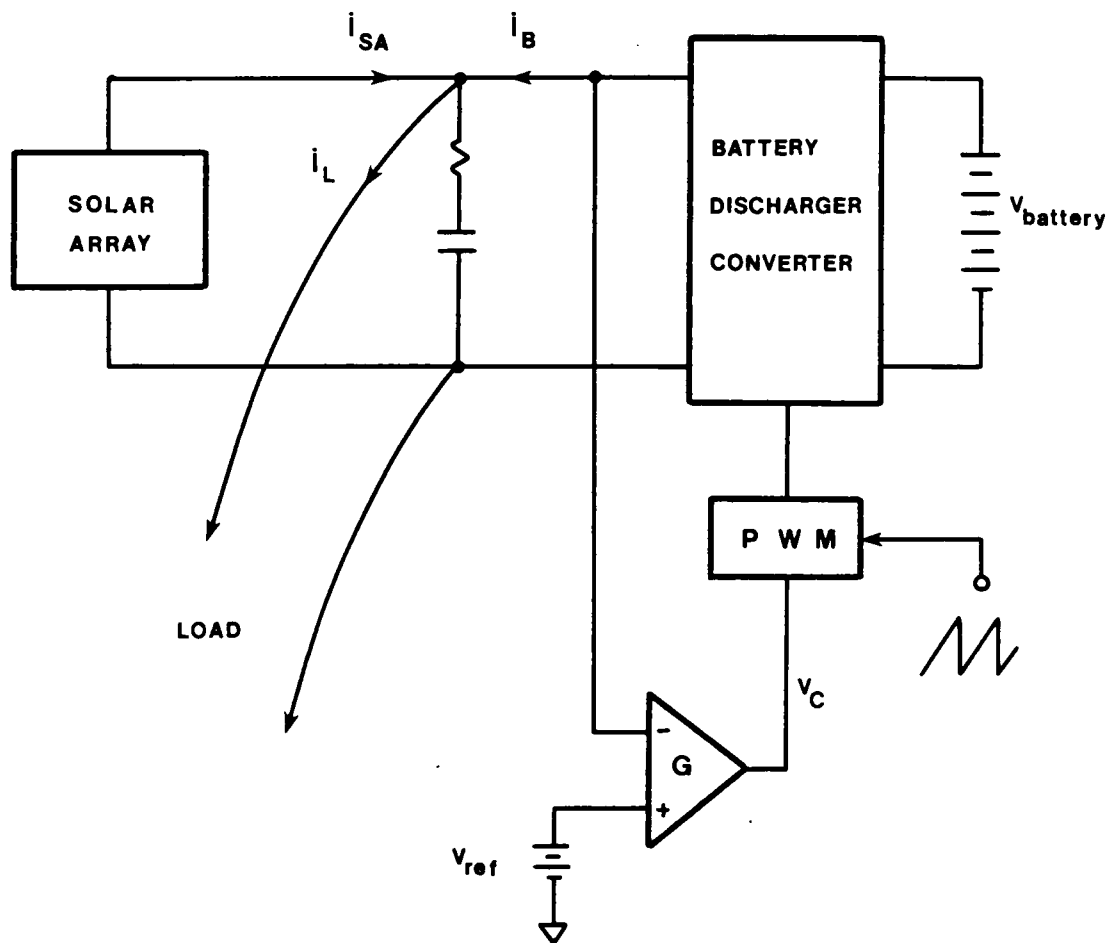


Figure 5.11 System in the battery discharge mode

mode (DCM) of operation and ultimately to the continuous conduction mode (CCM). Since the dc and ac dynamic behaviors of switching regulators for the two modes of operation are quite different, the analysis and the feedback controller design must take into consideration the two modes of operation.

A particular battery discharge switching converter power stage used in the DET system is shown in Fig.5.12. The circuit basically operates as a buck type regulator operating in push-pull mode. Since the terminal voltage of the battery is between 17 and 25 volts, an auto-transformer is used to boost the voltage to 28V. In this study, a simplified circuit is used as shown in Fig.5.13a. The two sources in Fig.5.13a represent the transformer action between the power switches ON and OFF intervals.

The state space averaging technique is employed to analyze the converter in the discontinuous conduction mode. As illustrated in Fig.3.2, there exist three intervals d_1T_s , d_2T_s and d_3T_s . While the first interval, d_1T_s , (inductor charging) is dictated by the "ON" time of the switching function and is a known quantity, the second interval, d_2T_s , (inductor discharging) is yet unknown and depends in general on both the length of the first interval and circuit parameters. However, d_2T_s can be determined using the additional constraint in the discontinuous conduction mode described in Eqs.(3.5) and (3.6)

While the state space expression in Eq.(3.8) was sufficient to describe the converter operating in continuous conduction mode, in discontinuous conduction mode, the state equations for the three switched intervals in

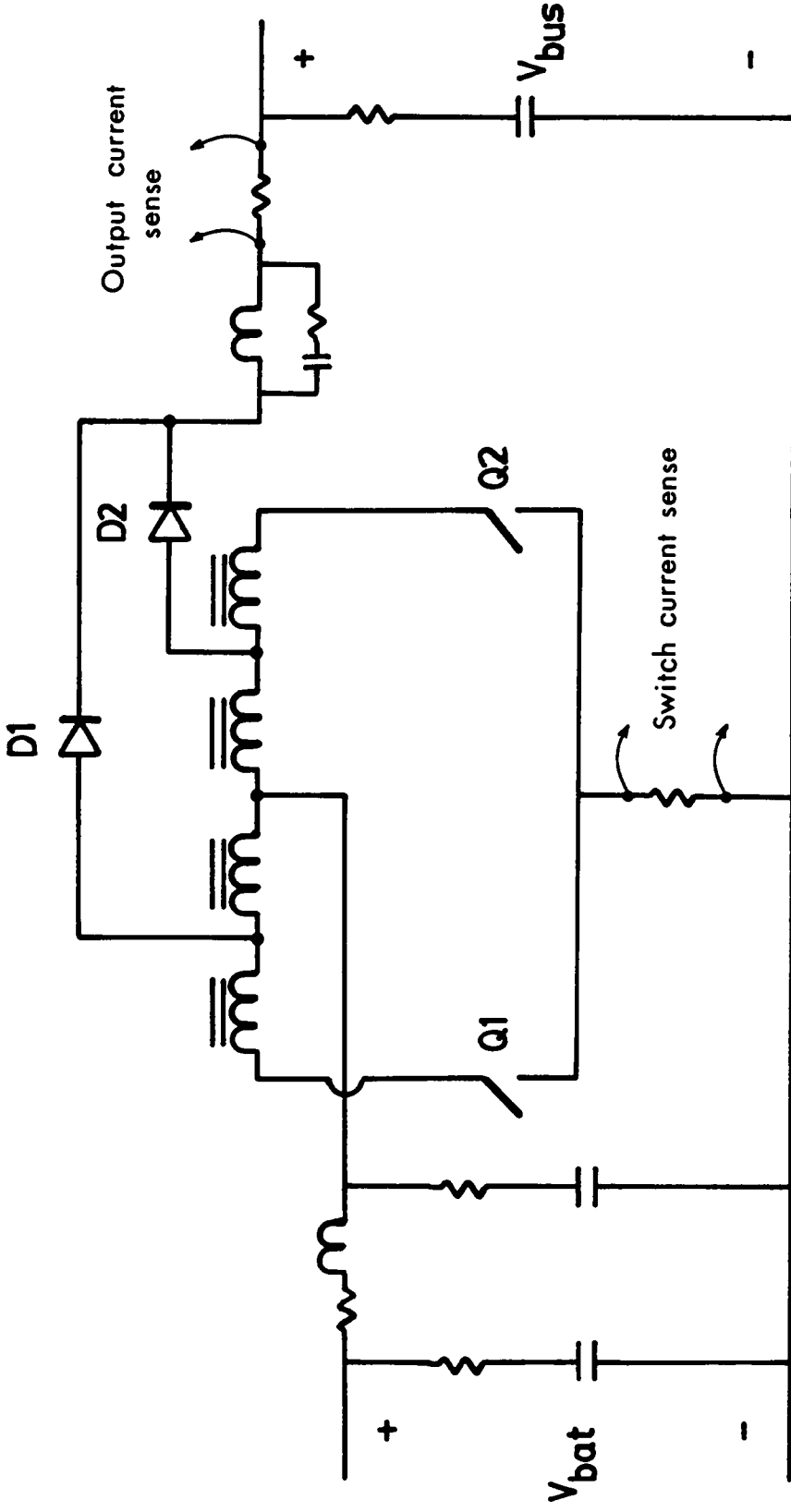
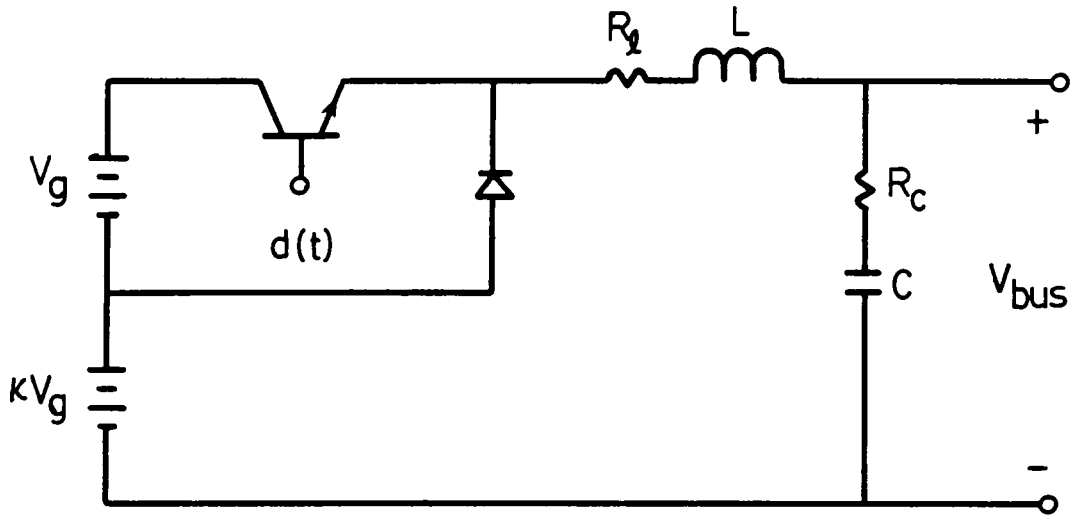
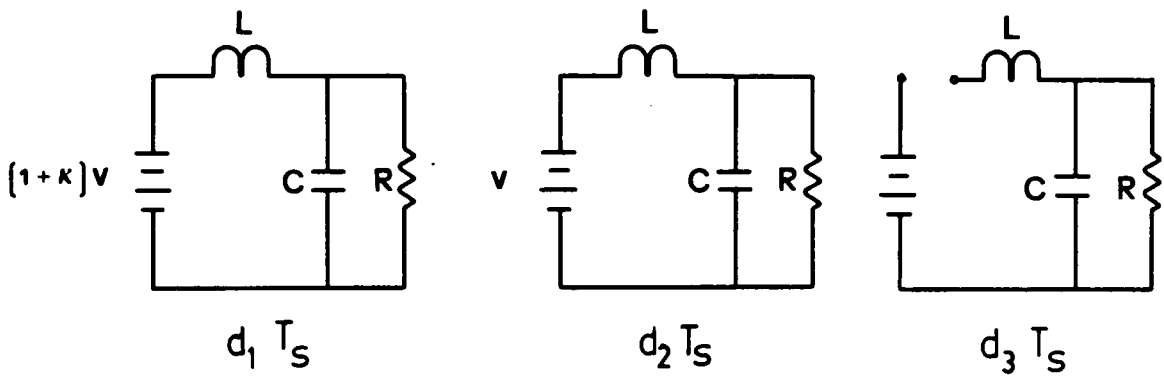


Figure 5.12 Battery discharger converter circuit



(a)



(b)

Figure 5.13 (a) Simplified circuit of the converter in Fig.5.12
 (b) Three switched circuits

Eq.(3.8) do not describe the switching converter completely. Namely, the instantaneous inductor current is restricted in its evolution such that

$$i(0) = i[(d_1+d_2)T_s] = 0$$

$$i(t) = 0 \quad \text{for interval } d_3T_s \quad (5.24)$$

The state equations of Eq.(3.8) together with Eq.(5.24) completely determine the behavior of the switching converter. Using ideal elements in the circuit in Fig.5.13a and ignoring load dynamics, three switched networks in the discontinuous conduction mode are shown in Fig.5.13b. For the choice of the state vector $x = [i \ v]^T$, the state space equations of the three switched networks becomes:

$$\begin{aligned} \dot{x} &= A_1x + B_1v_g \quad \text{for interval } d_1T_s \\ \dot{x} &= A_2x + B_2v_g \quad \text{for interval } d_2T_s \\ \dot{x} &= A_3x + B_3v_g \quad \text{for interval } d_3T_s \end{aligned} \quad (5.25)$$

where

$$A_1 = A_2 = \begin{bmatrix} 0 & -\frac{1}{L} \\ \frac{1}{C} & -\frac{1}{CR} \end{bmatrix}, \quad A_3 = \begin{bmatrix} 0 & 0 \\ 0 & -\frac{1}{CR} \end{bmatrix}$$

$$B_1 = \begin{bmatrix} \frac{(1+\kappa)}{L} \\ 0 \end{bmatrix}, \quad B_2 = \begin{bmatrix} \frac{\kappa}{L} \\ 0 \end{bmatrix}, \quad B_3 = 0 \quad (5.26)$$

In addition to this,

$$i = \frac{i_{\max}}{2} \quad (5.27)$$

Eqs.(5.25) and (5.27) contain all that is needed to determine both dc and ac small signal models. Let us first consider the steady state (dc) model.

Steady state (dc) model analysis

Averaging Eq.(5.26), the following linear algebraic static equations result.

$$\begin{bmatrix} 0 & -\frac{D_1+D_2}{L} \\ \frac{D_1+D_2}{C} & -\frac{1}{CR} \end{bmatrix} \begin{bmatrix} I \\ V \end{bmatrix} + \begin{bmatrix} \frac{D_1(1+\kappa)+\kappa D_2}{L} \\ 0 \end{bmatrix} V_g = 0 \quad (5.28)$$

$$A \cdot X + B \cdot V_g = 0$$

Then the steady state solution of Eq.(5.28) is

$$\frac{V}{V_g} \equiv M = \frac{D_1(1+\kappa) + \kappa D_2}{D_1 + D_2}$$

$$I = \frac{V}{R} \frac{1}{(D_1 + D_2)} \quad (5.29)$$

Hence, the dc conditions depend not only on D_1 and R , as in the continuous conduction mode, but also on D_2 . By use of the additional constraint in the average of inductor current for $(D_1 + D_2)T_s$,

$$I = \frac{(1+\kappa)V_g - V}{2L} D_1 T_s \quad (5.30)$$

the dc conditions are completely determined with the known quantities D_1 and R . For example, substitution of Eq.(5.30) into Eq.(5.28) results in

$$D_2 = \frac{1}{2} \left[\left(\frac{\kappa \tau_n}{D_1} - D_1 \right) + \left[\left(\frac{\kappa \tau_n}{D_1} - D_1 \right)^2 + 4\tau_n(1+\kappa) \right]^{\frac{1}{2}} \right] \quad (5.31)$$

where

$$\tau_n \equiv 2L / (R T_s) ; \text{ normalized time constant.}$$

The boundary condition between the two conduction modes is

$$D_2 < 1 - D_1 \quad ; \quad \text{discontinuous conduction mode}$$

$$D_2 > 1 - D_1 \quad ; \quad \text{continuous conduction mode} \quad (5.32)$$

Using Eqs.(5.31), the critical value of τ_n can be determined by solving $D_2=1-D_1$.

$$\tau_{ncrit} = \frac{1 - D_1}{1 + \kappa + \kappa(1 - D_1)/D_1} \quad (5.33)$$

Suppose now that the output voltage is regulated with some proper feedback circuit, then the dc voltage gain M can be treated as a constant for a fixed input voltage. The duty ratio, D_1 , in the discontinuous conduction mode can be derived as a function of M , or as a function of τ_n .

$$D_1 = \left[\frac{\tau_n M(M-\kappa)}{1 + \kappa - M} \right]^{\frac{1}{2}} \quad (5.34)$$

Using the results in Eqs.(5.29) through (5.34), the dc behavior of the battery discharger is illustrated in Fig.5.14. In Fig.5.14a, the boundary condition, τ_{ncrit} , between the two modes is plotted as a function of D_1 . The dc voltage gain, M , as function of D_1 for different values of τ_n is shown in Fig.5.14b.

Since the battery discharger operates from no-load condition to full-load, it is important to understand the start-up behavior of the converter. Suppose now that the solar array output power gradually decreases. Initially the battery discharging current is zero (i.e., $\tau_n=0$) and the duty ratio $D_1=0$. As the solar array output power decreases, the operating point moves along the solar array curve which causes the bus

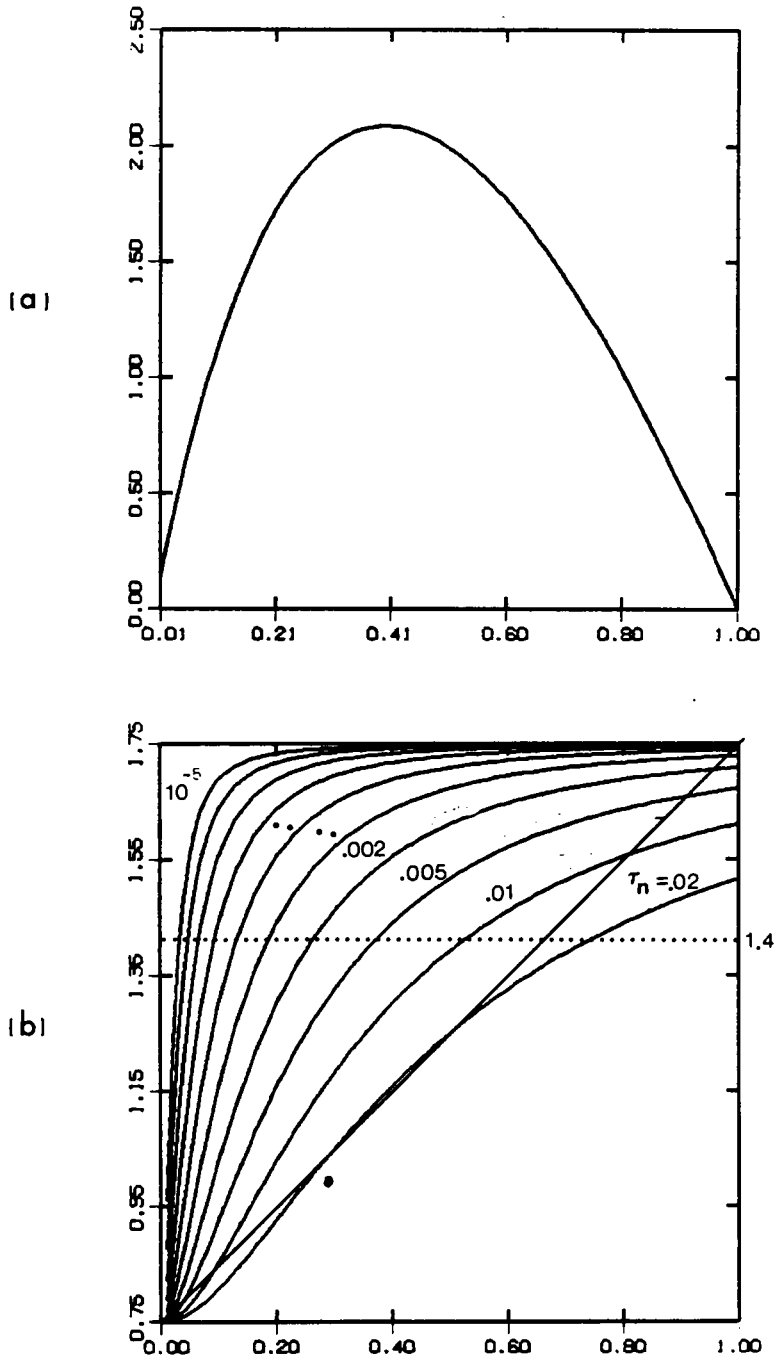


Figure 5.14 DC characteristics of the battery discharger converter

(a) τ_{ncrit} vs D_1

(b) M vs D_1

voltage to drop, thus activating the battery discharger electronics via an error amplifier. In this start-up mode, it is very important to design a proper error amplifier circuit for the following reasons.

For the high gain controller, since the sensitivity of the dc voltage gain with respect to the duty ratio is very high as shown in Fig.5.14b, any delay in the logic circuitry or the false triggering due to the noise will cause a high frequency oscillation in the bus voltage. While in a low feedback gain system the controller may not act fast enough to regulate the bus voltage, since the plant time constant of the output capacitor and load resistance is very slow. This may cause a large amplitude, low frequency oscillation. It is especially critical when the output voltage of the battery discharger converter is set such that its operation is close to the solar array maximum power point. Since the low gain controller allows the bus voltage to drop before the controller starts correcting the error, it is possible that the system may move into the unstable region as discussed previously. Another important concern when designing the error amplifiers is that, since the battery discharger operates from the discontinuous conduction mode to continuous conduction mode, the error amplifier should be able to stabilize the system in both modes. Thus, it is necessary to analyze the dynamic behavior of the converter in the discontinuous conduction mode.

Small-signal analysis: Perturbation and linearization of the averaged equations of (5.25) and (5.27) result in the following ac dynamic model.

$$\begin{aligned}
 \begin{bmatrix} \dot{\hat{i}} \\ \hat{v} \end{bmatrix} &= \begin{bmatrix} 0 & -\frac{D_1+D_2}{L} \\ \frac{D_1+D_2}{C} & -\frac{1}{CR} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{D_1(1+\kappa)+\kappa D_2}{L} \\ 0 \end{bmatrix} \hat{v}_g \\
 &+ \begin{bmatrix} \frac{(1+\kappa)V_g - V}{L} \\ I \\ C \end{bmatrix} \hat{d}_1 + \begin{bmatrix} \frac{\kappa V_g - V}{L} \\ I \\ C \end{bmatrix} \hat{d}_2 \quad (5.35)
 \end{aligned}$$

with additional constraints

$$\frac{d\hat{i}}{dt} = 0 \quad (5.36)$$

$$i = \frac{I}{D} \hat{d} + \frac{(1+\kappa)I}{(1+\kappa)V_g - V} \hat{v}_g - \frac{I}{(1+\kappa)V_g - V} \hat{v} \quad (5.37)$$

Substituting the additional constraints from Eqs.(5.36) and (5.37) into Eq.(5.35), the unknown modulation quantities \hat{d}_2 and \hat{i} can be determined in terms of other known dc and ac quantities. Thus, the complete dynamic model can be expressed as

$$\begin{aligned}
C \frac{d\hat{v}}{dt} = & \left[\frac{1}{R} \frac{(2M-\kappa) - (\kappa-M)^2}{(1+\kappa-M)(\kappa-M)} \right] \hat{v} \\
& + \left[\frac{M}{R} \frac{MD_2 + D_1 + \kappa D_1}{(1+\kappa-M)(\kappa-M)(D_1 + D_2)} \right] \hat{v}_g \\
& + \left[\frac{V}{R} \frac{(D_1 + D_2)(\kappa-M) - D_1}{D_1(D_1 + D_2)(\kappa-M)} \right] \hat{d}_1
\end{aligned} \tag{5.38}$$

It is now easy to obtain from (5.38) the two transfer functions of interest

$$\frac{\hat{v}}{\hat{v}_g} = G_{og} \frac{1}{1 + s/\omega_p} \tag{5.39}$$

$$\frac{\hat{v}}{\hat{d}_1} = G_{od} \frac{1}{1 + s/\omega_p} \tag{5.40}$$

where

$$G_{og} = M \tag{5.41}$$

$$G_{od} = V \frac{(D_1 - (D_1 + D_2)(\kappa - M))(1 + \kappa - M)}{D_1(D_1 + D_2)((2M - \kappa) - (\kappa - M)^2)} \tag{5.42}$$

$$\omega_p = \frac{1}{RC} \frac{(\kappa - M)^2 - (2M - \kappa)}{(1 + \kappa - M)(\kappa - M)} \tag{5.43}$$

The transfer function models are rather complicated for this particular converter circuit, however, they are expressed in terms of well defined constants which can easily be calculated. The qualitative interpretations of the results are as follows.

Eqs.(5.39) and (5.40) show that the transfer functions have a single pole and no zero. Due to the additional constraint in Eq.(5.36), any converter that can be described with two state variables of which one is an inductor current, has a single pole system transfer functions in the discontinuous conduction mode. Thus, the dynamic behavior of the converter operating in the discontinuous conduction mode is completely different from that of the continuous conduction mode, in which the system has a complex pole pair. Therefore, the feedback controller should compensate the plant dynamics in both modes of operation. This is clearly illustrated in the root locus diagram of the open loop converter as a function of the load resistance in Fig.5.15. Without a compensator, the system eigenvalue is close to the origin, thus the system response should be very slow in the discontinuous conduction mode as intuitively predicted in the dc analysis. Decreasing the load resistance beyond R_{crit} , the converter will operate in the continuous conduction mode, and the open loop eigenvalues become complex. In this range, the response is still slow and oscillatory. A further decrease of the load resistance will move the locus onto the circle with the radius of $\omega_o = \sqrt{LC}$, and finally the locus approaches to the real axis again.

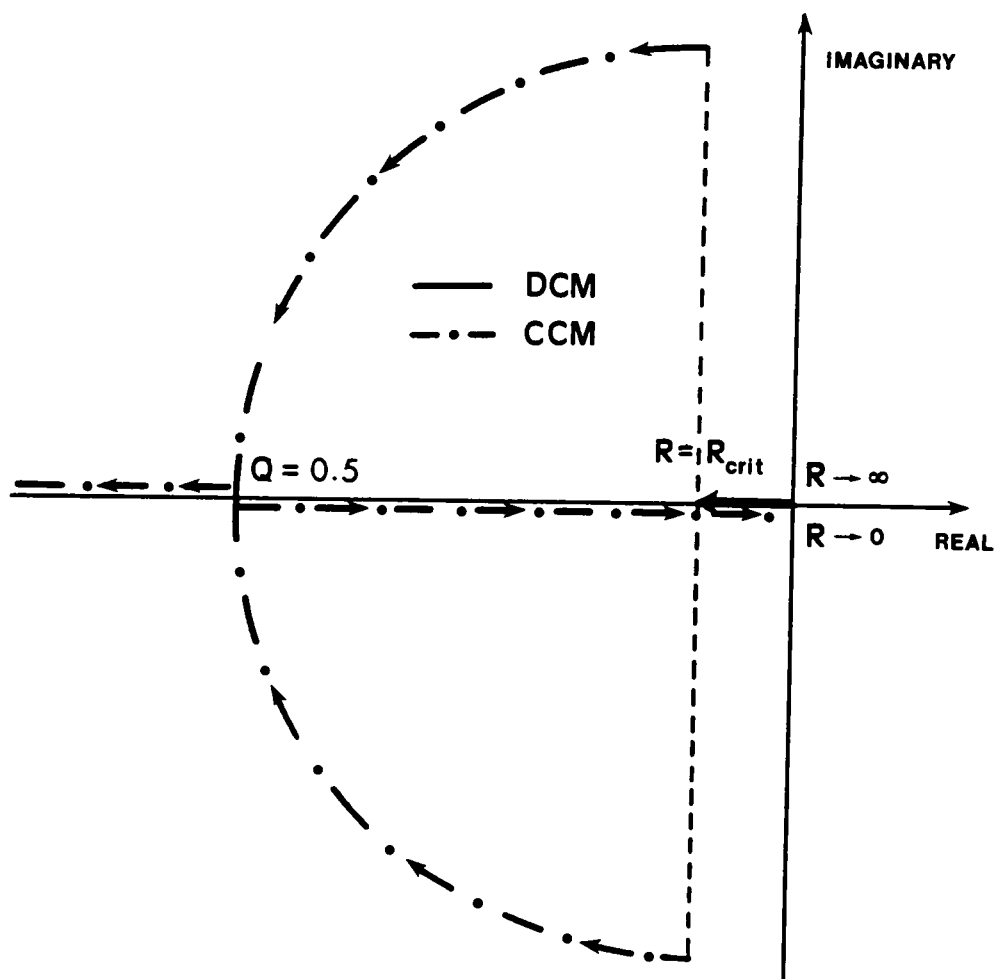


Figure 5.15 Root locus of the open loop buck-type converter as a function of load resistance, R for DCM and CCM

Fig.5.16 illustrates the root locus diagrams for different compensators for an arbitrary load resistance in both DCM and CCM. Let us first consider a compensator for the converter operating in the DCM. In order to compensate the open loop single pole near the origin as described in Eqs.(5.39) through (5.43), a simple proportional gain controller can be used to move the eigenvalue along the negative real axis to speed up the response, as shown in Fig.5.16a. However, this gain also moves the eigenvalues to the imaginary axis in the CCM and it is worse in a light-load condition in which the open loop poles are already close to the imaginary axis. A low gain may stabilize the system in the CCM, but the slow response in the DCM may cause severe bus voltage droop.

A typical integrator with lead-lag compensators as illustrated in Fig.5.16b is examined next. The integrator is used for the tight regulation of the output voltage, the zero compensates the excessive phase lag due to the presence of an integrator, and the pole attenuates the high frequency noise entering the controller. However, the integrator slows down the system response in DCM as shown in the figure, and the similar situation will occur as the low gain in the above case. In fact, for an optimum selection of the feedback gain for the regulator in CCM, the locus still stays near the imaginary axis in DCM.

Therefore, to achieve an optimum response for the both modes of operations, a compensator for switching converters in the battery discharger application must include a proportional gain to speed up the system response in DCM and the lead-lag type compensator for stable operation in CCM. This is illustrated in Fig.5.16c.

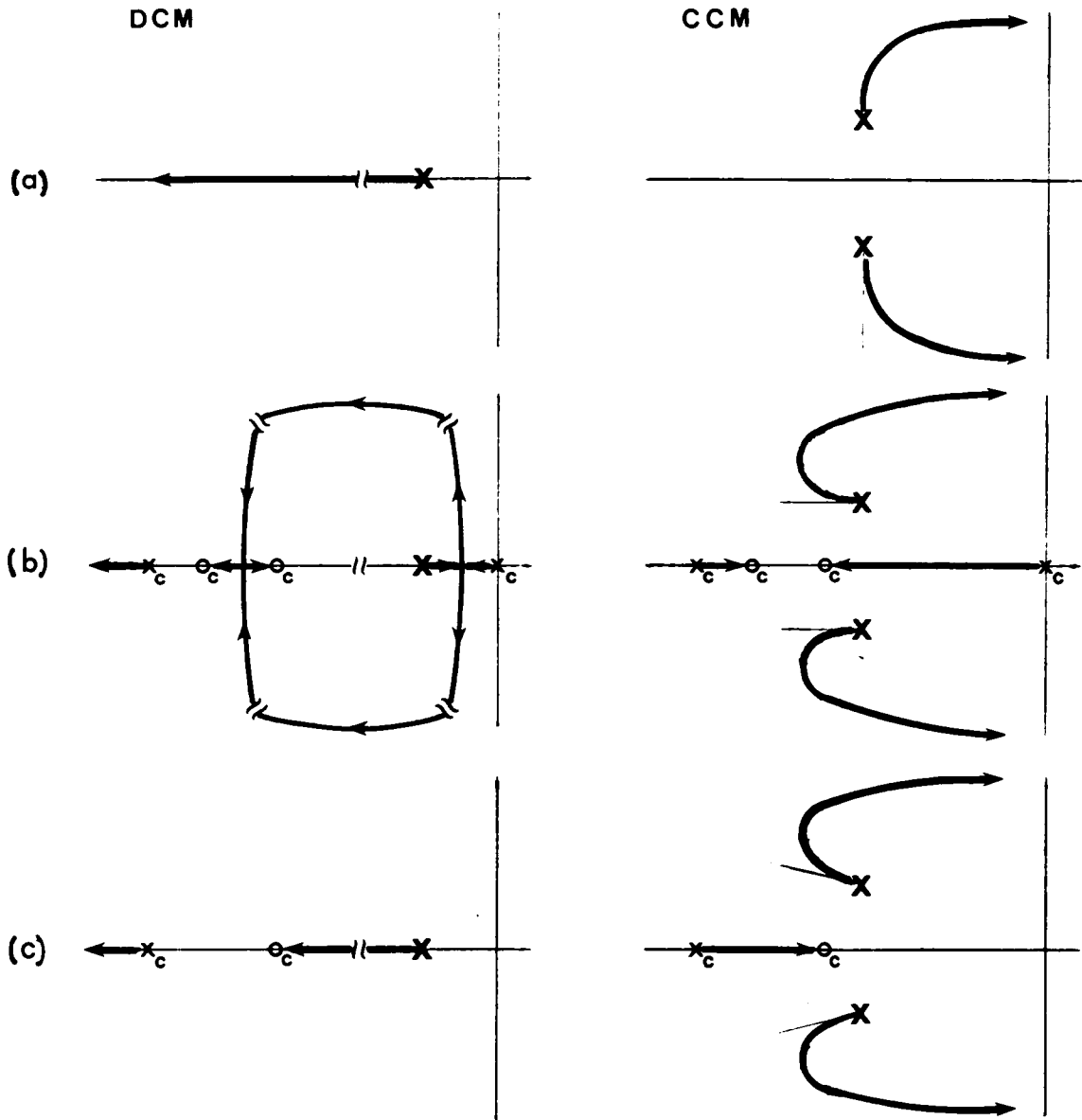


Figure 5.16 Root loci of the closed loop buck-type converter in DCM and CCM for three compensators

- (a) proportional gain
 (b) integrator plus lead-lag
 (c) proportional plus lead-lag

x_c : compensation pole

o_c : compensation zero

In order to verify the above analysis and to observe the effect of the integrator in the DCM, the DET system is simulated employing the compensators shown in Fig.5.17. Fig.5.18 shows the step load transient response (from 1300W to 1400W) of the system when the battery discharger operates in the continuous conduction mode. As shown in the figure, the system behaves equally well for both compensators. In Fig.5.19, the load power (i_{load}) is gradually increasing and various modes are illustrated. The system initially operates in the shunt mode until 0.6 ms, then it gets into the dead band mode where the solar array solely supplies the load power. The operating point moves along the solar array curve as the load power increases. The battery discharger employing the compensator with the proportional gain (Fig.5.16c) starts regulating the bus voltage at $t=1\text{ms}$ as shown in Fig.5.19a. While that using the integrator starts to operate at $t=2\text{ms}$ and slowly increases to the regulation voltage as shown in Fig.5.19b. As shown in this figure, the slow response in DCM of the integrator type controller may cause the bus voltage to drop out of the regulation range.

The results in Figs.5.16 and 5.19 show that a compensator designed for CCM mode of operation may not be suitable for DCM mode of operation, and consequently, may result in a bus voltage oscillation or large voltage droop due to a load transient. Design of a compensator for the boost or buck-boost type converter must be done more carefully. This is because such converters also have a single pole in the discontinuous conduction mode, but, in the continuous conduction mode, there are a complex pole pair and a right-half plane zero.

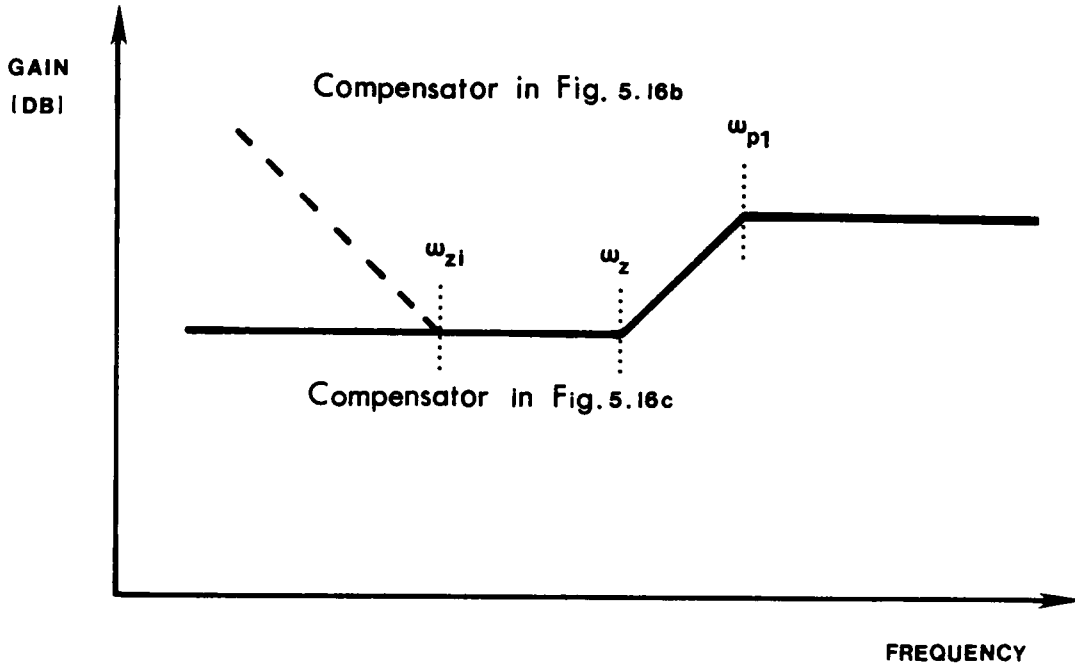


Figure 5.17 Two compensators used for the simulation

Power stage : $\omega_o = 3000$ in CCM

$0 < \omega_p < 12.4$ in DCM

Compensator : $\omega_{z1} = 1000$

$\omega_{z2} = 6000$

$\omega_{p1} = 50000$

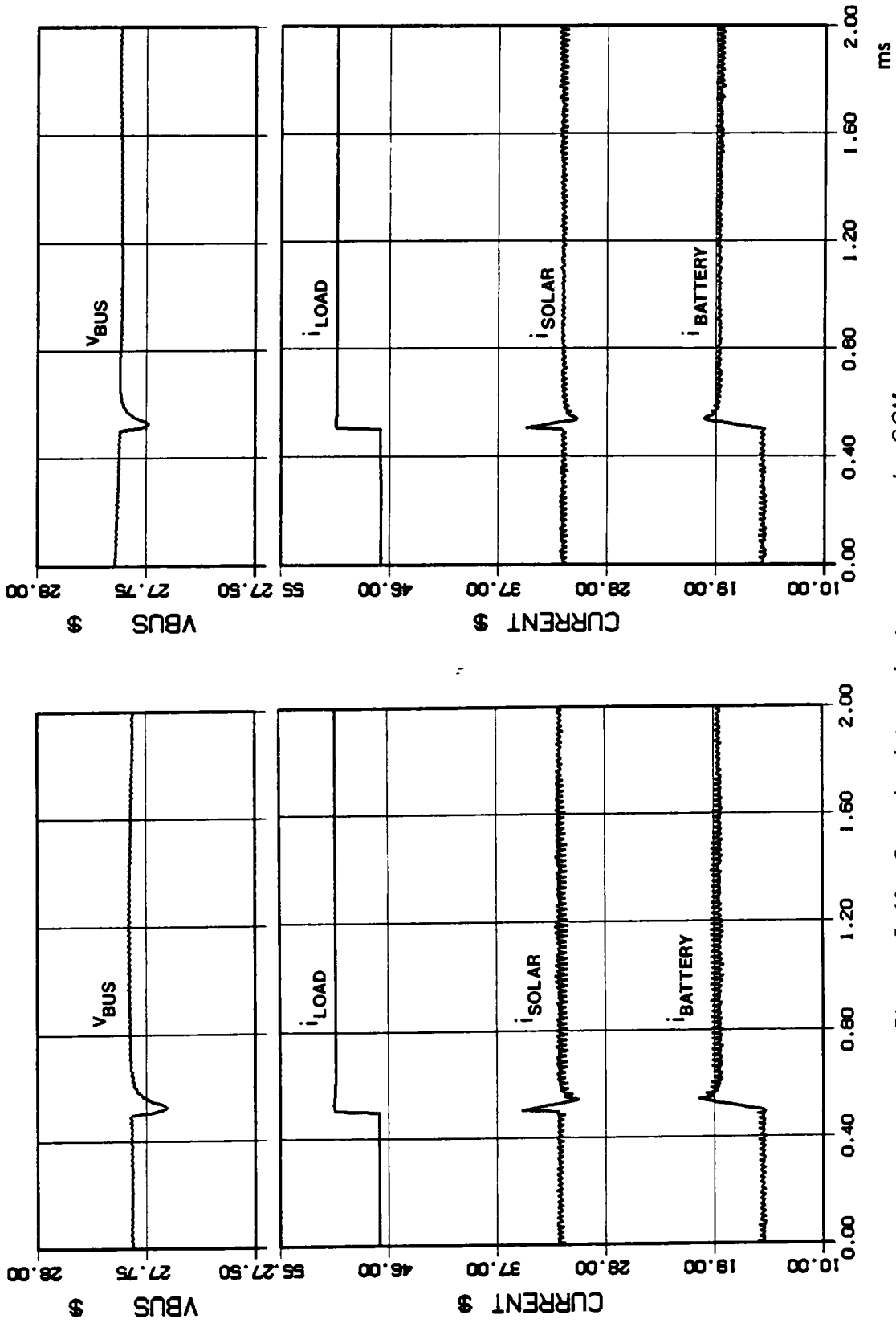


Figure 5.18 Step load transient responses in CCM

- (a) using the compensator shown in Fig.5.16c
- (b) using the compensator shown in Fig.5.16b

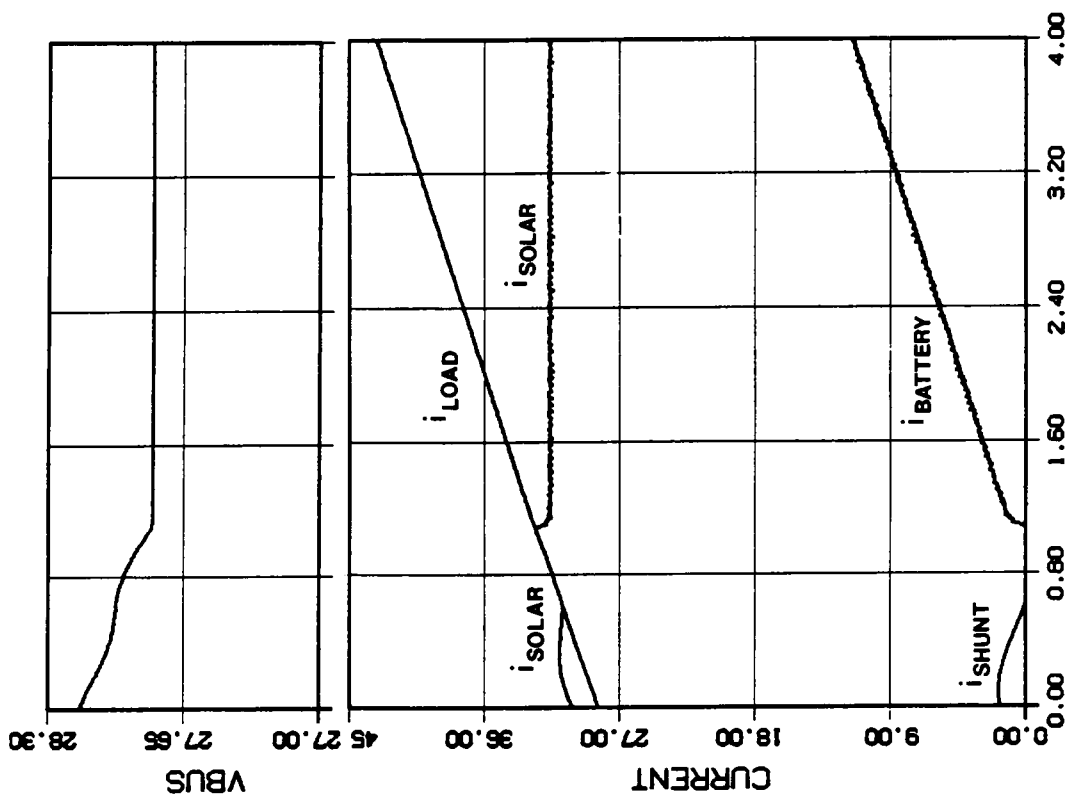
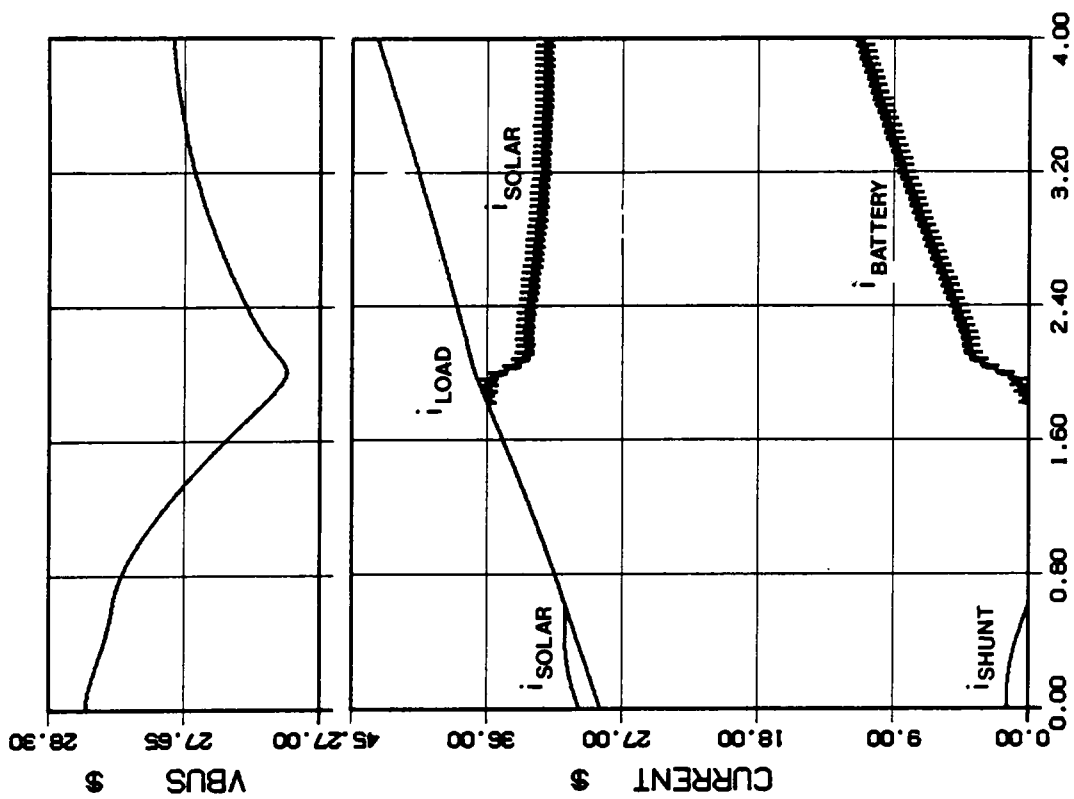


Figure 5.19 System responses in the transition mode (DCM)

- (a) using the compensator shown in Fig. 5.16c
- (b) using the compensator shown in Fig. 5.16b

5.5 Analysis of the solar array system modes of operation

The modes of operation of the DET system is described in this section. In Fig.4.1 the Central Control Unit (CCU) continuously monitors the bus voltage and commands the system to operate in the battery discharging mode when v_{bus} is less than V_{rb} or in the shunt active mode when v_{bus} is greater than V_{rs} . When v_{bus} is between V_{rb} and V_{rs} , there exists a "dead band" at which the load power is equal to solar array output power. When the system operates in the shunt active mode, any variation of the bus voltage generates a proportional shunt current, i_{sh} . The shunt current together with the load current sinks the solar array current and regulates the bus voltage. When the system operates in the battery discharge mode, the bus voltage is regulated by the battery discharger. The solar array current is set by the bus voltage on its output I-V curve and the battery discharging current is the difference between the solar array current and the load current.

The system operating condition is subject to change due to constantly varying load power consumption and the solar array power generations. The entire DET system shown in Fig.4.1 is simulated to observe how the system operating point moves from one mode to another. Fig.5.20 illustrates the case in which the solar array I-V characteristic varies according to the illumination level. In this example, the illumination level is set to be linearly time varying from Curve #1 (shunt active mode) to Curve #4 (battery discharging mode) as illustrated in Fig.5.20.

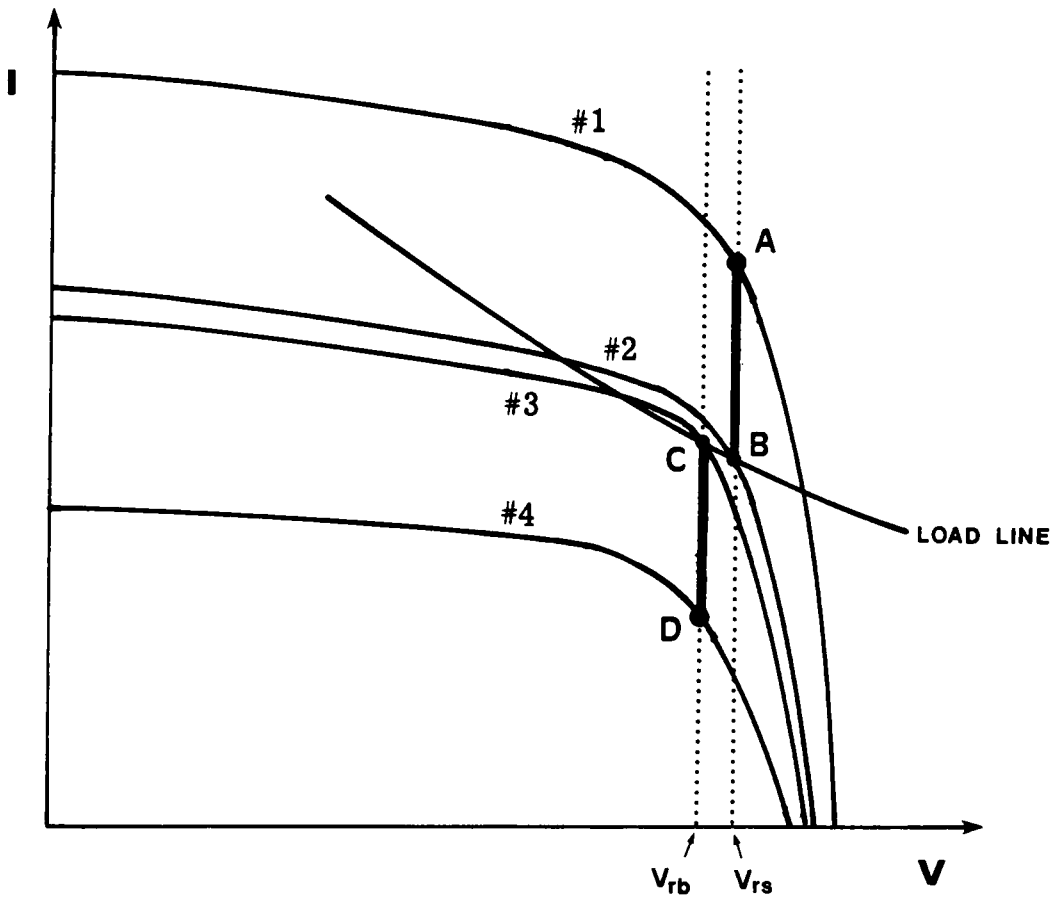


Figure 5.20 DET system mode of operation for the solar array illumination level changes

From Fig.5.20 and the simulation result in Fig.5.21, the system mode of operation is described as follows: Starting from Curve #1 and the corresponding operating point A, the operating point travels along the shunt regulation line, V_{rs} until the solar array power decreases to Curve #2. In this mode, the shunt regulator is active and it absorbs the excessive current generated by the solar array through the shunt elements (i_{sh}). Between Curve #2 and #3 the dead band mode exists where both the shunt regulator and battery discharger are deactivated. Thus, the operating point travels along the load line from point B to C. Further decrease in the illumination level activates the battery discharger. The operating point then follows the battery discharger regulation line, V_{rb} . In this mode, the load power is supplied by the solar array and the battery.

Fig.5.22 illustrates the case where the load power is varying while the solar array I-V curve is fixed. With the solar array curve fixed and the load line below Curve #2, the operating point is set at Point A. As the load power increases beyond Curve #2, the shunt regulator is deactivated, and the operating point travels on the solar array curve until Point B. Further increase in the load power beyond Curve #3 activates the battery discharger, and the operating point moves along the battery discharger regulation line to Point C.

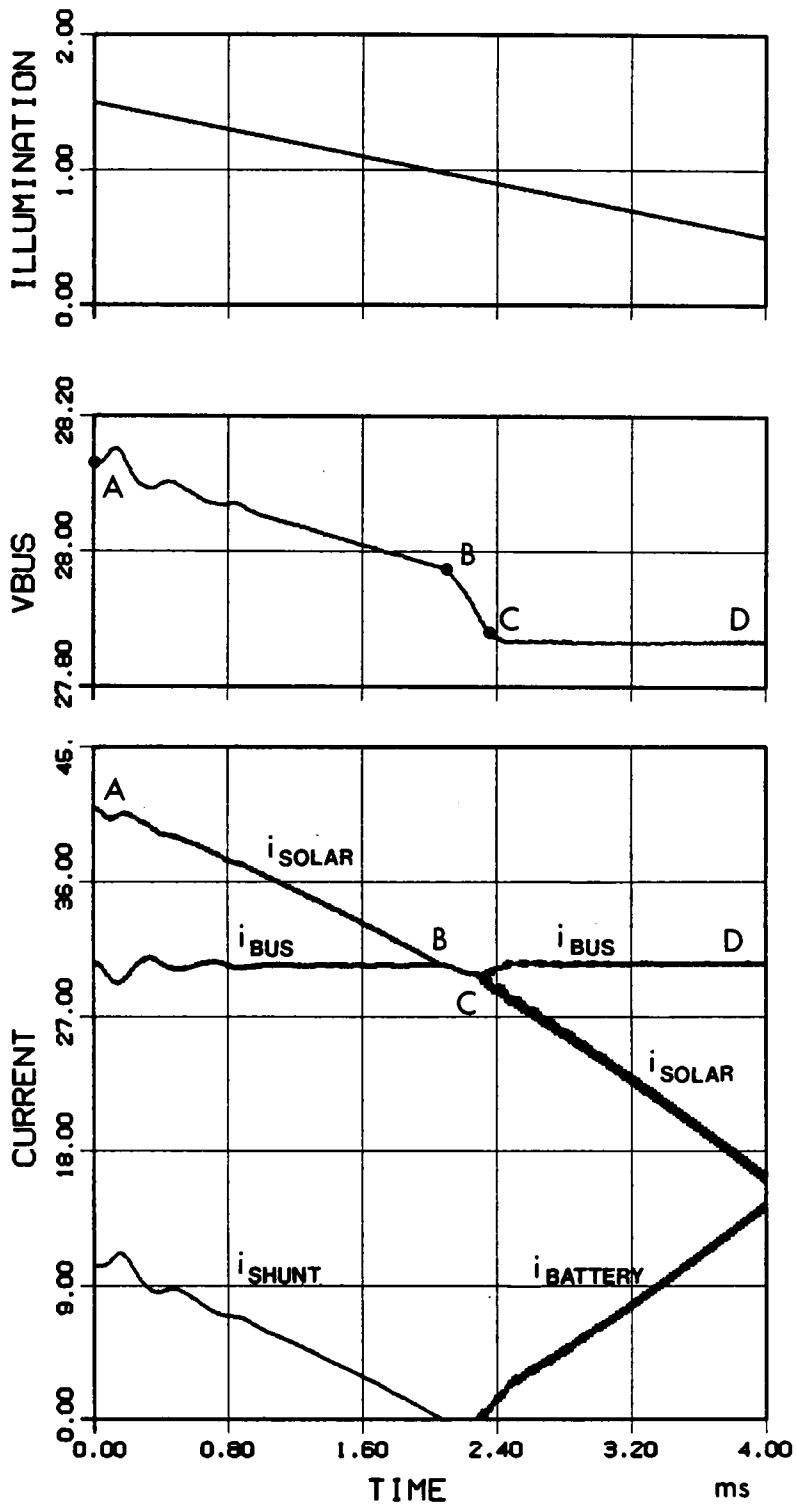


Figure 5.21 Simulation of the DET system for the solar array illumination changes in Fig.5.20

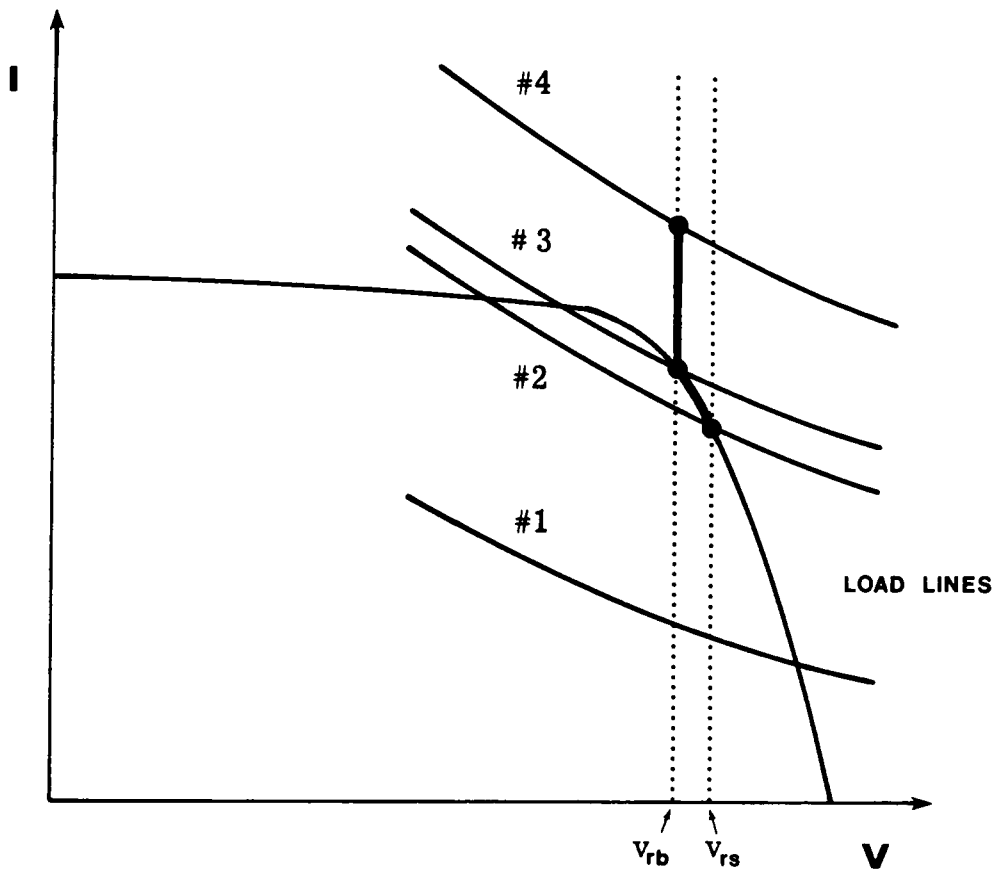


Figure 5.22 DET system mode of operation for the load power changes

5.6 Conclusions

In this chapter, various solar array system modes of operation in the large signal sense are analyzed using the state plane method. The analysis shows that any equilibrium point in Region #2 of Fig.5.1 is unstable with real eigenvalues. The state plane trajectories are separated by the separatrix that passes through Region #2 and converges to either of the two stable equilibrium points in Region #1 or #3. Thus the behavior of the system is quite different on either sides of the separatrix. Since only the equilibrium point in Region #3 is the desirable operating point, a control method to move the operating point from Region #1 to Region #3 is discussed. The analysis also provides a guide line for the system initial start-up condition that leads to the desired mode of operation.

The behavior of the operating point near the solar array maximum power point is analyzed. As far as stability and transient response are concerned, the equilibrium points near the maximum power point, in general, are less commendable than in the region where the magnitude of the solar array output resistance is small, or in the range of a stiffer voltage source.

Since the battery discharger in the DET system operates from the no-load condition through the discontinuous conduction mode (DCM) to the continuous conduction mode (CCM), the feedback compensator must be designed carefully to ensure stability in both modes of operation. Various compensators are compared. The results show that a proportional gain control is most appropriate for the converter operating in the discon-

tinuous conduction mode. It not only speeds up the system response but also prevents system oscillation near the no-load condition. In the CCM mode of operation, a phase lead type compensator is required to compensate the excessive phase lag due to the complex pole.

The solar array system modes of operation including the shunt regulator, battery and switching regulator load are described. The entire DET system is simulated for a continuously varying illumination level and the behavior of the system in various modes of operation are observed.

Chapter 6

SUBSYSTEM INTERACTION ANALYSIS

6.1 Introduction

One of the major difficulties in large scale system analysis involves the subsystem interaction problems. As a system becomes more complex, the interaction problems create a large degree of uncertainties to the system response even though each component may be well understood and documented. Thus, the total system analysis must include the interaction analysis that allows one to predict the system level responses and to trouble-shoot the system. The interaction analysis also provides design information of a component or subsystem at the initial design phase and for the future modification of the system.

As discussed in Chapter 2, the entire system can be considered as a doubly terminated two-port network for a component or subsystem. Thus, in general, the interaction problem can be defined as the interactions among an undetermined component and its terminating subsystems. For the component of interest, these terminating subsystems can be treated as the source and load impedances. The system performance parameters such as stability, audiosusceptibility, etc., are then dependent on the characteristics of these terminating subsystems.

Based on the analysis in Chapter 2, the interaction problems in spacecraft power systems are illustrated using the switching regulator examples.

Regarding the interaction problem the switching regulator is one of the most critical components in the spacecraft power systems. This is because the switching regulator's feedback loop gain characteristic depends largely on the characteristics of connected source and load impedances. Also, its power stage energy storage elements, L and C, generate resonance in the system that can cause peaking of the input or output impedance and give an undesired interaction. It is especially critical in the source interaction case because of the negative input impedance characteristic of switching regulator at low frequencies.

As stated in Chapter 3, it is better to consider the load and source interactions separately to obtain analytical insight into the system. In Section 6.2, the load interaction problem is discussed using an ideal source. In Section 6.3, a source interaction criteria is investigated with a load terminated stable regulator. Conclusions are presented in Section 6.4.

6.2 Load interaction analysis

Characteristics of the payloads for switching regulators, in many practical systems, are not simple resistive loads, but are complex and frequency dependent. Including the dynamics (additional states) of the loads into the state-space expressions of the switching regulator makes analytical derivations very cumbersome. Also the results can not provide physical insight into the system and the analytical interpretation of the results may be lost.

In order to examine the interactions between a switching regulator and its load, the performance parameters of a load terminated regulator should be derived as a function of the load terminal characteristic. Fig.6.1 illustrates the linear equivalent canonical circuit model for an unterminated switching regulator and load, Z_L . From the figure, the loop gain of the load terminated regulator, T_G^* , can be derived as a function of the g-parameters, the loop gain of the unterminated regulator, T_G , and load impedance, Z_L . However, the expression for T_G^* is still too complex to obtain any analytical insight, i.e., the stability of the regulator. Instead, as described in Section 3.2.5, Eqs.(6.1) and (6.2) provide an analytical closed form expression, from which the performance parameters of the regulator can be expressed as well defined quantities, load impedance, Z_L , and output impedance of the unterminated regulator, g_{22} .

$$\frac{v_2}{v_1} = \frac{g_{21}}{1 + g_{22}/Z_L} \equiv \frac{g_{21}}{1 + T_L} \quad (6.1)$$

$$Y_i = \frac{Y_{eq}}{1 + g_{22} / Z_L} \quad (6.2)$$

where

$$Y_{eq} = - (g_{11} + \Delta G / Z_L)$$

For the small signal analysis, the g-parameter for an unterminated regulator can easily be obtained from the known steady state output power.

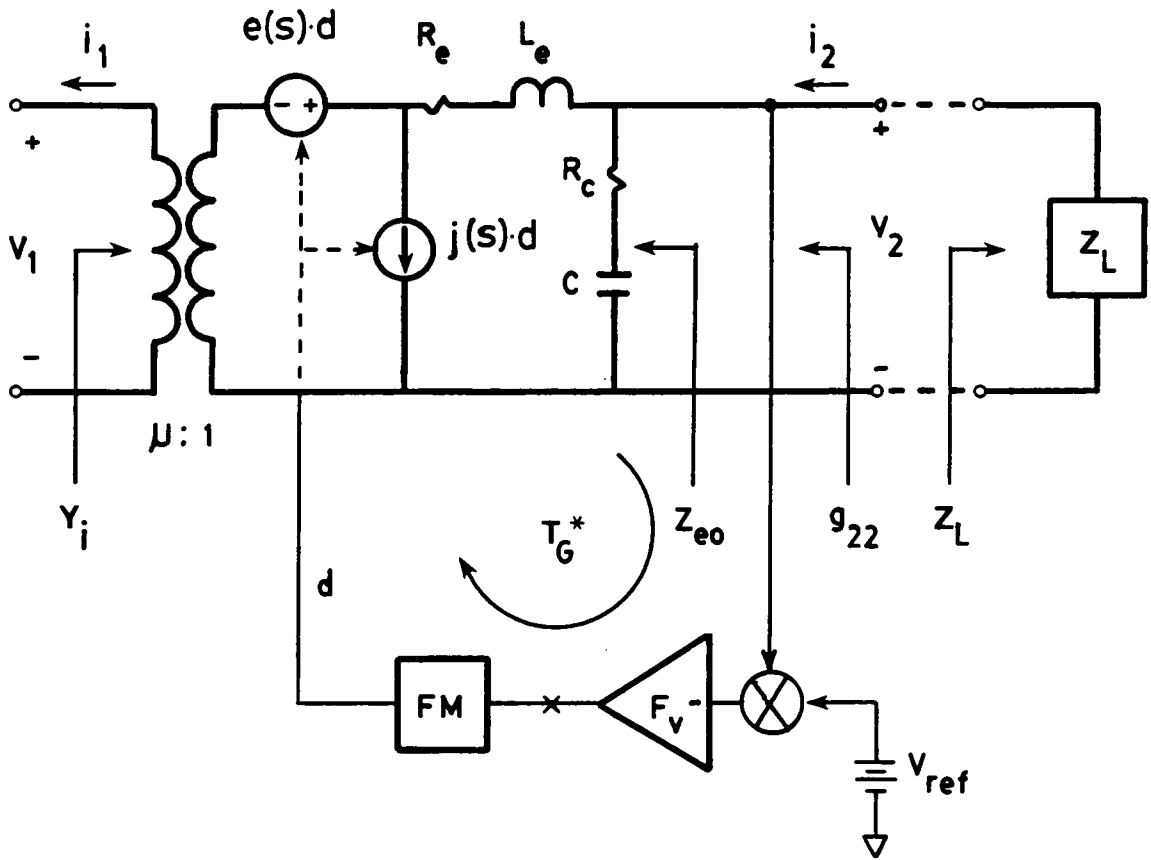


Figure 6.1 Load terminated switching regulator

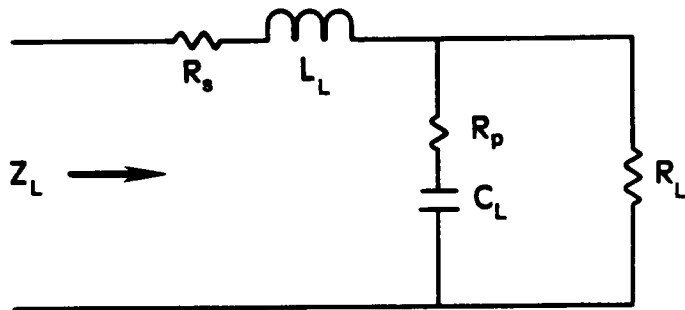


Figure 6.2 LCR load

Thus, using the g_{22} and Z_L , stability of the regulator can be determined by applying the Nyquist criterion to the quantity $T_L = g_{22}/Z_L$. Under the assumption of both g_{22} and Z_L being stable, which implies T_L does not have RHP poles, the Bode plot technique can be used to check the stability of the regulator. Thus, from the above assumption, the sufficient condition for the stability of the regulator is simply $|g_{22}| < |Z_L|$.

To illustrate the load interaction problem, supposed that a payload is characterized as a simple LCR load as shown in Fig.6.2. The load impedance, Z_L is then

$$Z_L = (R_L + R_s) \frac{1 + s/(Q_L \omega_L) + s^2/\omega_L^2}{1 + \omega_p} \quad (6.3)$$

where

$$\omega_L = \frac{1}{\sqrt{L_L C_L}} \frac{(R_L + R_s)}{(R_L + R_p)} \approx \frac{1}{\sqrt{L_L C_L}} \quad \text{for } R_L \ll R_p, \quad R_L \ll R_s \quad (6.4)$$

$$Q_L = \frac{1}{\omega_L} \frac{R_L + R_p}{L_L + C_L [(R_L + R_p)R_s + R_L R_p]} \approx \frac{1}{\omega_L} \frac{1}{L_L/R_L + C(R_s + R_p)} \quad (6.5)$$

$$\omega_p = \frac{1}{C(R_L + R_p) s} \quad (6.6)$$

To illustrate this case, a single-loop (output voltage) feedback controlled buck regulator is designed. The unterminated closed-loop output

impedance, g_{22} can be directly obtained from the canonical circuit model in Fig.6.1.

$$g_{22} = \frac{Z_{eo}}{1 + T_G} \quad (6.7)$$

where

$$Z_{eo} = (R_e + sL_e) \parallel (R_c + \frac{1}{sC}) ; \text{ open loop output impedance} \quad (6.8)$$

$$T_G = e(s) H_e(s) F_M F_v(s) ; \text{ loop gain of the unterminated regulator} \quad (6.9)$$

$$H_e(s) = \frac{1 + s/\omega_z}{1 + s/(Q_o\omega_o) + s^2/\omega_o^2} ; \text{ effective filter transfer function} \quad (6.10)$$

$$\omega_o = \frac{1}{\sqrt{L_e C}} , \quad Q_o = \sqrt{\frac{L_e}{C}} \frac{1}{R_e + R_c} , \quad \omega_z = \frac{1}{CR_c} \quad (6.11)$$

for the buck regulator,

$$L_e = L, \quad R_e = R_l, \quad e(s) = V_2 / D \quad (6.12)$$

Parameter values used for Z_L are $L_L = 5\mu\text{H}$, $R_s = 5\text{m}\Omega$ and $R_p = 10\text{m}\Omega$. In Fig.6.3, the locus of the critical eigenvalue as function of C_L of Z_L is plotted. As the value of C_L increases, the locus approaches the imaginary axis and finally cause the regulator to become unstable. The interaction criteria can be observed from the comparison of the magnitude

of g_{22} and Z_L as shown in Fig.6.4. For the larger value of C_L , ω_p in Eq.(6.6) becomes smaller and Q_L in Eq.(6.5) becomes larger, thus more interaction occurs between g_{22} and Z_L . Fig.6.5 shows the loop gain T_G^* defined at Point X for the corresponding values of C_L in Fig.6.4. It also includes T_G^* for a resistive load, R_L (i.e. $L_L=0$, $C_L=0$). As the value of C_L increases the bandwidth of the loop gain decreases and the phase margin also decreases, and the system becomes unstable for $C_L = 500 \mu\text{F}$.

As commonly exercised, a filter capacitor is added between the power supply and equipment (load). This can cause system instability as illustrated in the previous example. Equipment that require a more complicated input filter create a larger degree of uncertainty of the system stability. Also, the load interaction problem is more critical when a regulator supplies the power for equipment that requires a low voltage and a high current. Two-port modeling allows one to trouble-shoot the system and to obtain design guide lines of the input filter for the equipment.

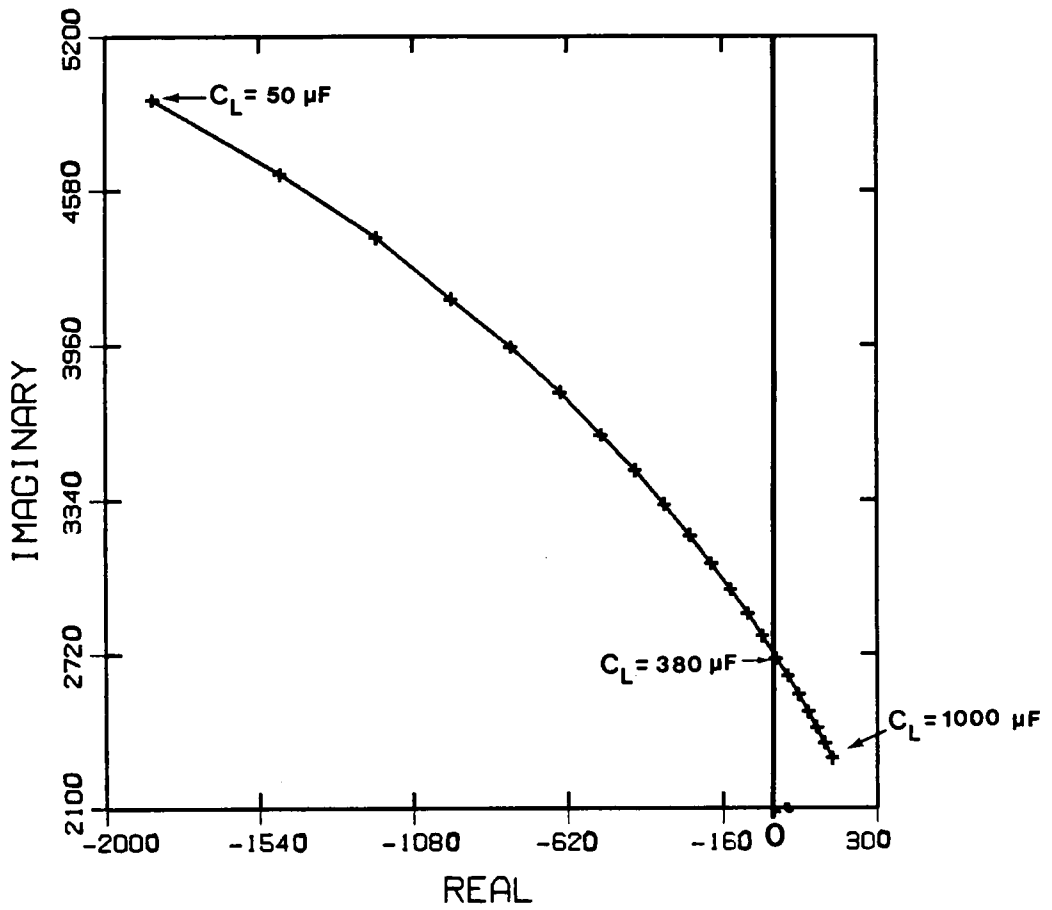


Figure 6.3 Locus of the critical eigenvalue as a function of the load capacitance C_L

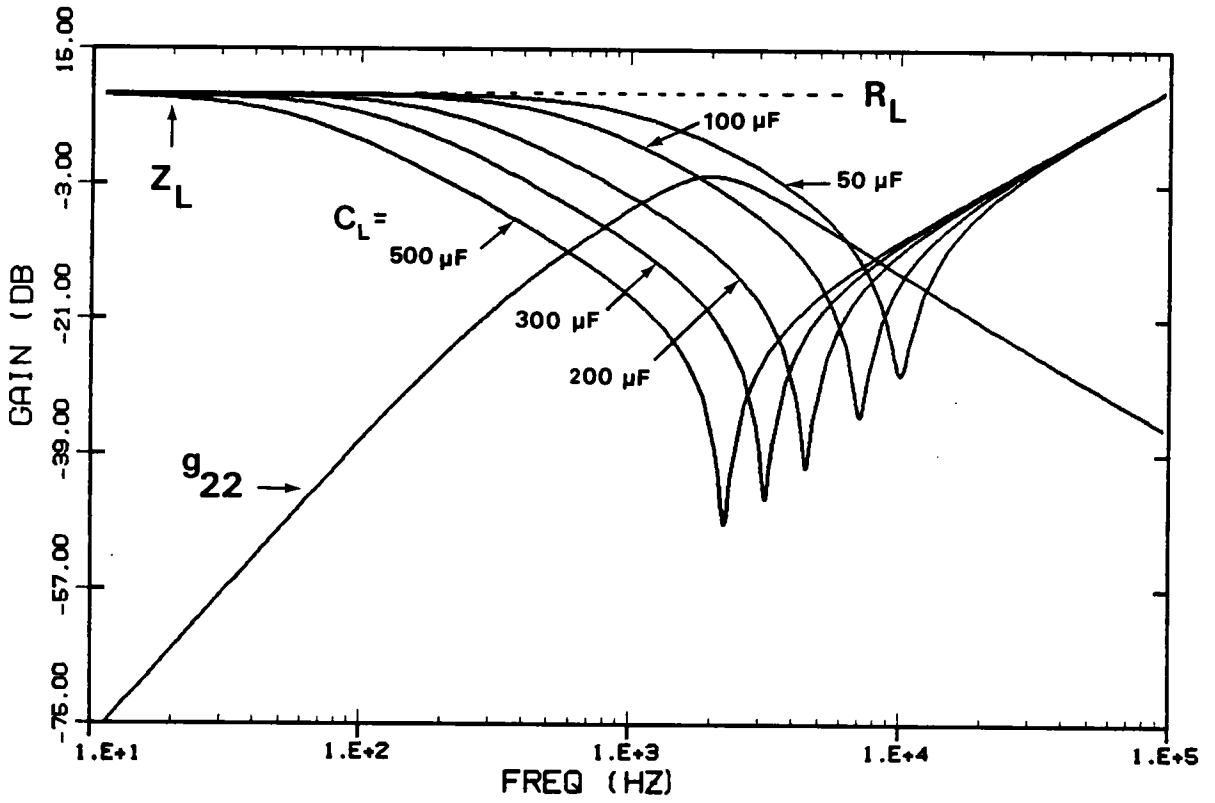


Figure 6.4 Magnitude of the impedance comparisons, g_{22} vs Z_L for various values of C_L

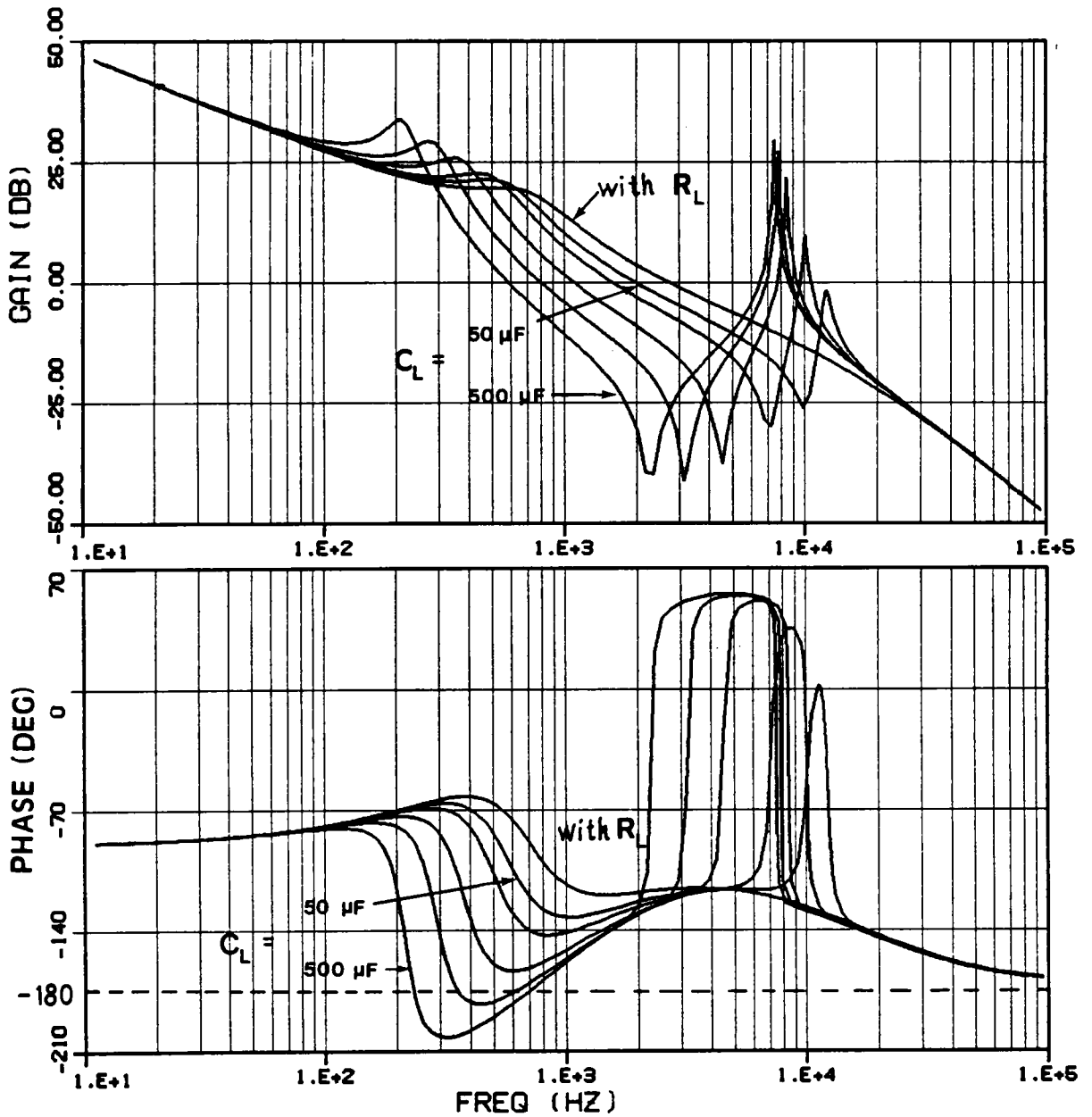


Figure 6.5 Loop gain (T_G^*) characteristics for the corresponding values of C_L in Fig.6.4

6.3 Source interaction analysis

The source interaction problem of switching regulators was first described between an input filter and a regulator [13]. The conclusion can easily be extended to a general cascade configuration where a preceding block is viewed as a source impedance to the following block which is a switching regulator. This interaction analysis can be further extended to a parallel configuration as in the DET system. Fig.6.6 shows the system in which switching regulators are sharing a common bus. The source block represents the solar array, the shunt regulator or the battery discharger, cable impedances and filters.

As described in the previous section, it is very difficult to analytically characterize the performance parameters of the regulator including dynamics of the source. However, using Eq.(6.13), the source interaction can easily be analyzed similar to the load interaction analysis.

$$H_s = \frac{v_1}{v_s} = \frac{1/Y_i}{1/Y_i + Z_s} = \frac{1}{1 + Z_s \cdot Y_i} \equiv \frac{1}{1 + T_S} \quad (6.13)$$

In this case, the parameters to be used for the analysis are the input impedance of the load terminated regulator, Z_i , and the source impedance seen by the regulator, Z_s .

In the parallel configuration shown in Fig.6.6, one can define two types of interactions: local and global. The local interaction is the interaction between the source impedance seen by j^{th} regulator, Z_{sj} and

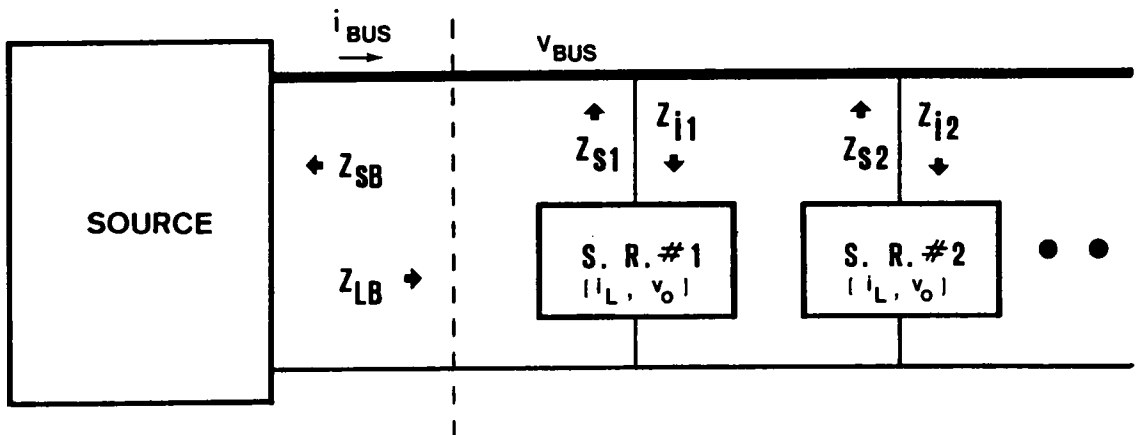


Figure 6.6 Parallel connection of switching regulators

its input impedance, Z_{ij} . The global interaction is the interaction between the source and load impedances seen by the main bus, Z_{SB} and Z_{LB} . For the source interaction, let us assume that each interacting quantity (Z_{sj} , Z_{ij} , Z_{SB} , Z_{LB}) is individually stable, otherwise the subsystem(s) is(are) already unstable before we consider this interaction problem. From Eq.(6.13), the system is stable if the polar plot of T_S does not encircle (-1,0) point under the above assumptions. Thus, the condition $|T_S| < 1$ ensures the above stability criterion. As the number of regulators increases the stability condition moves in the worse direction and may cause instability of the system because of the negative input impedance characteristic of the regulators.

Let us consider first the undesired interaction that occurs to a local j^{th} switching regulator. The source impedance seen by the j^{th} regulator, Z_{sj} is the parallel combination of the output impedance of the source block, Z_{SB} , and the input impedance of all other switching regulators, Z_{Lj} , as described in Eqs.(6.14) and (6.15).

$$Y_{sj} = Y_S + Y_{Lj}, \quad Z_{sj} = 1 / Y_{sj} \quad (6.14)$$

$$Y_{Lj} = \sum_{\substack{k=1 \\ k \neq j}}^n Y_{ik} \quad (6.15)$$

To see how other regulators might affect the stability of the j^{th} regulator, let us examine the summing effect of these impedances at low frequencies. Since, at low frequencies, switching regulators have the

negative input impedance characteristic, so as Z_{Lj} , and the magnitude of Z_{Lj} is supposed to be larger than that of Z_{SB} for a stable design, the magnitude of the equivalent source impedance seen by the j^{th} regulator, $|Z_{sj}|$ decreases as the number of regulators increases. This shows that a stable system can become unstable if more switching regulators are added to the main bus.

An undesired interaction in the global sense can occur when a large number of converters share the same DC bus which is not a stiff voltage source. In this case the magnitude of the load impedance seen by the bus $|Z_{LB}|$, which again is negative at low frequency, decreases as the number of regulators increases as shown in Eq.(6.16).

$$Y_{LB} = \sum_{k=1}^n Y_{ik} , \quad Z_{LB} = 1 / Y_{LB} \quad (6.16)$$

When the $|Z_{LB}|$ is of the same order of magnitude as the $|Z_{SB}|$ an undesired interaction can occur. In this case the entire bus may oscillate. In this case, the local interaction may not be directly observable from the impedance comparison between Z_{sj} and Z_{ij} , because Z_{sj} may be already unstable due to the global interaction thus it may have RHP poles.

The following cases are generated to illustrate the undesired interaction problems described above.

First, a stable system in Fig.6.6 is constructed with only one switching regulator (S.R.#1 - Buck regulator with input filter). Fig.6.7 shows the step load responses where the load power of S.R.#1 steps up from

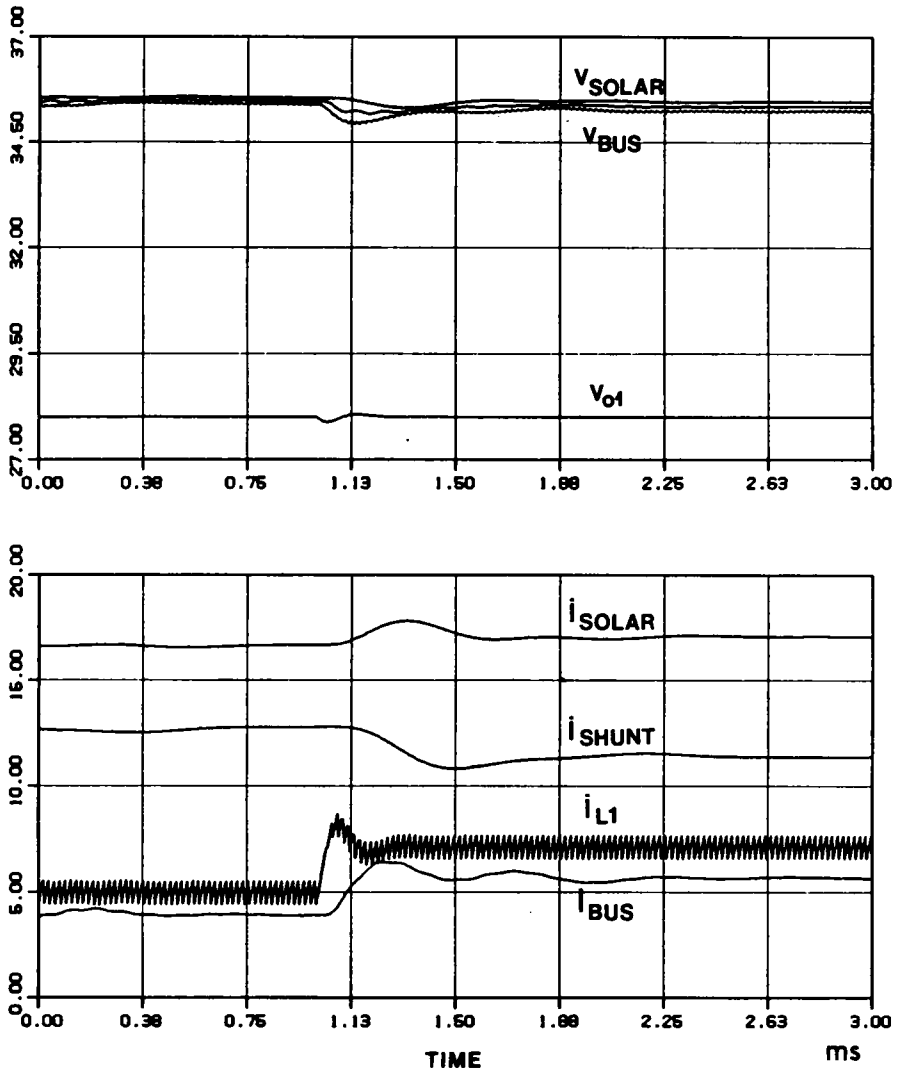


Figure 6.7 Step transient responses for the DET system with one switching regulator (S.R.#1)

140 to 200 watts at 1ms. The system eigenvalues calculated under the steady-state condition are listed in Table 6.1. The results of both small signal analysis and time domain simulation show that the system is locally and globally stable.

Next, a stable switching regulator (S.R.#2 - same structure as that of S.R.#1 except with different parameter values) is designed. The loop gain and step load transient responses of S.R.#2 with an ideal source are shown in Fig.6.8.

Now, the S.R.#2 is connected to the bus, in parallel with S.R.#1, and the transient analysis of the first case (Fig.6.7a) is repeated. Fig.6.9a shows that the new system is very oscillatory. The system eigenvalues as shown in Table 6.2 indicate a lower frequency oscillation of the bus voltage. Similarly, by comparing the input impedance Z_{i2} of S.R.#2 and the source impedance Z_{s2} seen by S.R.#2 as shown in Fig.6.9b, the oscillatory nature of the system can be predicted. Since the two impedance curves are overlapping in Fig.6.9b, the stability predicted with the individual regulator in Fig.6.8b is no longer valid because of a parallel interaction.

To illustrate the undesired interactions in the global sense, let us consider four well-behaved switching regulators sharing a common bus. In this case identical switching regulators are used. The system is stable with a single regulator as Table 6.3a indicates. However, the system becomes unstable with four regulators in parallel as shown in Table 6.3b. Fig.6.10 compares the source impedance, Z_{SB} , and the total load impedance, Z_{LB} , seen by the bus and reveals the undesired interactions.

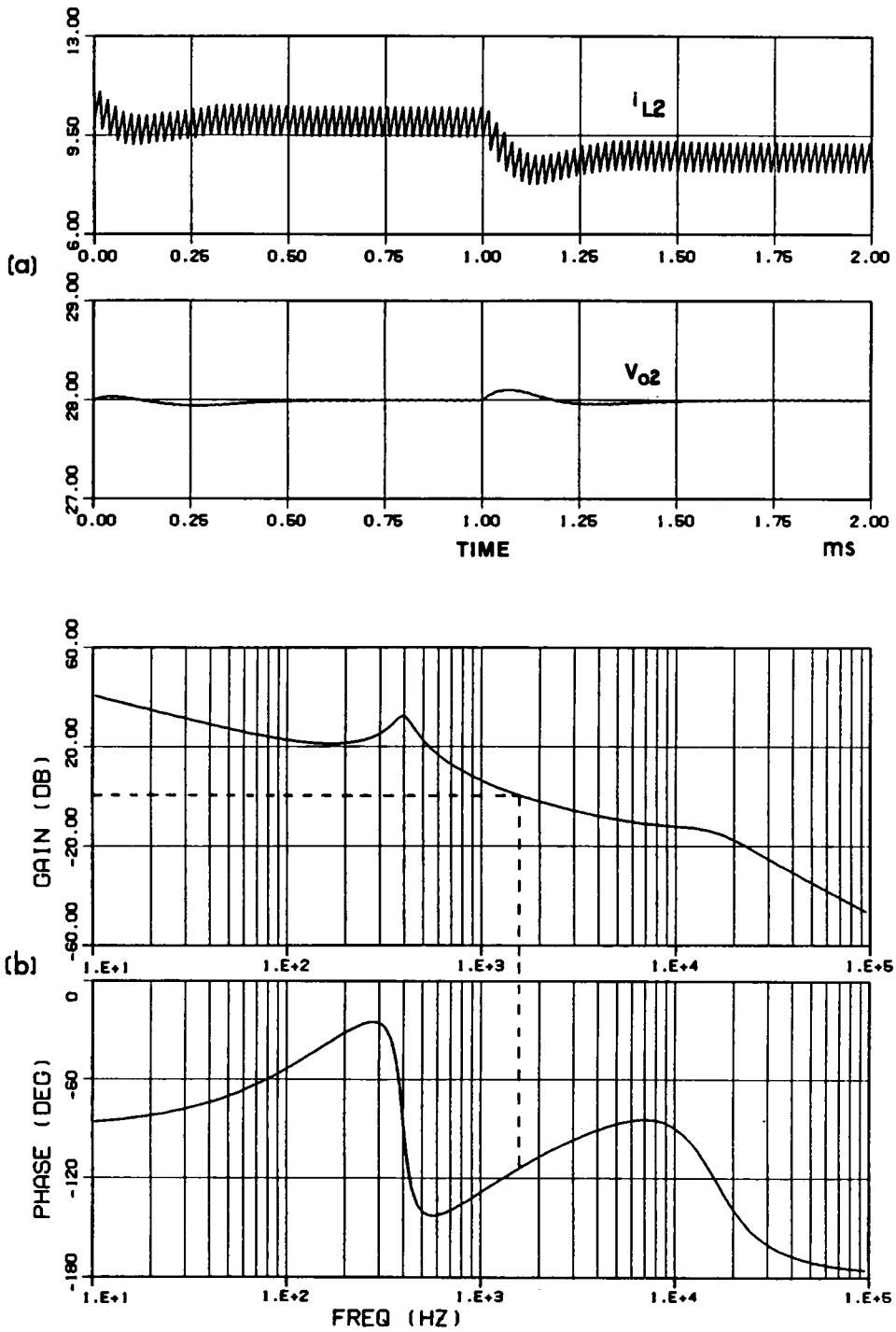


Figure 6.8 A switching regulator (S.R.#2) responses before it is connected to the system in Fig.6.7

(a) Step load transient response -
load step : 280 to 224 watt at 1 ms

(b) Loop gain characteristic

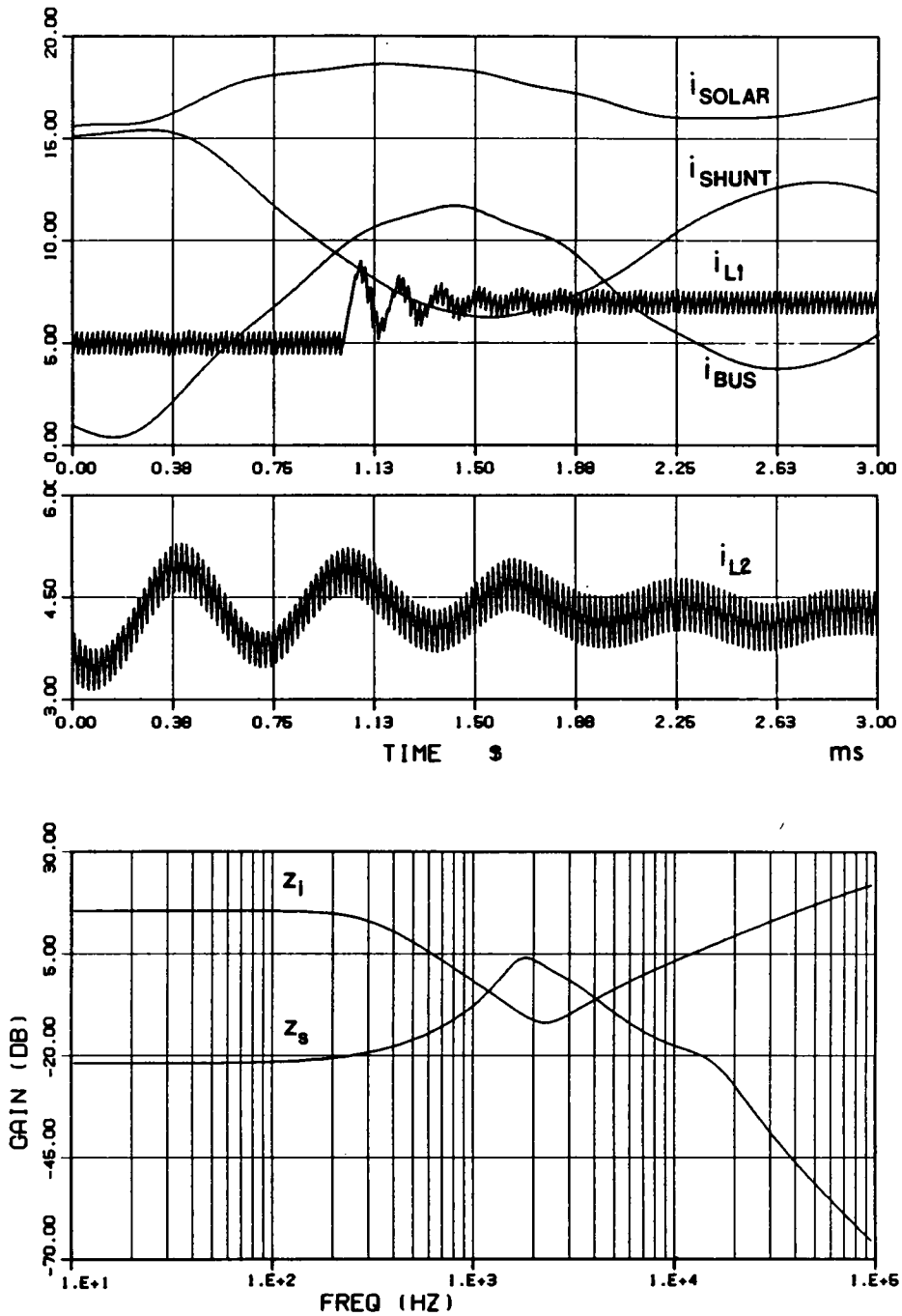


Figure 6.9 The system responses after the stable regulator (Fig.6.8) is connected to the stable system (Fig.6.7)

(a) Step transient responses -

the same step load for S.R.#1 as in Fig.6.7

(b) The magnitude of the impedances, $|z_{s2}|$ vs $|z_{i2}|$

15 EIGENVALUES

REAL	IMAGINARY
-692.671	+ 489.278
-708.619	0.000000E+00
-784.905	0.000000E+00
-4046.70	+ 4464.83
-10033.1	0.000000E+00
-13675.9	+ 112464.
-14876.1	0.000000E+00
-500394.	+0.316578E+08
-522133.	+ 660439
-.102681E+07	0.000000E+00

Table 6.1 The eigenvalues of the system for the responses in Fig.6.7

21 EIGENVALUES

REAL	IMAGINARY
-87.937	+ 2514.21
-592.471	0.000000E+00
-808.237	0.000000E+00
-971.228	0.000000E+00
-4047.86	+ 4465.36
-6164.15	0.000000E+00
-10033.1	0.000000E+00
-12460.7	+ 110556.
-14876.1	0.000000E+00
-22909.5	0.000000E+00
-157168.	0.000000E+00
-250118.	+0.223800E+08
-500391.	+0.316579E+08
-522131.	+ 660434.
-.102681E+07	0.000000E+00

Table 6.2 The eigenvalues of the system for the responses in Fig.6.9

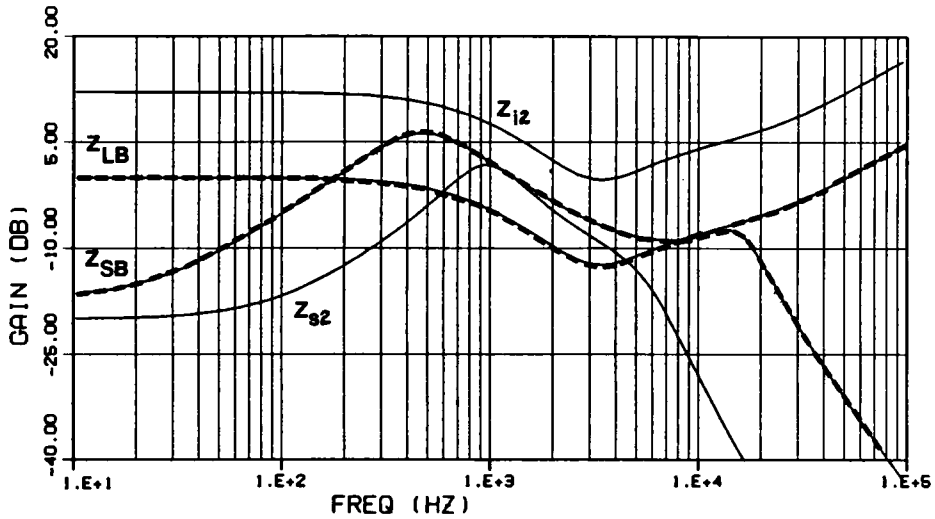


Figure 6.10 The magnitude of the impedances seen by the bus ($|z_{SB}|$ vs $|z_{LB}|$) and seen by S.R.#2 ($|z_{s2}|$ vs $|z_{i2}|$)

15 EIGENVALUES		33 EIGENVALUES	
REAL	IMAGINARY	REAL	IMAGINARY
-670.571	+ 127.77	168.2244	+ 909.417
-705.940	0.000000E+00	-342.149	0.000000E+00
-982.262	0.000000E+00	-855.722	0.000000E+00
-3447.72	+ 3670.94	-855.722	0.000000E+00
-7411.92	+ 12070.5	-855.722	0.000000E+00
-114902.	0.000000E+00	-1218.23	0.000000E+00
-233717.	0.000000E+00	-3463.95	+ 3681.63
-271348.	+ 184089.	-4083.17	+ 7396.58
-907316.	0.000000E+00	-4083.17	+ 7396.58
-.249996E+08	+ -0.999745E+09	-4083.17	+ 7396.58
		-17934.0	+ 16539.5
		-114106.	0.000000E+00
		-114106.	0.000000E+00
		-114106.	0.000000E+00
		-118324.	0.000000E+00
		-233717.	0.000000E+00
		-259219.	+ 179965.
		-907316.	0.000000E+00
		-.249996E+08	+ -0.999695E+09
		-.249996E+08	+ -0.999695E+09
		-.249996E+08	+ -0.999695E+09
		-.249996E+08	+ -0.999895E+09

Table 6.3 Eigenvalues of the system

- (a) with single switching regulator
 (b) with four switching regulators

However, by comparing the source impedance, Z_{s1} , and the input impedance, Z_{i1} , seen by the individual regulators, the presence of the undesired interaction to an individual regulator is not obvious. From Eq.(6.13), the sufficient condition for stability is $|Z_i| > |Z_s|$ under the assumption that both Z_i and Z_s are stable. In this case, the source impedance, Z_{s1} , seen by S.R.#1 is unstable (has RHP poles) because the undesired interaction already occurred between Z_{SB} and Z_{L1} . Thus, even though $|Z_{i1}| > |Z_{sj}|$ for all frequencies as shown in Fig.6.10, the system is unstable. This reveals that the analysis must be done at both the local regulator level and the system level.

6.4 Conclusions

The subsystem interaction criteria are investigated at component, subsystem, and system levels by comparing the impedances at an interacting point.

Both the load and the source interaction problems are illustrated using the switching regulator examples. The performance of the regulator depends upon the dynamic characteristics of the payload. As demonstrated in this chapter, a single filter capacitor between the regulator and equipment (load) can cause system instability due to the load interaction. The load interactions can be investigated using the output impedance of the unterminated two-port subsystem (component), g_{22} , and its load impedance, Z_L .

For a spacecraft power system that uses many switching regulators in parallel on the bus, such as the DET system, the interaction problem should be examined both at the local regulator level and the system level. Also, the analysis shows that as the number of regulators increases, the margin of stability of the system tends to diminish due to the parallel interactions.

Chapter 7

EMPIRICAL DATA MODELING AND ANALYSIS

7.1 Introduction

As discussed in Chapter 2, defining mathematical models through experimental testing of an actual system (or scaled model) is, generally, least intensive in modeling effort and the empirical data model is accurate in predicting the overall system behavior. In general, experimental data serves two major functions: model verification and model development. If a theoretical model has been developed, at some point its validity must be checked with actual experimental test data to have a high level of confidence in its predictive capabilities. Also, some systems defy refined theoretical treatment due to the lack of an assessment of certain parameter values, thus, experimentation is the only effective means of obtaining models with acceptable accuracy. In this chapter, model development based on empirical data is discussed and extended for the reduced order model realizations of a complex system.

In Section 7.2, model development from the frequency response data and its applications are described. Algorithms for identification of model parameters identification are reviewed in Section 7.3. In Section 7.4, model development and analysis using the DET system are presented. Conclusions are presented in Section 7.5.

7.2 Model development from frequency response data

Typical methods for linear system identification using empirical data are based on frequency, step, and impulse responses. Most of these methods apply to a linear process, but they may also be applicable to the linearized form of nonlinear systems if the input disturbance levels are kept low. Among these, frequency-response identification provides the most accurate model realization for the system stability. Since generation of the sinusoidal input signal and measurement of its responses, i.e. audio susceptibility, feedback loop gain, impedances, etc., are not foreign to power system design engineers, the frequency response technique is more attractive than others.

Using an automatic network analyzer that includes a multi-channel bandpass tracking filter for gain and phase measurements and a built-in sinusoidal function generator synchronized with the tracking filter, accurate frequency response data of the system can easily be obtained. Where a model of an existing subsystem is to be constructed, empirical data can be obtained that characterize the input-output properties. Based on the set of empirical data a mathematical relation can be established that reproduces the same input-output behavior as that of the actual subsystem without regard to the physical structure of the subsystem.

The empirical data modeling technique can be used for the following applications.

Model development from experimental data:

For certain components or subsystems, such as a complex payload, cable, etc., it is often quite difficult to develop an analytical model. Under such circumstance the component or subsystem is viewed as a black box. The port characteristics of the black box can be experimentally measured by exciting an appropriate terminal with a sinusoidal source. From the experimental data, in the form of the frequency response, an analytical transfer function model can be developed using the complex curve fitting technique. The required transfer function model to represent properly the input-output characteristics of a black box will be described in the following section.

Reduced order model realization:

The model development from frequency data using the complex curve fitting technique can also be applied to reduce the order of complexity of the system. The technique is particularly powerful for large scale system modeling and analysis. For a large scale power system, computer simulation of an entire system can be very time consuming and memory capacity often exceeded. It is desirable that the entire system be broken into smaller subsystems so that an available computer system and software can be adapted. Each subsystem can then be simulated by the computer, or experimentally measured to obtain the frequency response data. From either the analytical or the empirical data a reduced order model can be realized using the complex curve fitting technique. As long as a reduced order transfer function model fits the original data reasonably well the

dynamic performances of the reduced order subsystem should match well with that of the original system. This technique allows the overall system to be realized with a reduced order model. More importantly, it aids in the trouble-shooting and design of a component or a subsystem in a large scale system. Using this technique, attention can be focused on the "troubled" equipment or subsystem in greater detail while treating the rest of the system with a reduced order model to take into account any possible interactions.

7.3 Review of the existing complex curve fitting algorithms

As described in the previous section, the purpose of the frequency response approach is to develop a transfer function model from the two sets of data, the amplitude ratio and the phase difference of the input and output. While methods for fitting curves to empirical data are a well known part of classical mathematics, the need to fit two curves (amplitude and phase) simultaneously over a wide range of frequencies requires a somewhat different treatment from the classical approaches. Several methods documented in the literatures are reviewed in this section.

One of the earliest methods by Levy [14] that dealt with the fundamentals of complex curve fitting is briefly reviewed here. This method requires that the measured frequency response be given in terms of its real and imaginary parts:

$$\frac{Q_o}{Q_i}(j\omega) = F(j\omega) \equiv R(\omega) + j I(\omega) \quad (7.1)$$

Let the fitted analytical model be

$$\begin{aligned}
 G(j\omega) &= \frac{A_0 + A_1(j\omega) + A_2(j\omega)^2 + \dots}{B_0 + B_1(j\omega) + B_2(j\omega)^2 + \dots} \\
 &\equiv \frac{\alpha + j\omega\beta}{\sigma + j\omega\tau} \equiv \frac{N(\omega)}{D(\omega)} \qquad (7.2)
 \end{aligned}$$

The error of fitting, $\varepsilon(\omega)$ is then

$$\varepsilon(\omega) = F(j\omega) - G(j\omega) = F(j\omega) - \frac{N(\omega)}{D(\omega)} \qquad (7.3)$$

To minimize the square of the error, $\varepsilon(\omega)$ (conventional least-square method), a set of simultaneous nonlinear equations results, whose solution is difficult. Instead, a new error function is defined by multiplying $D(\omega)$ into Eq.(7.3)

$$\varepsilon' = D(\omega) \varepsilon(\omega) = D(\omega)F(\omega) - N(\omega) \equiv a(\omega) + j b(\omega) \qquad (7.4)$$

Then the cost function for $m+1$ points of frequency data is defined as

$$J = \sum_{k=0}^m [a^2(\omega_k) + b^2(\omega_k)] \qquad (7.5)$$

To minimize the cost function J with respect to the coefficient vector,

$p = [A_0 \dots A_m \ B_0 \dots B_m]^T$, set

$$\frac{\partial J}{\partial p} = 0 \quad (7.6)$$

Eq.(7.6) leads to the linear algebraic equation

$$\lambda p = Q \quad (7.7)$$

where

$\lambda = nxn$ coefficient matrix, $n = m+1$

$Q = nx1$ constant vector

Finally, the solution for the model coefficient is

$$p = \lambda^{-1} Q \quad (7.8)$$

This algorithm is simple allowing a standard library function for the matrix inversion routine to be directly adapted. However, this technique has some serious deficiencies. If the transfer function has to be determined for frequencies extending several decades, the matrix λ has a "rank deficiency". In other words, the elements of the matrix λ are such that lower frequency values have very little influence. Thus, large errors can be introduced at low frequencies. If the poles in $G(j\omega)$ vary widely throughout the experimental points a good fitted model cannot be obtained.

To overcome the deficiencies, an iterative procedure is introduced in [15] that eliminates the weighting problem in the above method. This method improves the above algorithm by modifying Eq.(7.4) such that

$$\varepsilon''(\omega)_k = \frac{\varepsilon(\omega_k) D(\omega_k)_L}{D(\omega_k)_{L-1}} = \frac{F(j\omega_k) D(\omega_k)_L}{D(\omega_k)_{L-1}} = \frac{N(\omega_k)_L}{D(\omega_k)_{L-1}} \quad (7.9)$$

where the subscript L corresponds to the iteration number. As $D(\omega)$ is not known initially, it is assumed to be 1. The coefficients are obtained by minimizing the sum of $|\varepsilon_k|^2$ at all the data points. This algorithm fits the data much better over a wider frequency range than the algorithm in [14] and the iterations tend to converge rapidly.

A more recent effort in [16] uses a conjugate gradient search technique [17] to minimize the nonlinear cost function

$$J = \int_0^{\infty} | F(j\omega) - A(\omega) e^{j\theta(\omega)} |^2 d\omega$$

$$\approx \sum_{k=0}^m | F(j\omega_k) - A(\omega_k) e^{j\theta(\omega_k)} |^2 (\omega_{k+1} - \omega_{k-1}) \quad (7.10)$$

In this algorithm, the model is allowed to be characterized by a gain, real poles and zeros, and complex poles and zeros with the damping ratios. It also allows for dead time in the model, which is not possible in the previous methods [14] and [15]. A FORTRAN program listed in [16] is

implemented in the host software for this study.

Curve fitting results, in general, are quite satisfactory as will be seen in the following section. However, the results (or the number of iterations required for the error to be in the specified bound) depend on the initial guess of the coefficients which is especially critical when one uses a wrong sign for the initial guess.

7.4 Empirical data modeling and analysis of power systems

As mentioned in Section 7.2, the required model for a subsystem depends on the modeling objectives. Whether a subsystem is considered as a black box or is realized as a reduced order model, the subsystem to be modeled can be defined in the system as either an one-port or two-port model.

One-port modeling:

To analyze the behavior of a component or subsystem whose load or source characteristic is unknown or is to be realized as a reduced order model, the load or source subsystem can be defined as an one-port network. In this case, only one transfer function model (impedance) is needed to represent the subsystem. From the frequency response data of the impedance obtained from either experimental measurement or computer simulation of the circuit model, an analytical model can be developed using the complex curve fitting technique. The system performance parameters such as stability, audiosusceptibility and impedances can be analyzed using the curve fitted model. Time histories including step transient

responses of the system can be simulated. The step transient analysis, however, is valid for a small disturbance level. The curve fitted analytical model also allows one to analyze the source and load interaction problems.

To illustrate one-port modeling, the following cases are considered using the DET system shown in Fig.4.1.

First, assume that the switching regulator subsystem, S.R.#1, is a black box that can be viewed as one of the loads on the main bus line. The black box can then be characterized by its input impedance, Z_{i1} , which can easily be measured experimentally. An analytical transfer function model is then developed that represents S.R.#1 as shown in Fig.7.1. Curve fitted results of Z_{i1} data using the 3rd order model is shown in Fig.7.2. Using this model, the behavior of the system (i.e., the bus voltage) due to the disturbances at the source or at the other load (S.R.#2) can be examined. To show the results of one of the aforementioned analyses, Fig.7.5 and 7.6 compare the transfer functions from the solar array output voltage, v_S , to the bus voltage, v_B , and to the output voltage of S.R.#2, v_{O2} , between the system with the actual circuit model and that with the curve fitted model. As the curve fitted result in Fig.7.2 predicted, the system responses are almost identical between the two system.

Secondly, model the subsystem of the total load on the bus line from empirical data as shown in Fig.7.3. The frequency response data of the load impedance seen by the bus, Z_{LB} , is obtained either by experimental measurement or by computer simulation to realize a reduced order model. The actual circuit model which is a 16th order model and curve fitted

results of Z_{LB} data using the 4th order model is shown in Fig.7.4. Using this model, the behavior of the bus voltage can be examined as in the previous case. Stability of the solar array equilibrium point can be analyzed in detail. Also, the performance parameters such as the loop gain characteristic of the shunt regulator can be analytically determined. Fig.7.5 also includes the frequency response from the solar array output voltage, v_S , to the bus voltage, v_B , for this case. Again the result matches extremely well with the original system.

Two-port modeling:

As mentioned in Chapter 3, modeling of the distribution cable is very difficult. To characterize such a component it is necessary to view it as an unterminated two-port network and model its terminal characteristics. Thus, in this case, four sets of frequency response data are needed to identify the four elements in the G-matrix. As described in Fig.2.2 of Chapter 2, port-1 is excited by a voltage source for g_{11} and g_{21} , and port-2 by a current source for g_{12} and g_{22} . Since the hybrid g-parameters can be converted to the open circuit z-parameters or short circuit y-parameters, as shown in Eq.(7.11), appropriate experimental measurements can easily be obtained.

$$G = \begin{bmatrix} -1 & z_{12} \\ \frac{z_{11}}{z_{11}} & \frac{z_{11}}{z_{11}} \end{bmatrix} = \begin{bmatrix} \frac{-\Delta Y}{y_{22}} & \frac{-y_{12}}{y_{22}} \\ \frac{-y_{21}}{y_{22}} & \frac{1}{y_{22}} \end{bmatrix} \quad (7.11)$$

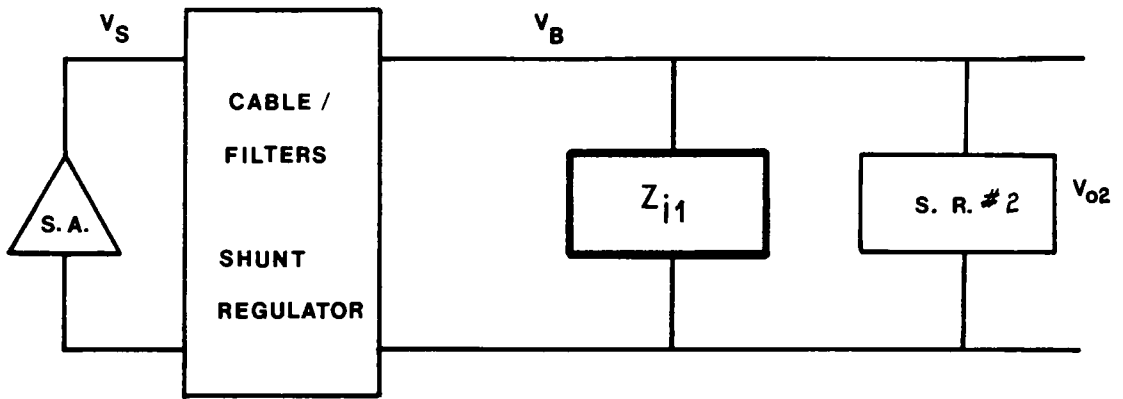


Figure 7.1 DET system with the empirical data curve fitted model for S.R.#1

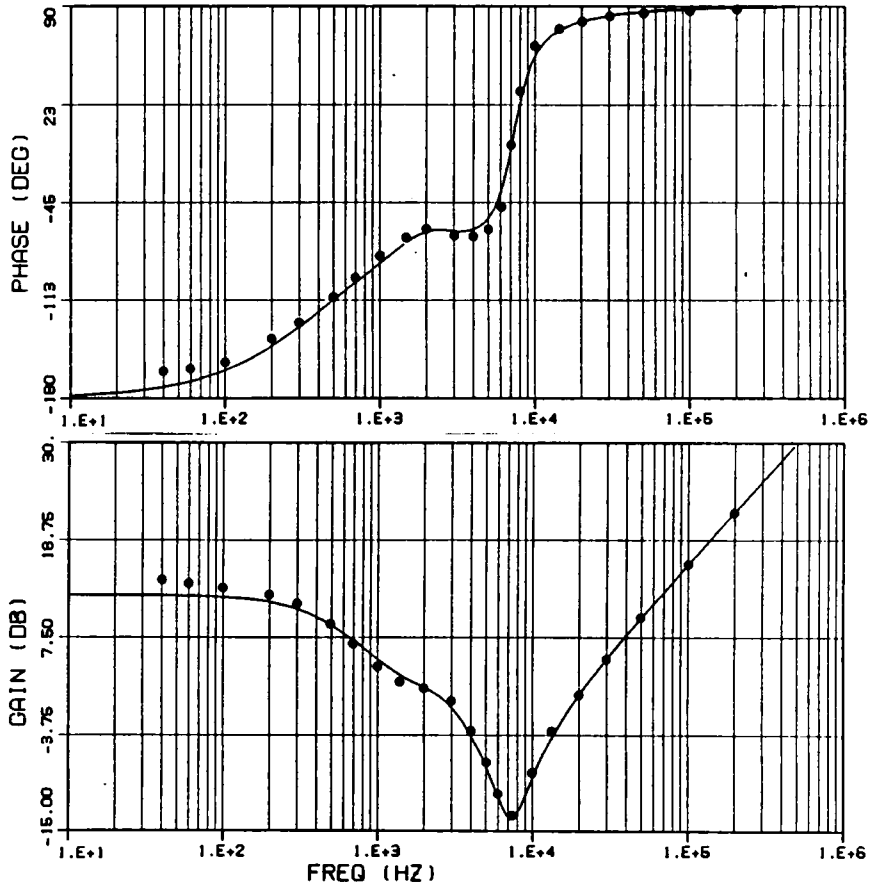


Figure 7.2 Curve fitting for z_{i1}

•••• : empirical data
 — : curve fitted analytical model

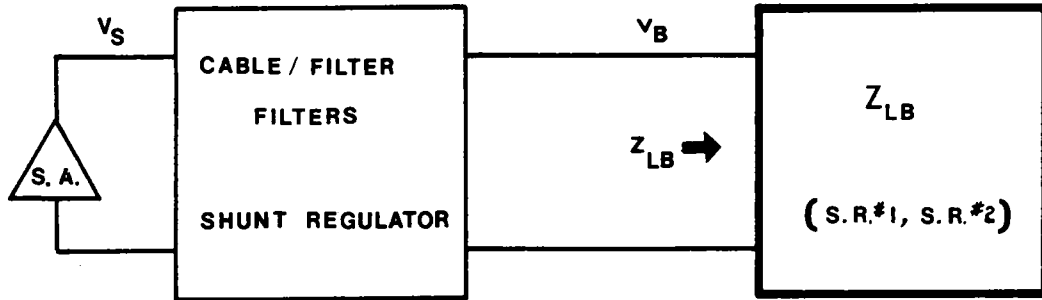


Figure 7.3 DET system with the reduced order model (z_{LB} for S.R.#1 and S.R.#2)

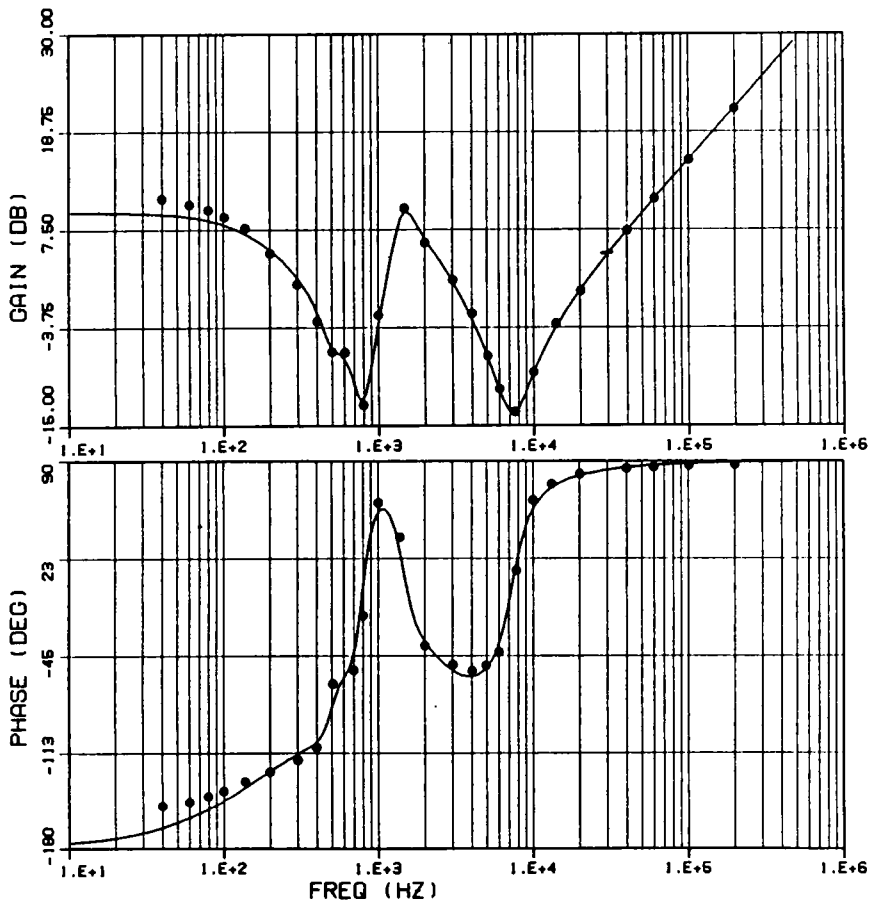


Figure 7.4 Curve fitting for z_{LB}

- : empirical data of the 16th order circuit model
- : curve fitted 4th order analytical model

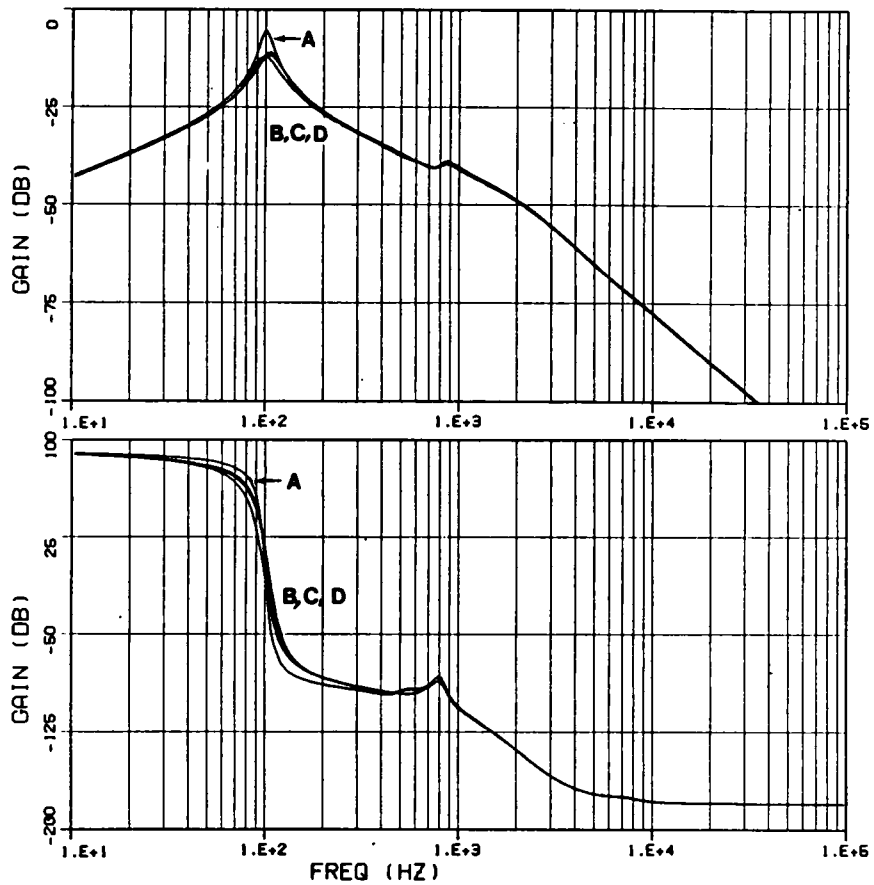


Figure 7.5 Frequency response from v_s to v_B

- A : The exact system model
- B : with empirical data model for z_{j1} in Fig.7.1
- C : with reduced order model for z_{LB} in Fig.7.3
- D : with two-port fitted model for the cable in Fig.7.7

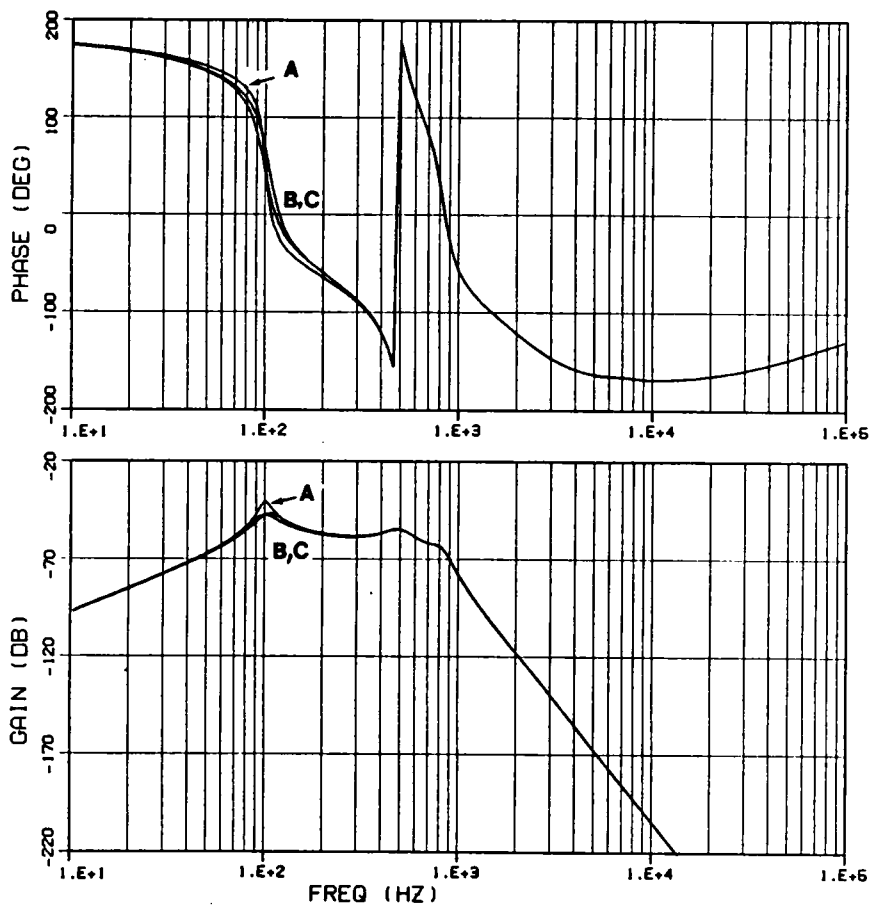


Figure 7.6 Frequency response from v_s to v_{o2}

A : The exact system model

B : with empirical data model for z_{i1} in Fig.7.1

D : with two-port fitted model for the cable in Fig.7.7

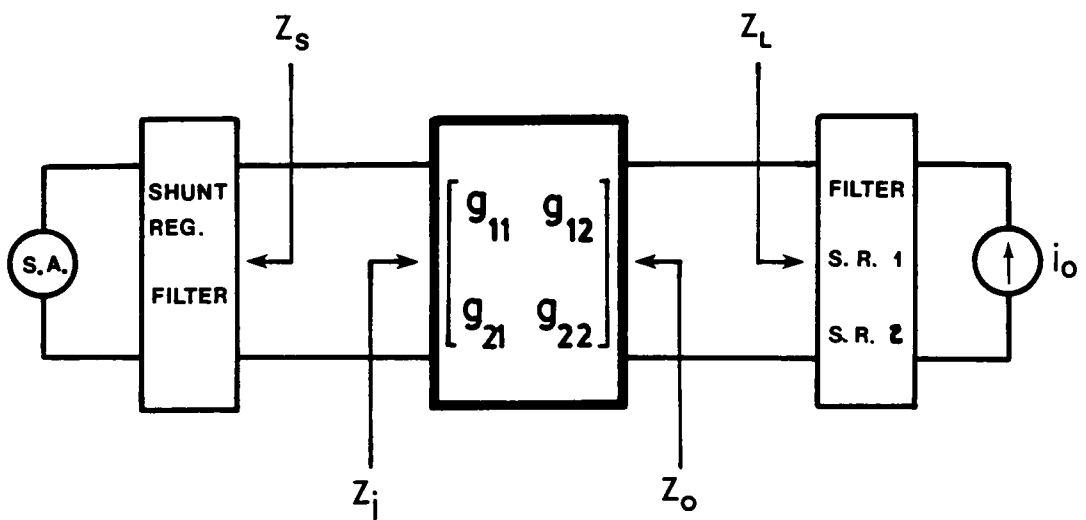


Figure 7.7 Empirical data modeling of two-port component (Cable)

The G-matrix model fully characterizes the two-port black box. The model is connected to the system according to the interconnection law which results in a doubly terminated two-port system.

As shown in Fig.7.7, the two-port cable/filter model in the DET system is modeled using empirical data. The frequency responses in Figs.7.5 and 7.6 using these fitted models again match very well with the exact model.

7.5 Conclusions

In this chapter, empirical data modeling and analysis of power systems is discussed. The analytical transfer function model of a one-port and two-port black box can be developed based on the measured frequency response of port characteristics.

A reduced order modeling technique is also implemented using the complex curve fitting method. This technique allows the elimination of non-dominating poles and zeros as well as poles and zeros in close proximity, which can be ignored for all practical purposes. The reduced order model greatly facilitates analysis and trouble-shooting of complex spacecraft power system and with much improved computational efficiency.

Chapter 8

CONCLUSIONS

The principal objective of this dissertation is the development of a comprehensive large scale power system modeling that facilitates the design and analysis of present and future spacecraft power systems. Methodologies of model building and analysis of large scale power system are presented.

The principal goal is accomplished by utilizing the modular concept such that the total system is configured by interconnecting individual component modules. The hybrid g -parameter two-port subsystem coupling method is employed as the standard interconnection law. This provides bi-directional information flow among components so that the maximum flexibility can be achieved in system model building. The flexibility in model building gives more than convenience but the success of the system level modeling depends largely on it due to the difficulties involved in large scale system modeling as stated in Chapter 1. The modular approach also allows the system performance parameters to be expressed as both analytically and empirically measurable quantities. Thus, the model can be verified at various component levels. Also, since the system performance parameters such as stability, audiosusceptibility, impedance characteristics, etc. are derived with a focus on either local or global behavior, the model allows one to predict and trouble-shoot the undesired interactions that often occur among various subsystems.

The success of the system level model building reduces the uncertainties in the model to the component level which implies the accuracy of the system model depends now on that of component models. As stated in Chapter 1, behaviors of individual components of spacecraft power system are mostly well understood and new and more powerful techniques for component level modeling are currently under development. In this study, existing component modeling techniques are adapted to develop untermi-nated component models (two-input, two-output). Each model consists of large signal time-domain model and small signal model. The large signal model simulates the actual behavior of the system including various non-linearities in the practical circuit. The small signal model characterizes dynamics of the system about a steady state operating condition. The integration of the component models is done by the host software system, EASY5, which was identified from the preliminary study of applicable existing software systems, as the most suitable general purpose program. It provides maximum flexibility in component model building such that each component can be modeled with state differential or difference equations, transfer functions, empirical data and combination of these.

The comprehensive analysis of a spacecraft power system, the secondary goal of the study, is performed using the Direct Energy Transfer (DET) power system as an example. Also, the large signal behavior of the general solar array spacecraft power system is analyzed.

The nonlinear solar array output characteristic coupled with a constant power load is shown to result in multiple equilibrium points. The state plane analysis shows that the equilibrium points in the region where

the solar array behaves as a constant current source are unstable. There exists a separatrix in this region which separates the state plane trajectories into two distinct patterns that ultimately lead to two quite discernible operating points. However, only one, corresponding to the solar array constant voltage source region, is the desirable system operating point. Thus, the system's initial start-up condition must be set in such a way that it leads to the desired operating point.

The behavior of the operating point near the solar array maximum power point is analyzed. Regarding stability and transient response, system operation in this region is less commendable than in the region where the solar array behaves as a stiffer voltage source. One way of improving dynamics of a system that utilizes the solar array maximum available power is to dissipate some power in a shunt circuit. This makes the source characteristic a stiff voltage source.

Since the battery discharger in the DET system operates over a range from a no-load condition through the discontinuous conduction mode (DCM), to the continuous conduction mode (CCM), the feedback compensator of the battery discharger regulator must be designed carefully to ensure the stability for all conditions. A proportional gain control is most appropriate for a converter operating in DCM to speed up the system response. In CCM, a phase lead type compensator is required to compensate the excessive phase lag from the complex pole. Thus, integrator is not suitable for a compensator in the battery discharger regulator because of the power stage open loop pole near the origin.

One of the major difficulties in large scale power system analysis is the subsystem interaction problem. The unterminated two-port component modeling allows one to investigate the interactions at the component, subsystem and system level viewed from either the source or the load.

For a spacecraft power system that uses many switching regulators connected in parallel on a common bus, such as in the DET system, interactions should be examined both at the local level and the system level. Analysis shows that as the number of regulators increases, the margin of stability of the system tends to diminish due to the parallel interactions.

As stated in Chapter 7, due to the lack of an assessment of certain parameter values in some spacecraft component models, experimentation is the only effective means of obtaining models with acceptable accuracy. Employing the complex curve fitting technique, the analytical transfer function model of a one-port and two-port black box is developed based on the measured frequency response of the port characteristics. If the fitted model reasonably matches (over the frequency range of interest) the frequency response of the black box, the two subsystems are expected to be dynamically equivalent. This is verified by performing a system level analysis using both the fitted model and the actual model for a subsystem.

A reduced order modeling technique is also implemented using the complex curve fitting method. This technique enables one to eliminate non-dominating poles and zeros as well as poles and zeros in close proximity which can be ignored for all practical purposes. The reduced order model

greatly facilitates the analysis and trouble-shooting of a complex spacecraft power system with much improved computational efficiency.

REFERENCES

1. J. Cassineli, et al., "Analytical Modeling of Spacecraft Power System," Final Report to NASA GSFC, 1982, TRW, Redondo Beach, CA
2. D. L. Sommer, I. S. Mehdi, "High Voltage DC Electrical System Stability Analysis," Final Report to Naval Air Development Center, Oct. 1979, Boeing Military Airplane Company, Seattle, WA
3. R. D. Middlebrook, S. Cuk, "A Generalized Unified Approach to Modeling Switching Converter Power Stages," IEEE PESC 1976
4. D. J. Short, "An Improved Switching Regulator Power Stage Model using Discrete-Average Techniques," Ph.D. Dissertation, 1982, VPI&SU
5. D. J. Packard, "Discrete Modeling and Analysis of Switching Regulators," Ph.D. Thesis, CIT Pasadena Calif., May 1976
6. B. H. Cho, F. C. Lee, "Feasibility Study of Analytical Modeling of Spacecraft Power System," Final Report to Naval Research Lab., Aug. 1983, VPI&SU
7. J. M. Voss, J. G. Gray, "A Regulated Solar Array Model - A Tool For Power System Analysis," IEEE PCSC 1970 pp.12-17
8. A. Capel, et al., "Power System Simulation of Low Orbit Spacecraft: The EBLOS Computer Program," IEEE PESC 1982, pp.272-285
9. R. D. Middlebrook, S. G. Kimble, "Design of the HEAO Main Bus Shunt Regulator," IEEE Trans. AES, March 1976
10. W. Burns, III, et al., "A Digital Simulation and Study of Direct Energy Transfer Power Conditioning System," IEEE PESC, 1975 pp.138-149
11. D. K. Decker, "Methods for Utilizing Maximum Power from a Solar Array," JPL Quarterly Technical Review, April 1972, pp.37-47
12. E. N. Costogue, S. Lindena, "Comparison of Candidate Solar Array Maximum Power Utilization Approaches," IECEC 1976, pp.1449-1456
13. R. D. Middlebrook, "Input Filter Considerations in Design and application of Switching Regulator," IEEE IAS 1976
14. E. C. Levy, "Complex-Curve Fitting," IRE Trans. Auto. Cont., May 1959
15. C. Sanathanan, J. Koerner, "Transfer Function Synthesis as a Ratio of Two Polynomials," IEEE Tran. on Auto. Cont., Jan. 1963, pp.56-58

16. R. Seidal, "Transfer Function Parameter Estimation from Frequency Response Data," NASA tm x3286, 1975
17. R. Fletcher, C. M. Reeves, "Function Minimization by Conjugate Gradients," Computer J., vol. 7, No. 2, July 1964, pp.149-154
18. D. Zerbel, D. Decker, "AC impedance of Silicon Solar Cells," IECEC, 1970
19. P. Chen, et al., "Experimental Determination of Series Resistance of p-n Junction Diodes in Solar Cells," IEEE Tran. on Elec. Devices, vol.ED-25, march 1978, pp.386-388
20. C. Fang, J. Hanser, "Comparison of Theoretical and Experimental Solar Cell Performance," IECEC 1978
21. P. Chetty, et al., "Microprocessor Controlled Digital Shunt Regulator," IEEE Tran. on AES, March 1980, pp.191-200
22. F. C. Lee, et al., "Generalized computer Aided Discrete Time Domain Modeling Analysis of DC-DC Converters," IEEE PESC 1977
23. F. C. Lee, et al., "Modeling of Switching Regulator Power Stage with or without Zero-Inductor-Current Dwell Time," IEEE IECI Trans. Aug. 1979
24. W. M. Rintala, et al., "Intelsat V Power Control Electronics System," IECEC 1979, pp.1349-1349
25. E. Doebelin, System Modeling and Response, John Wiley & Sons, 1980

**The vita has been removed from
the scanned document**