

Modeling of Some Coplanar Waveguide Discontinuities

(NASA-TM-1008(8) MODELING OF SCBE COPLANAR
WAVEGUIDE DISCONTINUITIES (NASA) 10 p
CSCL 20N

N88-17880

Unclassified
G3/32 0124550

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Prepared for the
IEEE MTT-S International
Microwave Symposium
New York, New York, May 25-27, 1988



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MODELING OF SOME COPLANAR WAVEGUIDE DISCONTINUITIES

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ABSTRACT

The paper presents lumped equivalent circuit models for several coplanar waveguide discontinuities such as an open circuit, a series gap, and a symmetric step, and their element values as a function of the discontinuity physical dimensions. The model element values are de-embedded from measured S-parameters. The frequency dependence of the effective dielectric constant was measured and compared to computed values.

INTRODUCTION

A coplanar waveguide (CPW) on a dielectric substrate (1) consists of a center strip conductor with semi-infinite ground planes on either sides (Fig. 1). Coplanar waveguide offers several advantages over conventional microstrip line: it facilitates easy shunt as well as series mounting of active and passive devices, it eliminates the need for wrap-around and via-holes, and it has a low radiation loss. These, as well as several other advantages, make CPW ideally suited for microwave integrated circuit applications (2,3). However, very little information is available in the literature on discontinuity models for CPW (4,5). This lack of sufficient discontinuity models for CPW has limited the extent of applications for CPW in microwave circuit design.

This paper presents for the very first time lumped element equivalent circuit models for various discontinuities, together with their element values as a function of the discontinuity physical dimensions. These element values are de-embedded from measured scattering parameters of the discontinuities. The discontinuities characterized in this paper are a series gap in the center conductor, a symmetric step in the center conductor, and an open circuit for two different center conductor widths.

The characteristic impedance for both geometries was 50Ω . The frequency dependence of the effective dielectric constant, ϵ_{eff} , was also measured and is compared to computed values from the literature.

DE-EMBEDDING DISCONTINUITY SCATTERING PARAMETERS

Precision CPW calibration standards such as open circuits, short circuits, and 50Ω matched loads for calibrating an automatic network analyzer (ANA) are currently not available. Consequently, a two-tier de-embedding technique which has been used successfully to characterize active devices such as GaAs MESFET's (6,7) was used to characterize the CPW discontinuities. The two-tier de-embedding technique consists of calibrating the ANA using precision coaxial standards and then using the calibrated ANA to characterize the test fixture. A CPW circuit whose characteristics are known is placed in the test fixture during the fixture characterization. The CPW circuit consists of a uniform section of a 50Ω CPW line terminated at both ends by a linearly tapered CPW line which provides impedance matching to a pair of coaxial connectors. Thus, the CPW linear tapers and the coaxial connectors form a part of the test fixture and are characterized initially. The characterization is done by measuring the four s-parameters on an HP8510 ANA. The test fixture with a CPW circuit on 0.125 in. RT-5880 Duroid is shown in Fig. 2.

A mixed lumped-distributed equivalent circuit topology to model the fixture was developed by taking into consideration the physical nature of the test fixture and the CPW circuit. The coaxial connectors were represented by a section of a lossy transmission line consisting of lumped elements in series with a small section of a coaxial line. The tapered CPW section was modeled as a multiple section stepped impedance transformer. EEsof Touchstone's (8) model of CPW as a lossless transmission line whose propagation characteristics are described by Ghione and Naldi (9) was used throughout the modeling experiment.

The elements of the equivalent circuit were then optimized to match the measured S parameters using the EEsof Touchstone circuit analysis and optimization programs. The circuit model is shown in Fig. 3. Figure 4 shows the measured and modeled S11 and S21 characteristics for the test fixture and thru line. This model was then used to de-embed the discontinuity characteristics from the fixture characteristics. The measured return loss with the 50Ω CPW circuit in place is better than 16 db over the frequency range 0.045 to 18 GHz. However, for

the purpose of de-embedding the discontinuity equivalent circuit parameters, a 1 GHz band centered at 6 GHz was chosen.

EFFECTIVE DIELECTRIC CONSTANT

The ϵ_{eff} is determined from the physical length of a pair of series-gap coupled straight resonators which are terminated in a short circuit and the measured resonant frequencies as described in (10). Figures 5(a) and (b) show ϵ_{eff} for 50 Ω CPW with two different strip widths over the frequency range of 3 to 18 GHz. Also shown in Figs. 5(a) and (b) is ϵ_{eff} computed using the expressions in (1), (9), and (11). Good agreement is observed between the measured and computed results above 6 GHz. The ϵ_{eff} does not appear to be a strong function of the center strip width for CPW lines of the same impedance.

CPW OPEN CIRCUIT

A CPW open circuit is formed by ending the center strip a short distance before the slot ends, thereby creating a gap, g_1 , as shown in Fig. 6. An electric field exists at the open circuit between the terminated center strip and the surrounding ground conductor and hence gives rise to a capacitive reactance. This reactance is seen at a plane coincident with the open end of the center strip. Thus the apparent position of the open circuit is beyond the physical end of the center strip. The open circuit capacitance, C_{oc} , is a parallel combination of the capacitance due to the fringing fields across the gap, g_1 , and those across the slot, W . The gap dependent capacitance varies proportionally as $1/g_1$. The slot dependent capacitance is constant. Figure 7 presents the de-embedded open circuit capacitance, C_{oc} , as obtained from the measured S-parameters.

SERIES GAP IN THE CENTER CONDUCTOR

A series gap of length g in the center conductor of a CPW is shown in Fig. 6. The gap is modeled as a lumped Pi-network consisting of a coupling capacitance, C_2 , and two fringing capacitances, C_1 . Analogous to the open circuit, the capacitance across the gap, C_2 , decreases proportionally as $1/g$. The fringing capacitance, C_1 , increases from the CPW line capacitance for $g = 0$ to the open circuit saturation capacitance for large g . Figure 8 shows the de-embedded capacitances obtained from the measured S-parameters as a function of the gap width g .

STEP CHANGE IN THE WIDTH OF CENTER CONDUCTOR

A step change in width of the center conductor of CPW is shown in Fig. 6. The step discontinuity perturbs the normal CPW electric and magnetic fields which give rise to additional reactances. These additional reactances are assumed to be lumped and located in the plane of the step discontinuity. The modeling experiments show that the reactances can be modeled as a shunt capacitance C_s . The influence of this capacitance is to effectively lengthen the lower impedance CPW line towards the higher impedance CPW line. Figure 9 shows C_s as

a function of the normalized step width. As the normalized step change, S_1/S_2 , increases, C_s approaches the open circuit saturation capacitance.

DISCUSSIONS

Although the results presented in this paper appear to be self consistent, a mention of the accuracy of the data is required. The two port circuit element models are more accurate than the one port circuit element models. This is because the two port models are based on all four S-parameters versus a single S-parameter for the one port models. Furthermore, the open circuit model is a function of the phase of S_{11} only. Therefore, the open circuit de-embedded capacitance is sensitive to phase errors. To minimize the phase errors, several precautions were taken. All measurements were made in the frequency range at which the measured propagation constant was close to that used by Touchstone. In addition, the line length from the open circuit to the transition was kept short to minimize the difference in electrical length between the measured and that used by Touchstone.

To take into account the errors intrinsic in printed circuit board fabrication, all line lengths were measured to a tenth of a mil. Even with these precautions, the error is believed to be 5 percent for the open circuit data. Further testing of a larger sample of circuits should yield more complete and accurate results.

CONCLUSIONS

The paper presents for the very first time lumped equivalent circuit models for the following CPW discontinuities: an open circuit, a series gap in the center conductor and, a symmetric step in the center conductor. The model element values are de-embedded from the measured S-parameters and presented as a function of the discontinuity physical dimensions. The measured frequency dependence of ϵ_{eff} is also compared to computed values and found to be in good agreement above 6 GHz.

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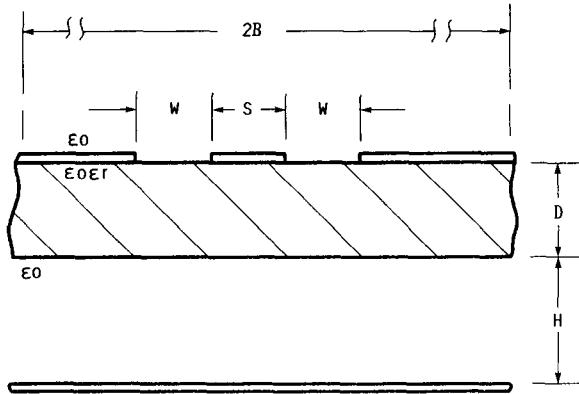


FIGURE 1. - CROSS-SECTION OF THE COPLANAR WAVEGUIDE IN THE TEST FIXTURE.

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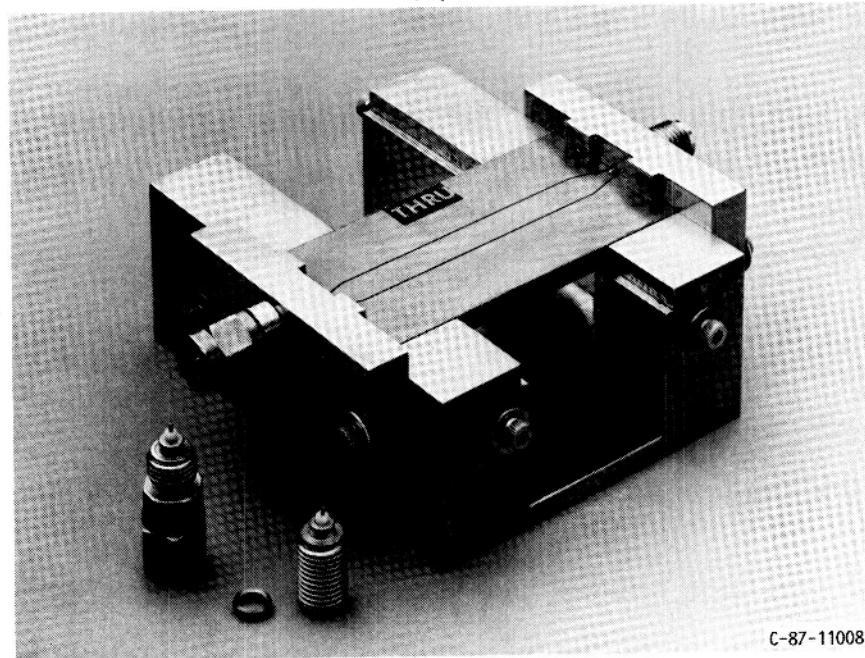


FIGURE 2. - TRANSITION BETWEEN A 50-OHM CPW AND COAXIAL CONNECTORS USING A LINEAR TAPER TRANSFORMER.

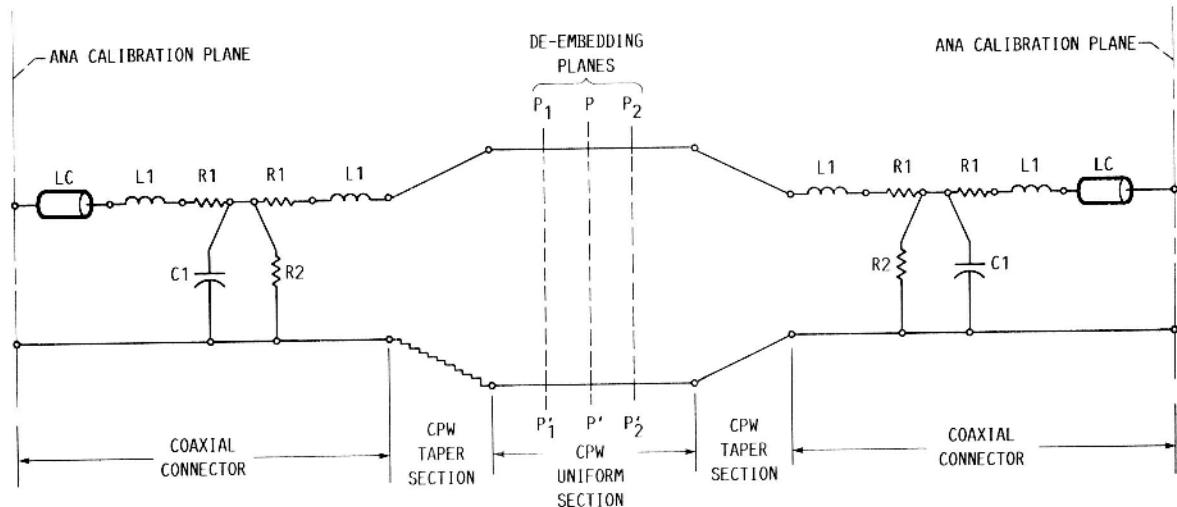


FIGURE 3. - A LUMPED ELEMENT EQUIVALENT CIRCUIT MODEL OF THE TRANSITION BETWEEN THE 50-OHM CPW AND COAXIAL CONNECTOR.

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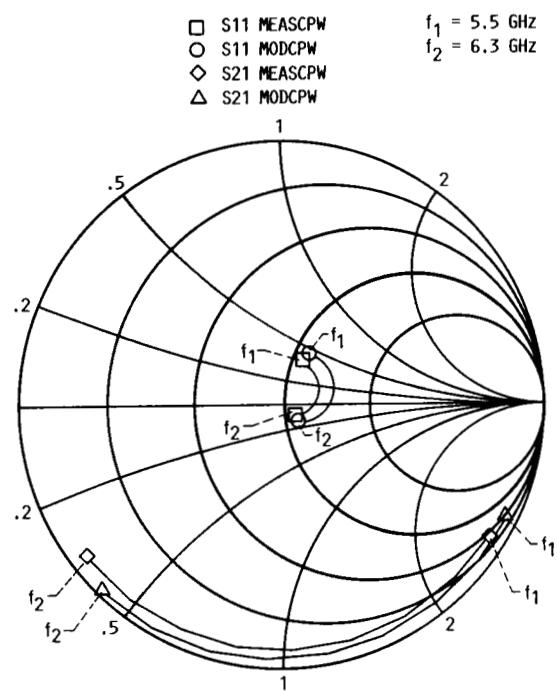


FIGURE 4. - MEASURED (MEASCPW) AND MODELED (MODCPW)
S-PARAMETERS OF THE TRANSITION BETWEEN CPW AND
COAXIAL CONNECTOR.

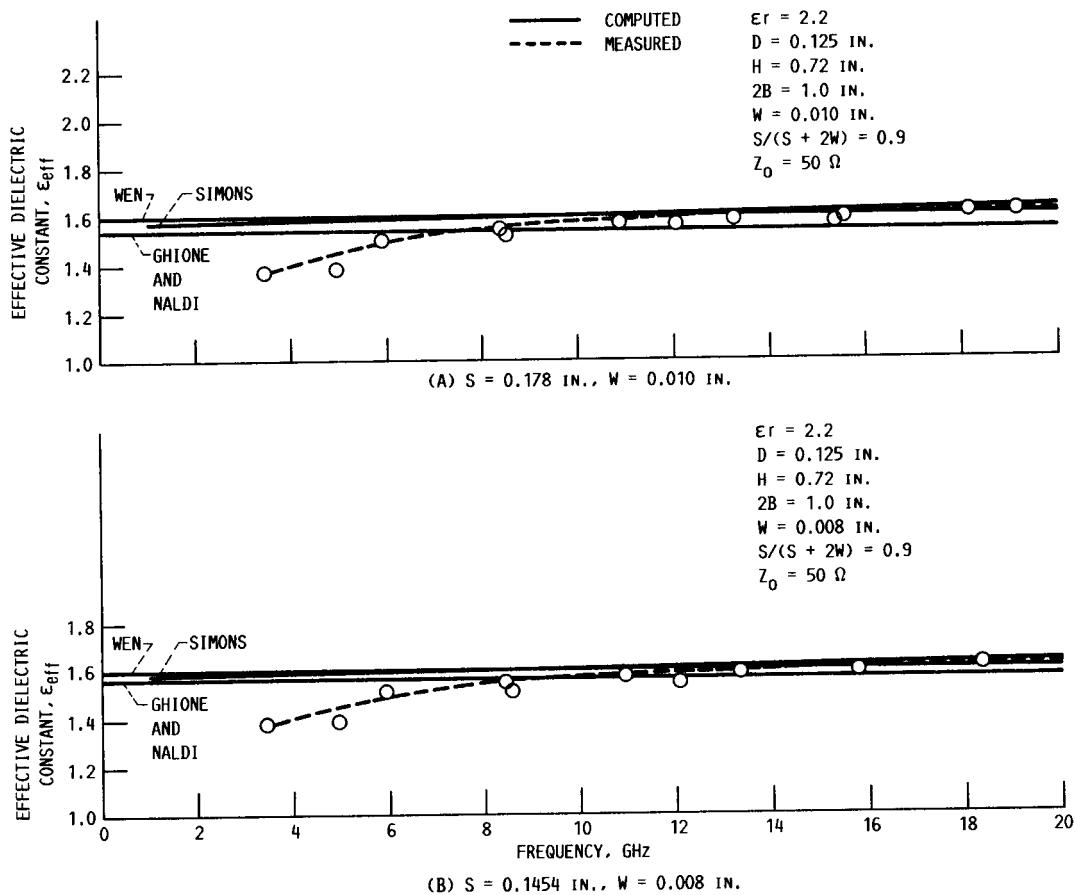
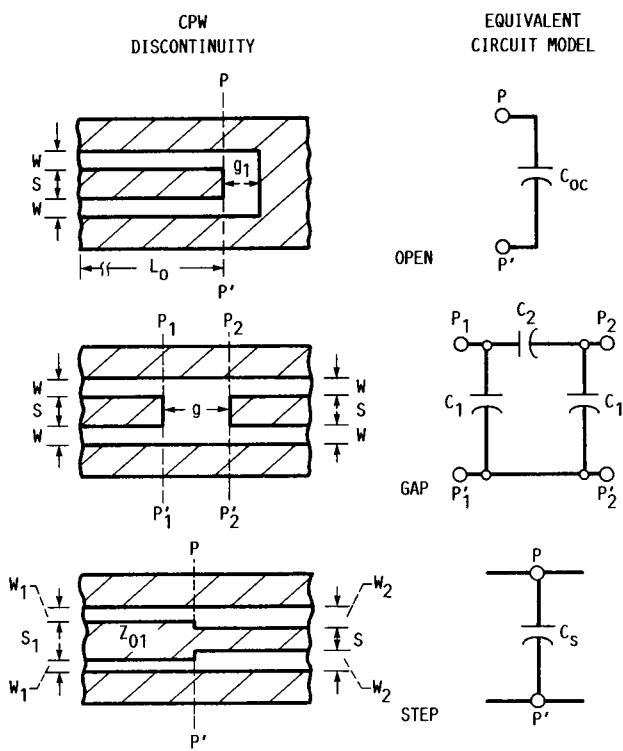


FIGURE 5. - MEASURED AND COMPUTED EFFECTIVE DIELECTRIC CONSTANT OF CPW AS A FUNCTION OF FREQUENCY.

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FIGURE 6. - COPLANAR WAVEGUIDE DISCONTINUITIES AND THEIR LUMPED ELEMENT EQUIVALENT CIRCUIT MODEL.

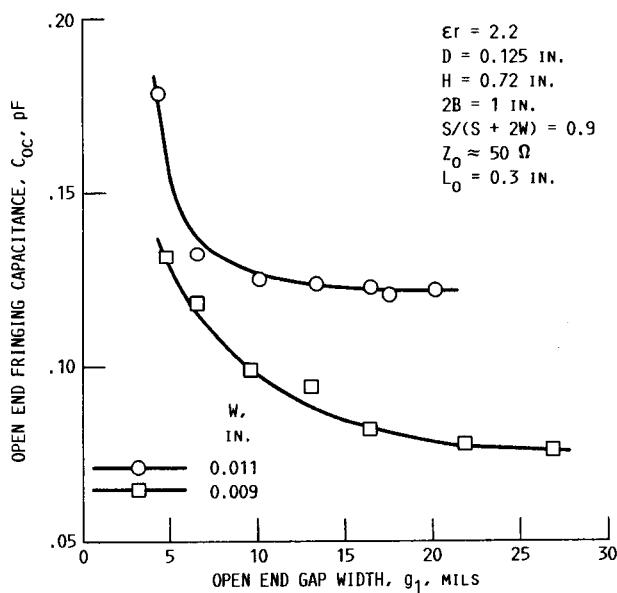


FIGURE 7. - DE-EMBEDDED LUMPED FRINGING CAPACITANCE FROM MEASURED SCATTERING PARAMETERS AS A FUNCTION OF THE OPEN END GAP WIDTH.

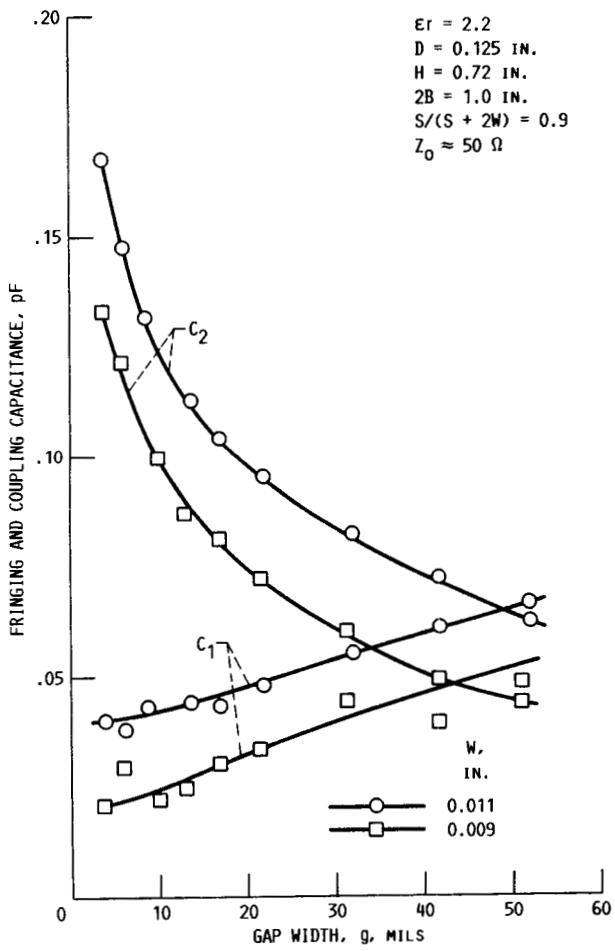


FIGURE 8. - DE-EMBEDDED LUMPED FRINGING AND COUPLING CAPACITANCE FROM MEASURED SCATTERING PARAMETERS AS A FUNCTION OF GAP WIDTH.

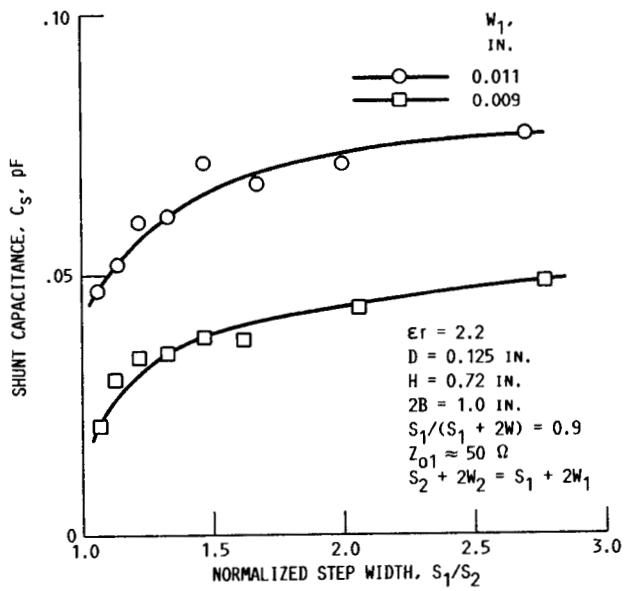


FIGURE 9. - DE-EMBEDDED LUMPED SHUNT CAPACITANCE FROM MEASURED SCATTERING PARAMETERS AS A FUNCTION OF THE NORMALIZED STEP WIDTH.



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Space Administration

Report Documentation Page

1. Report No. NASA TM-100808	2. Government Accession No.	3. Recipient's Catalog No.	
4. Title and Subtitle Modeling of Some Coplanar Waveguide Discontinuities		5. Report Date	
		6. Performing Organization Code	
7. Author(s) Rainee N. Simons and George E. Ponchak		8. Performing Organization Report No. E-3981	
		10. Work Unit No. 506-44-2C	
9. Performing Organization Name and Address National Aeronautics and Space Administration Lewis Research Center Cleveland, Ohio 44135-3191		11. Contract or Grant No.	
		13. Type of Report and Period Covered Technical Memorandum	
12. Sponsoring Agency Name and Address National Aeronautics and Space Administration Washington, D.C. 20546-0001		14. Sponsoring Agency Code	
15. Supplementary Notes Prepared for the IEEE MTT-S International Microwave Symposium, New York, New York, May 25-27, 1988.			
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17. Key Words (Suggested by Author(s)) Coplanar waveguide; Discontinuities; Microwave/millimeter wave transmission lines		18. Distribution Statement Unclassified - Unlimited Subject Category 32	
19. Security Classif. (of this report) Unclassified	20. Security Classif. (of this page) Unclassified	21. No of pages 10	22. Price* A02