# Monolithic thin film lithium niobate electro-optic modulator with over 110 GHz bandwidth

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High-performance thin film lithium niobate (LN) electro-optic modulators with low cost are in demand. Based on photolithography and wet etching, we experimentally demonstrate a thin film LN Mach–Zehnder modulator with a 3 dB bandwidth exceeding 110 GHz, which shows the potential of boosting the throughput and reducing cost. The fabricated modulator also exhibits a comparable low half-wave voltage-length product of  $\sim$ 2.37 V  $\cdot$  cm, a high extinction ratio of >23 dB, and the propagation loss of optical waveguides of  $\sim$ 0.2 dB/cm. Besides, six-level pulse amplitude modulation up to 250 Gb/s is successfully achieved.

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## 1. Introduction

Exponentially increasing global network traffic poses severe challenges to the bandwidth of optical transceivers. Electro-optic modulators (EOMs) with a large bandwidth, low-power consumption, a small footprint, and the possibility of large-scale manufacturing are in demand. In the past few years, tremendous efforts have been made towards a variety of platforms such as silicon (Si)<sup>[1,2]</sup>, germanium-Si (GeSi)<sup>[3]</sup>, indium phosphide (InP)<sup>[4]</sup>, polymers<sup>[5]</sup>, plasmonics<sup>[6]</sup>, and thin film lithium niobate (TFLN)<sup>[7-12]</sup>. Among them, monolithic TFLN EOMs, with etched lithium niobate (LN) waveguides, have attracted more and more attention since they retain excellent material and electro-optic (EO) properties while improving the ability of integration. Several monolithically integrated TFLN Mach-Zehnder modulators (MZMs)<sup>[13-16]</sup> demonstrated high bandwidth and low half-wave voltage-length product  $(V_{\pi}L)$ , which met the requirements for future photonic systems. In those high-performance devices, electron-beam lithography (EBL) with high-exposure resolution served a crucial function of defining high-quality waveguides or electrodes. However, the devices are still on the chip scale, need more exposure time, and are expensive for large-scale manufacturing. For application scenarios like data-center interconnects, EOMs with low cost are necessary. Therefore, photolithography is expected to be applied in the fabrication of monolithic TFLN EOMs due to its highefficiency exposure on the wafer scale. Previously, photolithography was utilized cooperating with dry etching, but the difficulty of obtaining smooth etching<sup>[17,18]</sup> via dry etching

limited the performance of the manufactured devices. A twostep masking technique<sup>[17]</sup> and wet etching<sup>[18]</sup> were proposed to address this problem. Recently, photolithography-exposed EOMs<sup>[19–21]</sup> were demonstrated on the wafer scale. Although a low  $V_{\pi}L^{[21]}$  was reported, further improvement of bandwidth is also a primary requirement for many applications. Figure 1 shows the comparison of the TFLN EOMs fabricated by EBL and photolithography.

In this paper, we present a TFLN EOM over 110 GHz fabricated by wafer-scale ultraviolet (UV) photolithography and wet etching. Wet etching eliminates the influence of micro-masks and redeposition in dry etching methods, thereby obtaining high-quality waveguides and electrodes. Meanwhile, the half-wave  $V_{\pi}L$  is 2.37 V·cm, the extinction ratio is >23 dB, and the propagation loss of optical waveguides is ~0.2 dB/cm, which indicate the overall superior performance. Based on our device, we also achieve modulation rates up to 250 Gb/s of six-level pulse amplitude modulation (PAM-6) signals and 200 Gb/s of PAM-4 signals. Our work demonstrates the feasibility of large-scale and low-cost manufacturing of LN EOMs and serves as an important step for making LN EOMs widely used in future cost-sensitive scenarios such as telecommunications and microwave photonics.

## 2. Device Design and Fabrication

The microscope image of the broadband LN modulator is shown in Fig. 2. The devices are fabricated on a commercial 4 in. (1 in. = 2.54 cm) *x*-cut TFLN wafer (from NANOLN) with a 600 nm



Fig. 1. Comparison of monolithic TFLN EOMs fabricated by EBL and photolithography.

thick LN layer, a 4.7 µm thick thermal oxidation SiO<sub>2</sub> layer, and a 525 µm thick Si substrate. The modulator is working at the Mach-Zehnder interferometer configuration that consists of two 5 mm long modulation arms connected by  $1 \times 2$  multimode interference (MMI) couplers to achieve beam splitting and beam combining. In order to realize efficient EO interaction, the electrodes are placed beside the TE mode ridge waveguides to ensure that the modal polarization and the electric field are both parallel to the direction of the largest EO coefficient  $\gamma_{33}$  (z axis) in LN. In this configuration, the modulation signals are applied to the central electrode, while the outer electrodes are grounded. The modulation electric fields appearing in the two arms point in opposite directions, resulting in a push-pull operation. The electrodes are constructed by active regions and non-active regions. Active regions are made of coplanar waveguide (CPW) transmission lines, which can carry microwave signals propagating along the same direction as the optical wave. Non-active regions are set up for signal injection and termination. Two groups of ground-signal-ground (GSG) pads are erected and connected with the active regions through electrode tapers.

As is well known, there are three key factors for broadband operation of a traveling-wave modulator: (1) group-velocity matching between the optical wave in the waveguide and the microwave in the transmission line; (2) frequency-dependent attenuation of the microwave modulation signal as it propagates along the transmission line; (3) impedance matching between the microwave transmission line and external electrical connectors. Therefore, the bandwidth performance of the device is closely related to the electrode structure [width of central electrode (*W*), gap between the electrodes (*G*), electrodes thickness (*T*)] and the dielectric layer, which is reflected in parameters of microwave loss ( $\alpha$ ), effective index ( $N_m$ ), and characteristic impedance ( $Z_0$ ). As displayed in Figs. 3(a)–3(c),  $\alpha$  and  $Z_0$  will both decrease with the increase of *W*, whereas  $N_m$  goes up.



**Fig. 3.** Simulated results of (a) the microwave loss ( $\alpha$ ), (b) the effective index ( $N_m$ ) of microwave signals, and (c) the characteristic impedance ( $Z_0$ ) at 100 GHz versus W and G, (d) simulated  $V_{\pi L}$  versus ridge height and G.

With the increase of G,  $Z_0$  increases, and  $N_m$  decreases, respectively. The  $\alpha$  will also decrease when widening G due to the alleviation of electrical current congestion in the gap. Besides, thickening the electrodes will bring down  $N_m$  and  $Z_0$  due to the increasing area of the air capacitor formed between the electrodes. However, it should be noticed that the expanded G will cause the degeneration of the EO efficiency, which should be a design trade-off. Considering reducing  $\alpha$  and the alignment tolerance of UV photolithography, we choose a slightly larger G of 7 µm, while cooperating with shallow etched ridges of 200 nm to maintain a comparable  $V_{\pi}L$  of 2.37 V·cm, as shown in Fig. 3(d). W and T are jointly optimized to be, respectively, 20  $\mu$ m and 830 nm to ensure impedance matching and low  $\alpha$ . Since the waveguide is 1.3  $\mu$ m wide with SiO<sub>2</sub> cladding, the effective optical group index  $(N_{\alpha})$  is ~2.24 for the fundamental TE mode at 1550 nm simulated in Lumerical Mode Solution. Thanks to the low permittivity of the SiO<sub>2</sub> ( $\varepsilon \sim 3.9$ ) cladding and the Si ( $\varepsilon \sim 11.9$ ) substrate, the velocity matching design has an additional degree of freedom by adjusting the thickness of the cladding layer in addition to the electrode's design. The thickness of under-cladding and over-cladding is 4.7 µm and 1 µm, respectively. Although the influence of the active regions dominates, the design of the non-active regions is also important at high frequency. We choose straight electrode tapers (100 µm long) and let the optical waveguides bend avoid extra  $\alpha$  caused by electrodes bending. To maintain the continuity of the impedance of



Fig. 2. (a) Microscope image and (b) cross section of the TFLN traveling-wave Mach-Zehnder EOM.

the electrodes, the gap and width of the tapers are also designed to ensure that  $Z_0$  equals 50  $\Omega$ .

Fabrication processes play an important role in the performance of final devices. Widely used dry etching methods with photoresists usually exhibit the problem of micro-masks, which will be transferred to the etched area causing roughness<sup>[17]</sup>. The roughness of the ridge induces high optical loss, while the roughness of the slab area affects the flatness of the electrodes placed there that induce high  $\alpha$ . To reduce the influence of roughness, we employ thin oxide masks and high-selectivity wet-etching. The TFLN MZM is fabricated as follows. It is worth noting that UV photolithography (by SUSS MA6) is used in all of the patterning. First, 100 nm thick Si oxide (SiO<sub>2</sub>) is deposited on the LN wafer by plasma enhanced chemical vapor deposition (PECVD) as hard-masks in the following LN etching process. Then, the photoresist (AZ6112) patterns are transferred into the SiO<sub>2</sub> layer through fluorine-based inductively coupled plasma dry etching. The power in the dry etching process is optimized at a low level to reduce roughness. After the removal of photoresist and etching residue, the circuits in the LN layer are produced by wet etching. Wet etching is developed in a mixed solution of NH<sub>4</sub>OH, H<sub>2</sub>O<sub>2</sub>, and H<sub>2</sub>O with a ratio of 2:2:1 at 85°C, which was used in the post-processing of LN waveguides<sup>[18]</sup>. The etching rate is  $\sim 4$  nm/min. During wet etching, the thickness of SiO<sub>2</sub> masks is almost constant, which indicates a high selectivity. Finally, the SiO<sub>2</sub> masks are removed with buffered oxide etching (BOE) solution. We have compensated for the width variation of the waveguides patterns in advance because wet etching leads to lateral etching. The original width of the waveguide is slightly larger than 1.3 µm, which has the advantage of reducing photolithography difficulty. CPW electrodes are formed using a standard lift-off process. After a thick negative photoresist is exposed, 50 nm of Ti is first deposited as an adhesive layer, and then 780 nm Au is deposited with an electron-beam evaporator method. Afterwards, 1 µm thick SiO<sub>2</sub> cladding is deposited on top of all of the structures using PECVD. The SiO<sub>2</sub> cladding on the pads is dry etched for probes contacts. The final devices are diced using the cleavage method to ensure smooth coupling faces. Hundreds of devices on a 4 in. wafer can be exposed in dozens of seconds by UV photolithography, yet it will take several days using EBL with a typical condition of 50 kV acceleration voltage and 10 nA beam current.

#### 3. Characterization and Transmission

Figures 4(a) and 4(b) show the scanning electron microscope (SEM) micrographs of the device. The sidewall angle of the waveguide is nearly 90° with respect to the horizonal direction. However, there is a ~20° slope at the bottom of one side, which is probably caused by the crystal orientation selectivity of wet etching. The root-mean-squared (RMS) roughness of the etched area is measured by atomic force microscopy (AFM), as shown in Fig. 4(c). In the slab area, the RMS roughness is about 0.3 nm over a  $3 \,\mu\text{m} \times 3 \,\mu\text{m}$  area, which suggests there is little effect on the  $\alpha$  of the electrodes placed on it. Extracted from the



**Fig. 4.** (a) SEM image of the etched LN waveguides and electrodes before  $SiO_2$  cladding deposition. (b) SEM image of the cross section of the waveguides. (c) AFM measurement of the waveguides. (d) EE S-parameters of the CPW electrodes. (e)  $Z_0$  and  $N_m$  of the electrodes extracted from S-parameters in (d).

cutback method, the propagation loss of the 1.3 µm wide waveguide is about 0.2 dB/cm. The total insertion loss of the device is  $\sim$ 18 dB, which is mainly from the mode mismatch between the lensed fiber and LN waveguide. Bending loss is also non-negligible. Optimizations of the coupling and bending waveguides are expected to produce a smaller total insertion loss. The most important part of the broadband modulator is the quality of the traveling-wave electrodes. In the case where the velocities of the optical wave and the microwave are perfectly matched, the 3 dB EO response can be estimated by the 6.4 dB bandwidth of the electrical-electrical (EE) transmission (S21). Hence, we characterize the CPW electrodes via high-frequency EE Sparameters, including S21 and reflection (S11) responses, as shown in Fig. 4(d). Two 110 GHz GSG probes are used to launch and receive microwave power from the two ports of a Keysight N4372E vector network analyzer (VNA), respectively. The EE S21 curve shows a frequency response up to the measured 110 GHz with a roll off of 4.5 dB. We also extracted the  $Z_0$ and the  $N_m$  from the EE S-parameters in Fig. 4(e). The  $Z_0$  is around 50  $\Omega$  so that the reflection is weak, which can be seen from the S11 parameters maintaining below  $-15 \, dB$  in Fig. 4(d).



**Fig. 5.** EO characterization of the LN modulator: (a)  $V_{\pi}$  test with 5 MHz triangular voltages sweep. (b) EO S21 measurement result from 10 MHz to 110 GHz, showing an ultra-high bandwidth over 110 GHz.

The  $N_m$  is nearly equal to the group index of the optical mode  $(N_o)$  with the difference below 2%, which indicates nearly perfect velocity matching between the microwave and optical wave.

To verify the performance of the modulator, we first measure the  $V_{\pi}$  with a triangular voltages sweep. We input a 5 MHz triangular wave signal into the modulator and the oscilloscope at the same time. The modulated signal is received by a 1 GHz photodiode (PD) to reduce the influence of out-of-band noise and subsequently loaded to the oscilloscope. The signals are shown in Fig. 5(a). The measured  $V_{\pi}L$  at 5 MHz is 4.74 V for our 5 mm long device, which is comparable to the  $V_{\pi}L$  in other referen $ces^{[13,14]}$ . The extinction ratio of our modulator is > 23 dB, which is obtained by DC measurement. Then, we show the broadband performance of our modulator through the smallsignal measurement of EO S21 [Fig. 5(b)]. The signals from the VNA are injected to the MZM with a 110 GHz bias-T and a 110 GHz probe. The CPW electrodes are terminated with an external 50  $\Omega$  load through another 110 GHz probe. Then, the received signals are sent to the VNA through the PD in the lightwave component analyzer (LCA). The EO response of the PD in the LCA is built-in, of which the influence will be automatically eliminated by the VNA during the test. The EO S21 presents a flat response over the frequency range with a roll-off of  $\sim 2.5$  dB from 10 MHz to 110 GHz. Further optimizations of the highfrequency characteristics of the load and the electrodes structures are expected to improve the EO response further.

To evaluate the performance of our device, high-speed digital transmission exceeding 200 Gb/s is performed. The experimental setup is depicted in Fig. 6(a). An external cavity laser at 1549.93 nm is employed as the optical source. The output laser power is 17 dBm. PAM-4/PAM-6 signals from an arbitrary waveform generator (AWG) drive the modulator after being amplified by an electrical amplifier (EA). The modulated signals are amplified to 1 dBm and subsequently detected by a single-ended 70 GHz PD, amplified by an EA, and sampled by a real-time digital storage oscilloscope (DSO) operating at 256 GSa/s. Finally, the signals are processed by the receiver-side digital signal processing, which includes re-sampling, matched root raise cosine filter, synchronization, and Volterra non-linear equalization. Note that the frequency response of the whole system is



Fig. 6. Transmission results. (a) Experimental setup for the transmission system. (b) Measured BER performance versus bit rates of PAM-4 and PAM-6 signals. (c), (d) Optical spectrum of different baud rate PAM-4 and PAM-6 signals. (e)–(h) Typical eye diagrams of 50 Gbaud PAM-6, 100 Gbaud PAM-6, 60 Gbaud PAM-4, and 100 Gbaud PAM-4 signals, respectively.

limited by typical components as 45 GHz AWG and 50 GHz EA instead of our modulator. Figures 6(c) and 6(d) show the measured optical spectrum of Nyquist PAM-6 and PAM-4 signals in the back-to-back scenario, respectively. Figure 6(b) shows the measured bit error rates (BERs) of PAM-6 and PAM-4 signals, respectively, for different data rates. The 250 Gb/s PAM-6 signals and 200 Gb/s PAM-4 signals can be transmitted with BERs of  $2.3 \times 10^{-2}$  and  $3.2 \times 10^{-3}$ , which are both below the 20% soft-decision forward error-correction (SD-FEC) threshold of  $2.4 \times 10^{-2}$ . Figures 6(e)–6(h) show the typical eye diagrams of the received PAM-6 and PAM-4 signals. We believe that our device will achieve better BER performance if there is no additional electrical bandwidth limitation in the transmission system.

## 4. Conclusion

In this work, a TFLN MZM with a 3 dB bandwidth over 110 GHz is experimentally demonstrated. The half-wave  $V_{\pi}L$ is 2.37 V·cm, and the extinction ratio is > 23 dB, supporting 250 Gb/s PAM-6 data transmission. With photolithography and wet etching, high-quality waveguides and traveling-wave electrodes are fabricated, which shows the potential of boosting the throughput and reducing cost. Although the presented performance has already met the requirements of many applications, tremendous efforts should also be made towards further improving the etching quality by combining dry etching and wet etching, reducing the coupling loss by designing the coupler structure, and so on. We believe that our work lays the groundwork for future applications of TFLN EOMs in cost-effective scenarios.

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