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# Multi-Dot Floating-Gates for Nonvolatile Semiconductor Memories - Their Ion Beam Synthesis and Morphology — Source link

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# Multi-dot floating-gates for nonvolatile semiconductor memories: Their ion beam synthesis and morphology

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Scalability and performance of current flash memories can be improved substantially by replacing the floating polycrystalline-silicon gate by a layer of Si dots. Here, we present both experimental and theoretical studies on ion beam synthesis of multi-dot layers consisting of Si nanocrystals (NCs) embedded in the gate oxide. Former studies have suffered from the weak Z contrast between Si and SiO<sub>2</sub> in transmission electron microscopy (TEM). This letter maps Si plasmon losses with a scanning TEM equipped with a parallel electron energy loss spectroscopy system. Kinetic Monte Carlo simulations of Si phase separation have been performed and compared with Si plasmon maps. Predicted and measured Si morphologies agree remarkably well, both change with increasing ion fluence from isolated NCs to spinodal pattern. However, the predicted fluences are lower than the experimental ones. We identify as the main reason of this discrepancy the partial oxidation of implanted Si by atmospheric humidity, which penetrates into the as-implanted SiO<sub>2</sub>. © 2004 American Institute of Physics. [DOI: 10.1063/1.1794856]

Metal-oxide-silicon field-effect transistors with an electrically isolated ("floating") gate layer embedded in the gate oxide are currently used as flash memories. The replacement of this floating-gate by a layer of discrete Si nanocrystals (NCs)<sup>1</sup> improves the performance of flash memories substantially.<sup>2</sup> The reduced probability for a complete discharging of the multi-dot floating-gate by oxide defects allows thinner tunnel oxides. In turn, the floating-gate will be charged/discharged by quantum mechanical direct electron tunneling (instead of defect-generating Fowler-Nordheim tunneling). The memory operation voltage can be reduced and scalability is improved. Using ion beam synthesis, the multi-dot floating-gate can be fabricated along with standard (CMOS) processing.<sup>3</sup> Si<sup>+</sup> ions are implanted at ultralow energies into the gate oxide, causing there a high Si supersaturation. During postimplantation annealing, this Si supersaturation leads to phase separation of elemental Si from SiO<sub>2</sub>.<sup>4</sup> Imaging this phase separation process is difficult. Transmission electron microscopy (TEM) has historically suffered from weak Z contrast between Si and SiO<sub>2</sub> phases. Recently, this problem was partially overcome by Fresnel imaging using under-focused bright field conditions.<sup>5</sup> Thus, the distance of the layer of phase separated Si from the transistor channel could be determined.<sup>6,7</sup> However, this technique fails to resolve the morphology of the phase separated Si. For instance, recent kinetic Monte Carlo (KMC) simulations of phase separation predict a pronounced fluence dependence of the precipitate morphology.<sup>4</sup> For low Si<sup>+</sup> fluences, spherical and isolated Si NCs form by nucleation and growth, while for higher Si<sup>+</sup> fluences spinodal decomposition occurs. The elongated, non-spherical Si structures, formed by spinodal decomposition, coalesce at even higher fluences to an interconnected, labyrinthine Si network. This letter uses a scanning TEM (STEM) with an efficient parallel electron energy loss spectroscopy (PEELS) system, to confirm the predictions of the KMC simulations. The contrast problem of conventional TEM could be overcome by mapping Si plasmon losses, which differ from SiO<sub>2</sub>. Comprehensive KMC studies and PEELS-STEM analysis has been performed to understand the complex process of phase separation in a thin buried layer.

To form Si NCs by phase separation, Si<sup>+</sup> ions have been implanted at 1 keV energy into 10-nm-thick SiO<sub>2</sub> layers, which were thermally grown on (001) Si substrates. Using an AXCELIS GSD-ULTRA ultralow-energy implanter, fluences of  $5 \times 10^{15}$ ,  $1 \times 10^{16}$ , and  $2 \times 10^{16}$  cm<sup>-2</sup> were implanted at room temperature. Surface charging due to implantation was compensated by a Xe plasma electron flood gun. The implanted samples were cleaned using a piranha solution and furnace annealed for 30 min in  $N_2$  at 950 °C. From all samples, cross sectional and plane view TEM specimens were prepared by the standard procedure of grinding, dimpling, and Ar<sup>+</sup> ion beam thinning. PEELS-STEM was performed on plane view samples using a scanning TEM VG-HB 501 operating at 100 kV that is equipped with a field emission cathode and a parallel Gatan 666 EELS spectrometer. Low-loss EELS spectra were recorded at each picture point, hence a spectrum image was acquired.<sup>8</sup> These spectra were fitted in the energy range of 15-30 eV by a weighted sum of two plasmon reference spectra, which were obtained from bulk Si and SiO<sub>2</sub>. Potential size effects like shifts of the Si plasmon resonance of small Si NCs were not compen-

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sated. The gray level of the PEELS-STEM image pixels is given by the ratio of the intensity of the Si plasmon peak and the total intensity of the Si and SiO<sub>2</sub> plasmon peaks and, therefore, maps the Si bulk plasmons. Quantitative Si concentrations, however, cannot be given, only the relative Si content is imaged. In Fig. 1, such Si plasmon maps (on the left-hand side) are compared to plane view snapshots of KMC simulations (on the right-hand side). This atomistic approach to phase separation of excess Si in thin SiO<sub>2</sub> layers by atomistic simulations was recently described in detail in Refs. 4 and 9. The depth profiles of excess Si due to high fluence Si<sup>+</sup> ion implantation into thin gate oxides have been calculated by the binary collision program TRIDYN<sup>10</sup> accounting for the effects of ion erosion, target swelling, and ion beam mixing dynamically. The postimplantation phase separation during thermal treatment is described by a kinetic three-dimensional lattice Monte Carlo program package.<sup>11,12</sup> Taking the TRIDYN profiles of Si excess as well as Si solubility and diffusivity in SiO<sub>2</sub> as input, the program describes excess Si diffusion, precipitation, and Ostwald ripening in the thin  $SiO_2$  layer under the constraints of the boundary conditions of a nearby Si/SiO<sub>2</sub> interface and a free SiO<sub>2</sub> surface. Here, we use a simplified version of the KMC program, i.e., only Ising-type nearest-neighbor interactions. It should be noted that KMC simulations with measured Si self-diffusivities<sup>13,14</sup> lead to too long annealing times or too high temperatures. Obviously, the diffusive Si mass transport by a mobile SiO<sub>2</sub> defect with local Si excess (that could either be named Si interstitial, SiO molecule,<sup>13</sup> or oxygen vacancy<sup>15</sup>) does not necessarily follow the same mechanism as the  ${}^{28}\text{SiO}_2/{}^{30}\text{SiO}_2$  interface broadening, which was analyzed in self-diffusivity studies. In this letter, the discrepancy between diffusive Si mass transport and Si self-diffusivity will not be discussed as our theoretical predictions aim at the reaction pathway of  $SiO_x$  decomposition rather than at a quantitative prediction of annealing time and temperature. Thus, KMC simulations utilize a relative time scale "Monte Carlo steps" (MCS) that allows *a posterior* recalibration with realistic Si diffusivities.<sup>12</sup>

Image processing of KMC simulation data allowed one to obtain KMC simulation snapshots that can be compared directly to PEELS-STEM or Fresnel images. In plane view, the simulation cell has been tripled laterally taking advantage of the periodic boundary conditions. The number of excess Si atoms in the vertical column below the pixel at (x, y) determines its gray level. The highest occurring number of Si atoms in such a column is assigned to white, whereas black means that no Si excess is found in the SiO<sub>2</sub> matrix.

No Si precipitates were observed in the sample for the lowest Si<sup>+</sup> fluence of  $5 \times 10^{15}$  cm<sup>-2</sup> (TEM images not shown here). For the medium Si<sup>+</sup> fluence of  $1 \times 10^{16}$  cm<sup>-2</sup>, white spots in a dark background appear in the plane view Si plasmon map shown in Fig. 1(a) indicating spherical Si NCs embedded in the SiO<sub>2</sub> matrix. During thermal annealing, these NCs have formed by nucleation and growth in the Si enriched region of the implanted SiO<sub>2</sub> layer. The observed NCs are relatively small with an estimated mean diameter of 2.7 nm and have a high area density of  $3.3 \times 10^{12}$  cm<sup>-2</sup>. A similar precipitate morphology with the same mean NC size and density is found in the KMC simulation snapshot of Fig. 1(b), but for a significantly lower Si<sup>+</sup> fluence of 3  $\times 10^{15}$  cm<sup>-2</sup> and a KMC simulation (annealing) time of 2100 kMCS. It should be noted that even this "medium" experimental Si<sup>+</sup> fluence of  $1 \times 10^{16}$  cm<sup>-2</sup> leads in the KMC simulation to the formation of coalesced poly-Si layer buried in the SiO<sub>2</sub>.<sup>4</sup> For the highest Si<sup>+</sup> fluence of  $2 \times 10^{16}$ Si<sup>+</sup> cm<sup>-2</sup>, the morphology of the phase separated Si has changed completely as observed in Fig. 1(c). A spaghetti-like network of white and black regions is found. This pattern clearly indicates that phase separation took place by spinodal decomposition. Even more, percolation is observed. Si precipitates are no longer spatially isolated, but an interconnected network of phase separated Si has formed. An equivalent morphology with the same typical distances as in Fig. 1(c) between the spinodal fingers is found in the KMC simulation snapshot of Fig. 1(d). This result was obtained for  $8 \times 10^{15} \text{Si}^{+} \text{ cm}^{-2}$  and a simulation time of 300 kMCS. Strikingly, the morphology agrees remarkably well between both images, while simulation snapshots for other fluences or annealing times deviate instead considerably from the morphology seen in Fig. 1(c).

Samples were studied additionally in cross section using a CM30 Phillips TEM equipped with a LaB<sub>6</sub> cathode operating at 300 kV. Fresnel imaging conditions were applied in order to achieve at least a weak contrast between Si and SiO<sub>2</sub> in cross sectional TEM (XTEM) images,<sup>5</sup> i.e., images were taken at out-of-Bragg alignment and under strongly underfocused bright field conditions. Details of the responsible contrast mechanism are given in Ref. 5. Figures 2(a) and 2(c)show the XTEM Fresnel images for fluences of 1  $\times 10^{16}$ Si<sup>+</sup> cm<sup>-2</sup> and  $2 \times 10^{16}$ Si<sup>+</sup> cm<sup>-2</sup>, respectively. Cross section KMC simulation snapshots are displayed in Figs. 2(b) and 2(d) for  $3 \times 10^{15} \text{Si}^{+} \text{ cm}^{-2}$  (2100 kMCS) and 8  $\times 10^{15}$ Si<sup>+</sup> cm<sup>-2</sup> (300 kMCS), respectively. Thereby, plane view image processing was applied to KMC cross section views with an inverted gray scale. In all images, the phase separated Si forms a single, sharp layer seen as gray band in the  $SiO_2$  that is well separated from the  $SiO_2/Si$  interface. Due to the extremely shallow Si excess profile obtained by low-energy Si<sup>+</sup> implantation, phase separation is quasicon-





FIG. 2. Cross-section view of the layer of phase separated Si in SiO<sub>2</sub>. Fresnel XTEM images for (a)  $1 \times 10^{16}$ Si<sup>+</sup> cm<sup>-2</sup> and (c)  $2 \times 10^{16}$ Si<sup>+</sup> cm<sup>-2</sup> are compared to cross-sectional KMC simulation snapshots for (b)  $3 \times 10^{15}$ Si<sup>+</sup> cm<sup>-2</sup> and (d)  $8 \times 10^{15}$ Si<sup>+</sup> cm<sup>-2</sup>.

fined to two dimensions. For  $1 \times 10^{16}$ Si<sup>+</sup> cm<sup>-2</sup>, Si precipitates align nicely in a thin layer, which is just a few nanometers thick, Fig. 2(a). When the Si<sup>+</sup> fluences are increased to  $2 \times 10^{16}$ Si<sup>+</sup> cm<sup>-2</sup>, the precipitate layer remains comparatively well localized in depth, Fig. 2(c), although the Si morphology observed in plane view has changed completely, Fig. 1(c). The total SiO<sub>2</sub> thickness is systematically smaller for the KMC simulations than for the corresponding Fresnel XTEM images. Nevertheless, the distance between the Si/SiO<sub>2</sub> interface and the phase separated Si agrees nicely for experiment and simulation.

The Si fluences in our KMC simulations were chosen in order to obtain morphologies of phase separated Si, which are similar to experimental ones. This adjustments of our simulation reveals a strong discrepancy between experimental and theoretical Si fluences. More Si than theoretically predicted has to be implanted. Not all Si that has nominally been implanted into the SiO<sub>2</sub> is available for phase separation. The reason for the missing Si excess might be twofold. (i) Recent time of flight secondary ion mass spectroscopy measurements on low-energy, low-fluence <sup>30</sup>Si<sup>+</sup> as-implanted SiO<sub>2</sub> samples indicate that only a fraction of about 0.5–0.7 of the nominal Si<sup>+</sup> fluence has been implanted into the SiO<sub>2</sub>.<sup>16</sup> (ii) It is known that (Si or Ge) NC formation in very thin SiO<sub>2</sub> films is extremely sensitive to humidity absorbed by the as-implanted (damaged) glass network<sup>17</sup> as well as to oxidants being present in the annealing ambient.<sup>18</sup> Thus, a considerable amount of the implanted Si might become oxidized during annealing. This explains also why no NCs have been observed by TEM for the lowest Si<sup>+</sup> fluence of 5  $\times 10^{15}$  cm<sup>-2</sup>. The implanted Si has been oxidized completely during the thermal annealing. At the same time, oxidation of the implanted Si leads to a volume expansion that increases the overall SiO<sub>2</sub> layer thickness.<sup>6</sup> This swelling of the SiO<sub>2</sub> due to Si oxidation can be seen in the XTEM images of Figs. 2(a)-2(c) if compared to the KMC simulation snapshots, Figs. 2(b) and 2(d), which just include the  $SiO_2$  expansion due to the incorporated Si atoms.9 Here, the KMC simulations do not account for oxidation and the swelling caused by it. To do so, a multicomponent KMC approach (Si+O) is needed. Though samples have been annealed under fixed experimental conditions, the KMC simulation snapshots of corresponding Si patterns refer to different simulation times. Two reasons might be responsible for this discrepancy in the evolution speed. At first, the oxidation of a substantial part of implanted Si might influence the kinetics, and second, the Si bulk diffusion in SiO<sub>2</sub> might differ substantially from the Si diffusion at the Si/SiO<sub>2</sub> interface. Studies that investigate this point are under way.

Summarizing, extensive studies on low-energy ion beam synthesis of multi-dot Si floating-gates embedded in thin  $SiO_2$  layers have been presented. The morphology of this floating-gate layer is imaged by PEELS-STEM. A direct comparison to kinetic three-dimensional lattice Monte Carlo simulation snapshots have been made and show a remarkable agreement between the atomistic simulations and the PEELS-STEM images. A strong fluence dependence of the precipitate morphology is confirmed. For low Si<sup>+</sup> fluences, isolated Si NCs form by nucleation and growth, while high fluences lead to spinodal pattern. Predicted and measured morphologies agree remarkably well. However, the predicted fluences are lower than the experimental ones. A substantial fraction of the implanted Si is lost by parasitic oxidation with atmospheric humidity.

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