Multi-Granular Optical Cross-Connect: Design, Analysis and Demonstration

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Abstract—A fundamental issue in all-optical switching is to offer efficient and cost-effective transport services for a wide range of bandwidth granularities. This paper presents multigranular optical cross-connect (MG-OXC) architectures that combine slow (ms regime) and fast (ns regime) switch elements, in order to support optical circuit switching (OCS), optical burst switching (OBS) and even optical packet switching (OPS). The MG-OXC architectures are designed to provide a cost-effective approach, while offering the flexibility and re-configurability to deal with dynamic requirements of different applications. All proposed MG-OXC designs are analysed and compared in terms of dimensionality, flexibility/re-configurability and scalability. Furthermore, node level simulations are conducted to evaluate the performance of MG-OXCs under different traffic regimes. Finally, the feasibility of the proposed architectures is demonstrated on an application-aware, multi bit rate (10 and 40Gbps), end-to-end OBS testbed.

Index Terms—Multi-granular optical cross-connect, optical circuit switching, optical burst switching, simulation analysis, demonstration.

I. INTRODUCTION

Generation of distributed network-based applications that combine scientific instruments, distributed data archives, sensors, computing resources and many others are emerging. Each application has its own traffic profile, resource usage pattern and different requirements originating in the computing, storage and network domains [1]. Dedicated networks do not offer sufficient flexibility to satisfy requirements of each application type, nor are they economically acceptable. Hence it is vital to understand and redefine the role of networking, to support applications with different requirements and also offer service providers a flexible, scalable and cost effective solution. A dynamic optical network infrastructure with the ability to provide bandwidth granularity at different levels is a potential candidate. In this way, the network can adapt to application requirements and also support different levels of Quality of Service (QoS). However, care must be taken to

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devise a solution that remains scalable and cost effective (see also Section II-C).

A multi-granular optical switched network is able to support dynamic wavelength and sub-wavelength bandwidth granularities with different QoS levels. As such, the network will support the three basic switching technologies in WDM networks; optical circuit switching (OCS), optical packet switching (OPS) and optical burst switching (OBS).

In OCS networks, bandwidth granularity is at the wavelength level since one or more wavelengths are allocated to a connection, while connectivity between source and destination is established using a two-way reservation which is in general a time-consuming procedure. The OCS scheme is suitable for applications that need continuous bandwidth for relatively long duration of time. However, this scheme is neither sufficiently flexible nor bandwidth-efficient to support applications that require sub-wavelength bandwidth granularity in an on-demand fashion or for a short duration of time.

OPS technology makes it possible to achieve subwavelength bandwidth granularity and exploit statistical multiplexing of bursty traffic flows. In OPS networks, one or more IP packets with similar attributes are aggregated in an optical packet and tagged with an optical header. This scheme does not need advance reservation, and it can provide ondemand connectivity by imitating best effort IP packet routing in the optical domain. These features make OPS technology a suitable candidate for applications that need transmission of small data sets in an on-demand manner [2]. However, the lack of practical optical buffering solutions, together with the high cost of switch fabrics that meet the constraints imposed by OPS, make this technology difficult to implement and deploy in practice.

OBS combines the advantages of OCS and OPS [3]. The fundamental premise is the separation of the control and data planes, and the segregation of functionality within the appropriate domain (electronic or optical). Prior to data transmission, a burst control header (BCH) is created and sent towards the destination. The BCH is typically sent out-of-band over a separate signaling wavelength and processed electronically at intermediate OBS routers. It informs each node of the impending arrival of the data burst, and in turn drives the allocation of an optical path. Data bursts remain in the optical plane end-to-end, and are typically not buffered as they transit the network core. The main advantages of OBS are

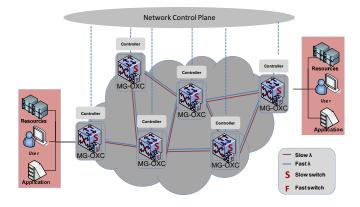


Fig. 1. Multi-granular optical network environment

that unlike OCS networks, the optical bandwidth is reserved only for the duration of the actual data transfer, and thus OBS increases bandwidth efficiency. Likewise, in contrast to OPS networks, OBS is not based on best-effort routing and potentially can be implemented without buffers.

In order to migrate from a strictly wavelength granular network (OCS) to a sub-wavelength one (OBS or even OPS) and/or also consider the possibility of hybrid OCS/OBS type of networks (Fig. 1), switching technologies with speeds on a millisecond range down to nanosecond range must be considered. The network resource reservation can be realized by first sending a reservation message (e.g. RSVP Path message for GMPLS or LOBS [4], BCH for OBS) to the network. The control mechanism then recognizes whether the data unit requires slow switching (usually long burst or circuit) or fast switching (usually short burst or packet). In the first case, the slow switch element is dynamically reconfigured so that when the long data set arrives, it is routed to the appropriate output port. In the other case, the short data unit (burst or packet) is routed through the fast switch, either by directly connecting incoming wavelengths or using pre-established soft-permanent connections through the slow switch to allow re-configurability (see Section III for more details). OCS can utilize millisecond switching technologies efficiently, whereas this switching speed causes bandwidth inefficiency and unpredictability for the performance of OBS, especially under high network load. This is mainly caused by the high overhead incurred by large offset times required to configure slow switches. Indeed, the throughput of a switch is defined as $\frac{T_{data}}{T_{offset} + T_{data}}$, where Tdata is the length of the actual data in time, and Toffset is the offset time between T_{data} the control message and the data. MEMS switches (Micro-Electro-Mechanical Systems) typically have a switching time of around 10ms, and thus provide a throughput of more than 95% on a 10Gbps system with data duration of 200ms. If only 1/10th of this data is sent (which still amounts to 25MByte of data), then the switch's throughput decreases to 67%. This effect becomes even more severe when the bandwidth is increased, for instance to 40 or even 160Gbps, or when OPS (data durations on nanosecond scale) must be supported. A SOA-based switch (Semiconductor Optical Amplifier) can achieve nanosecond switching speeds, and is thus much better adapted to support the full range of data sizes required for OCS, OBS and OPS.

The ideal solution would thus consist of deploying fast switches of large dimension and low cost; however these do not exist at this time (a brief review of existing switching technologies is provided in Section II-A). Therefore, one possible solution is an OXC architecture which combines both slow (e.g. MEMS) and fast (e.g. SOA based) switching elements. This way, users and applications can decide on slow or fast network provisioning, and additionally the network service provider can optimise bandwidth utilisation by allocating wavelengths or lightpaths according to the traffic's switching needs. Furthermore, our previous work [22] has shown the cost effectiveness of the proposed approach on the network level.

In conclusion, the MG-OXC has a number of distinct advantages over traditional single-fabric switches:

- Bandwidth provisioning and switching capability at fiber, wavelength and sub-wavelength granularities;
- Agility and scalability of switching granularities providing a dynamic solution;
- Fast re-configurability and flexibility on the electronic control of switching technologies;
- Cost-performance efficiency by offering an optimal balance between slow and fast switch fabric technologies.

The remainder of this paper is organised as follows. We first review existing switch fabric technologies for possible deployment (Section II-A), and discuss alternative proposals for multi-granular optical switching in Section II-B. Section III proposes different MG-OXCs architectures and presents an analysis on the dimensionality, scalability and flexibility of the designs. Simulations are used to evaluate the MG-OXC for various traffic and design parameters in Section IV. Furthermore, in Section V, we demonstrate an MG-OXC based on MEMS for circuits or long bursts, and SOA-MZI for medium to short bursts or optical packets. Results are shown for an end-to-end, application-aware, multi bit rate and QoS-enabled (based on bandwidth and latency parameters) OBS network testbed. Finally, Section VI summarises our conclusions.

II. RELATED WORK

A. Optical Switching Technologies

This section briefly reviews the most prominent switching technologies available, and comments on their usability in different network environments (OCS, OBS, OPS).

Three main categories considered for optical switch fabric selection [5] are a) signal quality, b) configuration, and c) physical performance. Each category has a number of attributes [6] which are listed below.

- Signal quality is largely determined by the bit error rate (BER) and the cascadability of the switch fabric. Important parameters are signal-to-noise ratio, jitter, amplitude distortion, crosstalk and extinction ratio.
- Configuration is mostly related to the implementation of the switch and is determined by its scalability, blocking characteristics, and cost effectiveness. Further design considerations include control requirements, dynamic range, polarization dependence, degree of multicast,

	Switching time	Scalability	Applications	
Opto-mechanical switch	4 msec	16x16	OCS, limited OBS	
Optical MEMS	3D:~10 msec, 2D:~3 msec	3D:1000x1000, 2D: 32x32	OCS, limited OBS	
Thermo-optical PLC	\sim 3 msec	4x4	OCS, limited OBS	
PLZT Switch	~ 20 nsec	4x4	OCS, OBS, OPS	
Semiconductor Optical Phase Array	~ 30 nsec	64x64	OCS, OBS, OPS	
Wavelength Routing Switch	~ 1 nsec	65336x65336	OCS, OBS, OPS	
SOA broadcast-and-select	~ 1 nsec	32x32	OCS, OBS, OPS	
SOA Cross-point	~ 1 nsec	4x4	OCS, OBS, OPS	
Optical RAM	~ 1 nsec	64x64	OCS, OBS, OPS	

 TABLE I

 SWITCH FABRICS FOR OCS,OBS AND OPS BASED ON SWITCHING TIME AND SCALABILITY

latching/non latching, promptness (store-and-forward versus cut-through), upgradeability, power requirements, and size.

• Finally, the physical performance of a switch is determined by optical bandwidth, switching speed, insertion loss, bit rate limit, bit pattern dependence, uniformity and optical transparency.

In [7], Baldine et al. realized an OBS testbed using microelectro-mechanical system (MEMS) OXCs from ATDnet. In [8], [9], NTT and Fujitsu demonstrated burst switching with GMPLS-based two-way signalling protocol, utilizing planar lightwave circuit (PLC) and MEMS switches. In [10] , commercial PLC switches were adopted to construct a 1616 non-blocking switch matrix with a switching speed of less than 3ms. OBS schemes with hybrid optical and electrical switching technologies have been investigated in the past [11]. A recent study demonstrated PLZT switch (lead lanthanum zirconate titanate, [12]) with shared wavelength conversion allowed variable-length 3.5ns OBS. A photonic random access memory (RAM) was used on [13] to write and read highspeed asynchronous burst optical packets. In this paper, the main criteria used in the decision process are the switching time and scalability. Thus, according to Yoo et al. in [5], Table I identifies possible switch fabrics for the development of a multi-granular switch. A detailed discussion based on the attributes mentioned above, can be found in Wei et al. [11] and Papadimitriou et al. [6]. Clearly, one of the most appropriate slow switching technologies is the optical MEMS switch, since it is already a mature technology under production and because of the high dimensions available at low cost [14], [15]. On the other hand, most fast switch matrices (ns regime) listed in Table I are still in the research stage; some are not vet integrated into a single device (e.g. wavelength routing switch) or are highly sensitive to polarization (e.g. SOA based switches). As such, it is not yet possible to select the most appropriate technology at this stage.

B. Multi-Granular Optical Cross Connects

The principle of Multi-Granular Optical Cross-Connects (MG-OXC) that can switch traffic at fibre, waveband and wavelength granularities [16]–[18] has been proposed to reduce cost and complexity of traditional OXCs. The MG-OXC is a key element for routing high speed WDM data traffic in a multi-granular optical network. A Three-Layer switching

fabric consisting of a fiber cross-connect (FXC), a band crossconnect (BXC) and a wavelength cross-connect (WXC) was presented in [19], [20], and the application of such Three-Layer MG-OXC architectures to metro-area networks was demonstrated in [21]. The work presented in this paper can be considered as complementary to the studies mentioned above. Instead of extending the granularity towards higher capacities (e.g. wavebands to full fibers), we propose OXC architectures that are able to switch at the sub-wavelength level. By combining the two extensions, the full range of bandwidth granularities can be supported by a single OXC design.

C. Cost Effectiveness

In our previous work [22], we have shown that multigranular switching also provides economic advantages on the network level, which provides further motivation for the cost effectiveness of the proposed approach. For this, an ILPbased network dimensioning algorithm was introduced to compare multi-granular switching with approaches based on a single technology (MEMS for slow switching, SOA for fast). Results indicated that significant cost savings can be obtained when implementing multi-granular optical switching. Another important conclusion was that the reconfigurable MG-OXC design (see Section III-A) leads to only a negligable cost increase compared to the non-reconfigurable design. Furthermore, reduced node costs can be achieved as well, in order to minimize scalability problems corresponding to emerging fast switching fabrics.

III. MULTI-GRANULAR OPTICAL CROSS CONNECT ARCHITECTURES

This section presents, analyses and evaluates different architectures aiming to support OCS, OBS, OPS (or even a combination of them, e.g. hybrid OCS/OBS) with a single design.

A. MG-OXC Design

Before discussing the proposed switch designs, we state our design goals. We first note that neither wavelength conversion nor buffering capabilities are available, as these technologies are both costly and impractical.

• Each design must support multi-granular optical switching, and consists of both slow and fast switching fabrics.

TABLE II NOTATIONS FOR ANALYSIS OF DIMENSIONS AND SCALABILITY

Symbol	Meeting
F	number of fibres
W	number of wavelengths per fibre
Y	number of fast wavelengths per fibre
Z_w	number of fibres having wavelength w switched fast

- The architectures must be *non-blocking* in the sense that any input wavelength can be connected to any output fiber. The stricter form where any input wavelength can be connected to any output wavelength is not achievable since we assume no wavelength conversion is available.
- A number of wavelengths of each fiber should be able to access the fast switch, although only limited fiberwavelength pairs can do so simultaneously.
- We favour designs that allow the set of fiber-wavelength pairs, that have access to the fast switch, to be *reconfigurable*. However, we also show switch designs in which this functionality can only be achieved through manual intervention. In the following paragraph, we explain reconfigurability in more detail.
- The design should be able to scale in fast or slow switch ports depending on network requirements (i.e. adding new wavelengths and/or links)
- Lastly, the number of fast switch elements should be limited as much as possible, in order to create a cost-effective design.

An additional design constraint is that neither wavelength conversion nor buffering capabilities are available. It is particularly important to provide a flexible and scalable solution both in switching dimensionality and in bit rate. The architecture is determined by two fundamental design choices, and considers the configuration and connectivity of the slow and fast switch fabrics separately. The first choice is related to the slow switch, and is based on the well-defined concept of wavelength or link modularity. The former means that identical wavelengths from different fibers are switched in the same fabric, whereas link modularity implies that all wavelengths of a link are grouped in a switching block. The second part of the design introduces the fast switch elements to the multi-granular architecture, and is based on the relative connectivity to the slow switch fabric. Possible approaches are either parallel or sequential integration, and this choice has important consequences on the configurability and dimensions of the designs. In Table II, a number of notations are introduced, that are relevant for the design and analysis of the MG-OXC. Each architecture consists of F input and F output fibers, labelled f_1 to f_F . The input fibers are first split into different wavelengths by a de-multiplexer (DEMUX, by using for example an AWG (Arrayed Waveguide Grating)). Before leaving the OXC, wavelengths are multiplexed (MUX, e.g. AWG) on a fiber. First, the conceptually simple parallel designs (2 and Fig. 3 provide no connectivity between the slow and fast switch elements, which implies that there is no flexibility to rearrange slow and fast wavelengths, unless if it is done manually. In the link modular design, the role of the multiplexing devices (e.g. optical couplers) that are attached to the output of the slow switches, is to ensure the

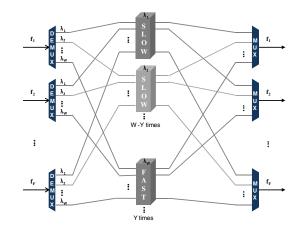


Fig. 2. Parallel wavelength modular architecture

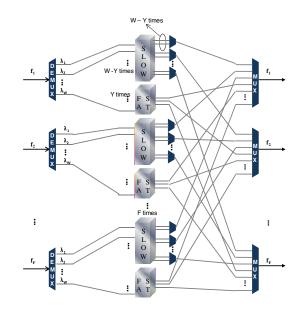


Fig. 3. Parallel link modular architecture

non-blocking behaviour as previously defined. The following designs (Fig. 4 and Fig. 5) are referred to as the sequential architectures; these provide two-stage switch connectivity for provisioning of fast cross-connections. The fast optical ports can be reached through the slow switches, and as such allow (re-)configuration of fast connections. shared among all slow switches in order to combine wavelengths from all incoming fibres. Fig. 4 is based on the assumption that not only the slow, but also the fast switch is configured in a wavelength modular fashion. However, it is also possible to share a fast switch block between different wavelengths, in order to reduce the number and dimensions of the fast switch fabrics. Also note that for the sequential wavelength modular design, the parameter K is given by: $K = \sum_{w=1}^{W} (1 - \delta_{Z_w0})$, where δ_{ij} is the Kronecker delta¹, i.e. K represents the number of distinct wavelengths (total W) that are switched fast on at least 1 fiber. The sequential link modular design also requires the coupling devices at the output of the slow switch, in order to adhere to the non-blocking requirement. In Fig. 5, the assumption with regard to the wavelength modularity of the fast switch has

¹By definition, the Kronecker delta δ_{ij} equals 1 if i = j, 0 otherwise.

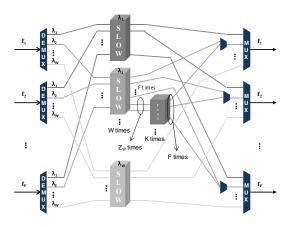


Fig. 4. Sequential wavelength modular architecture

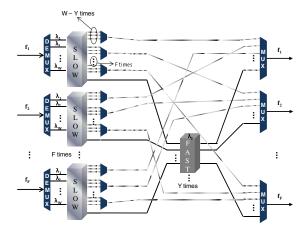


Fig. 5. Sequential link modular architecture

been made as well. Again, further reductions in fast switch ports and fabrics are possible by sharing a fast block between different wavelengths.

B. Dimensions and Scalability

To analyze the dimensions in terms of number of crossconnects required and number of ports on each cross-connect, refer to the notations as listed in Table II. The assumption has been made that all fibers support an identical number of fast wavelengths Y.

$$\sum_{w=1}^{W} Z_w = Y \times F \tag{1}$$

Also note that it holds that Table III presents the number of slow and fast switches required to realise the different architectures, while also indicating their dimensions.

C. Qualitative Analysis

In this section, we discuss qualitative advantages and disadvantages between the proposed architectures. The focus lies on demonstrating additional features or shortcomings related to the flexibility, reconfigurability and construction of these switch designs. Since a major design goal of the MG-OXC is cost-efficiency, the focus should be on minimizing both the number of fast switching fabrics and ports. Table III does not allow general conclusions to be drawn, as a comparison

TABLE III COUNT AND DIMENSIONS OF SLOW AND FAST SWITCHES

Type Modularity		Count	Dimensions					
Slow Switch								
Sequential	Link	F	$W \times ((W - Y) \times F + Y)$					
	Wavelength	W	$F \times F$					
Parallel	Link	F	$(W - Y) \times ((W - Y) \times F)$					
	Wavelength	W - Y	$F \times F$					
		Fast Swit	ch					
Sequential	Link	Y	$F \times F$					
	Wavelength	K	$Z_w \times F$					
Parallel	Link	F	$Y \times (Y \times F)$					
	Wavelength	Y	$F \times F$					

between the sequential and parallel approaches require us to make assumptions on the number of fibers F, the number of fast wavelengths Y and the number of distinct fast wavelengths K. Recall however that the sequential designs offer reconfigurability of fast wavelengths, which the parallel architectures lack (see Section III). In Figure 4, the sequential design assumes identical wavelengths are switched in a single fast block. A further reduction of fast blocks and/or ports is possible by allowing different wavelengths to share a single fast block, as depicted in Figure 5. In that case however, the switch's control unit must prevent identical wavelengths to arrive at the MUX of a single fiber. Another possibility is to introduce wavelength conversion inside the MG-OXC, a solution which is currently impractical due to its high cost and technological immaturity.

As previously stated, an important distinction lies in the modularity of the switch designs, which has a major influence on scalability. The link modular approaches easily allow a new link to be added to an existing switch, although extending a link with an extra wavelength is much less straightforward. Likewise, the extensibility of the wavelength modular approaches is straightforward when adding a wavelength, but it is much less convenient to introduce a new link in the switch.

Depending on integration and/or production costs, a potential advantage can arise for architectures having fast switches, whose input dimensions are smaller than the number of output ports. This is obviously the case for the parallel link modular and sequential wavelength modular approaches.

Finally, the limited scalability of fast switching technologies (e.g. SOA up to 32x32 ports) may prove an issue when larger fast switch blocks are required. It is however possible to combine multiple switch blocks in a multi-stage design as shown in e.g. [23], [24], but careful analysis of physical performance (signal loss, Bit Error Rate or BER, etc.) is advised.

IV. SIMULATION ANALYSIS

In this section, simulation analysis is used to provide insight in the behaviour of the MG-OXC [25]. The implementation allows us to evaluate an MG-OXC in a generic way, independent of architectural details (modularity, reconfigurability). Note that for fixed F, W and Y values, the designs presented in the previous section are functionally equivalent. A comparison between MG-OXC and traditional, single-speed OXCs (slow only, fast only) is presented, and results are given for varying traffic load, fractions of slow/fast traffic and number of

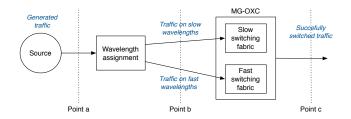


Fig. 6. Overview of node simulations

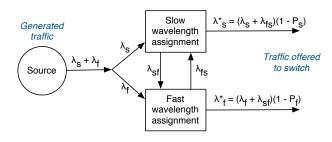


Fig. 7. Overview of wavelength assignment

slow/fast ports available. However, we start with introducing the different approaches to wavelength assignment, which are necessary for mapping incoming data bursts to a suitable wavelength. For a general overview of the node simulations, and to observe the different steps in which traffic is processed, refer to Fig. 6.

A. Wavelength Assignment

The introduction of an MG-OXC in a network effectively creates a wavelength partitioning, by grouping wavelengths that are switched on the same type of switching fabrics. As such, an algorithm is required to assign generated traffic to a suitable wavelength partition, and the available wavelengths within a partition. This algorithm will be executed at the network's edge, thus before entering the all-optical data transport network (see Fig. 6). In the following, the assumption was made that only two partitions (corresponding to slow and fast) are introduced.

As shown in Fig. 7, generated traffic is first classified in slow (arrival rate λ_s) and fast (λ_f) traffic flows, by inspecting the offset time T_{offset} between the burst header and the actual data burst. Obviously, for slow traffic it holds that $T_{offset} > T_{slow}$ (T_{slow} the switching speed of the slow switch), while $T_{offset} < T_{slow}$ is true for fast traffic (T_{fast} the switching speed of the fast switch fabric). Based on this classification, a number of alternatives are now possible for assignment of traffic to the wavelength partitions.

The approaches differ in the way traffic is transferred between the slow and fast wavelengths partitions. *Simple* wavelength assignment is the most basic approach, whereby slow bursts are assigned to the slow wavelength partition, and the burst is dropped in case no free wavelength is available. Fast bursts are considered for assignment to the fast wavelength partition in a similar way. The *slow-to-fast* approach differs from the simple algorithm by allowing slow bursts on the fast wavelengths, only in case these can not be accommodated on the slow wavelengths. The corresponding *fast-to-slow* wavelength assignment allows transfer of fast

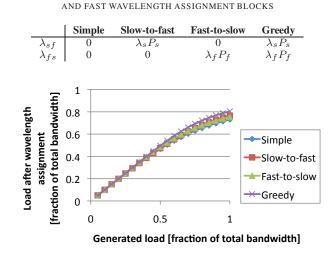


TABLE IV

TRANSFER RATES (NUMBER OF BURSTS PER TIME UNIT) BETWEEN SLOW

Fig. 8. Total load after wavelength assignment is similar for the various algorithms

bursts onto slow wavelengths (again only in case the fast burst can not be assigned to a fast wavelength). We motivate the use of this algorithm as follows: although the slow switch can not be configured in time for a fast burst, it is possible that the preceding (slow) burst requests the same output, and thus reconfiguration of the switch is not required. Finally, the *greedy* approach allows transfer of traffic between both wavelength partitions, again only when no available capacity can be found for the original wavelength assignment.

Let λ_{sf} be the transfer rate from the slow to the fast wavelength assignment block, and λ_{fs} from fast to slow. Then Table IV shows the transfer rates for the different wavelength assignment approaches. Here, P_s and P_f represent the blocking probabilities of the slow and fast wavelength assignment blocks, and are given by (assuming Poisson arrivals):

$$P_s = Erl\left(\frac{\lambda_s + \lambda_{fs}}{B}, W_s\right)$$
 and $P_f = Erl\left(\frac{\lambda_f + \lambda_{sf}}{B}, W_f\right)$

In these expressions, B represents the bandwidth of a wavelength, W_s and W_f are the number of slow and fast wavelengths in the slow and fast partitions respectively, and Erl(.) is the Erlang-B function defined as:

$$Erl(\rho, W) = \frac{\frac{\rho^W}{W!}}{\sum_{i=0}^{W} \frac{\rho^i}{i!}}$$

. Note that in case of *greedy* wavelength assignment, P_s and P_f depend on each other, and thus an iterative substitution is required to obtain the respective blocking probabilities.

To demonstrate the influence of these alternatives, Fig. 8 shows the load remaining after wavelength assignment, i.e. the plot shows the total load at point b for varying generated loads at point a (Fig. 6). This result was obtained by assuming $W_f = 2$ and $\beta = \frac{\lambda_f}{\lambda_s + \lambda_f} = .2$, which reflects the expected low number of (expensive) fast wavelengths. It also follows that β represents the fraction of generated fast traffic to the total generated (slow and fast) traffic. Even though the total

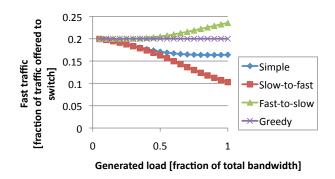


Fig. 9. Fraction of fast traffic after wavelength assignment: depending on wavelength assignment, the fraction of traffic sent on fast wavelengths deviates from the generated $\beta = 0.2$.

load after wavelength assignment is similar for the various approaches, Fig. 9 provides more insight into which type of traffic is favoured. The figure shows the fraction of fast traffic to the total traffic after wavelength assignment, and this for a varying total generated load. In other words, we plot the fraction of fast traffic to the total traffic at point b in Fig. 6, and do this for various load averages at point a. Clearly, the fast-to-slow approach allows more fast traffic than the greedy approach, since the latter also allows slow bursts to use valuable fast wavelengths. It should be noted however that, although not shown, the behaviour of fast-to-slow converges to greedy for increasing values of β . Both the simple and slow-tofast algorithms preserve only small fractions of fast traffic, and are thus not well-adapted to support a multi-granular optical network scenario, as a non-negligable amount of fast traffic will be lost because of inappropriate wavelength scheduling. Since our main interest is the effect of fast traffic and fast wavelengths, the simulation results presented in Section IV-B have been made using the *fast-to-slow* wavelength assignment algorithm. As mentioned before, another important decision is how bursts are assigned to individual wavelengths within a partition. Strategies such as first-fit or best-fit have previously been investigated in e.g. [26], [27], however this subject falls outside the scope of this work. The simulation studies in the following Section IV-B assume a first fit strategy.

B. Single Node Simulations

This section presents discrete event simulation results of several OXC alternatives (slow only, fast only and MG-OXC). All designs support 2 input and 2 output fibers, each fiber carrying 10 wavelengths. Neither wavelength conversion nor buffering capability is present in any of the switch designs. Each incoming data burst has a 50% probability of choosing the first output fiber. The bandwidth of each wavelength is 10 Gbps, and traffic is generated according to a Poisson process with an average inter-arrival time of 15 ms. Data sizes follow an exponential distribution, with a varying average to establish the generated load. Because of the limited scale of currently deployed OBS networks², there is no conclusive data available on a number of relevant traffic parameters. Thus, to

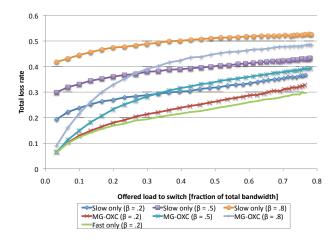


Fig. 10. Higher fractions of fast traffic increase the total loss rate for fixed number of fast wavelengths ($W_f = 2$)

control and evaluate the influence of different traffic types, the offset times between control packet and data are modeled as a 2-phase hyper-exponential distribution. The probability density function (pdf) f is given by: $f = \alpha \times f_{slow} + \beta \times f_{fast}$, with $\alpha + \beta = 1$ and α and β representing the fractions of generated slow and fast traffic³. The pdf of the slow f_{slow} (respectively fast f_{fast}) traffic is an exponential distribution with average 100 ms (respectively 10 ns). The slow switching fabric has a switching speed of $T_{slow} = 10$ ms, while the fast switch has $T_{fast} = 1$ ns. These values are representative for a MEMS-based (respectively SOA-based) switch [5], [6]. This leads to $1 - e^{-.1} = 9.5\%$ of slow traffic that actually belongs to fast traffic, and an identical fraction of generated fast traffic will have $T_{offset} < T_{fast}$.

In the following sections, we show the performance of the MG-OXC switch, and compare the results to designs composed of a single switch fabric (slow only, fast only). To allow fair comparison of the results, the wavelength assignment algorithm is also applied in case single-fabric designs are used. This way, the offered traffic pattern at the switches' input ports is identical in all cases. As such, wavelengths are also partitioned in these single-fabric scenarios, even though the switching speeds are identical for all wavelengths. The *fast*-to-slow wavelength assignment algorithm (Section IV-A) was implemented, together with a first-fit approach for mapping data bursts on a specific wavelength within a partition. The results shown focus on the total loss rate of the switch; bursts can be lost either due to contention or because the switching speed is insufficient for a given burst.

1) Varying fraction of fast traffic: In the first experiment, 2 wavelengths are available in the fast partition, while the remaining 8 are allocated for the slow partition. Simulations were performed to evaluate the influence of the fraction of fast traffic for the three switch designs. The resulting Fig. 10 shows the total loss rate (i.e. ratio of dropped traffic to the

²OBS is still considered an immature technology, and as such OBS testbeds/prototypes are composed of at most a few nodes.

³Note that this arrival model does not generate these precise fractions of slow and fast traffic. For bursts generated according to f_{slow} , it is still possible that $T_{offset} < T_{slow}$. Using the cumulative distribution function of an exponentially distributed variable, this holds for the following fraction of $\frac{-T_{slow}}{T_{slow}}$. The set of the following fraction of the following fracting fracting fraction of the

traffic: $P[T_{offset} \leq T_{slow}] = 1 - e^{\frac{-\epsilon_{slow}}{T_{offset}}}$. The same argument holds for fast traffic, where $T_{offset} < T_{fast}$.

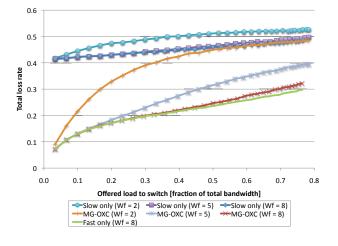


Fig. 11. Higher number of fast wavelengths decrease the total loss rate for a fixed fraction of fast traffic ($\beta = .8$)

offered load measured at point c in Fig. 6) for a varying offered load (point b in Fig. 6). First observe that for low loads, the relatively high loss rates can be attributed to the fraction of fast traffic which has an offset time lower than the fast switching speed. However, some of these bursts can still be switched correctly as consecutive bursts taking the same output port does not require reconfiguration of the switch fabric (this explains the loss rate close to 6.5% of the fast only design in comparison to the predicted 9.5%).

2) Varying number of fast wavelengths: Then, an increasing fraction β of fast traffic causes higher loss rates, since the number of fast switching ports remains fixed (0 for the slow only, 2 for the MG-OXC). This does not apply to the fast only design (only shown for $\beta = .2$), whose performance is very similar for all fractions of fast traffic. Also, it is readily apparent that the MG-OXC outperforms the slow only design for all values of β . Another observation is that the MG-OXC offers loss rates similar to the fast only design, unless high fractions of fast traffic are generated ($\beta = .5$ and .8). This is not surprising considering the small number of fast switching ports available to the MG-OXC.

In the following experiment, the generated traffic consisted of 80% fast traffic ($\beta = .8$). Now, simulations focus on varying the number of slow/fast wavelengths in each partition, and hence also the exact number of slow/fast wavelengths available to the MG-OXC. Fig. 11 shows the total loss rate (point c in Fig. 6) for a varying offered load (point b in the same figure), where one can immediately observe that an increased number of fast wavelengths results in a lower loss rate. That this result holds even for the slow only designs, is due to the use of the *fast-to-slow* wavelength assignment and the simulation setup: the initial switch configuration connects the top input and output fibers (and likewise for the bottom fibers), and traffic is generated with a 50% probability of choosing either output fiber. Consequently, more or less half of the traffic on the W_f wavelengths can be switched correctly, and this explains why increasing values of W_f reduce the total loss rate. As before, the MG-OXC can provide an overall improved loss performance compared to the slow only design (behavior of slow only and MG-OXC are similar only for high loads and a severely under-dimensioned fast switching block). For high numbers of fast wavelengths ($W_f = 8$), the loss rate of the MG-OXC approaches the performance of the fast only design. Note again that results of the fast only design are shown only for $W_f = 8$, as other values for W_s lead to very similar loss rates. A final observation is that, although not shown, the loss rate of the slow only design is slightly higher in case greedy wavelength assignment is used, due to a assignment of fast bursts to slow wavelengths.

In conclusion, this section clearly demonstrated that an MG-OXC, equipped with only a limited amount of fast ports, can offer significant improvements in loss rates when compared to a slow only design. Furthermore, as long as the mismatch between fast traffic and fast wavelengths remains within acceptable bounds, the MG-OXC can approach the performance levels offered by a fast only design.

V. DEMONSTRATION OF MG-OXC ARCHITECTURES IN A Hybrid Network Testbed

The functionality and performance of the multi-granular switching concept has been evaluated and experimentally demonstrated on an application-aware multi bit rate OBS testbed at University of Essex [28]. The application-aware core OBS router utilizes a PowerPC processor embedded in FPGA and a MG-OXC comprising of a fast and widely tuneable SG-DBR (Sample Grating Distributed Bragg Reflector) laser, a 2D MEMS switch and a fast SOA-MZI switch. The multigranular switch fabric is wavelength selective and is controlled by extraction and processing of the incoming BCH through a Clock and Data Recovery (CDR) and a Word Alignment Unit (WAU). Then the header processing block (HP) is used to retrieve all the required information from the BCH and the header re-insertion (HRI) unit is used to re-insert the BCH on the control plane lambda. The BCH consists of several fields as illustrated in Table V. The Class of Service field (CoS) explicitly determines the latency requirements of the burst and thus selects the appropriate switch fabric. The offset time together with the burst length, wavelength (Burst λ), source (Src) and destination (Dest) provide detailed configuration parameters for either the slow or fast switch fabric. The FPGA provides the appropriate electronic control signals for the MEMS switch state and the tuneable laser that optically controls the SOA-MZI. The MG-OXC comprises a low cost, commercial, high-dimension, slow 2D MEMS switch (10 ms) and a high cost, low-dimension but fast (1 ns) SOA-MZI based switch. Fig. 12 10 a) illustrates the functional blocks of the testbed, including the parallel (b), and sequential (c) switch architectures. The egress node detects incoming bursts and can measure the BER of individual bursts, based on the appropriate control (gating) signals generated by an FPGA. The node processor integrates a CDR mechanism, a WAU as well as a Header Processing (HP) Unit. When an incoming BCH is received, the HP unit extracts the header length, offset and burst length and provides a signal (envelope) used to gate the BER test-equipment for the duration of the received burst. Finally, the control plane is based on Just in Time (JIT) signalling and operates out-of-band in a full duplex mode at 2.5Gbps.

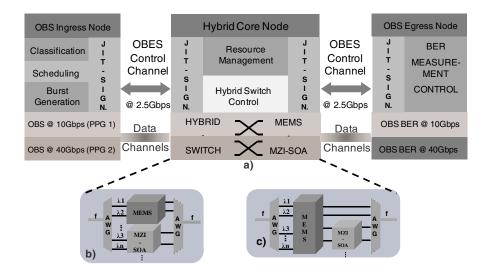


Fig. 12. a) Multi bit rate OBS testbed to demonstrate and evaluate different MG-OXC architectures b) parallel, c) sequential

TABLE V Burst Control Header (BCH) format

Burst	Header	CoS	Burst	Offset	Burst	Src.	Dest.
ID	Length		Length		λ		

A. Control and Data Plane Implementation

Fig. 13 shows the testbed architecture and experimental setup. The OBS test-bed operates at 2.5Gbps for the control plane and 10 and 40Gbps for the data plane. All routers utilize a Xilinx high-speed and high-density VirtexII-Pro FPGA with embedded PowerPC processor. The OBS control channel is full duplex (to allow synchronization) for both links and is transmitted on dedicated wavelength channels using the proprietary Optical Burst Ethernet Switched (OBES) transport protocol [29]. The data plane transports variable sized bursts with variable time intervals on burst mode. The electronic section of the ingress edge router incorporates Differentiation Scheduler (DS), Classification Scheduler (CS), Wavelength Allocation Scheduler (WAS) and BCH assembly and assignment. Burst generation is performed by controlling two 10Gbps PPGs that generate the emulated bursts carrying $2^7 - 1$ Pseudo Random Binary Sequences (PRBS). The control signals for the bursts are envelope patterns with variable duration and time intervals, and are generated via the Rocket IO interface of the FPGA to gate the PPGs. The output from one of the two PPGs is used to externally modulate an optical carrier at $\lambda_5 = 1553.00 nm$ from a DFB laser using a Mach-Zehnder Modulator (MZM) producing the 10Gbps NRZ burst data stream. The second PPG modulates a 10GHz optical pulse stream (1.4ps FWHM, sech2-shaped pulse) from a tuneable mode locked laser (TMML) at $\lambda_6 = 1556.65nm$, generating a 10Gbps RZ burst data stream. Further, the bursts are timemultiplexed to 40Gbps through a two-stage optical multiplexer (OMUX) that maintains the $2^7 - 1$ randomness. At the output of the ingress node, the 10 and 40Gbps burst data streams are coupled and transmitted over a single fibre. In parallel to the data plane, the edge router generates and optically transmits the BCH at 2.5Gbps on an out-of-band wavelength $(\lambda_2 = 1543$ nm) towards the core router controller. A second out-of-band control channel at $\lambda_1 = 1544$ nm is generated by the core router and received at the ingress node to establish the full duplex control operation. A delay element inside the FPGA at the ingress node is used to synchronize the data plane and control plane, by providing the appropriate offset time between the BCH and data burst. The actual value of the offset time is based on known propagation delays in the system and processing delays in the core router.

The core router integrates an FPGA, a MEMS switch, a widely tuneable SG-DBR laser and a fast SOA-MZI switch. The FPGA receives and processes the information from the control channel and in turn controls the tuneable laser and both switch fabrics. More specifically, the fabric is controlled by means of extracting and processing the incoming BCH through Clock and Data Recovery (CDR), a Word Alignment Unit (WAU) as well as a Header Processing (HP) Unit. It should be noted that the switch is dynamically configured by the FPGA controller, which performs on-the-fly processing of the incoming BCH header information. According to CoS, offset, burst length, wavelength and destination, the FPGA provides the appropriate electronic control signals for the MEMS switch state (for slow switching), and the appropriate tuning sequences for the tuneable laser that optically controls and configures the SOA-MZI (for fast switching). The MEMS switch is configured to have some soft-permanent dedicated cross-connections in order to direct the small and medium sized bursts towards the SOA-MZI (see Fig. 12b and Fig. 12c). At the SOA-MZI switch, fast switching of the shorter bursts between the bar and cross-bar state of the switch is achieved based on the existence of a control wavelength from the tuneable laser and the temporal position of this control. The SOA-MZI operation is based on standard switching interference when a wavelength is applied to one of the two SOA modules. Finally, the controller reinserts a new BCH for the next node. The combination of slow and fast switch is wavelength selective and can effectively support OCS, OBS or even OPS.

Finally at the egress side, the node integrates - similar to the

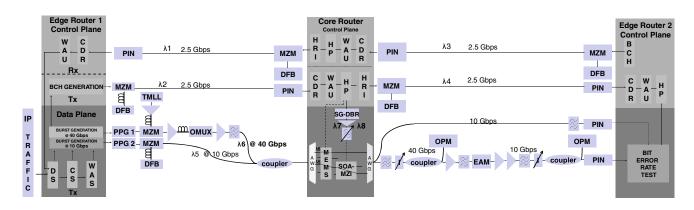


Fig. 13. Physical layer representation of application-aware, multi-granular, multi bit rate OBS testbed

core router - the CDR, WAU and HP units. The control plan is realised for duplex operation (between core router and edg router 2) with out-of-band control channels at $\lambda_3 = 1541.3$ nn and $\lambda_4 = 1547.4$ nm for the two directions respectively. After the arrival of a BCH on the control channel, the extracted (b' the HP unit) information about the offset and the burst lengtl is processed and the appropriate burst envelope is generated and fed to the BER tester for direct BER measurements or the burst level. On the data plane, the 10Gbps burst data are detected with a pre-amplified receiver, while the 40Gbps burst are first de-multiplexed to 10Gbps by means of an Electro Absorption Modulator (EAM) and then passed to the pre amplified receiver. In practice, a slightly shorter envelope o the exact burst length is used to gate the BER tester, in order to ease synchronization between the BER tester and the received burst.

B. Experimental Results

For the experimental evaluation of the MG-OXC, the OBS testbed described above is used to demonstrate dynamic con trolled switching of variable-length optical bursts (from 200n up to 30ms) transmitted at 10Gbps over $\lambda_5 = 1553.00$ nn and 40Gbps over $\lambda_6 = 1556.65$ nm. The 40Gbps data stream carries small and medium-sized bursts (200ns, $3.6\mu s$, $5\mu s$) which are passed through the pre-configured MEMS to the SOA-MZI for fast switching. The 10Gbps data stream cai be considered as long bursts or short circuits (30ms) and is switched by the MEMS. In parallel to these data bursts the appropriate BCH is generated by the ingress node and transmitted to the core router, which in turn controls the MEMS and the SOA-MZI (via the tuneable laser). Fig. 14L shows the 3.6 μs long control signal on $\lambda_7 = 1538.94$ nm from the tuneable laser that controls the SOA-MZI, and the resulting switched bursts at both output ports for the cross-bar switching state. Another burst is switched using a $5\mu s \log$ control signal on $\lambda_8 = 1542.17$ nm (Fig. 14c). Fig. 14d shows the 200ns control signal on $\lambda_9 = 1548$ nm and the switched bursts on both output ports. The SOA-MZI gate is biased at the saturated point, and configured to achieve maximum static extinction ratio between the two output ports at an operating current of 354mA and 360mA for both SOAs respectively. The configuration is based on a counter propagating set-up between the data and the control signals while a holding beam

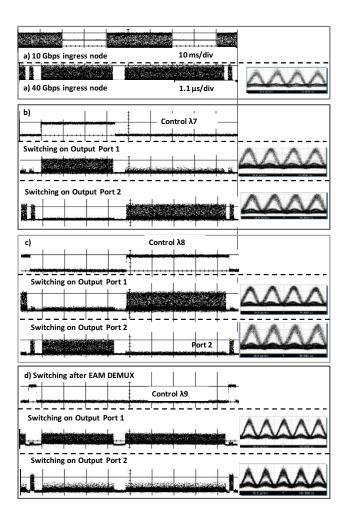


Fig. 14. a) Variable sized bursts at 10 and 40Gbps, b) 40Gbps SOA-MZI burst switching on both output ports based on control 1, c) control λ_8 , and d) control λ_9

is used (co-propagating with data) in order to reduce the noise output of the SOAs and improve the functionality of the gate for bursty switching.

BER measurements of the fast switched bursts at 40Gbps through the controllable SOA-MZI switch have been obtained on the burst level after rate de-multiplexing with an EAM. The results (Fig. 15) show an acceptable penalty for this type of fast switch that varies between 1dB and 2.2dB for all the

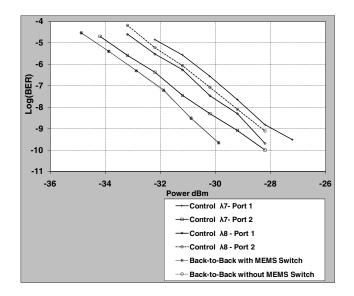


Fig. 15. BER performance of the multi-granular switch

studied cases; these are the four possible bar and cross-bar switching states and for both control signals at λ_7 and λ_8 . BER measurements at the output port of the MEMS switch revealed a negligible penalty for both the 10 and 40Gbps burst data streams, implying that signal quality is not affected by the slow switch fabric.So the overall performance of the MG-OXC is independent of the relative connectivity of the fast switch element (SOA-MZI) to the MEMS switch element. So both sequential and parallel MG-OXC architectures have the same physical layer performance.

It is also noteworthy that the utilized SOA-MZI-based setup for fast switching is polarization sensitive. The optimization of polarization controllers and dynamic extinction ratio of the switch is done only once for the control signal on λ_7 at switching port 2 and maintained for all other cases. This is the reason why this particular case shows better BER performance ($\hat{1}$ dB penalty) compared to the other three cases (up to 2.2dB penalty).

In this experiment, the main purpose was to successfully demonstrate the functionality of a multi-granular switch able to operate at multiple bit rates in an application-aware mode, defined by the appropriate control messages generated at the ingress node and processed at the core and egress router. For this reason, a simple type of fast switch has been chosen with certain known limitations. However, the intrinsic operating flexibility of the multi-granular OBS set-up presented here, as well as the flexibility in the node architecture, allows the implementation of different types of fast switching set-ups, as the control signals are generated from the core router processor and can be easily adjusted to any switching type. In this sense, polarization insensitive switching types [30], [31] or even regenerative switching schemes [32] can be applied, offering superior performance in terms of BER and better stability almost independent of the input signals. Additionally, the use of the fast tuneable laser-based controller at the core router allows the adoption of wavelength conversion schemes either for switching (through passive routing via AWGs) or for contention resolution purposes [33]. The utilised tuneable laser is capable of switching between 85 different wavelengths in less than 100ns and with a maximum peak power difference of only 0.5dB.

VI. CONCLUSIONS

Existing and emerging applications require dynamic optical networks able to support different traffic profiles with different levels of QoS (e.g. latency, loss). Simultaneously, technologies like OCS, OBS and OPS seek flexible and agile combinations of wavelength and sub-wavelength granularity for switching and routing of circuits, packets and bursts directly in the optical layer. MG-OXC is able to realise all mentioned functionality by employing a flexible and scalable combination of relatively simple, low-cost and high-dimension optical switching fabrics with millisecond switching speeds and low-dimension and expensive switch elements with fast switching speeds. This paper presented the design and nodelevel analysis of MG-OXCs. Furthermore, simulation analysis was used to evaluate a number of important parameters concerning traffic load and switch design. Results indicate that a MG-OXC with a limited number of fast ports, has similar performance to a design which consist solely of fast ports. Finally, we demonstrated the feasibility of this architecture on an application-aware multi bit rate end-to-end OBS testbed. The deployed hardware consisted of a low-cost, commercial, high-dimension, slow 2D MEMS switch (10ms) and a highcost, low-dimension but fast (1ns) SOA-MZI based switch.

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