

Multi-objective optimal sizing of distributed generation by application of Taguchi desirability function analysis



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Abstract

Distributed Generation (DG) is modular power generating technology. DG has beneficial impact on tail end voltages, line losses and operating cost. The sizing and location of DG in distribution system is a vital undertaking. In this paper Taguchi Desirability Function Analysis (TDFA) is presented. The remarkable highlight of TDFA is the skillful handling of simultaneous optimization of nearly thousand objectives and its ability to generate multiple optimal solutions. It facilitates the effortless addition of each objective. TDFA is used for finding the optimal size of single as well as multiple DG. Size estimation is carried out for multiple objectives of minimizing the line losses, improving the voltage profile and improving voltage stability index (VSI). The objectives may be minimized, maximized or assigned target values simultaneously. In the scope of this paper more than nine objectives have been optimized simultaneously. TDFA has been implemented to determine optimal size of DG for different load conditions. The proposed approach is tested and verified on IEEE 33-bus and IEEE 85- bus radial distribution system (RDS).

Keywords Distributed generation · Radial distribution system · VSI · TDFA

List of symbols

DG	Distributed generation	T_i	Target value of response
RDS	Radial distribution system	S_1, S_2, S	Weights
F	No. of input factors	w/lmp	Importance value of response
l	No. of levels	V_{B1}	Voltage at bus 1
FFD	Full factorial design	V_{B2}	Voltage at bus 2
df	Degree of freedom	R_B	Line resistance
Y	Response	X_B	Line reactance
R	No. of responses	I_B	Line current
x_1, x_2	Input factors	I_{Bmax}	Maximum current carrying capacity
β	Regression coefficients	P_{LD}	Active power load connected at bus 2
ϵ	Error term	Q_{LD}	Reactive power load connected at bus 2
2FI	Two-factor interaction	P_{DG}	Active power injection at bus 2
R^2	Coefficient of determination	Q_{DG}	Reactive power injected at bus 2
d	Desirability function	P_2	Total active power load at bus 2
ID	Individual desirability	Q_2	Total reactive power load at bus 2
CD	Composite desirability	P_{loss}	Real power loss
L_i	Minimum acceptable value of response	Q_{loss}	Reactive power loss
U_i	Maximum acceptable value of response	P_{load}	Total system active power load
		Q_{load}	Total system reactive power load

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n	Total no. of responses
nb	Number of total branches
mb	Number of total buses
pf _{DG}	Power factor of DG
VSI ₂	Voltage stability index at bus 2
LL	Lowest limit of response obtained
UL	Highest limit of response obtained
LW	Lower weight
UW	Higher weight

1 Introduction

DG is power generating unit in direct connection with distribution system or load. Recently there is rising interest in DG particularly in context of concern over environmental issues, electricity market restructuring, advanced development in power electronics and essential energy storage devices. DG offers a wide scope of opportunities and has a prime role to play in the modern distribution system. DG offers benefits like loss minimization, improved voltage profile and reliability enhancement of the overall system. Additionally DG will prove to be economically viable with fuel cost cut as well as lowered operation and maintenance cost. Moreover DG provides ancillary services such as reactive power support and frequency control. However integration of DG puts many constraints on distribution system operations. DG will have unfavorable impact on the system parameters if not sized and placed optimally. Such situation frequently results in reverse power flow, increase in fault levels, voltage rise, and harmonic distortion.

Many diverse techniques have been suggested to find optimal location as well as to estimate near optimal DG size for various objectives. The techniques found in the literature can be extensively classified as analytical approaches, numerical methods and heuristic techniques.

Authors have assessed optimal sizing of DG using analytical strategy based on exact loss formula and then most suitable bus for DG placement is found using Fuzzy Expert System [1]. The placement and optimal size of DG is evaluated with the objective of congestion management using analytical technique [2]. The optimization of DG type, size and placement in RDS was modeled as a problem of Mixed-Integer Linear Programming (MILP) for minimizing the total investment and operating costs [3]. Authors have implemented Mixed-Integer Nonlinear Programming (MINLP) technique for placement and size formulation of DG [4]. The objective is improvement in the stability margin. Authors have considered probabilistic nature of load as well as renewable DG generation in the work. A feed forward artificial neural network approach is applied to determine the optimal size of DG units [5]. Authors presented Genetic Algorithm (GA) to search for optimal DG

allocation [6]. Adel A. et al. proposed Water Cycle Algorithm (WCA) for optimal placement and sizing of DGs and capacitor banks (CBs). Authors optimized a multi-objective function which included minimizing power losses, voltage deviation, total electrical energy cost, total emissions and improving the voltage stability index [7]. A Modified Particle Swarm Optimizer (MPSO) based approach is suggested for placing multiple WPDGs (Wind Power Distributed Generators) optimally along with capacitors [8]. Fuzzy-Genetic Algorithm (FGA) has been used for simultaneous optimization of various DG parameters [9]. GA-based Tabu Search method (GATS) has been applied for investigating the problem of optimal placement of multi types DG units [10]. A novel meta-heuristic technique Modified Gbest-guided artificial bee colony (MGABC) is used for the multiple objective of power loss reduction, voltage stability improvement and enhancement of voltage level [11]. To identify the ultimate DG location, loss sensitivity analysis (LSA) is used whereas hybrid Artificial Bee Colony and Cuckoo search (ABC-CS) algorithm is implemented for optimal sizing DG [12]. Ant Lion Optimization Algorithm (ALOA) [13] along with LSA has been implemented for deducing optimal sizing and placement of renewable DG. Many other heuristic techniques such as hybrid of ABC and Ant Colony Optimization (ACO) [14], Krill Herd Algorithm (KHA) [15] and Simulated Annealing (SA) [16] have been employed for estimating optimal placement and sizing of DG.

Some of the techniques discussed can handle multi-objective function however addition of each objective leads to complex procedure. None of the techniques cited in the literature have presented multiple near optimal solutions. Taguchi Desirability Function Analysis (TDFA) is presented in this paper. It is an extremely robust statistical tool, which is used extensively in the field of quality control [17]. A combined TDFA and GA approach is utilized to acquire the optimal combination of parameters for manufacturing absorption film required in solar power sector [18]. Taguchi method coupled with the fuzzy logic based desirability function analysis is used for the optimization of bone drilling procedure to limit the drilling induced damage of bone in orthopaedic surgery [19]. Authors have presented TDFA for optimizing of injection moulding process parameters [20]. Taguchi's orthogonal array (TOA) method has been applied for probabilistic load flow studies [21] and state estimation of hybrid power system [22]. To the best of author's knowledge application of TDFA approach for distribution system problem is not found in literature. Contribution of the paper is presentation of TDFA which is remarkably competent in handling complex framework such as power system. TDFA skillfully handles simultaneous optimization of nearly thousand objectives and presents multiple near optimal solutions.

In this paper the TDFA approach is applied for finding the optimal size of DG with multiple objectives of power loss minimization, improvement of voltage profile, improvement in voltage stability index (VSI) and improvement in power factor of DG. The objectives may be minimized, maximized or assigned target values simultaneously. TDFA is used to compute optimal size of multiple DG for IEEE 33-bus and IEEE 85-bus RDS considering different load conditions. The paper is organized in nine sections. The proposed methodology is introduced in Sect. 2 whereas in Sect. 3 problem formulations are described. The Sect. 4 gives description of test systems used. The Sect. 5 explains the methodology adopted for optimal bus location of DG and implementation of TDFA for optimal sizing of DG is described in Sect. 6. Optimization results are discussed in Sect. 7. The results are validated in Sect. 8 followed by conclusion in Sect. 9.

2 The proposed methodology: TDFA

In this paper *Design Expert version 10 software* is used for implementation of TDFA. TDFA is implemented in four steps viz.

1. Taguchi design of experiments (TDOE)
2. selection of model
3. analysis of responses
4. desirability function analysis (DFA)

2.1 Taguchi design of experiments (TDOE)

The optimal sizing of DG is embarked on as an experiment. An experiment is a process which can be divided broadly into three parts as follows.

1. *System* The system can be considered as the heart of the process. For optimal sizing of DG, the system is a topology of the RDS, incorporating all buses and line data.
2. *Input Factors* These are variable signals which serve as starting mechanisms of the process. In the scenario considered, input factors are real and reactive power injected by DG and change in load demands.
3. *Response* Response is nothing but the performance output of the system. Each response constitutes an objective. In this case, responses are the voltage magnitudes at load buses, real power losses, reactive power losses etc.

The design of experiments (DOE) endeavors to plan systematic conduction of experiments in order to acquire data in an intelligent and controlled manner with minimum efforts [23].

Data obtained from DOE provides the information necessary to establish the relationship between specified input factors and the responses of the given process. The possible estimates of input factors are termed as *levels*. The selection of the input factors, their levels and responses is the most important and critical stage in the implementation. In DOE when all possible combinations of given input factor levels are considered, it is termed as a *full factorial design* (FFD). The maximum possible combinations in FFD are given by F^l . Each combination is considered as an *experiment/trial/run*. Figure 1 shows general model for design of experiments. As shown in Fig. 1, if 4 input factors, each having 5 levels are considered then the total no. of the experiments for FFD are 625. Clearly, conduction of full factorial experiments will be a prohibitive task. Number of experiments required for TDOE is given by equation no. (1).

$$\text{No. of experiments} = \{(F \times df) + 1\} \quad (1)$$

The *degree of freedom* is statistical term which refers to the no. of parameters that can vary [24] and given by equation no. (2).

$$df = (l - 1) \quad (2)$$

TDOE reduces the no. of experiment significantly. The example considered earlier will now need only 17 experiments instead of 625.

2.2 Selection of model

An empirical model is selected to establish the relationship that exists between the important design input factors and the response. A regression model is selected in this work. A first order *regression model* also referred as the *main effect* model, for two variables is given by equation no. (3).

$$Y = \beta_0 + \beta_1 X_1 + \beta_2 X_2 + \varepsilon \quad (3)$$

The unknown parameter β s are estimated from the data acquired from the experiments [23]. *Main effect* model can be extended by adding *two-factor interaction* (2F). In such case response will be given by equation no. (4).

$$Y = \beta_0 + \beta_1 X_1 + \beta_2 X_2 + \beta_{12} X_1 X_2 + \varepsilon \quad (4)$$

The main effect model needs less no. of experiments than its extension. Each experiment will produce a set of responses. The response values obtained from comparatively few experiments enables *response prediction* for FFD.

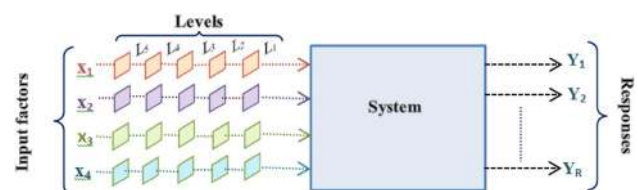


Fig. 1 General model for design of experiments

2.3 Analysis of responses

ANOVA is an efficient statistical tool which is used to interpret experimental data and help decision making. It provides information about how well the chosen model fits the responses. ANOVA supplies information about the effect of the different input factors and their probable interaction as well as the significance of these effects. The *p value* and R^2 of model are checked. The *p-value* is a statistical term used for hypothesis testing. The p-value less than 5% indicate a good model whereas the p-value below 1% confirms that the model is highly significant. R^2 is a measure used for assessing the degree of accuracy with which a model can explain and predict future results. R^2 closer to 1 shows a good fit between the predicted response value and the actual response value. [25].

2.4 Desirability Function Analysis (DFA)

DFA, a well-known multi-response optimization technique, was first implemented by Derringer and Suich [26]. DFA treats each *response* as an *objective* and facilitates transformation of complex multi-response problem into a single optimization problem. Assume that there are 'R' responses, denoted by $Y_i(x)$ (where $i = 1 \dots R$) which are to be optimized simultaneously. For each response, a desirability function is constructed which is denoted by $d_i(Y_i)$ where d_i is *individual desirability* (ID). Depending on the optimization goal set, the three different individual desirability functions may be constructed within an acceptable scope of response values given by $(U_i - L_i)$ viz.

1. Target is the best
2. Lower the best
3. Higher the best

These three functions are given by Eqs. (5), (6) and (7) respectively.

1. Target is the best

$$d_i(\hat{Y}_i)^{target} = \begin{cases} \left(\frac{\hat{Y}_i(x) - L_i}{T_i - L_i}\right)^{S_1} & \dots \text{if } L_i \leq \hat{Y}_i(x) \leq T_i \\ \left(\frac{\hat{Y}_i(x) - U_i}{T_i - U_i}\right)^{S_2} & \dots \text{if } T_i \leq \hat{Y}_i(x) \leq U_i \\ 0 & \dots \text{otherwise} \end{cases} \quad (5)$$

The exponents S_1 and S_2 in Eq. (5) are *weights*. These weights decide how important it is for $\hat{Y}_i(x)$ to be closer to the T_i . The shape of the desirability function depends on these values hence these exponents are also known as

shape constants of desirability function. If the response is required to be in close proximity to the target, the weight can be set to the larger value; otherwise, the weight can be set to the smaller value. Generally the *weights* are assigned the values in the range from 0.01 to 10. The response $d_i(Y_i)$ equal to zero indicates highly undesirable response whereas $d_i(Y_i)$ of value one signals an ideal or highly desirable response.

2. Lower the best

$$d_i(\hat{Y}_i)^{min} = \begin{cases} 0 & \dots \text{if } \hat{Y}_i(x) > U_i \\ \left(\frac{\hat{Y}_i(x) - U_i}{L_i - U_i}\right)^S & \dots \text{if } L_i \leq \hat{Y}_i(x) \leq U_i \\ 1 & \dots \text{if } \hat{Y}_i(x) < L_i \end{cases} \quad (6)$$

The exponent S in Eq. (6) is the weight which regulates how important it is for $\hat{Y}_i(x)$ to be closer to the L_i . If $\hat{Y}_i(x)$ exceeds maximum acceptable value of response then desirability will be equal to zero.

3. Higher the best

$$d_i(\hat{Y}_i)^{max} = \begin{cases} 0 & \dots \text{if } \hat{Y}_i(x) < L_i \\ \left(\frac{\hat{Y}_i(x) - L_i}{U_i - L_i}\right)^S & \dots \text{if } L_i \leq \hat{Y}_i(x) \leq U_i \\ 1 & \dots \text{if } \hat{Y}_i(x) > U_i \end{cases} \quad (7)$$

If $\hat{Y}_i(x)$ is lesser than minimum acceptable value of response then desirability will be equal to zero. Choice of L_i, U_i, T_i, S_1, S_2 and S of is done by investigator.

Once all IDs of 'R' responses are computed, they are consolidated in a unique function called as *composite desirability* (CD) as given by Eq. (7).

$$CD = \left(\prod_{i=1}^R d_i^{w_i}\right)^{\frac{1}{\sum w_i}} \quad (8)$$

w_i is the *importance* (Imp) of each response relative to the others. In the *Design Expert software*, the importance, chosen by the analyst, may fluctuate from 1 for the least important response to 5 for the most important one. The CD value is the measure of extent to which the assigned goal has been achieved. Usually this unique function finds more than one combination of input factor levels for which the responses are acceptable. These combinations are nothing but near optimal solutions. The optimal solutions are arranged in the descending order of their CD value. This feature enables TDFA to offer *multiple optimal solutions*.

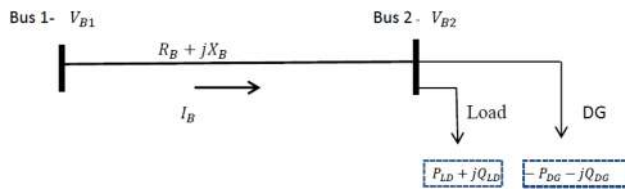


Fig. 2 Two-bus system

3 Problem Formulation

The DG contributing real as well reactive power proves more effective for loss reduction than DG contributing real power only.

In this paper, DG is considered to inject both real as well as reactive power at power factor closer to unity. The DG is modeled as *negative PQ buses*. A simple two-bus network is shown in Fig. 2.

3.1 Objectives

1. minimization of real power losses

$$Min \left\{ P_{loss} = \sum_{i=1}^{nb} |I_{Bi}|^2 R_{Bi} \right\} i = 1, 2, \dots, nb \tag{9}$$

2. minimization of reactive power losses

$$Min \left\{ Q_{loss} = \sum_{i=1}^{nb} |I_{Bi}|^2 X_{Bi} \right\} i = 1, 2, \dots, nb \tag{10}$$

3. voltage profile improvement

$$V_{B2} = V_{B1} - I_B(R_B + jX_B) \tag{11}$$

4. Voltage stability index (VSI) improvement: A VSI has been proposed [27]. For stable operation of the radial distribution networks $VSI_2 \geq 0$ for $n=2, 3, \dots, mb$

$$VSI_2 = \{V_{B1}\}^4 - 4\{P_2X_B - Q_2R_B\}^2 - \{P_2R_B + Q_2X_B\}V_{B1}^2 \tag{12}$$

5. Pf_DG improvement

Grid code for reactive power injection by DG must be complied. The new standard IEEE P1547-2018 [28] provides more flexibility for DG interconnection.

$$Max \left\{ pf_{DG} = \frac{Q_{DG}}{P_{DG}} \right\} \tag{13}$$

3.2 Constraints

1. real power generation constraint

$$0 \leq P_{DG} \leq P_{load}$$
2. reactive power generation constraint

$$0 \leq Q_{DG} \leq Q_{load}$$
3. voltage constraint

$$|V_i| \leq 1 \pm 0.05 p.u. \text{ where } i = 1, 2, 3, \dots, mb$$
4. line current limit constraint

$$I_{Bi} \leq I_{Bmax} i = 1, 2, \dots, nb$$

Line current must be less than or equal to maximum current carrying capacity of that branch.

4 Description of test systems

The test systems used are IEEE 33 and IEEE 85-bus RDS.

4.1 IEEE 33-bus RDS

A single line diagram of IEEE 33-bus, 4369.35 kVA RDS, which has base active and reactive load of 3715 kW and 2300 kVAR [29] respectively, is shown in Fig. 3. The IEEE 33-bus RDS without DG has real and reactive power losses of 210.07 kW and 142.44 kVAR respectively.

4.2 IEEE 85-bus RDS

The Fig. 4 shows a single line diagram of IEEE 85-bus, 3638.71 kVA RDS, which has base active and reactive load of 2550.56 kW and 2595.16 kVAR [30] respectively. The IEEE 85-bus RDS without DG has real and reactive power losses of 313.24 kW and 196.11 kVAR respectively.

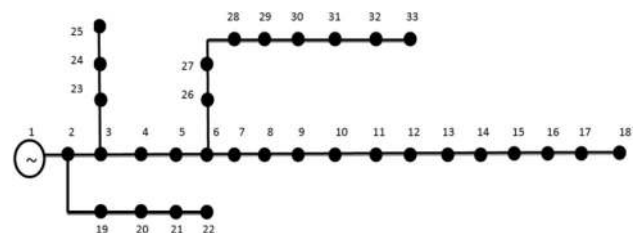


Fig. 3 Single line diagram of IEEE 33-bus RDS

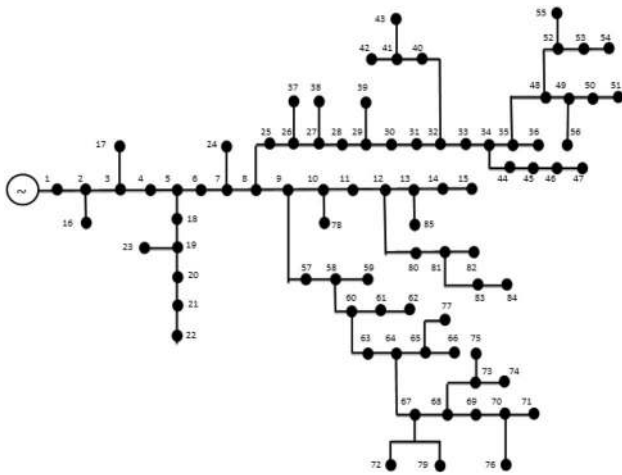


Fig. 4 Single line diagram of IEEE 85-bus RDS

Table 1 Optimal bus selection

Test systems	Bus no.	VSI	Voltage (p.u.)
IEEE 33-bus	18	0.668	0.904
	17	0.670	0.905
	16	0.676	0.907
IEEE 85-bus	54	0.587	0.908
	53	0.588	0.909
	55	0.588	0.909

The values in bold signify the bus chosen for 1 DG placement and its VSI value

5 Determination of Optimal bus location for DG

In this paper candidate buses are identified using VSI for reducing search space for optimal location. The bus having the lowest VSI value is the most sensitive to voltage collapse and is chosen as candidate bus for DG placement. A MATLAB program is prepared for computing losses, bus voltages and VSI of test systems. For each test system, three buses having the minimum VSI are identified after arranging them in an ascending order and same three buses have the minimum voltage as well. These three buses along with their VSI and voltage values are given in Table 1. In case of 1 DG placement bus no. 18 is selected for IEEE 33-bus RDS whereas bus no. 54 is chosen for IEEE 85-bus RDS. The values of VSI for all buses are *recalculated* after placement of first DG and bus having lowest VSI is identified for placement of second DG. The same procedure is followed for placement of next DG.

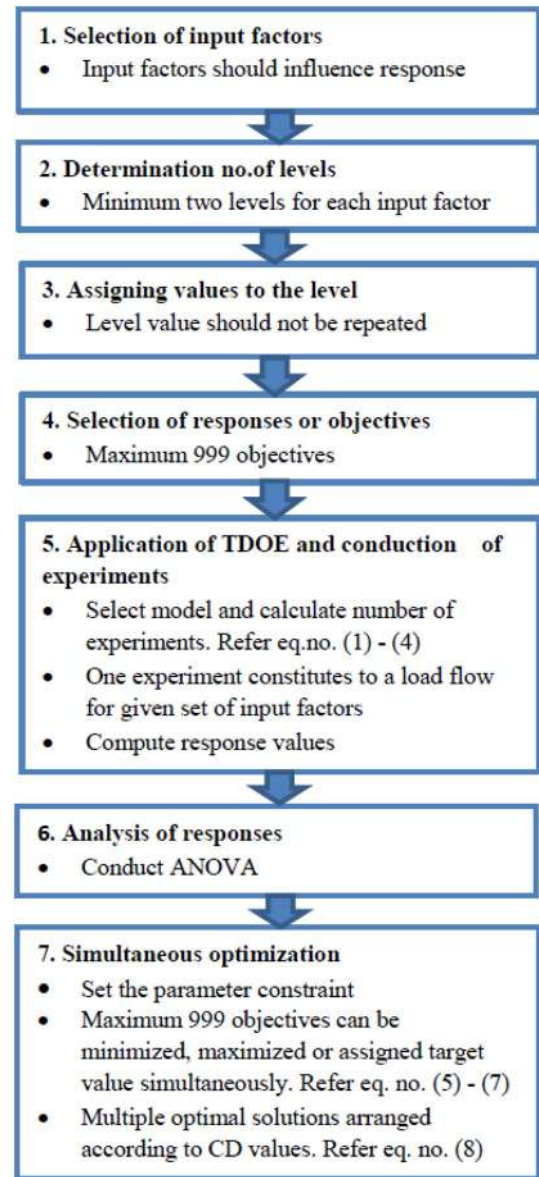


Fig. 5 Steps for implementation of TDFA

Table 2 Input factors and their level values

Level no.	P_{load} (kW)	Q_{load} (kVAR)	P-DG@18 (kW)	Q-DG@18 (kVAR)
1	3715.00	2300.00	557.25	115.00
2	3752.15	2323.00	668.70	184.00
3	3789.30	2346.00	780.15	253.00
4	3826.45	2369.00	891.60	322.00
5	3863.60	2392.00	1003.05	391.00
6	3900.75	2415.00	1114.50	460.00

6 Implementation of TDFA for optimal sizing of DG

Implementation of TDFA for DG placement is carried out following the seven step procedure given in Fig. 5. Detailed description of implementation of approach for 1 DG placement for IEEE 33-bus is given here

Table 2 shows the inputs factors chosen viz. (i) total real power load (P_{load}) in kW (ii) total reactive power load (Q_{load}) in kVAR (iii) real power injected by DG at bus18(P-DG@18) in kW (iv) reactive power injected by DG at bus 18 (Q-DG@18) in kVAR. The next step is to determine the no. of levels and assign level value to each

factor. Four input factors and their respective level values are shown in the Table 2.

All four factors have six levels each. The first level of P_{load} is its base value and subsequent levels are incremented in steps of 1% of base value. The Q_{load} level values are assigned following similar pattern. The first level of P-DG@18 is 15% of base P_{load} and subsequent levels are incremented in steps of 3% of base P_{load} . The first level Q-DG@18 is 5% of base Q_{load} and subsequent levels are incremented in steps of 3% of base Q_{load} . It should be noted that it is *not mandatory* to increment the level values

in uniform steps.

The lowest and highest possible kVA injection by DG would be 568.99 and 1205.70 respectively. The level values are entered in *randomized factorial design* chosen for carrying out the experimentation. Then next step is response selection. As mentioned earlier *each response is treated as an objective*. There is a provision for 999 responses to be minimized, maximized or assigned target value *simultaneously*. This remarkable feature enables TDFA to simultaneously optimize losses, voltages of *all* buses and VSI of *all* buses. However for simplicity nine responses have been simultaneously optimized. Response parameters chosen here are i) real power losses (P_{loss}) in kW ii) reactive power losses (Q_{loss}) in kVAR iii) voltage at bus 16 (V@16) in p.u iv) voltage at bus 17 (V@17) in p.u v) voltage at bus 18 (V@18) in p.u vi) VSI at bus 16 (VSI @16) vii) VSI at bus 17 (VSI @17) viii) VSI at bus 18 (VSI @18) ix) power factor of DG (pf_ DG).

For the no. of input factors and levels considered, FFD will require 6^4 i.e. 1296 experiments. TDOE is applied to reduce the no. of experiments. Hence for considering only main effects of the factors, no. of experiments required is 21 whereas if *2FI* is to be studied, the no. of experiments required is 171. Five experiments are suggested as a *lack of fit*. In this paper *2FI model* is selected. Therefore conduction of total 176 experiments is required.

A load flow calculation for a combination of given input factor levels corresponds to an experiment. The response values for each combination are obtained using MATLAB

Table 3 Parameter settings

Parameter	LL/UL	LW/UW	Imp
Input factors			
1. P_{load} -set = 3715	3715/3900.75		
2. Q_{load} -set = 2300	2300/2415		
3. P-DG@18-in range	557.25/1114.5		
4. Q-DG@18-in range	115/460		
Reponses or objectives			
1. P_{loss} -minimize	126.08/156.25	-/1	3
2. Q_{loss} -minimize	87.06/105.99	-/1	3
3. V@16-target = 1	0.948/1.002	1/1	3
4. V@17-target = 1	0.952/1.015	1/1	3
5. V@18-target = 1	0.955/1.021	1/1	3
6. VSI@16-target = 1	0.808/1.009	1/1	3
7. VSI@17-target = 1	0.822/1.058	1/1	3
8. VSI@18-target = 1	0.831/1.085	1/1	3
9. pf_ DG-maximize	0.771/0.995	1/-	3

program and are fed into the design for further analysis. For each response, an ANOVA is carried out. After analyzing responses of 176 experiments, LL as well UL of all responses are obtained and are shown in Table 3. The exponents in Eqs. (5)–(7) are referred as LW and UW in *Design expert*. LW, UW and Imp of all responses can be varied as per requirement.

7 Optimization results and discussion

7.1 IEEE 33-bus RDS

7.1.1 One DG placement

The parameter setting is shown in Table 3. When parameter is *set equal* to any level only that level is available to be used in optimal solution. But when it is set *in range* any level of that parameter can be chosen. When all the input factors shown are set *in range* then total 6^4 i.e. 1296 solutions would be available arranged in the descending order of respective CD values. The input factors P_{load} and Q_{load} are set *equal* to their base values whereas P-DG@18 and Q-DG@18 are set to *in range*. Then effectively six levels of only two factors i.e. P -DG@18 and Q-DG@18 are available. Under these settings total 6^2 i.e. 36 solutions would be available. P_{loss} and Q_{loss} are to be minimized. The three bus voltages are assigned the target of 1.0 p.u as its UL is more than 1. Similarly VSI at three buses are assigned the target of 1. Response pf_DG is to be maximized. All responses are given importance of 3. LW and UW are set to 1.

Table 4 Optimal solutions for IEEE 33-bus RDS

	P_{loss} (kW)	Q_{loss} (kVAR)	$V@16/V@17/V@18$ (p.u.)	$V@16/V@17/V@18$ (p.u.)	$V@16/V@17/V@18$ (p.u.)	Pf_DG	CD
Without DG & base load	210.07	142.44	0.907/0.905/0.904	0.907/0.905/0.904	0.676/0.670/0.669		
Without DG & 2% increased load	219.27	148.68	0.905/0.903/0.902	0.905/0.903/0.902	0.670/0.664/0.662		
1 DG-Base load $P_{load} = 3715$ kW $Q_{load} = 2300$ kVAR							
Sol. no	P-DG@18/Q-DG@18 (kW/kVAR)	P_{loss} (kW)	Q_{loss} (kVAR)	$V@16/V@17/V@18$ (p.u.)	$V@16/V@17/V@18$ (p.u.)	Pf_DG	CD
1	891.6/391	124.25	87.22	0.986/0.996/1.001	0.946/0.983/1.004	0.92	0.860
2	1003.05/253	128.78	91.30	0.987/0.997/1.002	0.950/0.985/1.007	0.97	0.858
3	1003.05/322	126.63	90.07	0.99/1.000/1.006	0.962/0.999/1.022	0.95	0.849
4	891.6/322	125.79	87.91	0.983/0.992/0.997	0.935/0.969/0.988	0.94	0.834
5	891.6/460	123.46	87.16	0.989/1.000/1.005	0.958/0.998/1.02	0.89	0.831
1 DG-2% increased load $P_{load} = 3789.3$ kW $Q_{load} = 2346$ kVAR							
1	1003.05/322	131.91	93.48	0.989/0.998/1.004	0.955/0.992/1.015	0.95	0.816
2	891.6/391	129.71	90.76	0.985/0.994/0.999	0.94/0.976/0.996	0.92	0.816
2 DG-Base load $P_{load} = 3715$ kW $Q_{load} = 2300$ kVAR							
Sol. no	PDG@18/QDG@18 (kW/kVAR)	P_{loss} (kW)	Q_{loss} (kVAR)	$V@16/V@17/V@18/V@33$ (p.u.)	$V@16/V@17/V@18/V@33$ (p.u.)	Pf_DG	CD
1	780.15/253	54.25	43.81	0.994/1.001/1.005/1.000	0.977/1.005/1.021/0.999	0.920	0.884
2	780.15/207	55.34	44.46	0.992/0.999/1.003/1.000	0.968/0.995/1.011/0.998	0.927	0.876
3	780.15/253	56.32	44.94	0.994/1.001/1.005/0.998	0.975/1.003/1.019/0.991	0.932	0.872
4	780.15/299	53.45	43.39	0.996/1.004/1.008/1.000	0.985/1.015/1.032/1.000	0.911	0.871
5	780.15/207	61.66	49.52	0.993/1.000/1.003/1.000	0.96/0.998/1.000/1.000	0.955	0.867
2 DG-2% increased load $P_{load} = 3789.3$ kW $Q_{load} = 2346$ kVAR							
1	780.15/253.00	54.54	44.17	0.992/1.000/1.004/1.000	0.971/0.999/1.016/1.001	0.907	0.872
2	780.15/253.00	56.40	45.12	0.992/0.999/1.003/0.998	0.969/0.997/1.014/0.993	0.920	0.863

Out of these 36 solutions first 5 solutions are given in Table 4. The first solution for the base load, P-DG@18 and Q-DG@18 of size of 891.6 kW and 391 kVAR respectively has the highest CD value of 0.86. The P_{loss} and Q_{loss} are 124.25 kW and 87.22 kVAR respectively. The voltages V@16, V@17 and V@18 are improved to 0.986, 0.996 and 1.001 respectively. Similarly VSI@16, VSI@17 and VSI@18

are improved to 0.946, 0.983 and 1.004 respectively with pf_{DG} of 0.92. The ID values of responses are not shown here.

Figure 6 shows CD as well as ID variation of responses for first 5 solutions given in Table 4. It should be noted that the fifth solution has P_{loss} and Q_{loss} lesser than first solution whereas V@16, V@17, VSI@16 and VSI@17 are

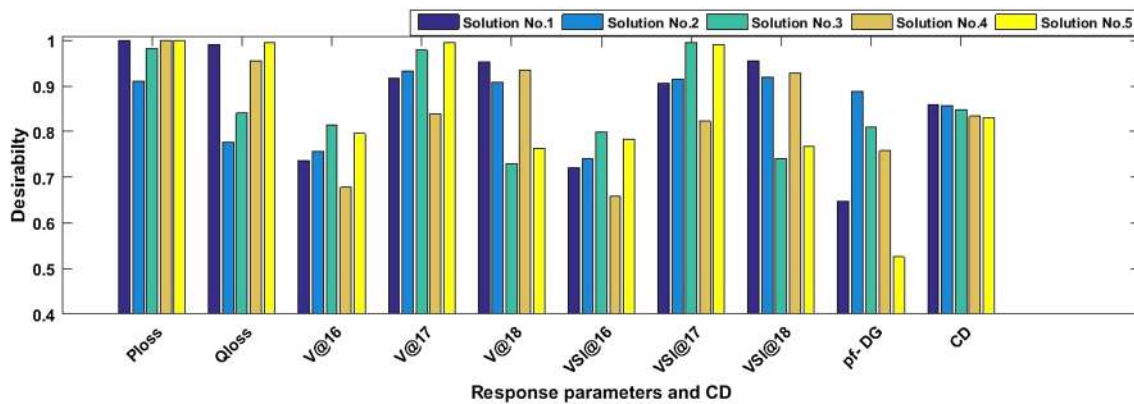


Fig. 6 CD and ID value values of responses for first 5 solutions for IEEE 33 BUS - 1DG placement

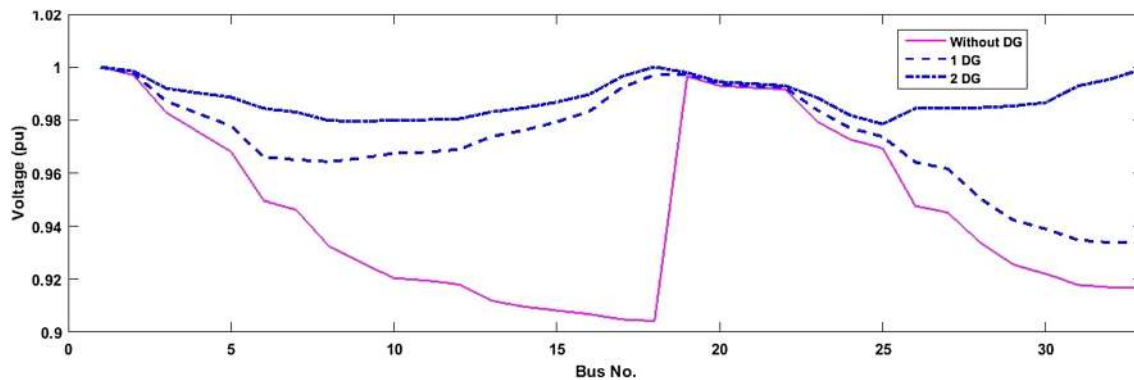


Fig. 7 Voltage profile of 33-bus RDS with and without DG

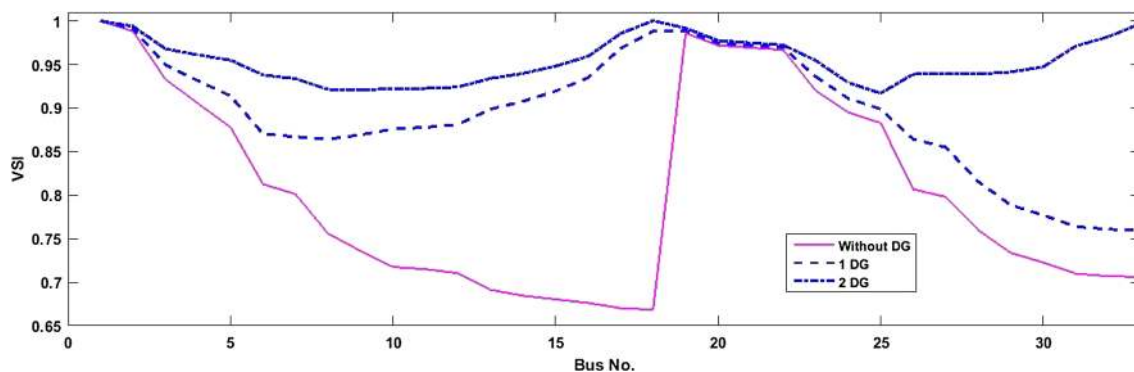


Fig. 8 Variation of VSI for 33-bus RDS with and without DG

Table 5 Optimal solutions for IEEE 85-bus RDS

	P_{loss} (kW)	Q_{loss} (kVAR)	$V@53V@54N@55$ (p.u)	$V@53V@54N@55$ (p.u)	Q_{loss} (kVAR)	P_{loss} (kW)	Q_{loss} (kVAR)	$V@53V@54N@55$ (p.u)	$V@53V@54N@55$ (p.u)	Pf_DG	CD	
Without DG & Base load	313.24	196.11	0.8757/0.8754/0.876	0.8757/0.8754/0.876	196.11	160.56	91.26	0.998/1.004/0.993	0.992/1.015/0.972	0.87	0.815	
Without DG & 2% increased load	327.53	205.06	0.8729/0.8726/0.873	0.8729/0.8726/0.873	205.06	162.28	92.70	0.997/1.002/0.992	0.987/1.01/0.967	0.89	0.809	
1 DG-Base load $P_{load} = 2550.56$ kW $Q_{load} = 2595.16$ kVAR												
sol. no	P-DG@54/Q-DG@54 (kW/kVAR)	P_{loss} (kW)	Q_{loss} (kVAR)	$V@53V@54N@55$ (p.u)	$V@53V@54N@55$ (p.u)	Q_{loss} (kVAR)	P_{loss} (kW)	Q_{loss} (kVAR)	$V@53V@54N@55$ (p.u)	$V@53V@54N@55$ (p.u)	Pf_DG	CD
1	1118/622.84	160.56	91.26	0.998/1.004/0.993	0.998/1.004/0.993	91.26	160.56	91.26	0.998/1.004/0.993	0.992/1.015/0.972	0.87	0.815
2	1118/583.91	162.28	92.70	0.997/1.002/0.992	0.997/1.002/0.992	92.70	162.28	92.70	0.997/1.002/0.992	0.987/1.01/0.967	0.89	0.809
3	1118/661.77	159.05	89.92	0.999/1.005/0.994	0.999/1.005/0.994	89.92	159.05	89.92	0.999/1.005/0.994	0.997/1.021/0.976	0.86	0.809
4	1118/540.00	164.53	94.47	0.995/1.001/0.99	0.995/1.001/0.99	94.47	164.53	94.47	0.995/1.001/0.99	0.981/1.004/0.962	0.90	0.793
5	1045.73/661.77	157.25	90.12	0.994/0.999/0.989	0.994/0.999/0.989	90.12	157.25	90.12	0.994/0.999/0.989	0.975/0.997/0.955	0.85	0.778
1 DG-2% increased load $P_{load} = 2601.57$ kW $Q_{load} = 2647.06$ (kVAR)												
1	1118/661.77	165.70	94.27	0.997/1.003/0.992	0.997/1.003/0.992	94.27	165.70	94.27	0.997/1.003/0.992	0.988/1.012/0.967	0.86	0.738
2	1118/622.84	167.32	95.67	0.996/1.002/0.991	0.996/1.002/0.991	95.67	167.32	95.67	0.996/1.002/0.991	0.983/1.006/0.963	0.87	0.732
2 DG-Base load 2550.56 kW $Q_{load} = 2595.16$ kVAR												
Sol. no	PDG@54/QDG@54	P_{loss} kW	Q_{loss} kVAR	$V@53V@54N@55$ p.u	$V@53V@54N@55$ p.u	P_{loss} kW	Q_{loss} kVAR	$V@53V@54N@55$ p.u	$V@53V@54N@55$ p.u	Pf_DG	CD	
1	867.19/493.08	790.67/622.84	78.64	42.50	1.000/1.004/0.996/1.000	78.64	42.50	1.000/1.004/0.996/1.000	0.999/1.017/0.983/0.999	0.830	0.859	
2	867.19/493.08	790.67/674.74	77.06	41.19	1.000/1.005/0.997/1.001	77.06	41.19	1.000/1.005/0.997/1.001	1.002/1.020/0.986/1.006	0.818	0.853	
3	867.19/544.98	790.67/622.84	77.09	415.2	1.002/1.006/0.998/1.000	77.09	415.2	1.002/1.006/0.998/1.000	1.006/1.025/0.990/1.002	0.818	0.853	
4	867.19/493.08	714.16/778.55	73.83	39.12	1.000/1.005/0.996/1.000	73.83	39.12	1.000/1.005/0.996/1.000	1.001/1.019/0.985/0.998	0.799	0.849	
5	867.19/544.98	790.67/570.94	78.94	42.63	1.001/1.005/0.997/0.999	78.94	42.63	1.001/1.005/0.997/0.999	1.004/1.022/0.988/0.996	0.830	0.846	
2 DG-2% increased load $P_{load} = 2601.57$ kW $Q_{load} = 2647.06$ kVAR												
1	867.19/544.98	790.67/674.74	78.63	42.16	1.000/1.005/0.996/1.000	78.63	42.16	1.000/1.005/0.996/1.000	1.000/1.019/0.984/1.001	0.805	0.847	
2	867.19/596.89	790.67/622.84	79.00	42.31	1.001/1.006/0.997/0.999	79.00	42.31	1.001/1.006/0.997/0.999	1.005/1.024/0.988/0.997	0.806	0.833	
3 DG-Base load 2550.56 (kW) $Q_{load} = 2595.16$ (kVAR)												
Sol. no	P-DG@54/Q-DG@54 kW/kVAR	P-DG@76/Q-DG@76 kW/kVAR	P-DG@84/Q-DG@84 kW/kVAR	P_{loss} kW	Q_{loss} kVAR	$V@53V@54N@55$ p.u	$V@53V@54N@55$ p.u	$V@53V@54N@55$ p.u	$V@53V@54N@55$ p.u	Pf_DG	CD	
1	739.66/415.23	612.13/622.84	535.618/337.371	53.45	27.03	0.999/1.003/0.996/1.000/1.000	0.999/1.003/0.996/1.000/1.000	0.997/1.013/0.984/1.002/1.001	0.997/1.013/0.984/1.002/1.001	0.808	0.888	
2	739.66/467.13	612.13/570.94	535.618/337.371	53.31	27.00	1.001/1.004/0.997/0.999/1.000	1.001/1.004/0.997/0.999/1.000	1.002/1.018/0.988/0.998/1.001	1.002/1.018/0.988/0.998/1.001	0.809	0.885	
3	739.66/363.32	612.13/622.84	535.618/337.371	55.25	28.28	0.998/1.001/0.994/1.000/1.000	0.998/1.001/0.994/1.000/1.000	0.990/1.005/0.977/0.999/0.999	0.990/1.005/0.977/0.999/0.999	0.819	0.884	
4	739.66/467.13	612.13/622.84	535.618/285.468	53.36	27.04	1.000/1.004/0.997/1.000/0.999	1.000/1.004/0.997/1.000/0.999	1.002/1.018/0.988/1.002/0.997	1.002/1.018/0.988/1.002/0.997	0.808	0.884	
5	739.66/415.23	612.13/570.94	535.618/337.371	54.80	28.12	0.999/1.003/0.995/0.999/1.000	0.999/1.003/0.995/0.999/1.000	0.995/1.010/0.981/0.995/0.998	0.995/1.010/0.981/0.995/0.998	0.819	0.883	
3 DG-2% increased load $P_{load} = 2601.57$ kW $Q_{load} = 2647.06$ kVAR												
1	739.66/467.13	688.65/467.13	535.62/389.27	59.24	30.30	0.999/1.003/0.996/1.000/1.000	0.999/1.003/0.996/1.000/1.000	0.997/1.013/0.983/1.001/1.002	0.997/1.013/0.983/1.001/1.002	0.829	0.877	
2	739.66/467.13	612.13/674.74	612.13/181.66	59.55	30.71	0.999/1.003/0.996/1.000/1.000	0.999/1.003/0.996/1.000/1.000	0.997/1.013/0.983/1.001/1.002	0.997/1.013/0.983/1.001/1.002	0.829	0.876	

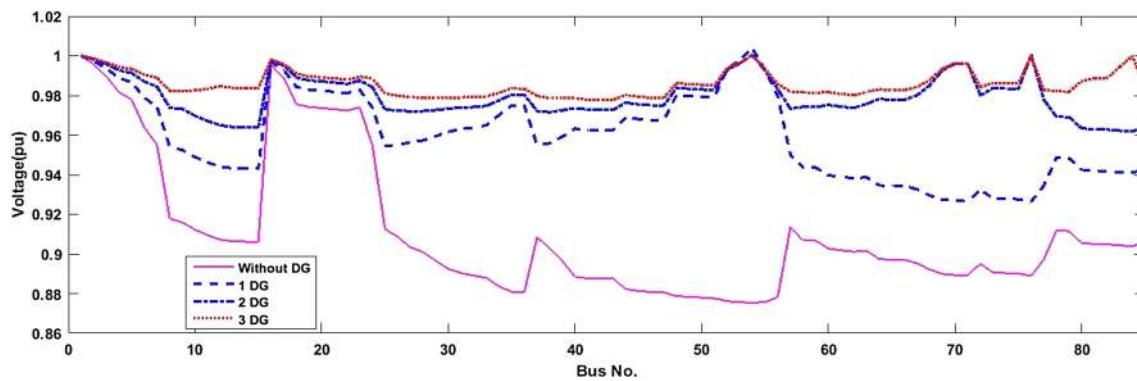


Fig. 9 Voltage profile of 85-bus RDS with and without DG

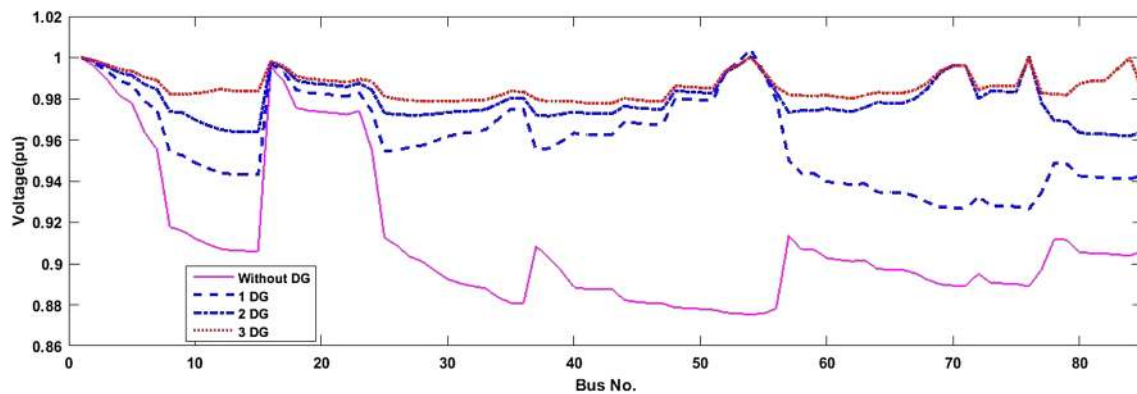


Fig. 10 Variation of VSI for 85-bus RDS with and without DG

higher than first solution. Accordingly ID of P_{loss} , Q_{loss} , $V@16$, $V@17$, $VSI@16$ and $VSI@17$ for fifth solution is higher than that of first solution. However $V@18$ and $VSI@18$ of fifth solution deviate from target value of 1 which results in lesser ID values. ID of $V@18$, $VSI@18$ and pf_{DG} of first solution is higher than that of fifth solution. Hence first solution has highest CD. Investigator can choose the optimal solution as per requirement. For instance, if it is required to have higher pf_{DG} , then second optimal solution which have DG power factor of 0.97 may be selected.

Optimal size of DG for 2% increase in load can be found by setting the P_{load} and Q_{load} to their third levels. Table 4 shows first 2 optimal solutions when base load is increased by 2% and 1 DG is placed.

7.1.2 Two DG placement

For second DG placement next candidate bus having the lowest VSI after 1 DG placement is to be identified. Using MATLAB program VSI values at all the buses are recalculated after 1 DG placement. It is found that bus 33 has the lowest VSI value.

Second DG is placed at bus 33. In case of 2 DG placement there would be 6 input factors of different level values. The results obtained are given in Table 4. The P_{loss} and Q_{loss} are reduced to 54.25 kW and 43.81 kVAR respectively. Table 4 shows first 2 optimal solutions when base load is increased by 2% and 2 DG are placed. Voltages at weak buses have improved significantly. The voltage profile as well as VSI variation of IEEE 33, with and without DG, are shown in Fig. 7 and Figure 8. The voltage and VSI values of the first optimal solution shown in Table 4 are used in Figs. 7 and 8.

7.2 IEEE 85-bus RDS

TDFA is implemented for estimating optimal DG sizes for IEEE 85-bus RDS in case of 1 DG, 2 DG and 3 DG placement by following the same procedure. In case of 3 DG placement there would be 8 input factors of different level values and 13 responses have been simultaneously optimized. The results obtained are given in Table 5.

P_{loss} is reduced to 160.56 kW, 78.64 kW and 53.45 kW while Q_{loss} is reduced to 91.26 kVAR, 42.50 kVAR and 27.03 kVAR for 1 DG, 2 DG and 3 DG placement resp. The

Table 6 Comparison of results

Test System	Technique [Ref no.]	No. of DG	Optimal bus location	Size of DG (kVA)	P _{loss} (kW)	Q _{loss} (kVAR)	Improved Vmin@bus (p.u.)	Pf_DG	%P _{loss} reduction	%Q _{loss} reduction
33-bus	without DG		-	-	210.07	142.44	0.904@18	-	-	-
	AA [31]	1	6	2968	131.16	-	0.924@18	0.82	35.41	-
	VSIM [32]	1	16	1200.00	112.79	77.45	0.937@18	0.90	46.31	45.63
	WOA [33]	1	15	1255.89	108.41	74.77	0.9583@18	0.90	48.39	47.5
	TDFA	1	18	973.57	124.25	87.22	1.001@18	0.92	40.85	38.77
	AA [30]	2	6,14	2396.86	131.53	-	0.914@18	0.80	37.55	-
	TDFA	2	18,33	2058.57	54.25	43.81	1.005@18	0.92	74.18	69.24
85-bus	without DG		-	-	313.24	196.11	0.8754@54	-	-	-
	WOA [32]	1	55	1289	157.49	90.98	0.910@54	0.90	49.72	53.61
	TDFA	1	54	1279.79	160.56	91.26	1.004@54	0.87	48.74	53.46

voltage profiles as well as VSI variation of IEEE 85 RDS, with and without DG, are shown in Figs. 9 and 10. The voltage and VSI values of the first optimal solution shown in Table 5 are used in Figs. 9 and 10.

8 Comparison of results

Comparison of TDFA solutions with results obtained by other techniques is carried out. Authors [31] have used novel Analytical Approach (AA) for minimizing the loss associated with the active and reactive components of DG branch current. This approach is not applicable for unbalanced and meshed distribution system. TDFA has no such constraints. In [32] comparison of four methods for optimally allocating DG is presented. One of those methods is Voltage Sensitivity Index Method [VSIM]. Authors have emphasized that losses can be reduced significantly with reactive power management of DG. Whale Optimization Algorithm (WOA) [33], a novel heuristic algorithm is used to estimate optimal DG size. Authors have computed the optimal size of DGs at different power factors to reduce the power losses of the distribution system and to enhance the voltage profile of the system. However results of heuristic method are not reproducible. The above mentioned techniques are used for comparing proposed approach. None of these techniques offer multiple near optimal solutions or have ability to optimize nearly thousand objectives simultaneously. Table 6 shows the comparison of results IEEE 33-bus RDS as well as IEEE 85-bus RDS. The first optimal solution given in Tables 4 and 5 is used for comparison. It can be observed that voltage improvement by proposed technique is superior than obtained by any of other technique. It should be noted that in all of the first five optimal solutions shown in Tables 4 and 5 for IEEE 33-bus RDS and 85-bus respectively Vmin is significantly improved.

9 Conclusion

In this paper optimal DG sizing is carried out by applying an extremely robust statistical tool, Taguchi Desirability Function Analysis (TDFA) for single as well as multiple DG units. Application of TDFA for optimal sizing of DG is not cited in the literature. The salient features of TDFA are the skillful handling of simultaneous optimization involving a large no. of objectives for complex system and its ability to produce multiple optimal solutions. In this paper more than nine objectives have been simultaneously optimized viz. minimization of reactive power losses (Q_{loss}), improving voltages of various weak buses, improving the Voltage Stability Index (VSI) at various weak buses and improving the

power factor of DG (pf_{DG}). Using TDFA optimal DG size can be found for various load conditions. This approach is tested on IEEE 33 and IEEE 85-bus RDS. Comparison of TDFA results with results obtained by implementation of other optimization techniques is carried out. TDFA can be extended to larger bus systems and nearly thousand objectives can be optimized simultaneously.

Compliance with ethical standards

Conflict of interest On behalf of all authors, the corresponding author states that there is no conflict of interest.

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