

Multiple Second Order Generalized Integrators for Harmonic Synchronization of Power Converters

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Abstract – This paper presents a new frequency-adaptive synchronization method for grid-connected power converters which allows estimating not only the positive- and negative-sequence components of the power signal at the fundamental frequency, but also other sequence components at multiple frequencies. The proposed system is called the MSOGI-FLL since it is based on a decoupled network consisting of multiple second order generalized integrators (MSOGI) which are frequency-adaptive by using a frequency-locked loop (FLL). In this paper, the MSOGI-FLL is analyzed and its performance is evaluated by both simulations and experiments.

Index Terms-- Synchronization, Frequency-locked loops, Harmonic analysis, Frequency estimation, Monitoring.

I. INTRODUCTION

The electricity networks of the future will make an extensive use of power electronics devices and ICT applications [1], as an interface between the generator systems and the electrical network. One of the most important issues in the connection of power converters to the grid is the synchronization with the voltage at the point of common coupling (PCC). In three-phase systems, PLLs based on a synchronous reference frame (SRF-PLL) [2] have become a conventional synchronization technique. Although these systems have shown to be fast and accurate under balanced conditions its response is unacceptably deficient when the utility voltage is unbalanced.

Different advanced synchronization systems, able to deal with this grid conditions, have been proposed as an alternative to SRF-PLL such as the decoupled double synchronous reference frame PLL (DDSRF-PLL), the three phase enhanced PLL (3PH-EPLL) and the dual second order generalized integrator resting on a frequency-locked loop (DSOGI-FLL) was proven to be an effective solution under adverse grid conditions [3]-[9].

Although these advanced synchronization systems are capable to perform an accurate detection of the fundamental components of the voltage under unbalanced conditions they are unable to perform a more precise characterization of the power signals, something that is necessary in those applications where harmonics components, of either the voltage or the line current, should be controlled. Within this

field a synchronization technique based on using adaptive notch filters for estimating multiple frequencies in a single-phase system was described in [9].

This paper proposes a new technique for detecting multiple frequency harmonics, with positive- and negative-sequences, in three-phase systems using an extended approach to that presented in [9]. The proposed system has been named as the MSOGI-FLL. In the following, the main building blocks of the MSOGI-FLL will be presented, putting a special attention on the description of the SOGI and the FLL and its applications for three phase systems. The discrete equation for implementing this system in digital signal processor will be presented as well. Finally the performance of the proposed system will be evaluated by both simulations and experiments.

II. THE SECOND ORDER GENERALIZED INTEGRATOR

An adaptive filter structure, based on the generalized integrator (GI) structure [10], named second order generalized integrator (SOGI) was formally presented in [11]-[12]. The adaptive filter based on the SOGI is shown in Fig. 1.

The two in-quadrature output signals of the filter of Fig. 1,

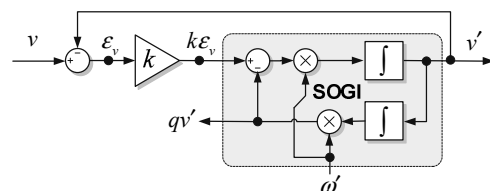


Fig. 1. Adaptive filter based on a SOGI, the SOGI-QSG.

v' and qv' , are defined by the following transfer functions:

$$D(s) = \frac{v'}{v}(s) = \frac{k\omega's}{s^2 + k\omega's + \omega'^2} \quad (1)$$

$$Q(s) = \frac{qv'}{v}(s) = \frac{k\omega'^2}{s^2 + k\omega's + \omega'^2} \quad (2)$$

As it can be concluded from (1) the bandwidth of the band-pass filter is independent of the center frequency ω' , being exclusively set by the gain k . The same happens with the low-pass filter of (2), where the static gain only depends on the value of k . From (1) it can be concluded that the qv' output is always 90-degrees lagged from the v' output; independently of both the frequency of the v input signal and the resonance frequency of the SOGI. For this reason, the adaptive filter of Fig. 1 was named as SOGI quadrature signal generator (SOGI-QSG).

III. THE FREQUENCY LOCKED LOOP

The frequency-locked loop (FLL) was presented in [13] as an effective mechanism for adapting the center frequency of the SOGI-QSG. The adaptive filter, including the FLL, is shown in Fig. 2.

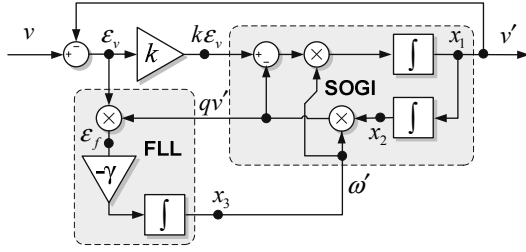


Fig. 2. The SOGI-FLL, a single-phase grid synchronization system.

The relationship between the in-quadrature output signal qv' and the error signal ε_v should be analyzed in order to understand the behavior of the FLL. The transfer function from the input signal v to the error signal ε_v , $E(s)$, is given by (3), likewise the relationship between v and qv' , $Q(s)$, is detailed in (2).

$$E(s) = \frac{\varepsilon_v}{v}(s) = \frac{s^2 + \omega'^2}{s^2 + k\omega's + \omega'^2} \quad (3)$$

Therefore, a frequency error variable ε_f can be defined as the product of qv' by ε_v . The average value of ε_f will be positive when $\omega < \omega'$, zero when $\omega = \omega'$ and negative when $\omega > \omega'$. Hence, as shown in Fig. 2, an integral controller with a negative gain $-\gamma$ can be used to make zero the dc component of the frequency error by shifting the SOGI resonance frequency ω' until matching the input frequency ω . Since the building blocks of the frequency adaptive system of Fig. 2 are the SOGI and the FLL, it was named as the SOGI-FLL.

A. Linearization of the FLL

As demonstrated in [14], the averaged dynamics of the FLL with $\dot{\omega}' \approx \omega$ can be described by (4).

$$\dot{\bar{\omega}}' = -\frac{\gamma V^2}{k\omega'}(\bar{\omega}' - \omega) \quad (4)$$

The equation (4) discloses the relationship between the dynamic response of the FLL and the grid variables and SOGI-QSG gain. From (4), the value of γ can be normalized

according (5) in order to obtain the linearized system of Fig. 3, which is nondependent on neither the grid variables nor the SOGI-QSG gain.

$$\gamma = \frac{k\omega'}{V^2} \Gamma \quad (5)$$

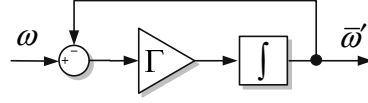


Fig. 3. Simplified frequency adaptation system of the FLL.

The transfer function of the first-order frequency adaptation loop of Fig. 3 is given by:

$$\frac{\bar{\omega}'}{\omega} = \frac{\Gamma}{s + \Gamma} \quad (6)$$

Therefore, the settle time is exclusively dependent on the design parameter Γ and can be approximated by:

$$t_{s(FLL)} \approx \frac{5}{\Gamma} \quad (7)$$

A practical implementation of the feedback-based linearized FLL is shown in Fig. 4. In this system, the FLL gain is online adjusted by feeding-back the estimated grid operating conditions, which guarantees a constant settle time in grid frequency estimation independently of input signal characteristics.

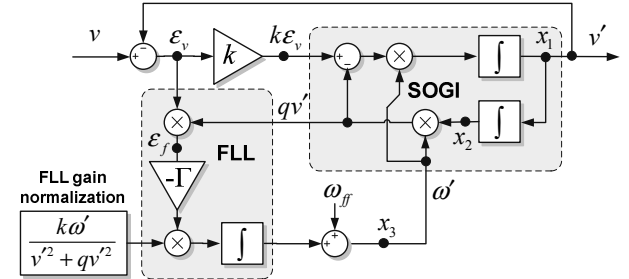


Fig. 4. SOGI-FLL with FLL gain normalization.

IV. DISCRETE IMPLEMENTATION OF THE SOGI-FLL

Considering that the grid synchronization algorithms are normally programmed in microprocessor, the discrete representation of the SOGI-FLL will be presented as follows.

Among different techniques for discretizing continuous systems, a bilinear transform of the derivative term, such as Tustin approximation, is the best choice when matching the frequency response of the continuous-time system is a matter of interest. This approximation is very suitable for discretizing the SOGI-FLL, a pure resonant system in which the effect of the aliasing in the frequency response should be cancelled in the discretization process, mainly when sampling time increases. Therefore, the SOGI continuous-time state-space equations:

$$\dot{\mathbf{x}} = \begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \mathbf{A}\mathbf{x} + \mathbf{B}v = \begin{bmatrix} -k\omega' & -\omega'^2 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} k\omega' \\ 0 \end{bmatrix} v, \quad (8)$$

$$\mathbf{y} = \begin{bmatrix} v' \\ qv' \end{bmatrix} = \mathbf{C}\mathbf{x} = \begin{bmatrix} 1 & 0 \\ 0 & \omega' \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix}, \quad (9)$$

$$\dot{\omega}' = -\gamma x_2 \omega' (v - x_1). \quad (10)$$

will be discretized by using $\dot{x} \approx s'x$ approximated functions with:

$$s' = \frac{2}{T_s} \frac{z-1}{z+1}, \quad (11)$$

being T_s the sampling period. As a result, the SOGI algorithm can be implemented in a digital signal processor by using the following discrete state-space equations:

$$\mathbf{x}_{[n+1]} = \mathbf{A}'\mathbf{x}_{[n]} + \mathbf{B}'v_{[n]}, \quad (12)$$

$$\mathbf{y}_{[n]} = \mathbf{C}'\mathbf{x}_{[n]} + \mathbf{D}'v_{[n]}, \quad (13)$$

being the matrices \mathbf{A}' , \mathbf{B}' , \mathbf{C}' and \mathbf{D}' detailed in (14)-(18). It worth to highlight that the value of ω' in these matrices will be updated with the frequency estimated by the FLL algorithm every computation step.

$$\mathbf{A}' = \left(\mathbf{I} + \frac{T_s}{2} \mathbf{A} \right) \left(\mathbf{I} - \frac{T_s}{2} \mathbf{A} \right)^{-1} \quad (14)$$

$$\mathbf{A}' = \frac{1}{\Delta} \begin{bmatrix} 4 - 2T_s k \omega'_{[n-1]} - T_s^2 \omega'^2_{[n-1]} & -4T_s \omega'^2_{[n-1]} \\ 4T_s & 4 + 2T_s k \omega'_{[n-1]} - T_s^2 \omega'^2_{[n-1]} \end{bmatrix}$$

$$\mathbf{B}' = \left(\mathbf{I} - \frac{T_s}{2} \mathbf{A} \right)^{-1} \mathbf{B} = \frac{1}{\Delta} \begin{bmatrix} 4k \omega'_{[n-1]} \\ 2T_s k \omega'_{[n-1]} \end{bmatrix}, \quad (15)$$

$$\mathbf{C}' = T_s \mathbf{C} \left(\mathbf{I} - \frac{T_s}{2} \mathbf{A} \right)^{-1} \quad (16)$$

$$\mathbf{C}' = \frac{1}{\Delta} \begin{bmatrix} 4T_s & -2T_s^2 \omega'^2_{[n-1]} \\ 2T_s^2 \omega'_{[n-1]} & 2T_s \omega'_{[n-1]} (2 + T_s k \omega'_{[n-1]}) \end{bmatrix}$$

$$\mathbf{D}' = \mathbf{C} \left(\mathbf{I} - \frac{T_s}{2} \mathbf{A} \right)^{-1} \frac{T_s}{2} \mathbf{B} \quad (17)$$

$$\mathbf{D}' = \frac{1}{\Delta} \begin{bmatrix} 2T_s k \omega'_{[n-1]} \\ kT_s^2 \omega'^2_{[n-1]} \end{bmatrix}$$

$$\Delta = 4 + 2T_s k \omega'_{[n-1]} + T_s^2 \omega'^2_{[n-1]} \quad (18)$$

The FLL algorithm is executed after processing the SOGI algorithm and it is discretized by using a trapezoidal integrator, which results in the following discrete equations:

$$\omega'_{[n]} = \omega'_{[n-1]} + \frac{T_s}{2} \gamma_{[n]} (\varepsilon_{f[n]} + \varepsilon_{f[n-1]}), \quad (19)$$

$$\varepsilon_{f[n]} = x_{2[n]} (v_{[n]} - v'_{[n]}), \quad (20)$$

$$\gamma_{[n]} = \Gamma \frac{k \omega'_{[n-1]}}{v_{[n-1]}^2 + q v'^2_{[n-1]}} \quad (21)$$

The initial values of for the SOGI-FLL algorithm are:

$$x_1 = x_2 = 0; \omega'_{[0]} = \omega_{ff}, \quad (22)$$

V. THE DSOGI-FLL

The SOGI-FLL is a single phase grid synchronization system. However, by means of combining two SOGI-QSG and a single FLL it is possible to obtain a structure for three-phase applications.

Considering that most of the three-phase grid-connected power converters employ a three-wire connection, it is only necessary to be synchronized with the positive- and negative-sequence components of the grid voltage. This feature permits to transform the grid voltage vector from the abc to the $\alpha\beta$ stationary reference frames by using the Clarke transformation as indicated in (15).

$$\mathbf{v}_{\alpha\beta} = [T_{\alpha\beta}] \mathbf{v}_{abc}; \quad [T_{\alpha\beta}] = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \quad (23)$$

In turn, the instantaneous positive- and negative-sequence voltage components on the $\alpha\beta$ reference frame can be calculated as shown in (16), where $q = e^{-j\pi/2}$ is a 90-degrees-lagging phase-shifting operator applied on the time-domain to obtain an in-quadrature version of the input waveforms.

Since the SOGI-QSG behaves as a quadrature signal generator itself, the in-quadrature signals to be computed in (16) in order to calculate the instantaneous positive and negative sequence components of the three-phase input vector can be obtained by means of using a SOGI-QSG for each α and β components of the transformed input vector.

$$\mathbf{v}_{\alpha\beta}^+ = [T_{\alpha\beta}] \mathbf{v}_{abc}^+ = [T_{\alpha\beta}] [T_+] \mathbf{v}_{abc} \quad (24)$$

$$= [T_{\alpha\beta}] [T_+] [T_{\alpha\beta}]^T \mathbf{v}_{\alpha\beta} = \frac{1}{2} \begin{bmatrix} 1 & -q \\ q & 1 \end{bmatrix} \mathbf{v}_{\alpha\beta}$$

$$\mathbf{v}_{\alpha\beta}^- = [T_{\alpha\beta}] \mathbf{v}_{abc}^- = [T_{\alpha\beta}] [T_-] \mathbf{v}_{abc} \quad (25)$$

$$= [T_{\alpha\beta}] [T_-] [T_{\alpha\beta}]^T \mathbf{v}_{\alpha\beta} = \frac{1}{2} \begin{bmatrix} 1 & q \\ -q & 1 \end{bmatrix} \mathbf{v}_{\alpha\beta}$$

This reasoning permits to obtain the synchronization structure of Fig. 5, which is able to detect the positive- and negative-sequence components of a three-phase input vector at a certain frequency ω .

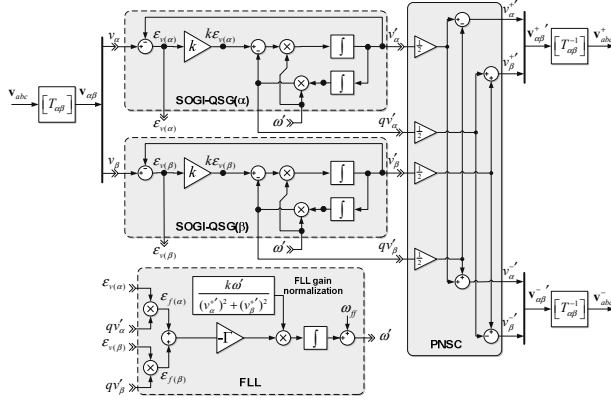


Fig. 5. Block diagram of the DSOGI-FLL divided into its building blocks: SOGI-QSG(β), SOGI-QSG(α), FLL and PNSC.

In the system of Fig. 10, two SOGI-QSGs working on the $\alpha\beta$ stationary reference frame provide the inputs signals to a positive-/negative-sequence calculation block (PNSC), which implements the transformations of (16). This system was extensively discussed in [13] and named as Dual SOGI-FLL (DSOGI-FLL).

VI. PERFORMANCE OF THE DSOGI-FLL

Although the DSOGI-FLL has an inherent pass band filtering behavior, it is not able to estimate accurately the symmetrical components of the fundamental frequency signal if there is an important harmonic distortion in the input. The filtering capability of a single DSOGI-FLL, tuned at 50Hz, is presented in Fig. 6.

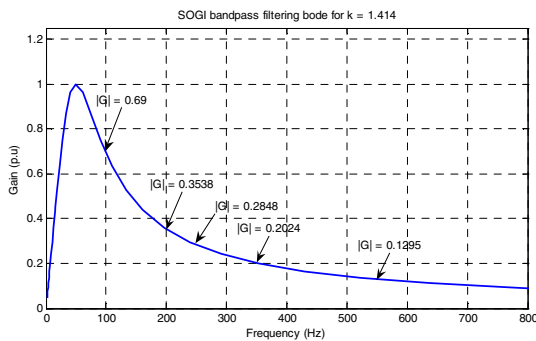


Fig. 6. Filtering capability of the DSOGI-FLL in function of the harmonic order.

This feature has been tested in simulation, considering the DSOGI-FLL structure, shown in Fig. 5, where the input signal has a THD of 40%. This signal has been depicted in Fig. 7.

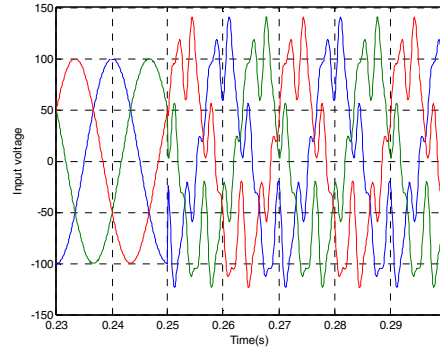


Fig. 7. Testing signal with a THD = 40%.

After processing the signal of Fig. 7 the estimation of the fundamental frequency component, performed by the DSOGI-FLL, gave rise to the waveforms shown in Fig. 8, whose FFT is represented as well.

It can be noticed from the figure how the DSOGI is not

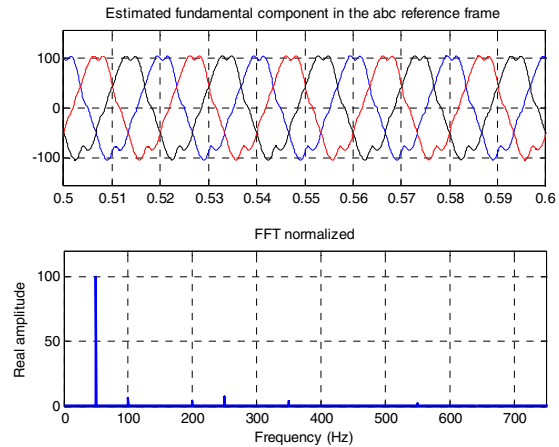


Fig. 8. Estimation of the fundamental component using the DSOGI-FLL

able to extract a sinusoidal signal, due to the high distortion of the input. The reliability of this test can be proven by means of comparing the theoretical and simulated values concerning the harmonic content of the resulting signal, shown in the following TABLE I.

TABLE I
FILTERING CAPABILITY OF THE DSOGI-FLL

Order	Input harmonic	Simulated output	Theoretic output
2 nd	10 %	6.802 %	6.9 %
3 rd	10 %	0 %	0 %
4 th	10 %	3.474 %	3.538 %
5 th	25 %	7.188 %	7.12 %
7 th	20 %	4.076 %	4.048 %
11 th	15 %	1.967 %	1.9425 %

VII. THE MULTIPLE SOGI.FLL

In this section, a cross-feedback network consisting of multiple ‘ n ’ individual DSOGI-QSGs, like the one shown in Fig. 5, tuned at different frequencies, and working in a collaborative way, is presented as an effective solution to accurately detect the sequence components of the grid voltage, even under very extreme distortion conditions. This new detection system will be referred from now on as the Multiple SOGI-FLL (MSOGI-FLL). The main structure of the MSOGI-FLL is presented in Fig. 9.

As it can be noticed from the figure the MSOGI-FLL can be understood as a set of n selective and adaptive filters, tuned at different frequencies, working in parallel. The FLL block is connected only to the DSOGI-QSG-1, in order to detect the fundamental frequency of the input signal. Then, the estimated frequency is used to tune the frequency of each of the DSOGI-QSGs, by multiplying its value by a coefficient which determines the order of the harmonic to be detected by each DSOGI-QSG. This structure permits thus to detect the different harmonic components of the input signal.

The accuracy of this estimation can be highly affected when the harmonic components are in a narrow range, due to the intrinsic bandwidth of the DSOGI-QSG. In order to overcome this drawback the MSOGI-FLL presents an additional and extremely relevant advantage resulting from the cross-feedback network.

As shown in Fig. 9, the input signal of each DSOGI-QSG results from subtracting the output of all the rest of DSOGI-QSGs from the original input signal v . In this way, after a transient process, the input signal of each DSOGI-QSG will be cleaned up from the harmonic components detected by the

rest of DSOGI-QSGs, which will contribute to attenuate the distortion at their outputs.

The Bode diagram for the DSOGI-QSG- i , shown in Fig. 10, for a MSOGI-FLL, consisting of four individual DSOGI-QSGs tuned at the 2nd, 4th, 5th and 7th harmonics, show how the cross-feedback network of the MSOGI-FLL gives rise to notches in the frequency response curve at the frequencies which the individual DSOGI-QSG are tuned to.

As a consequence, the selective filtering characteristic of

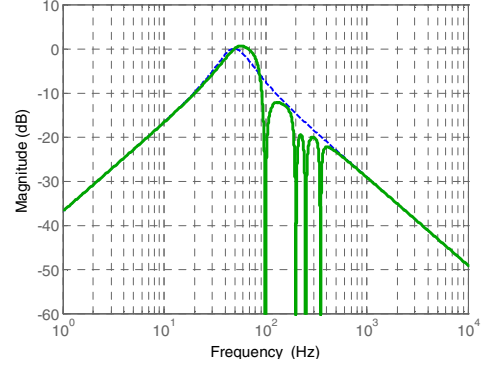


Fig. 10. Frequency response of the single-phase MSOGI-FLL.

each DSOGI-QSG is improved and its response is enhanced in case of high distortion level on the input voltage if compared with the dashed line in Fig. 10, which represents the frequency response curve for the case in which the cross-feedback network is disabled.

This statement can be proven easily by means of applying

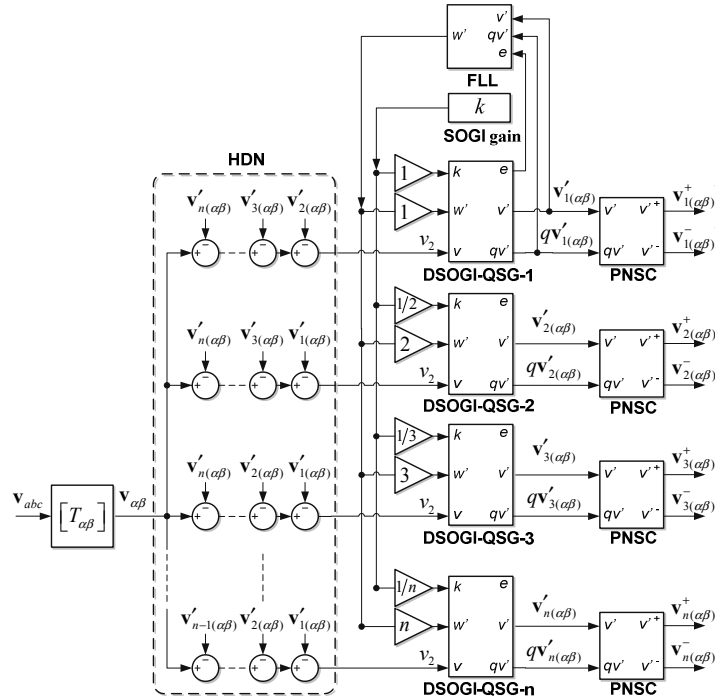


Fig. 9. MSOGI-FLL for three-phase systems able to synchronize with ‘ n ’ harmonic components of the input vector.

the testing signal of Fig. 7 to the input of the MSOGI. The results obtained in this case are shown in the following figure.

The waveforms in Fig.11 stand out clearly the better

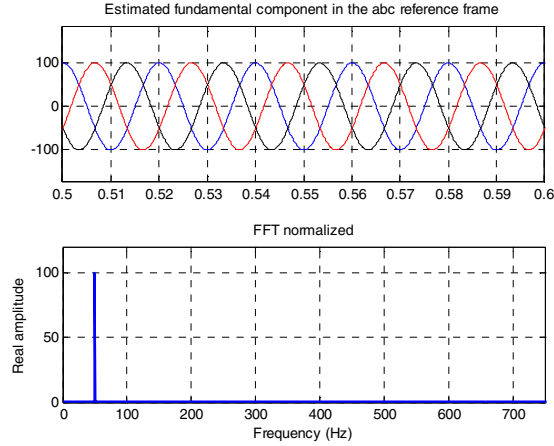


Fig. 11. The SOGI-FLL, a single-phase grid synchronization system.

behavior of the MSOGI if compared with a single DSOGI response, displayed in Fig.8.

VIII. EXPERIMENTAL RESULTS

To experimentally validate the performance of the synchronization system presented in this paper, the algorithm of the MSOGI-FLL was implemented in a control board based on the floating point DSP Texas Instruments TMS320F28335 at 150 MHz. The unbalanced and distorted input voltage was generated by means of an AC programmable source ELGAR SM5250A.

The MSOGI-FLL was implemented with seven embedded DSOGIs (one per each harmonic plus the fundamental one), as detailed in TABLE II.

In this study case, the sampling frequency was set to 10

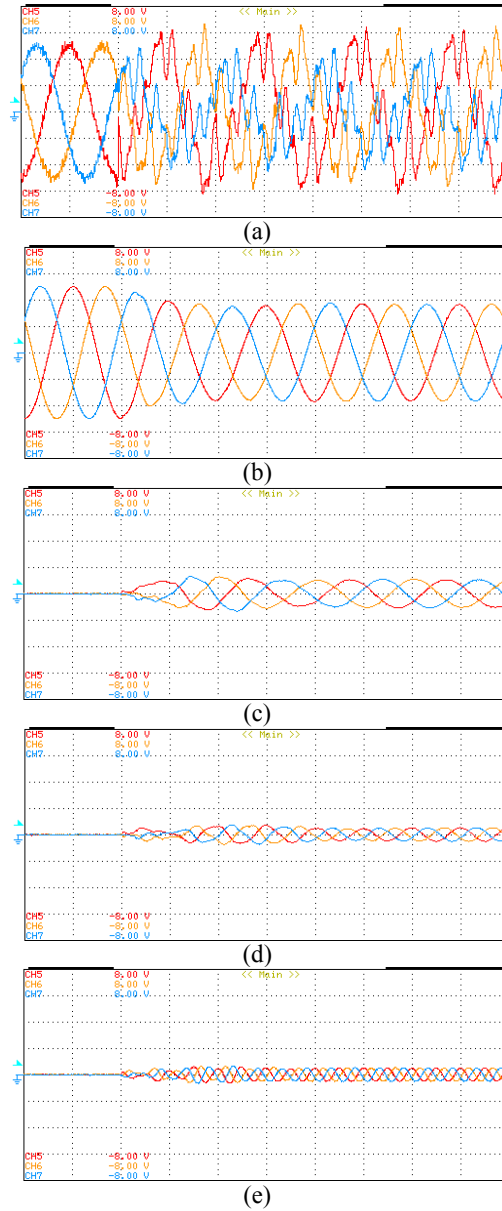
TABLE II
PARAMETERS OF THE INPUT VOLTAGE

Voltage component	Value [p.u.]
Fundamental positive-sequence	$\vec{V}^{+1} = 0.733\angle 5^\circ$
Fundamental negative-sequence	$\vec{V}^{-1} = 0.210\angle 50.4^\circ$
2 nd harmonic negative-sequence	$\vec{V}^{-2} = 0.10\angle 0^\circ$
3 rd harmonic zero-sequence	$\vec{V}^{03} = 0.10\angle 45^\circ$
4 th harmonic positive-sequence	$\vec{V}^{+4} = 0.10\angle 180^\circ$
5 th harmonic negative-sequence	$\vec{V}^{-5} = 0.25\angle 45^\circ$
7 th harmonic positive-sequence	$\vec{V}^{+7} = 0.20\angle 180^\circ$
11 th harmonic negative-sequence	$\vec{V}^{-11} = 0.15\angle 180^\circ$

kHz and the computational cost of the whole MSOGI-FLL took 34.86 μ s.

Fig. 12 shows some scopes recorded from the control board by using the PWM outputs and a low-pass filters as digital-to-analog converters [15].

Fig. 12(a) shows the input voltage, which was unbalanced and extremely distorted. Figs. 12(b) and 12(c) show the waveforms detected for the positive- and negative-sequence components at the fundamental frequency. Figs. 12(d), 12(e), 12(f), 12(g) and 12(h) show the waveforms detected for the 2nd, 4th, 5th, 7th and 11th harmonics, respectively.



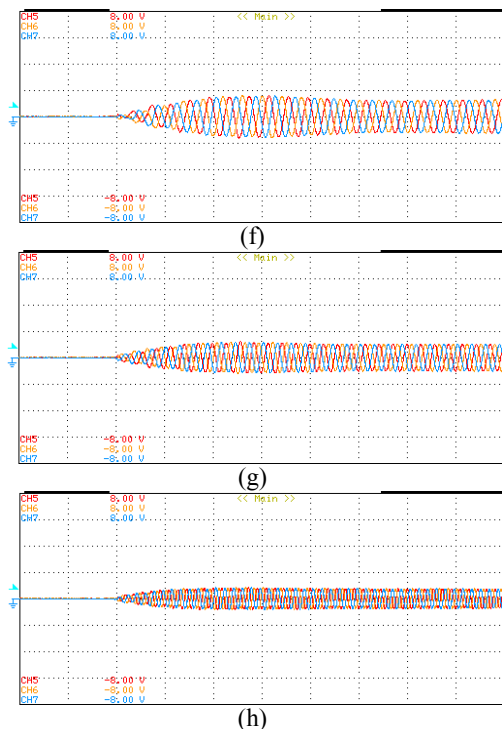


Fig. 12.- Experimental response of the MSOGI-FLL.

According to the results shown in Fig. 12, it should be pointed out the excellent performance of the MSOGI-FLL.

IX. CONCLUSIONS

The MSOGI-FLL permits detecting the positive and negative component of each harmonic of the measured grid voltage under extremely polluted conditions.

As shown in the paper, the resulting MSOGI-FLL is a frequency-adaptive algorithm thanks to the FLL block, that permits detecting directly the fundamental frequency of the voltage/current. This feature is an additional advantage of this synchronization system, if compared with other advanced PLLs, as the grid frequency variable is more stable than voltage phase-angle during transient events.

Regarding its structure it should be pointed out that the MSOGI-FLL does not need to perform any trigonometric function, since neither synchronous reference frame nor voltage controlled oscillator are used in its algorithm. This feature contributes to reduce its computational cost, getting finally a low computational burden time algorithm.

The simulated and experimental results presented in this work shows that the MSOGI-FLL is a very suitable solution to the detection of fundamental-frequency positive- and negative-sequence components of unbalanced and distorted grid voltages.

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