## Nitric acid pretreatment for the passivation of boron emitters for *n*-type base silicon solar cells

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(Received 22 December 2007; accepted 24 January 2008; published online 14 February 2008)

We have developed a simple method to passivate industrially produced boron-doped emitters for *n*-type base silicon solar cells using an ultrathin (~1.5 nm) silicon dioxide layer between the silicon emitter and the silicon nitride antireflection coating film. This ultrathin oxide is grown at room temperature by soaking the silicon wafers in a solution of nitric acid prior to the deposition of the silicon nitride antireflection coating film. The *n*-type solar cells processed in such a way demonstrate a conversion efficiency enhancement of more than 2% absolute over the solar cells passivated without the silicon dioxide layer. © 2008 American Institute of Physics. [DOI: 10.1063/1.2870202]

*n*-type base silicon has been proven to have a higher tolerance to common transition metal impurities, such as those present in silicon produced from quartz and carbon (i.e., metallurgical routes), potentially resulting in higher minority carrier diffusion lengths compared to *p*-type substrates.<sup>1,2</sup> Additionally, it does not suffer from degradation of recombination lifetime due to the boron-oxygen related defect.<sup>3</sup> In spite of these advantages, at present, with the exception of two particular high-efficiency cell types produced by two companies,<sup>4,5</sup> all silicon solar cells produced by the industry are based on *p*-type base substrates. This is caused mostly by an insufficient development and industry implementation of the *n*-type cell processes, which is in turn for a large part due to a poor development of an industrial low-cost technique for passivation of *p*<sup>+</sup> emitters.

Since the conventional way to passivate  $n^+$  emitters, for the *p*-type solar cell process, using a plasma enhanced chemical vapor deposition (PECVD) of hydrogenated silicon nitride  $(SiN_x)$  resulted in a poor or no passivation at all for  $p^+$  emitters,<sup>6</sup> a method to passivate  $p^+$  surfaces needs to be developed. Reported approaches to solve this issue are by using an intermediate silicon dioxide (SiO<sub>2</sub>) layer between  $SiN_r$  and the  $p^+$  diffused emitter, or even by using PECVD silicon carbide  $(SiC_x)$  as a passivation layer instead of  $SiN_r$ .<sup>6–8</sup> However, the SiO<sub>2</sub> layer is thermally grown at high temperature for a long time, which could result in degradation of Si wafers<sup>9</sup> (especially if they are multicrystalline) and an increase in production cost of the cells. Recent results by Chen *et al.* demonstrated that  $SiN_x$  of particular composition can result in good passivation of boron doped emitters,<sup>10</sup> while Hoex et al. showed that Al<sub>2</sub>O<sub>3</sub> films provide also an excellent level of surface passivation on highly doped boron surfaces.<sup>11</sup> Nevertheless, a simple and effective method to passivate  $p^+$  emitters is still demanded by the industry if *n*-type solar cells are to be produced on large scale and in a cost-effective way.

In the present study, we demonstrate that an ultrathin  $SiO_2$  layer formed by nitric acid oxidation of Si (NAOS) (Ref. 12–14) can be used in combination with  $SiN_x$  to passivate industrially produced  $p^+$  (i.e., boron) doped emitters for *n*-type solar cells. This method relies on the same PECVD-SiN<sub>x</sub> technology as is widely used in industry to

passivate  $n^+$  (i.e., phosphorous) emitters, which is industrially applicable with no substantial increase in cost or process time.

In order to investigate this passivation method on  $p^+$ emitters, we have fabricated solar cell devices made from phosphorous-doped n-type Czochralski (Cz) Si (100) wafers of 1.4–1.6  $\Omega$  cm resistivity and 210  $\mu$ m thickness. Both surfaces of the wafers received a texture etch with random pyramids in NaOH/isopropanol solution. Subsequently, a  $p^+$ emitter with a sheet resistance of 60  $\Omega/sq$  was diffused on the front side (light receiving side) of the wafers from a boron tribromide source in a quartz tube furnace of an industry-compatible scale. A  $n^+$  (phosphorous) doped region with a sheet resistance of 30  $\Omega/sq$  was also diffused at the back side of the solar cells to serve as a back surface field (BSF) and to provide a good electrical contact to the *n*-type base. After the diffusion processes, all wafers received a standard cleaning followed by etching with a diluted hydrofluoric acid (HF) immediately before the application of the passivating layers. From here, the wafers were randomly divided in three groups in which one group received directly SiN, on both sides. The other two groups were immersed in a nitric acid aqueous solution of azeotropic concentration (68%) at room temperature for 15 min. During this period, a SiO<sub>2</sub> layer with a thickness of approximately 1.5 nm (as determined by our ellipsometry measurements) is grown on the wafer surfaces. After rinsing with de-ionized water, one of the groups received again a diluted HF dip in order to strip off the SiO<sub>2</sub> deposited by the NAOS method and to allow for the investigation of a nitric acid cleaning effect on the passivation quality of the  $p^+$  emitters. Then, the same SiN<sub>x</sub> was deposited on both sides of these two groups followed by screen printing of the front and back contacts on the wafers of all groups, in order to complete the solar cell devices. It should be noted that firing at a high peak temperature (typically greater than 750 °C) for a short period is necessary to be able to make Ohmic contact between printed metal contacts and Si.

In addition to the solar cells, symmetric  $p^+/n/p^+$  devices were also fabricated by diffusion of boron emitter on both side of polished Cz wafers. This device structure is widely used in literature in order to study the surface passivation or the emitter quality of a cell process by measuring the effective lifetime of minority charge carriers using quasi-steady-

0003-6951/2008/92(6)/063510/3/\$23.00

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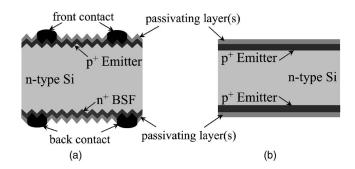


FIG. 1. Schematic layout of the fabricated solar cells (a) and lifetime test devices (b).

state photoconductance (QSSPC) setup.<sup>6,7</sup> A schematic layout of the fabricated devices is shown in Fig. 1.

Table I shows the mean values of the solar cell parameters processed using different passivation treatments as described above. For each passivation method tested a total of ten solar cells were fabricated with a cell area of 155.72 cm<sup>2</sup>. These measurements were carried out under standard test conditions (STCs) using a class A solar simulator. As can be seen from the table, a substantial enhancement of 2% points in the conversion efficiency  $(\eta)$  is observed for the solar cells passivated using nitric acid SiO<sub>2</sub>/SiN<sub>x</sub> stack layers as compared with the solar cells which do not have the ultrathin SiO<sub>2</sub> layer. Furthermore, the results shown in Table I demonstrate that there is no statistical significant difference at 95% confidence intervals between solar cells passivated using only the  $SiN_r$  layer and those that received an extra nitric acid  $SiO_2$  which was stripped off prior to the  $SiN_r$  deposition (denoted as  $SiO_2/HF/SiN_x$ ). This clearly indicates that just the cleaning effect of the wafer surfaces by nitric acid is not the cause of the observed enhancement in efficiency but the enhancement is rather caused by the ultrathin ( $\approx 1.5$  nm) SiO<sub>2</sub> grown by the NAOS method. With the measured efficiency reaching 18.3%, the highest reported so far for this type of solar cells,<sup>16</sup> the results clearly demonstrate the superior performance of nitric acid  $SiO_2/SiN_x$  passivation.

Since in this study, both front and back surfaces of the solar cells were passivated using the same method, internal quantum efficiency (IQE) measurements of the solar cells were used to disentangle front side (boron emitter) and back side (phosphorous BSF) contributions of improved passivation. Figure 2 shows the IQE data of solar cells selected from each of the groups of Table I. Since the absorption coefficient of Si for blue light (short wavelength photons) is very large, this light is absorbed directly near the front surface of the cell. Therefore, the IQE for short wavelength photons strongly reflects the recombination at the front surface of a cell, thus, it reflects the passivation of boron emitter in our solar cells. As can be seen in Fig. 2, the IQE of the solar cell passivated using the SiO<sub>2</sub>/SiN<sub>x</sub> stack shows a strong im-

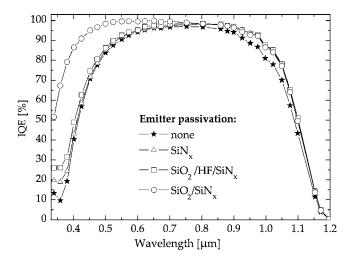


FIG. 2. IQE data of *n*-type solar cells fabricated using different surface passivating layers based on PECVD  $SiN_x$  method.

provement for wavelengths below 0.6  $\mu$ m, as compared with the cells passivated using the other two methods of Table I or with an unpassivated front surface. Consequently, the opencircuit voltage ( $V_{OC}$ ) and short-circuit current ( $J_{SC}$ ) are increased, as can be seen in the table. On the other hand, the IQE for high wavelength photons, which reveal the amount of back surface recombination is nearly identical for all cells. These two observations clearly indicate that the improvement in the boron emitter passivation is the cause for the much better solar cell performance when using the ultrathin nitric acid SiO<sub>2</sub>/SiN<sub>x</sub> stack layers.

To get further insight into the possible passivation mechanisms of  $p^+$  (boron) emitters using the ultrathin nitric acid SiO<sub>2</sub>, we have investigated the effective lifetime of symmetric  $p^+/n/p^+$  devices [see Fig. 1(b)] passivated using  $SiN_x$  or  $SiO_2/SiN_x$  stack. For the high quality Cz wafers which were processed here, the bulk lifetime is specified by the supplier to be higher than 1 ms. Thus, the effective lifetime remains limited by the surface and emitter recombination, where the latter only plays a significant role at high injection levels (typically more than  $10^{15}$  cm<sup>-3</sup>). Figure 3 shows the effective lifetime as a function of excess carrier density measured before and after a firing step similar to the one performed for solar cell devices. The experimental data show that the effective lifetime increases an order of magnitude for the devices passivated using the  $SiO_2/SiN_r$  stack after the firing step, while no significant enhancement is observed for devices passivated with only SiN<sub>x</sub>. Without this high temperature firing step, the  $SiO_2/SiN_x$  stack does not appear to be a good passivation scheme for  $p^+$  emitters. This suggests that the effect of  $SiN_r$  on the ultrathin  $SiO_2$  layer is similar to that observed by annealed aluminum layer and is, therefore, likely that the atomic hydrogen in the  $SiN_x$  layer is

TABLE I. Mean values and 95% Tukey HSD intervals (Ref. 15) (for efficiency) of the solar cells parameters measured under STC (AM1.5G, 100 mW/cm<sup>2</sup>, 25  $^{\circ}$ C).

Passivating layer(s)	Counts (-)	$J_{\rm SC}$ (mA/cm <sup>2</sup> )	V <sub>OC</sub> (mV)	FF (%)	$\eta$ (%)
SiN <sub>x</sub>	10	35.1	599	75.4	15.9(±0.14)
$SiO_2/HF/SiN_x$	10	35.2	600	75.7	$16.0(\pm 0.15)$
$SiO_2/SiN_r$	10	37.3	627	76.7	$17.9(\pm 0.14)$

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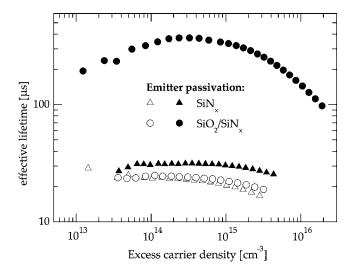


FIG. 3. Effective lifetime of minority charge carriers as a function of injection level measured using QSSPC setup performed on lifetime test devices [see Fig. 1(b)] before (empty symbols) and after (filled symbols) firing step.

diffusing to the SiO<sub>2</sub>/Si interface during the firing step and passivate dangling bonds.<sup>12–14</sup> A similar scenario is reported also for the thermal SiO<sub>2</sub>/SiN<sub>x</sub> scheme.<sup>17,18</sup> However, the reasons why the nitric acid SiO<sub>2</sub>/SiN<sub>x</sub> stack is able to passivate better the  $p^+$  diffused surfaces than just a SiN<sub>x</sub> layer still needs to be understood. The passivation quality of nitric acid SiO<sub>2</sub>/SiN<sub>x</sub> stack shown in Fig. 3 was further quantified by the emitter saturation current density ( $J_{0e}$ ), which is determined at high injection level (up to  $2 \times 10^{16}$  cm<sup>-3</sup>). The obtained  $J_{0e}$  of  $\approx 23$  fA/cm<sup>2</sup> (per side) is in a good agreement with the best literature values for the same boron sheet resistance ( $\approx 60 \Omega/sq$ ).<sup>10,11</sup>

One of the most important criteria for commercialization of solar cells is their stability under normal operational conditions of temperature and illumination. The passivation method for  $p^+$  emitters introduced here is proven to be very effective but it must also be stable during the operational lifetime of a solar cell. Therefore, accelerated aging tests under the influence of illumination and an elevated temperature of 60 °C was carried out, in order to study and compare the behavior of the solar cells passivated with the nitric acid  $SiO_2/SiN_r$  stack or with  $SiN_r$ . Solar cells were light soaked under open-circuit condition for a period of 1000 h by a xenon lamp at one-sun intensity. The lamp light was filtered with a UVB and infrared cutoff filter. During this period, the solar cells received a total radiation dose of  $2 \times 10^9$  J/m<sup>2</sup> (or 555 kW  $h/m^2$ ). Figure 4 shows the power conversion efficiency as a function of illumination time for the two solar cells investigated. Even after extensive light soaking of 1000 h and strong UVB radiation, the cell efficiency decreases by less than 2% relative to the initial value for both passivation schemes used. This shows that the ultrathin nitric acid  $SiO_2/SiN_x$  stack can be expected to be a reliable passivation scheme for  $p^+$  emitters.

In summary, we have shown conclusive evidence that an ultrathin silicon dioxide layer grown by soaking the silicon wafers in a solution of nitric acid prior to the deposition of silicon nitride antireflection film is a reliable and effective method to passivate boron doped emitters for *n*-type solar cells. This method demonstrates an efficiency enhancement of more than 2% points, which leads to a record-high effi-

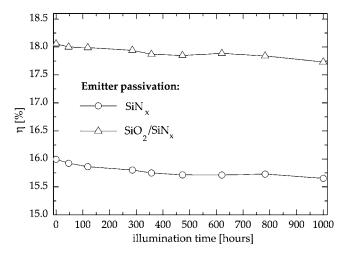


FIG. 4. Accelerated ageing test of solar cells passivated using either a  $SiN_x$  layer or a stack of nitric acid  $SiO_2/SiN_x$  layers (see legend).

ciency of 18.3% on phosphorous-doped Czochralski silicon. These results open up the possibility to explore the higher potential possessed by *n*-type silicon using an easy-tofabricate and industrially feasible process.

This work was financially supported by the European Commission within the FoXy project under the Contract No. 019811 (SES6). E.C.N. acknowledges cooperation with Tempress.

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