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OPTIMAL PARALLEL ALGORITHMS FOR INTEGER SORTING AND GRAPH CONNECTIVITY

John H. Reif TR-08-85

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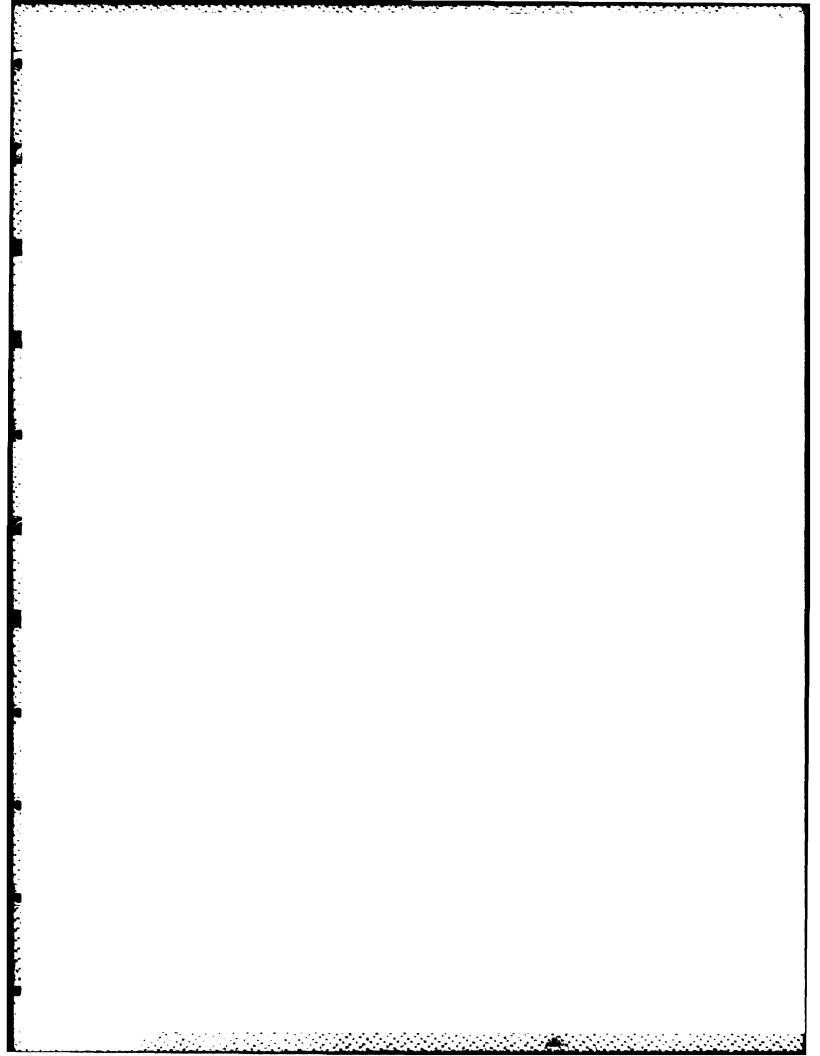
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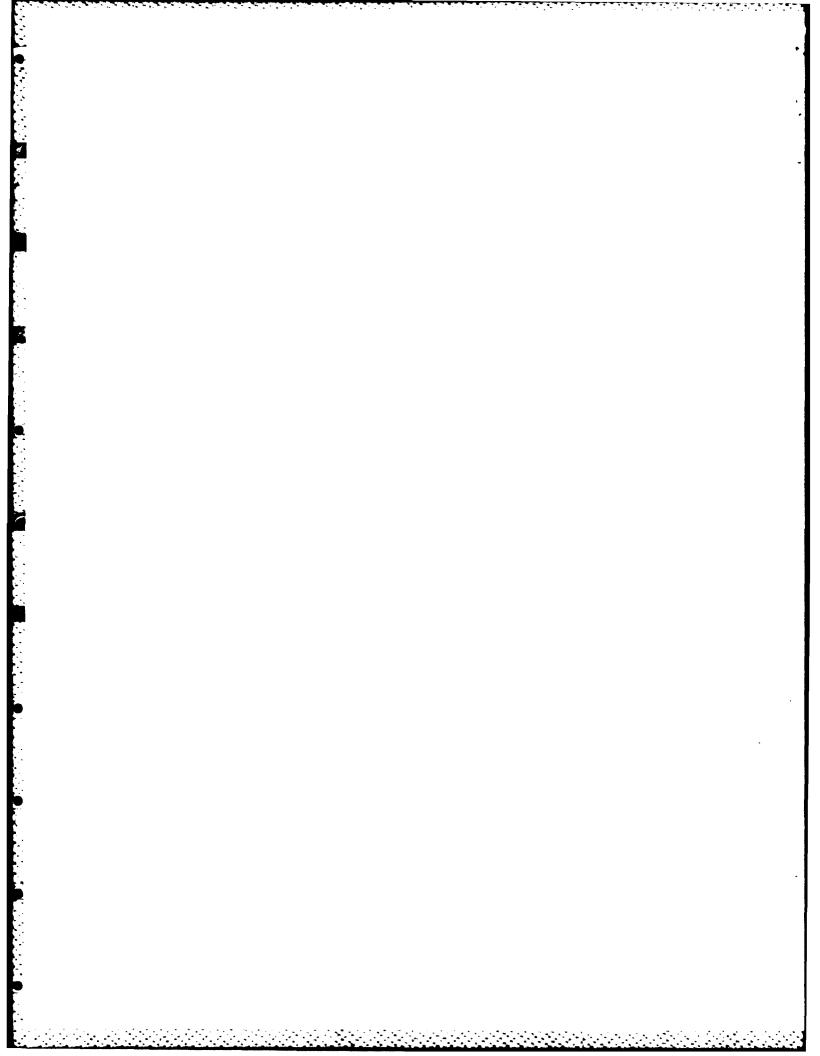


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0. ABSTRACT

We assume a parallel RAM model which allows both concurrent writes and concurrent reads of global memory. Our algorithms are randomized: each processor is allowed an independent random number generator. However our stated resource bounds hold for worst case input with overwhelming likelihood as the input size grows.

We give a new parallel algorithm for integer sorting where the integer keys are restricted to at most polynomial magnitude. Our algorithm costs only logarithmic time and is the first known where the product of the time and processor bounds are bounded by a linear function of the input size. These simultaneous resource bounds are asymptotically optimal. All previous known parallel sorting algorithms required at least a linear number of processors to achieve logarithmic time bounds, and hence were nonoptimal by at least a logarithmic factor.

A large literature exists on efficient sequential RAM algorithms with time bound linear in the input size. Many of these algorithms require sorts to be done on integers of at most polynomial magnitude. For example, the depth first search algorithms of [Tarjan, 72] and [Hopcroft and Tarjan, 73] require the edges (which may be considered integers) to be sorted into adjacency lists. A $\Omega(n \log n)$ comparison sort such as QUICK-SORT or HEAP-SORT would not be sufficiently efficient for these applications. Instead, the BUCKET-SORT (see [Aho, Hopcroft, and Ullman, 74]) is used to sort in linear time. The BUCKET-SORT algorithm is sufficiently simple and elegant so that it is widely used in practice.

The goal of this paper is to develop an efficient and possibly practical integer sorting algorithm for a parallel RAM model, but we will utilize quite different techniques—such as randomization.

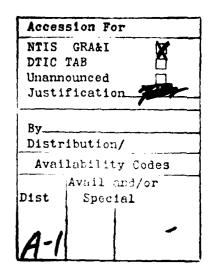
Optimal Parallel Algorithms for

Integer Sorting and Graph Connectivity

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March, 1985



O. ABSTRACT

we give new parallel algorithms for integer sorting and undirected graph connectivity problems such as connected components and spanning forest. Our algorithms cost only logarithmic time and are the first known that are optimal: the product of their time and processor bounds are bounded by a linear function of the input size. All previous known parallel algorithms for these problems required at least a linear number of processors to achieve logarithmic time bounds, and hence were nonoptimal by at least a logarithmic factor.

We assume a parallel RAM model which allows both concurrent writes and concurrent reads of global memory. Our algorithms are randomized; each processor is allowed an independent random number generator; however our stated resource bounds hold for worst case input with overwhelming likelihood as the input size grows.

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1. INTRODUCTION

1.1 Optimal Sequential RAM Algorithms

A large literature exists on efficient sequential algorithms with time bound linear in the input size. This literature generally assumes the sequential Random Access Machine Model (RAM); for an introduction to this literature see [Aho, Hopcroft, and Ullman, 74]. Perhaps the most influential works done in this area were the graph algorithms of [Tarjan, 72] and [Hopcroft and Tarjan, 73]. These efficient sequential algorithms relied on linear time algorithms for (1) bucket sort, and (2) depth first search.

This linear time bucket sort was essential to depth first search since the edges must be sorted into adjacency lists. By ingenious use of both (1) and (2), Hopcroft and Tarjan derived linear time algorithms for graph problems such as connected components, spanning forest, and biconnected components.

The goal of this paper is to achieve similar results (i.e., optimal algorithms) for a parallel RAM model, but we will utilize quite different techniques (i.e., randomization).

1.2 Known Parallel RAM Algorithms

The performance of a parallel algorithm can be specified by bounds on its principal resources: processors and time. We generally let P denote the processor bound and T denote the time bound. For most nontrivial problems such as sorting and the above graph problems, the product P·T is lower bounded by at least a constant times the input size. Thus for these problems, we consider a parallel algorithm to be optimal if P·T = O(input size). For example, given a graph of n vertices and m edges, a parallel graph connectivity algorithm is optimal if P·T = O(n+m). Of course, if we have an optimal algorithm with any processor bound P, then we also have (by the obvious processor simulation) an optimal algorithm for any processor bound P', where $P \ge P' \ge 1$. Hence an optimal algorithm may also be useful in practical situations where we have a limited number of processors.

We assume a parallel RAM model of [Shiloach and Viskin, 81]. The processors are synchronous, and each is a unit cost sequential RAM which in a single step may either read or write into a memory cell or register, or perform an arithmetic operation on an integer. Each memory cell and register may contain at most a logarithmic number of bits in the input size. This parallel RAM model allows multiple reads at a single memory cell and also allows multiple writes at a single memory cell, where multiple writes are allowed to be resolved arbitrarily. This model is known as the CRCW parallel RAM and is quite robust, see [Kucera, 82] for its relation to other parallel machine models. In addition we allow each processor an independent random number generator.

There are a number of known algorithms for sorting in logarithmic time using a linear number of processors; for example [Reischuk, 82] gives a randomized parallel RAM algorithm (which unfortunately requires memory cells of $n^{1/2}$ bits each). [Reif and Valiant, 83] give a randomized parallel algorithm (which has only moderate constant bounds and requires memory cells of O(log n) bits each), and [Ajtai, Komlos, and Szemerédi 83; and Leighton, 84] give a deterministic parallel algorithm. result of [Leighton, 84] appeared to finally settle the problem of parallel sorting since PT = 1 (n logn) is a known lower bound in the case of comparison sorting. However, these lower bounds on PT need not hold for integer sorting: sorting n integers on the range [n]* (note that the restriction to the range [n] is natural, since RAM memory cells can only contain numbers with at most a logarithmic number of bits.) Integer sorting is all that is required for most practical applications of interest, for example for putting a list of edges into adjacency list representative by sorting the edges by the vertices from which they depart. On the other hand, an optimal integer sort is essential in the derivation of any optimal parallel graph algorithm which requires the edges to be put in adjacency list representation.

Note throughout this paper, we let [n] denote {1,...,n}.

Previously $T=O(\log n)$ time bounds and simultaneous P=n+m processor bounds have been given for connected components [Shiloach and Vishkin,83] and spanning trees [Averbuch and Shiloach, 83] of graphs with n vertices and m edges. All these previous algorithms had a $PT=\Omega((n+m)\log n)$ bound, which was a logarithmic factor more resources than optimal for logarithmic time bounds. [Tarjan and Vishkin, 83] pose as an open problem to find optimal parallel graph algorithms.

In fact no optimal graph searching method has been proposed for parallel RAM, for any sublinear time bounds, except in the special case where the graph is extremely dense (i.e., $m = \Omega(n^2)$). [Chin, Lam and Chen, 82] and [Vishkin, 81], both give $O(\log n)^2$ time connectivity algorithms requiring $(n^2+m)/(\log n)^2$ processors, which is optimal only if $m = \Omega(n^2)$.

Vishkin conjectured that randomized techniques would be needed to get optimal parallel graph connectivity algorithms. Indeed the literature contains some interesting attempts to use randomization to derive optimal parallel algorithms for graph problems. For example [Vishkin, 84] recently gave a randomized algorithm for finding the number of successors on a linear list which used an optimal number of processors with an almost logarithmic time bound. (However, Vishkin's algorithm assumed an oracle which provided a random permutation, but he provided no efficient method for parallel construction of random permutations.) Also [Reif, 84] gave a randomized parallel graph algorithm which had optimal processor bounds only for graphs with $m \ge n(\log n)^2$ edges.

1.3 Our optimal Parallel RAM Algorithms

Our main results are optimal randomized parallel RAM algorithms:

- (1) Õ(log n) time, n/log n processor algorithms for integer sorting
- (2) $\tilde{O}(\log n)$ time, $(m+n)/\log n$ processor algorithms for connected components and spanning forests for any graph of n vertices and m edges.

Here \tilde{O} denotes that the upper bound holds within a constant factor with overwhelming likelihood, for the worst case input. In particular, we let $T(n) = \tilde{O}(f(n))$ denote $\exists c \ \forall \ \alpha \geqslant 1$, \forall sufficiently large n, $T(n) \leqslant c\alpha f(n)$ holds with probability at least $1 - 1/n^{\alpha}$.

Our integer sorting algorithm is quite easy to implement and may be of some practical use, since it has very moderate constant factors.

1.4 Organization of This Paper

In Section 2, we give a known optimal algorithm for parallel prefix computation which will be of some use in devising our optimal parallel algorithms.

In Section 3, we give our optimal parallel algorithm for integer sorting, which achieves its efficiency by some interesting new randomization techniques. As an immediate consequence, (see Appendix A3) we get an optimal parallel algorithm for computing a random permutation.

In Section 4 (and Appendix A4) we give our algorithm for graph connectivity. It is derived in stages where we consider graphs of decreasing density. We first give a simple logarithmic time algorithm called RANDOM-MATE, which is nonoptimal, but utilizes randomization in an essential and new way. We next modify this algorithm so that it is optimal for graphs of n vertices with at least $m \ge n(\log n)^2$ edges. Then we give efficient parallel reductions from various cases of sparse graphs to the case $m \ge n(\log n)^2$.

In the Appendix Al we give some useful upper bounds for the tails of various probability distributions which arise in the analysis of our algorithms.

In a separate paper we give applications of our optimal parallel graph connectivity algorithm to finding Euler cycles, biconnected components, and minimum spanning trees.

2. PARALLEL PREFIX COMPUTATION

2.1 Prefix Circuits

Let D be a domain and let o be an associative operation which takes O(1) sequential time over this domain. The prefix computation problem is defined as follows:

input $X(1),...,X(n) \in D$

output $X(1), X(1) \circ X(2), ..., X(1) \circ ... \circ X(n)$.

[Ladner and Fischer, 80] show prefix computation can be done by a circuit of size n and depth $O(\log n)$.

Known techniques attributed to Brent, give the following processor improvement:

LEMMA 2.1. Prefix computation can be done in time O(log n) using n/log n F-FAM processors.

The prefix sum computation problem is defined as follows: Given input integers $X(1), \ldots, X(n) \in [n]$, output the vector PREFIX-SUM(X) = $(Y(0), Y(1), \ldots, Y(n))$ where Y(0) = 0 and $Y(i) = \sum_{j \le i} X(j)$ for $i \in [n]$. By Lemma 2.1, we can do this computation in time $O(\log n)$ using $n/\log n$ processors.

3. AN OPTIMAL PARALLEL SORTING ALGORITHM

3.1 Known Sorting Algorithms

The integer sorting problem of size n is defined:

input keys
$$k_1, \ldots, k_n \in [n]$$

output permutation $\sigma = (\sigma(1), \ldots, \sigma(n))$ such that $k_{\sigma(1)} \leq \ldots \leq k_{\sigma(n)}$.

The input keys k_1, \ldots, k_n are not necessarily distinct. By use of the well known and quite practical BUCKET-SORT algorithm [Aho, Hopcroft, and Ullman, 74],

LEMMA 3.1. Integer sorting can be done in time O(n) by a deterministic sequential RAM.

Any comparison based sort requires $PT = \Omega(n \log n)$, and the best known parallel sorts actually achieve these bounds. In particular, [Reif and Valiant, 83] show LEMMA 3.2. n keys can be sorted in time $\tilde{O}(\log n)$ using n processors in a constant degree network.

This algorithm uses memory cells of O(log n) bits. It can also be implemented by the randomized P-RAM model. In addition, [Ajtai, Komlós, and Szemerédi83], [Leighton, 84] give a deterministic sorting network which takes O(log n) time with O(n) processors. In the following, we prove: THEOREM 3.1. Integer sort can be done in time Õ(log n) using n/log n F-RAM processors.

We will achieve $PT = \tilde{O}(n)$ for integer sorting, making essential use of the fact that the input keys k_1, \ldots, k_n are integers in $\{n\}$ as in the case of all our graph applications. We would be quite surprised if any purely deterministic methods yield PT = O(n) for parallel integer sort in the case of time bounds $T = O(\log n)$. Although we will use deterministic methods to solve some restricted integer sorting problems, (see Lemmas 3.4 and 3.5 below) our optimal parallel algorithm for the general integer sorting problem requires some interesting, new use of randomization techniques (see Lemmas 3.6 and 3.7).

3.2 Easy Integer Sorting Problems

Given a sequence of keys $k_1, \ldots, k_n \in [n]$ let the key index sets be $I(k) = \{i \mid k_i = k\}$ for each key value $k \in [n]$. We will assume $log\ n$ divides n.

LEMMA 3.3. Given $I(1), \ldots, I(n)$, we can sort k_1, \ldots, k_n in $O(log\ n)$ time using $P = n/log\ n$ processors.

Proof. See Appendix A3.

A sorting algorithm is *stable* if given k_1, \ldots, k_n , the algorithm outputs a permuation σ of $(1, \ldots, n)$ where $\forall i, j \in [n]$ if $k_i = k_j$ and i < j then $\sigma(i) < \sigma(j)$.

LEMMA 3.4. A stable sort of n keys $k_1, \ldots, k_n \in [\log n]$ can be computed in $O(\log n)$ time using $P = n/\log n$ processors.

Proof. See Appendix A3.

LEMMA 3.5. n keys $k_1, ..., k_n \in [(\log n)^2]$ can be sorted in $O(\log n)$ time using $P = n/\log n$ processors.

Proof. See Appendix A3.

Note: We can similarly extend Lemma 3.5 to apply to key values in $[(log n)^{O(1)}]$.

3.3 Randomized Sampling and Sorting in Key Domain [n/(log n)²]

In the following subsection, we fix a key domain [D] where $D=n/(\log n)^2$. (We assume $(\log n)^2$ divides n). Let the input keys be $k_1, \ldots, k_n \in [D]$ and their index sets be $I(k) = \{i \mid k_i = k\}$ for each key value $k \in [D]$.

LEMMA 3.6. Given as input $k_1, \ldots, k_n \in [D]$, we can compute $N(1), \ldots, N(D)$ in $\tilde{O}(\log n)$ time using $P = n/\log n$ processors, such that $\Sigma_{k \in [D]} N(k) \leq O(n)$ and furthermore with high likelihood (in fact with probability $\geq 1 - 1/n^{\alpha}$ for any given $\alpha \geq 1$) $N(k) \geq |I(k)|$ for each $k \in [D]$.

As proof, we execute the following randomized sampling algorithm

Step 1 for each processor $\pi \in [P]$ in parallel do

 \underline{do} choose a random $s_{\pi} \in [n]$ \underline{od}

$$s \leftarrow \{s_1, \ldots, s_p\}$$

Comment. Here we randomly choose a set $S \subseteq [n]$ of P key indices.

Step 2 Sort k_s ,..., k_s and compute index set $I_s(k) = \{i \in S | k_i = k\}$ for each key value $k \in [D]$.

Comment. Applying Lemma 3.2, this sorting can be done by known parallel algorithms in $\tilde{O}(\log n)$ time using P processors.

Step 3 for each k € [D] do

$$N(k) + d_0(\log n) (|I_S(k)| + \log n)$$

Comment. d_0 is a constant to be determined in the probabilistic analysis. output $N(1), \ldots, N(D)$.

See Appendix A3 for a proof of the probabilistic bounds given in Lemma 3.6.

Lemma 3.7. n keys $k_1, \ldots, k_n \in [D]$, (where $D = n/(\log n)^2$) can be sorted in $\tilde{O}(\log n)$ time using $P = n/\log n$ processors.

<u>Proof.</u> (We will actually use O(P) processors, but we observe that we can then slow the computations down by a constant factor to reduce the processor bound to P.) Our randomized algorithm is given below.

Step 1 Compute N(1), ..., N(D) as defined in Lemma 3.6.

Comment. Here we use the random sampling algorithm of Lemma 3.6.

Step 2 $(\tilde{N}(0),...,\tilde{N}(D)) \leftarrow PREFIX-SUM(N(1),...,N(D))$

Comment. This prefix-sum computation is done by Lemma 2.1 in $O(\log n)$ time and O(P) processors.

Step 3 for each key value k € [D]

do $P_k + \{\pi \mid \pi \in [D] \text{ or } \bar{N}(k-1) + D < \pi \leq \bar{N}(k) + D\}$. Using these P_k processors, construct a table $A_k = (A_k(1), A_k(2), \dots, A_k(N(k)), A_k(N(k)+1))$ and initialize each element of the table to be an empty list.

<u>od</u>

Step 4 for each $\pi \in [P]$ in parallel do

for each $t = 1, ..., \log n$ sequentially do $i_{\pi} \leftarrow (\pi - 1) \log n + t$ choose a random number $r_{\pi} \in [N(k)]$ attempt to add i_{π} to front of list $A_{k_{i_{\pi}}}(r_{\pi})$ if successful (i.e., i_{π} is now in front of list $A_{k_{i_{\pi}}}(r_{\pi})$)

then CONFLICT $(i_{\pi}) \leftarrow 0$ else CONFLICT $(i_{\pi}) \leftarrow 1$ if

od od

Comment. Each processor $\pi \in [P]$ is responsible for keys $k_{(\pi-1)\log n+1}, \dots, k_{\pi} \log n$. The inner loop for $t=1,\dots,\log n$ is executed sequentially so as to minimize conflicts. In the t-th iteration of the inner loop, processor π attempts to add the index $i_{\pi} = (\pi-1)\log n+t$ of the key $k_{i_{\pi}}$ to the front of list $A_{k_{i_{\pi}}}(r_{i_{\pi}})$ where r_{π} is a randomly chosen integer in [N(k)]. This may not be successful if some other processor π' simultaneously attempts to add some other index $i_{\pi'}$ to the front of list $A_{k_{i_{\pi}}}(r_{i_{\pi}})$. Only one addition to this list will succeed. But this conflict will only happen in the case $k_{i_{\pi'}} = k_{i_{\pi}}$ and π' makes the same unlucky choice of $r_{\pi'} = r_{\pi'}$. Claim 3.1. Let $n' = \sum_{i=1}^{n} CONFLICT(i)$. Then $n' \leq \tilde{O}(P)$. In particular, $\exists c \forall \alpha \geq 1$ Prob $(n' \leq \alpha \text{ cn/log } n) \geq 1 - 1/n^{\alpha}$.

Proof. See Appendix A3.

Step 5
$$(u(0),...,u(n)) \leftarrow PREFIX-SUM(CONFLICT(1),...,CONFLICT(n))$$

 $n' \leftarrow u(n)$

for each $\pi \in [P]$ in parallel

do for each t=1,...,log n sequentially

do
$$i_{\pi}$$
 + $(\pi-1)\log n+t$
if $CONFLICT(i_{\pi})$ then $j_{u(i_{\pi})}$ + i_{π} fi

od od

<u>Comment</u>. $(j_1, ..., j_n)$ is the list of indices j such that CONFLICT(j) = 1. Again, the prefix computations can be done by applying Lemma 2.1.

Step 6. Sort k_{j_1}, \dots, k_{j_n} , and for each key value $k \in [D]$ assign $A_k^{(N(k)+1)} + \{j_k^{|k=k}\}.$

Comment. In $A_k^{(N(k)+1)}$ we place the list $\{j_k|_{k=k}\}$ of conflicted indices with key value k. Assuming $n' \leq O(P)$, this step can be done by known parallel sorting algorighms in time $\tilde{O}(\log n)$ using P processors.

Step 7. for each key value $k \in [D]$

do Construct table A_k' consisting of a list of all the elements of the lists $A_k(1)$, $A_k(2)$,..., $A_k(N(k))$, $A_k(N(k)+1)$.

od

Comment. This is done in $O(\log n)$ time by careful use of the processor set P_k . In particular, we first compute $(a_k^{(0)}, \ldots, a_k^{(N(k)+1)}) + PREFIX-SUM(|A_k^{(1)}|, |A_k^{(2)}|, \ldots, |A_k^{(k)}|, |A_k^{(N(k)+1)}|)$. Note that $|A_k^{(i)}| \le d_0 \log n$ for each i. Hence for each $i=1,\ldots,N(k)+1$ in parallel we can place the elements of $A_k^{(i)}$ into locations $A_k^{(i)}(a_k^{(i-1)+1)},\ldots,A_k^{(i)}(a_k^{(i)})$ using a single processor $\pi \in P_k$ with time $O(\log n)$. Step 8. Compute a permutation σ of $(1,\ldots,n)$ such that the elements of $A_1^{(i)},\ldots,A_D^{(i)}$ appear in order.

Comment. We apply here Lemma 3.3.

output. $\sigma = (\sigma(1), \ldots, \sigma(n))$.

The total time for steps 1-8 is $\tilde{O}(\log n)$ using P processors.

3.4. Summary of Our Parallel Sorting Algorithm

Finally, we prove Theorem 3.1, by combining the above techniques. (We again assume $(\log n)^2$ divides n.)

Input keys $k_1, \ldots, k_n \in [n]$

Step 1 Assign $k_i' = \frac{r_k}{i} / (\log n)^2 + 1$ and $k_i'' = k_i - (k_i' - 1) (\log n)^2 + 1$ for each $i \in [n]$

Comment. $k_1', \ldots, k_n' \in [D]$ where $D = n/(\log n)^2$ and $k_1'', \ldots, k_n'' \in [(\log n)^2]$

Step 2 Sort $k'_1, ..., k'_n \in [D]$ resulting in index sets $I'(k) = \{i | k'_i = k\}$ for each key value $k \in [D]$

Comment. This is done by applying Lemma 3.7.

Step 3 Sort $\{k_i^n | i \in I^n(k)\} \subseteq [(\log n)^2]$ yielding ordered list L(k) of indices in $I^n(k)$ for each key value $k \in [D]$

Comment. This is done by applying the stable sort of Lemma 3.5 to the ordered list of keys I'(1)...I'(D).

Step 4 Compute the permutation σ which orders the indices as L(1),...,L(D)

Comment. Here we apply Lemma 3.3, σ satisfies $k_{\sigma(1)} \leq ... \leq k_{\sigma(n)}$

output o

The Lemmas 3.2-3.7 and the appropriate use of prefix-sum computation (Lemma 2.1) imply that each step can be done in $\tilde{O}(\log n)$ using $P = n/\log n$ processors.

3.5 Optimal Parallel Generation of a Random Permutation

COROLLARY 3.1. A random permutation σ of (1,...,n) can be constructed in $\tilde{O}(\log n)$ time using $P=n/\log n$ P-RAM processors.

Proof. See Appendix A3.

4. OPTIMAL PARALLEL GRAPH ALGORITHMS

Given a graph G, let CC(G) be the connected components of G. We prove in this section:

THEOREM 4.1. For any graph G with n vertices and m edges we can compute CC(G) in $\tilde{O}(\log n)$ time using $(m+n)/\log n$ parallel RAM processors.

(Note: Simple modifications of our algorithms also give a spanning forest of G within the same resource bounds.)

The proof of Theorem 4.1 will be separated into three cases of decreasing density of edges. In each case, we efficiently reduce the connected components problem to one for a denser graph. The density reductions use various randomized sampling techniques (see details in Appendix A4).

4.1 A New, But Nonoptimal Randomized Algorithm

We begin by describing a new randomized algorithm RANDOM-MATE for computing CC(G) of G = (V, E) with n vertices $V = \{1, ..., n\}$ and m edges E. We will associate a distinct processor with each vertex of V and each edge of E. This algorithm will be nonoptimal since it runs in $\tilde{O}(\log n)$ time using n+m processors as did previous parallel graph connectivity algorithms [Shiloach and Vishkin, 83]. However, RANDOM-MATE has the advantage (not shared by the previous deterministic algorithms) that it can be modified to an optimal algorithm, as we prove in the Appendix A4.

Our randomized connectivity algorithm will be motivated by the following LEMMA 4.1. (The Random Mating Lemma) Let G = (V, E) be any graph. Suppose for each vertex $v \in V$, we randomly, independently assign $SEX(v) \in \{\text{male}, \text{female}\}$. Let vertex v be active if there exists at least one departing edge $\{v,u\} \in E$ where $u \neq v$, and let vertex v be mated if SEX(v) = male and SEX(u) = female for at least one edge $\{v,u\} \in E$. Then with probability 1/2 the number of mated vertices is at least 1/8 of all active vertices.

Proof. See Appendix A4.

To represent collapsed subgraphs, we use an array R which we view as pointers mapping $V \rightarrow V$. Let the graph collapsed by R be defined R(G) = (R(V), R(E)) where $R(V) = \{R(V) \mid V \in V\}$ and $R(E) = \{(R(V), R(U)) \mid \{v, u\} \in E, R(V) \neq R(U)\}$. Each vertex $r \in R(V)$ is named a R-root. Our algorithm below (and the ones to follow) will always satisfy R(R(V)) = R(V) for each $V \in V$. Hence the R pointers define a directed forest $(V, \{(v, R(V)) \mid v \in V - R(V)\})$. Each tree in this forest will be called a R-tree; it will have height ≤ 1 and will consist of a maximal set of vertices of V mapped to the same R-root.

Initially we set R(v) = v for all $v \in V$. We will prove that at the end of the algorithm the vertices of R-trees are the connected components CC(G).

We execute the main loop $c_0 \log n$ times, where c_0 is a constant defined in the proof below. On each execution of male, we merge together connected subgraphs by a

randomly assigning R-roots <u>male</u> or <u>female</u> with equal probability, and then letting each R-root assigned <u>male</u> to be merged into a R-root assigned <u>female</u>, if there is an edge between those corresponding subgraphs. Note that we can view this a mating process where each male may be mated and merged into at most one female but many males may merge into the same female.

It will be useful to define $D(E) = \{(v,u) \mid \{v,u\} \in E\} \cup \{(u,v) \mid \{v,u\} \in E\}$ to be the directed edges derived from E.

algorithm RANDOM-MATE

input graph G = (V, E) with n = |V| and m = |E|.

initialize for each $v \in V$ in parallel do $R(v) \leftarrow v$ od

main loop: for $t = 1, ..., c_0$ log n

do

assign sex: for each v ∈ V in parallel do

if R(v) = v then

comment v is currently a R-root

randomly assign $SEX(v) \in \{male, female\}$

fi od

merge: for each $(v,u) \in D(E)$ in parallel do MATE(v,u)

collapse: For each v E V in parallel

do $R(v) \leftarrow R(R(v))$

comment collapse the R-trees to depth

od od

output $R(1), \ldots, R(n)$

Also we define

procedure MATE(v,u)

<u>if</u> SEX(R(v)) = male and SEX(R(u)) = female

then R(R(v)) + R(u) fi

comment attempt to mate $\underline{\text{male}}$ R-root R(v) with $\underline{\text{female}}$ R-root R(u).

Claim 4.1. The vertex set of each R-tree is always within a single connected component of CC(G).

Proof. See Appendix A4.

Note RANDOM-MATE may have incorrect output if after $c_0 \log n$ iterations, there still exists an active R-root. But the main body can easily be altered to test if $\exists \{v,u\} \in E$ such that $R(v) \neq R(u)$ and if so, go back to the main loop.

RANDOM-MATE then yields the following (nonoptimal) result:

LEMMA 4.2. For any graph G with n vertices and m edges, we can compute CC(G) in time $\tilde{O}(\log n)$ using m+n processes.

Proof. See Appendix A4.

4.2-4.4 Optimal Parallel Algorithms for Various Edge Densities

We hope our careful description of RANDOM-MATE has interested the reader enough to read the proof of Theorem 4.1 given in the Appendix. The proof is broken into three cases:

- (1) $m \ge n (\log n)^2$
- (2) $m \ge n (\log n)^{1/3}$
- $(3) \quad m \leq n (\log n)^{1/3}$

Cases (1) and (2) apply random sampling techniques and various modified and improved forms of RANDOM-MATE which use (m+n)/log n processors. Case (3) uses a variant of RANDOM-MATE with a randomized conflict resolution technique similar to the conflict resolution techniques used in our integer sorting algorithm. The details are found in Appendix A4.

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APPENDIX Al: Probabilistic Bounds

The randomized algorithms in the preceding sections are analyzed by applying the following probabilistic bounds on the tails of binomial and hypergeometric distributions (see also [Feller, 80]).

Let random variable X upper bound random variable Y (and Y lower bound X) if for all x such that $0 \le x \le 1$, $Prob(X \le x) \le Prob(Y \le x)$.

Al.1 Binomial Distributions

A binomial variable X with parameters n,p is the sum of n independent Bernoulli trials, each chosen to be 1 with probability p and 0 with probability 1-p. The binomial distribution function is $Prob(X \le x) = \sum_{k=0}^{x} \binom{n}{k} p^{n} (1-p)^{n-k}$. The bounds of [Chernoff, 52] and [Angluin and Valiant, 79] imply

LEMMA Al.1. $\forall \epsilon, p, n$ where $0 \le p \le 1$ and $0 \le \epsilon \le 1$,

Prob
$$(X \le (1-\epsilon)pn_1) \le \exp(-\epsilon^2 np/2)$$

Prob $(X \ge (1+\epsilon)np_1) \le \exp(-\epsilon^2 np/3)$.

Al.2 Hypergeometric Distributions

Fix p,s where $0 \le p \le 1$ and $0 \le s \le n$. Let A be a subset of $\{1, ..., n\}$ of size np. A hypergeometric variable Y with parameters s,np,n is defined as $Y = |S \cap A|$ where S is a random sample of s elements of $\{1, ..., n\}$ chosen without replacement.

Suppose we independently choose $s \le n$ random integers $r_1, \ldots, r_s \in \{1, \ldots, n\}$. Let index i be the *conflicted* if \exists distinct a,b such that $r_a = r_b = i$. Let Z be the total number of conflicted indices $i \in \{1, \ldots, n\}$.

LEMMA Al.3. Z is upper bounded by a hypergeometric variable with parameters s,s,n.

[Johnson and Katz, 69] attribute the following bound to Uhlmann.

LEMMA A1.4. If x is binomial with parameters s,p and y is hypergeometric with parameters s,np,n then

$$\operatorname{Prob}(X \leqslant \mathbf{x}) > \operatorname{Prob}(Y \leqslant \mathbf{x}) \qquad \text{for} \qquad 0 < \mathbf{p} \leqslant \frac{n\mathbf{x}}{(\mathbf{s}-1)(\mathbf{n}+1)}$$
 and
$$\operatorname{Prob}(X \leqslant \mathbf{x}) > \operatorname{Prob}(Y \leqslant \mathbf{x}) \qquad \text{for} \qquad \frac{(1+n\mathbf{x}/(\mathbf{s}-1))}{(\mathbf{n}+1)} \leqslant \mathbf{p} \leqslant 1$$

APPENDIX A3: Proof of Parallel Sorting Algorithms

Proof of Lemma 3.3. Compute $(h_0, ..., h_k) = PREFIX-SUM(|I(1)|, ..., |I(n)|)$ in $O(\log n)$ using P processors by Lemma 2.1. We then set $\sigma(h_{k-1}+1), \ldots, \sigma(n_k)$ to consecutive elements in I(k) using a total of O(log n) time and P processors (the required processor assignment can easily be done by using the prefix sum computation.) Then $k_{\sigma(1)} \leq ... \leq k_{\sigma(n)}$ is a sort. <u>Proof of Lemma 3.4.</u> To each processor $\pi \in [P]$, we assign key indices $J(\pi) =$ $\{j \mid (\pi-1) \log n < j \le \min(n, \pi \log n)\}$. Let each processor π sequentially sort the keys $\{k_j \mid j \in J(\pi)\}$ by BUCKET-SORT in time $O(\log n)$, and so compute each list $J_{\pi,k} = 0$ $(j \in J(\pi) | k_j = k)$ in increasing order of indices for each key value $k \in [\log n]$. Then for each key value $k \in [\log n]$ we compose the lists $J_{1,k} \dots J_{p,k}$ to form the list I(k) of indices with key value k. Finally, we apply Lemma 3.3 to compute the required permutation σ ordering the indices as they appear in I(1),...,I(P). The total time is O(log n) using P processors. <u>Proof of Lemma 3.5.</u> Let $k_i' = {}^{t}k_i/\log n! + 1$ and let $k_i'' = k_i - (k_i' - 1)\log n + 1$ for each i \in [P]. We first apply Lemma 3.4 to get a sort of k_1', \ldots, k_n' , yielding a permutation σ. Then we apply Lemma 3.4 again to get stable sort of $k_{\sigma(1)}^{"},...,k_{\sigma(n)}^{"}$, yielding a permutation σ' . Then $k_{\sigma'(1)} \le \ldots \le k_{\sigma'(n)}$, and hence σ' is a sort of k_1, \ldots, k_n . Proof of Lemma 3.6. If $d_0(\log n)^2 \ge |I(k)|$ then always $N(k) \ge d_0(\log n)^2 \ge |I(k)|$. Else suppose $d_{\Omega}(\log n)^2 < |I(k)|$. $|I_{S}(k)|$ is upper bounded by a binomial variable with parameters $n/\log n$, |I(k)|/n. The Chernoff bounds given in Appendix A.1, Lemma Al.1, imply $\exists c \ \forall \alpha \ge 1$ if $c_0 = (c\alpha)^{-1}$ then

 $\text{Prob} \left(\left| \mathbf{I}_{S}(\mathbf{k}) \right| \geqslant \tilde{a_{0}}^{1} \mathbf{I}(\mathbf{k}) \left| / \log n \right| \geqslant 1 - 1 / n^{\alpha}. \text{ Since } N(\mathbf{k}) \geqslant d_{0} \left| \mathbf{I}_{S}(\mathbf{k}) \right| \log n, \text{ the probability bounds hold as claimed.}$

Proof of Claim 3.1. By Lemma 3.6, with likelihood $\geqslant l-1/n^{\alpha}$, we can assume $N(k) \geqslant |I(k)|$. Let $n_k = \sum_{i \in N(k)} CONFLICT(i)$. The key observation is that on each stage t, $1/\log n$ of the key indices of I(k) are assigned to random positions of the table A_k . Let $n_{k,t}$ be the number of indices $i \in N(k)$ where CONFLICT(i) is set to 1 on stage t. Then by definition $n_k = \sum_{t=1}^{log n} n_{k,t}$. We now apply the probabilistic bounds given in Appendix A.1, and we consider upper

bounds on probability variables to be over the range of probability densities from $1/n^{\alpha}$ to $1-1/n^{\alpha}$. By Lemma Al.3, each $n_{j,t}$ is upper bounded by a hypergeometric variable with parameters $|I(k)|/\log n$, $|I(k)|/\log n$, |I(k)|. Then Lemma Al.4 implies each $n_{k,t}$ is upper bounded by a binomial variable with parameters $N(k)/\log n$, $1/\log n$. Hence by (Hoeffding's inequality) Lemma Al.2, $n_k = \sum_{t=1}^{\log n} n_{k,t}$ is upper bounded by a binomial variable with parameters N(k), $1/\log n$. Furthermore $\sum_{k \in [D]} N(k) \leq O(n)$, so $\sum_{k \in [D]} n_k$ is upper bounded (by Hoeffding's inequality) by a binomial variable with parameters O(n), $1/\log n$. The Chernoff bounds given in Lemma Al.1 immediately imply the claimed probabilistic bounds on n'.

Proof of Corollary 3.1. We execute the following algorithm.

Step 1 for each processor $\pi \in [P]$ in parallel

do for each t = 1, ..., log n

 \underline{do} $i_{\pi} \leftarrow (\pi-1) \log n + t$

randomly chose $k_{i_{-}} \in [P]$

od od

Step 2 Sort k_1, \ldots, k_n and compute $I(k) = \{i \mid k_i = k\}$ for each key value $k \in [P]$ Comment. The sort can be done by Lemma 3.1 in time $\widetilde{O}(\log n)$ using P processors.

CLAIM 3.2. With high likelihood, $|I(k)| \le O(\log n)$ for each $k \in [P]$. In particular $\exists c \forall \alpha \ge 1$ Prob $(|I(k)| \le c\alpha \log n) \ge 1 - 1/n^{\alpha}$.

Proof. Each |I(k)| is upper bounded by a binomial variable with parameters n, log n/n. Hence the claimed bounds follow from the Chernoff bounds of Lemma Al.1.

Step 3 for each $\pi \in [P]$ in parallel

do let L(k) be a random permutation of the elements of I(k) od Comment. A random permutation I(k) can easily be sequentially computed in O(|I(k)|) time by a single processor.

Step 4 Compute $\sigma = (\sigma(1), ..., \sigma(n))$, the permutation of (1, ..., n) which gives the order of appearance of the indices in L(1), ..., L(P).

Comment. This can be done in O(log n) time by Lemma 3.3.

output random permutation of σ .

The total time for the steps 1-4 is $\tilde{O}(\log n)$ using P processors.

APPENDIX A4: Proof of Theorem 4.1

A4.1 Analysis of RANDOM-MATE

Proof of Lemma 4.1

Let F be a spanning forest of G. By deleting at most 1/2 the edges of F (but no active vertices), we get F' \subseteq F, a forest of trees of height 1, which contains all the active vertices. On the average, at least 1/4 of the leaves of each tree of F' are mated, since their root has probability 1/2 of being assigned female, and half of the leaves on the average will be (independently) assigned male. Hence with probability 1/2, at least 1/8 of all active vertices are mated. (Note: we can improve this result to show $\ge 1/4$ of all active vertices are mated on the average.)

Proof of Claim 4.1. We prove this by induction on the number of relations of the main loop. This initially holds when R(v) = v for all $v \in V$. Suppose the claim holds up to the t-1 iteration of the main loop. Then a R-root r is merged into an R-root r' by assigning R(R(r)) + R(r') only if $\exists \{v,u\} \in E$ such that r = R(v) and r' = R(u). Hence the claim holes after the t'th iteration of the main loop.

Proof of Lemma 4.2.

Let R_t be the value of the array R just before the beginning of the t'th iteration of the main loop. Let a R_t -root r be active if $\exists \{u,v\} \in E$ such that $R_t(v) = r$ but $R_t(v) \neq R_t(u)$. Let n_t be the number of distinct active R_t -roots on the t'th iteration. Let the execution of RANDOM-MATE of the t'th iteration be a success if $n_{t+1} \leq \gamma n_t$ where $\gamma = 1/8$. By Lemma 4.1, the total number of successes after t_0 iterations is lower bounded by a binomial variable with parameters t_0 , 1/2. Observe that if we have $\log_{\gamma} n + 1$ successes after t_0 iterations, then $n_t = 0$. By the Chernoff bounds on the binomial given in Lemma Al.1 of the Appendix A, $\forall \alpha \geq 1 \ \exists c_0$ such if $t_0 = c_0 \log n$ then $|t_0| > 1 + \log_{\gamma} n >$

Thus with probability $\geq 1-1/n^{\alpha}$, after $c_0 \log n$ iterations of RANDOM-MATE there are no remaining active vertices.

A4.2 An Optimal Algorithm for $\geq n(\log n)^2$ Edges

In this subsection we take as input a graph G = (V,E) such that $V = \{1,...,n\}$ and the edge set E is of size $m \ge n (\log n)^2$.

Our algorithm RANDOM-MATE will be a simple modification of RANDOM-MATE.

To avoid unnecessary notation (ie, the use of ceiling and floor functions) we assume without loss of generality that $\log n$ divides m.

We will use a total of $P = n/\log n$ processors. We will begin by sorting the list D(E) of directed edges into adjacency list arrays $E(1), \ldots, E(n)$ where E(v) is an array containing the sets of directed edges departing vertex v. Since |D(E)'| = 2|E|, by Theorem 3.1, this sorting can be done in $\tilde{O}(\log n)$ time using P processors.

We assign to each vertex $v \in V$ a set of log n consecutive processors $P_{v} = \{(v-1) \log n + 1, \dots, v \log n\}.$ We alter the main loop of RANDOM-MATE to execute c_{1} log n times (instead of c_{0} log n times) where c_{1} is a consent to be determined below. We also delete the original code at label merge, and substitute in its place;

merge: for each v ∈ V in parallel

do for each processor - ∈ P in parallel

do if E(v) ≠Ø then

choose a random edge (v,u) ∈ E(v) fi

MATE (u,v)

od

od

An edge $\{v,u\}$ is an R-loop if R(v) = R(u).

Claim 4.2. $\forall \alpha \ge 1 \ \exists c_1$, with probability $\ge 1 - 1/n^{\alpha}$ there are at most m/log n edges of E which are not R-loops after the c_1 log n iterations of the main loop of RANDOM-MATE.

 $\underline{\text{Proof.}}$ Let R₊ be the value of the R array just before the t'th iteration of the main loop. Let R_{+} -root r be semiactive if at least $1/\log n$ of the edges $\{\{v,u\} \in E \mid R(v)=r\}$ are not R_t -loops. Let n_t^t be the number of semiactive R_-roots. We can assume without loss of generality that $n \ge 4$. For any semiactive R_t -root r, with probability at least $(1-1/\log n)^{\log n} \ge 1/4$, some process of P_t chooses an edge $\{v,u\} \in E$ on step t such that R(v) = r, $R(u) \neq r$ and we execute MATE(v,u). Also, prob(SEX(R(v)) = male and SEX(R(u)) = female) = 1/4. Hence using arguments similar to Lemma 4.1 we have with probability at least 1/2, at most γ 'n semiactive R_{t} -roots are not merged on step t to other R_{t} -roots where $\gamma' = 31/32$. Let the t'th iteration of the main loop be successful if $n_{t+1}^* \le n_t^* \gamma^*$. We have just shown the t'th iteration is successful with probability at least 1/2. The total number of successes after $t_1 = c_1 \log n$ iterations is lower bounded by a binomial variable with parameters t_1 , 1/2. The Chernoff bounds of Lemma Al.1 imply: $\forall \alpha \ge 1 \ \exists c_1$ with probability $\ge 1 - 1/n^{\alpha}$, the number of successes after t_1 iterations is $> \log_{\gamma}$, n. But $n_{t_*}^* = 0$ after $1 + \log_{\gamma}$, n successful iterations, and hence there are no remaining semiactive R-roots.

After completing execution of these modified main loop, RANDOM-MATE' deletes each R-loop edge $\{u,v\} \in E$ (where R(u) = R(v)) in time $O(\log n)$ using P processors. Finally, RANDOM-MATE' executes the original procedure RANDOM-MATE described in 4.1 to collapse the resulting graph to its connected components. Hence we have

<u>LEMMA 4.3.</u> In time $\tilde{O}(\log n)$ using m/log n processors we can compute CC(G) for any graph G with n vertices and $m \ge n(\log n)^2$ edges.

A4.3 An Optimal Algorithm for $\geq n(\log n)^{1/3}$ Edges

LEMMA 4.4. Given any graph G = (V,E) with n vertices and $m \ge n(\log n)^{1/3}$ edges, we can compute CC(G) in time $\tilde{O}(\log n)$ using $(m+n)/\log n$ processors.

To prove this lemma, we describe another modification of RANDOM-MATE which we call RANDOM-MATE". We will give a simplified description of RANDOM-MATE". We will take as input a graph G = (V, E) with n vertices $m \ge n(\log n)^{1/3}$ edges.

In this case, we assign to each processor $\pi \in [m/\log n]$ a set V_{π} of $(\log n)^{1/2}$ distinct consecutive vertices of $V = \{1, ..., n\}$. Also we again construct, by sorting E, adjacency list arrays E(1), ..., E(n).

In this case we will execute the main loop only $c_2(\log n)^{1/4}$ iterations where c_2 is a constant to be defined below. We modify the main loop by substituting in place of the code at label <u>merge</u>, an assignment of R'(v) + R(v) for each vertex $v \in V$ and then the following code:

merge: for each processor $\pi \in [m/\log n]$ in parallel do

for each $v \in V_{\pi}$ do for $i = 1, ..., (\log n)^{1/4}$ if R(v) = R'(v) and $E(v) \neq \emptyset$ then

do choose a random edge $(v, u) \in E(v)$

MATE(v,u) fi od

od

The test R(v) = R'(v) insures that the resulting R-trees will be of height ≤ 1 after executing the code at label <u>collapse</u>. Note that the resulting main loop takes time $O(\log n)^{3/4}$ per iteration, and so the total time is $O(\log n)$ using m/log n processors CLAIM 4.3. $\exists c_2$ such that with probability 1 as $n \to \infty$, there are at most m/(log n) 1/12 edges of E which are not R-loops after $c_2(\log n)^{1/4}$ iterations of the main loop of RANDOM-MATE".

Proof of Claim 4.3. The proof is almost identical to that of Claim 4.2, except that in this case we must redefine a R-root to be semiactive if at least $1/(\log n)^{1/12}$ of the edges $\{\{v,u\}\in E\mid R(v)=v\}$ are not R-loops. If we let n_t^n be the number of (so defined) semiactive R-roots, then again we have $\Pr o(n_{t+1}^n \le n_t^n \gamma^n) \le 1/2$ where again $\gamma^n = 31/32$. Hence with probability $\geq 1-2^{-(\log n)}$ no semiactive R-root exists after $c_2(\log n)^{1/4}$ iterations, where c_2 is determined by Lemma Al.1.

Claim 4.3 implies that after 12 applications of RANDOM-MATE", the resulting graph has only $m/\log n$ edges, and hence we can apply RANDOM-MATE, Lemma 4.1, to completely collapse the graph and hence to determine its connected components in $\tilde{O}(\log n)$ time using $m/\log n$ processes.

A4.4 An Optimal Algorithm for $\leq n(\log n)^{1/3}$ Edges

Let G = (V, E) to be a graph with n vertices and $m \le n(\log n)^{1/3}$ edges. By Lemma 4.4, it suffices to show in time $\tilde{O}(\log n)$ using $P = (m+n)/\log n$ processors we can reduce the problem of computing CC(G) to the problem of computing the connected components of a partially collapsed graph with $\le O(m/(\log n)^{1/3})$ vertices and $\le m$ edges. Without loss of generality we can assume $m \ge n-1$ and 2m is divisible by $\log n$.

Let $D(E) = ((v_1, u_1), \dots, (v_{2m}, u_{2m}))$ be a list of the directed edges derived from E. We begin by computing a random permutation σ of $(1, \dots, 2m)$ by Corollary 3.1 in time $\tilde{O}(\log n)$ using P processors. We initially assign R(v) = v and $SEX(v) = \underline{female}$ for each vertex $v \in V$. This can easily be done in $O(\log n)$ time using P processors. Then we execute the following $\log n$ steps:

for t=1,...,log n do for each processor $\pi \in [2m/\log n]$ in parallel

do MATE'($v_{\sigma((\pi-1)\log n+t)}$, $u_{\sigma((\pi-1)\log n+t)}$) od

do

where we define:

procedure MATE'(v,u)

 $SEX(R(v)) \leftarrow male$

if SEX(R(u)) = female then R(R(v)) + R(u) fi

Note that each of iteration step takes only time O(1) using P processors. Let a vertex of R(G) be special if either it is isolated, or has degree $\geq (\log n)^{1/3}$, or is adjacent (by an edge of R(G)) to a vertex of degree $\geq (\log n)^{1/3}$.

CLAIM 4.4. The resulting partially collapsed graph R(G) has $\leq \tilde{O}(n/\log n)^{1/3}$) vertices which are not special, and $\leq m$ edges.

<u>Proof of Claim 4.4.</u> Let R_t be the value of R just before the t'-th iteration. Let E_t be the set of directed edges chosen on the t'-th iteration, so $D(E) = U_t E_t$. Let M_t be the number of edges $(v,u) \in E_t$ such that

- (i) v has degree $(\log n)^{1/3}$ in $R_t(G)$ and
- (ii) a processor π executes MATE(v,u) but finds SEX(R(u)) \neq female, so does not assign R(R(v)) +R(u).

Observe that initially, all vertices $v \in V$ have been assigned SEX(v) = \underline{female} , and that on successive stages $t = 1, \ldots, \log n$ at most $m/(\log n-t) \le n(\log n)^{1/3}/(\log n-t)$ vertices $v \in V$ have been assigned SEX(v) = male.

We can upper bound M_t by a hypergeometric variable, and then apply Lemma Al.4 to show that M_t is upper bounded (for probabilities in the range from $1/n^{\alpha}$ to $1-1/n^{\alpha}$) by a binomial variable with parameters $m/\log n$, $\max((\log n)^{1/3}/(\log n-t),1)$. Applying (Hoeffding's inequality) Lemma Al.2, we get $\sum_{t=1}^{\log n} M_t$ is upper bounded by a binomial with mean $\sum_{t=1}^{\log n} m/((\log n)^{2/3}((\log n-t))) \le ((m \log\log n)/((\log n)^{2/3}))$ $\le O(n/((\log n)^{1/3}))$ and parameters m, $O(((\log\log n)/((\log n)^{2/3})))$. Then $\sum M_t + O(n/((\log n)^{1/4}))$ gives an upper bound on the number of vertices of R(G) which are not special. Finally we apply the Chernoff bounds of Lemma Al.1 proving the Claim.

To complete the reduction, we delete each isolated R-root of R(G), and for each $r \in R(V)$ with degree $< (\log n)^{1/3}$ in R(G), we reassign R(r) + r' if there exists an edge $(r,r') \in R(E)$ such that r' has degree $> (\log n)^{1/3}$ in R(G). We also update R'(v) + R(R(v)) for each $v \in V$. These final steps can easily be done in O(log n) time using $(m+n)/\log n$ processors. The resulting further collapsed graph R'(G) has $\le \tilde{O}(n/(\log n)^{1/3})$ vertices and $\le m$ edges. Therefore we can apply Lemma 4.4 to completely collapse R'(G) to R"(G). The array R" specifies the connected components of G. Thus we have shown:

LEMMA 4.5. Given any graph G with n vertices and $m \le n(\log n)^{1/3}$ edges, we can compute CC(G) in $\tilde{O}(\log n)$ time using $(m+n)/\log n$ processors.

This completes the proof of Theorem 4.1.

END

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