

# Performance Trade-Offs in Using NVRAM Write Buffer for Flash Memory-Based Storage Devices

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# Outline

- Introduction
- Backgrounds
- NVRAM write buffer management policies
- Performance evaluation
- Optimistic FTL
- Conclusion

# Introduction(1/3)

- Flash memory has a few drawbacks, such as the asymmetric speed of read and write operations, inability to in-place updates, very slow erasure operation, among others.
- One of the approaches to achieve the objective is by exploiting the buffer cache in volatile memory to delay write operations.

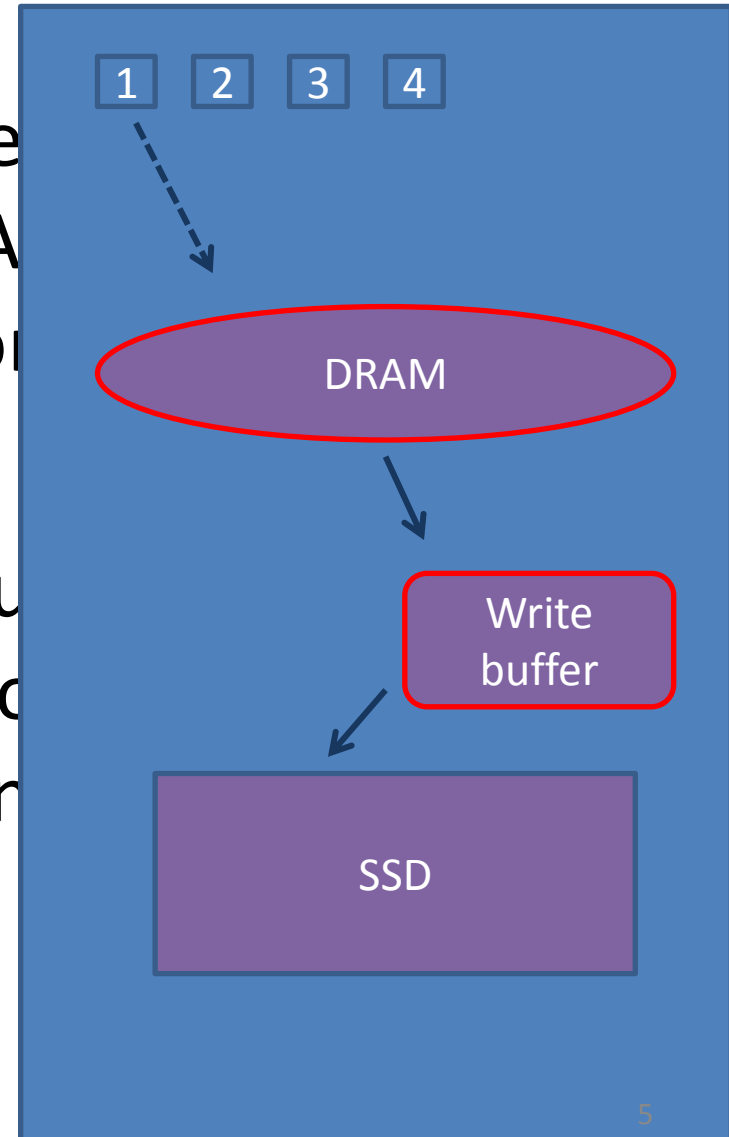
## Characteristics of Storage Media

Media	Access time		
	Read	Write	Erase
MRAM	10~50ns (2B)	10~50ns (2B)	N/A
FeRAM	30~100ns (2B)	30~100ns (2B)	N/A
PRAM	20~80ns (2B)	20~80ns (2B)	N/A
DRAM	20ns (2B)	20ns (2B)	N/A
NAND Flash	25μs (2KB)	200μs (2KB)	1.5ms

- Memory (NVRAM) price has long
- For the past decade, **next-generation nonvolatile memory** has been under active development.

# Introduction(3/3)

- In this paper, we suggest the sized, next-generation NVRA to improve the overall performance.
- We propose a novel write buffer translation layer algorithm, which is designed to harmonize NVRAM write buffers.



# Backgrounds(1/4)

- Characteristics of the NAND Flash Memory
  - Three basic operations
  - Few drawbacks :
    1. Asymmetric operations speed
    2. Inability to in-place update
    3. Limited lifetime
    4. Random page write prohibition within a block

# Backgrounds(2/4)

- Flash Translation Layer (FTL)
  - It provides a few core functionalities such as address mapping, bad block management, and ECC check.
  - The overall performance of the flash memory-based storage system highly depends on the mapping scheme.

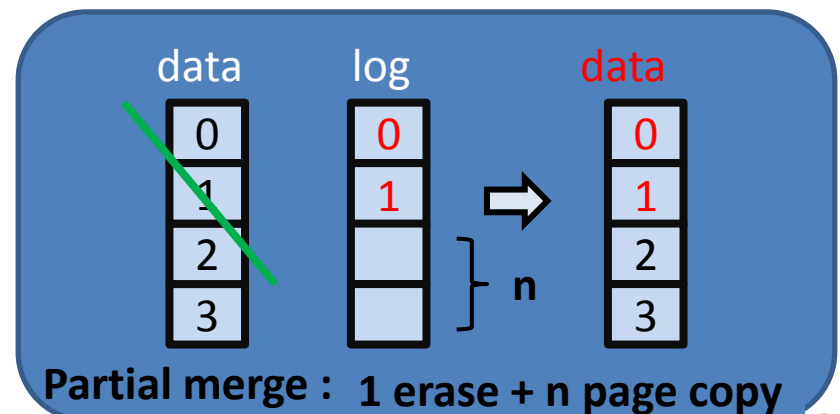
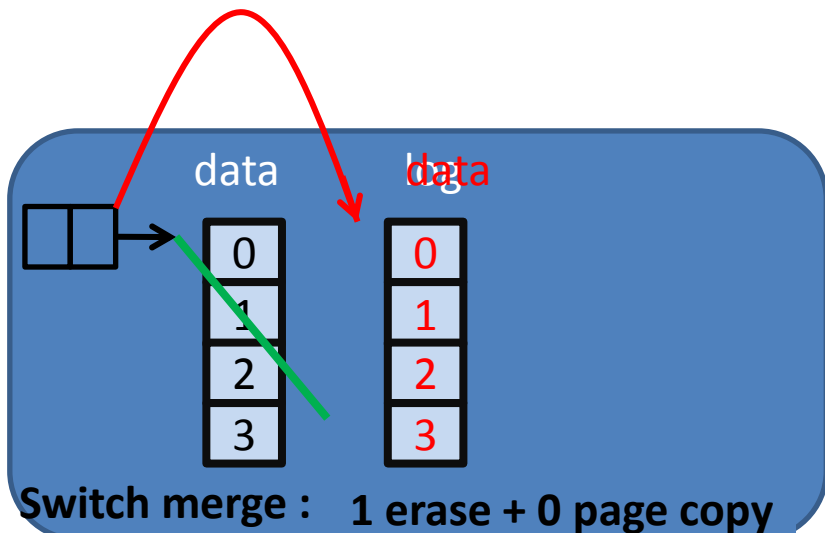
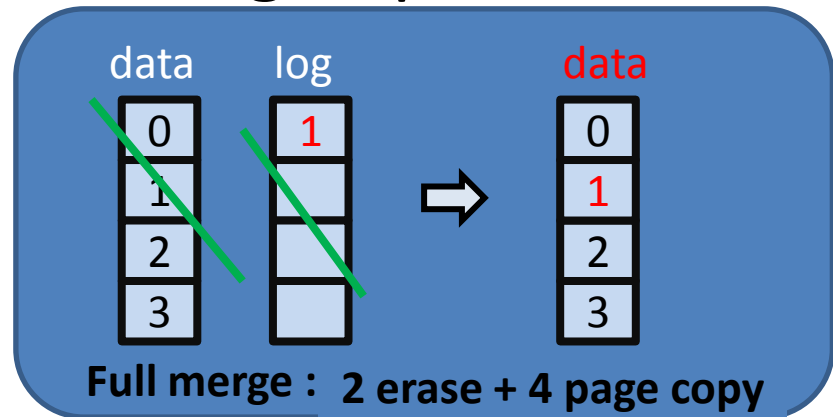
# Backgrounds(3/4)

- Log Block-Based Address Mapping
  - There are two representative log block schemes: BAST and FAST.
  - When there is no available log block, they select a victim log block and merge it with its corresponding data block(s)—a **merge** operation.



# Backgrounds(4/4)

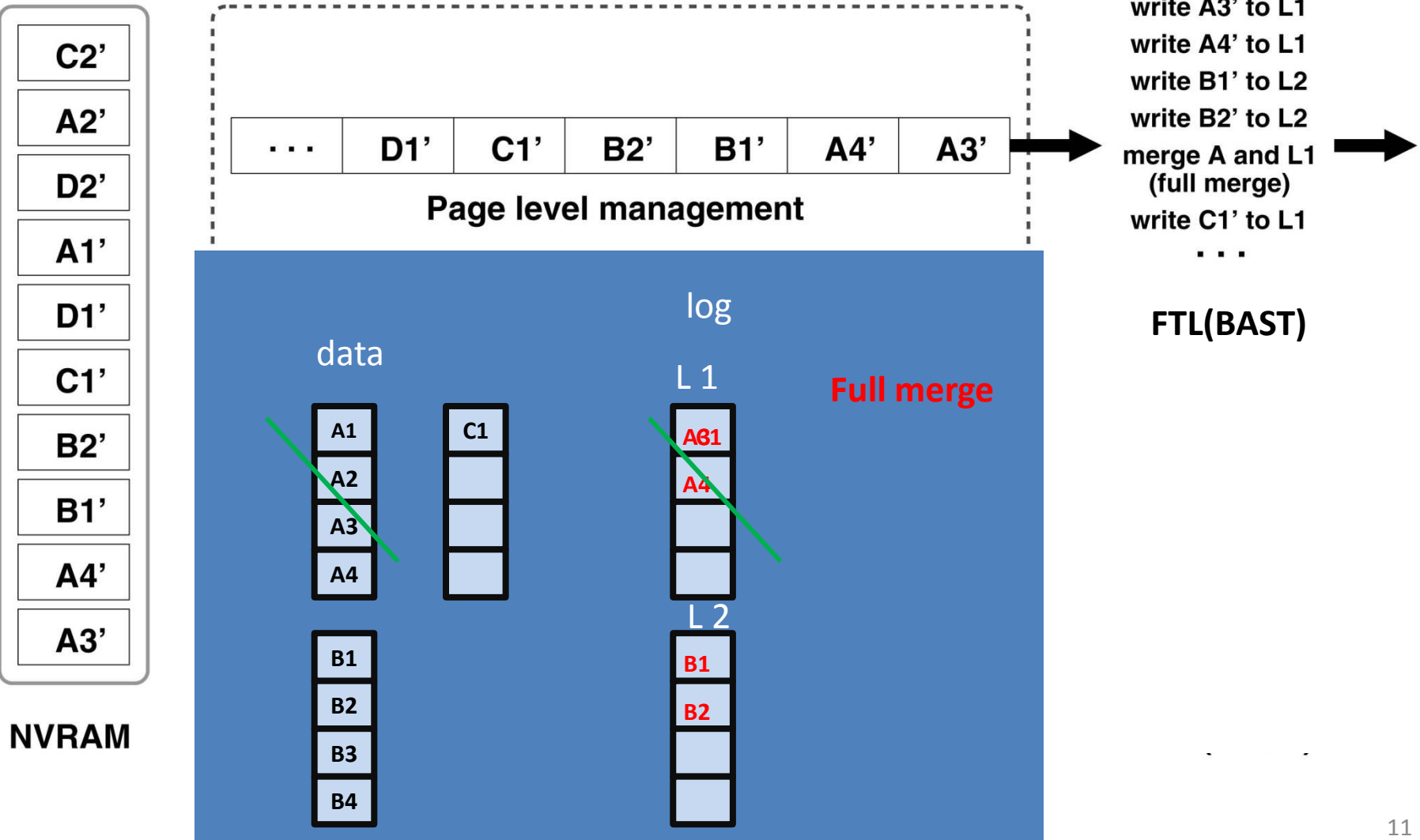
- Three different forms of merge operations :
  - Switch merge
  - Partial merge
  - Full merge

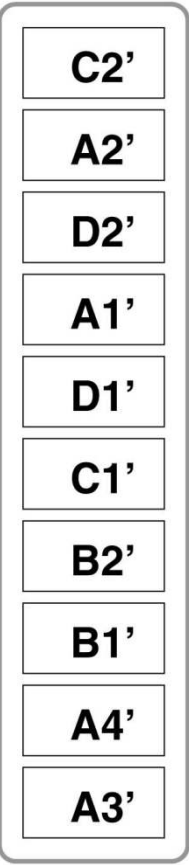


# NVRAM write buffer management policies for flash memory

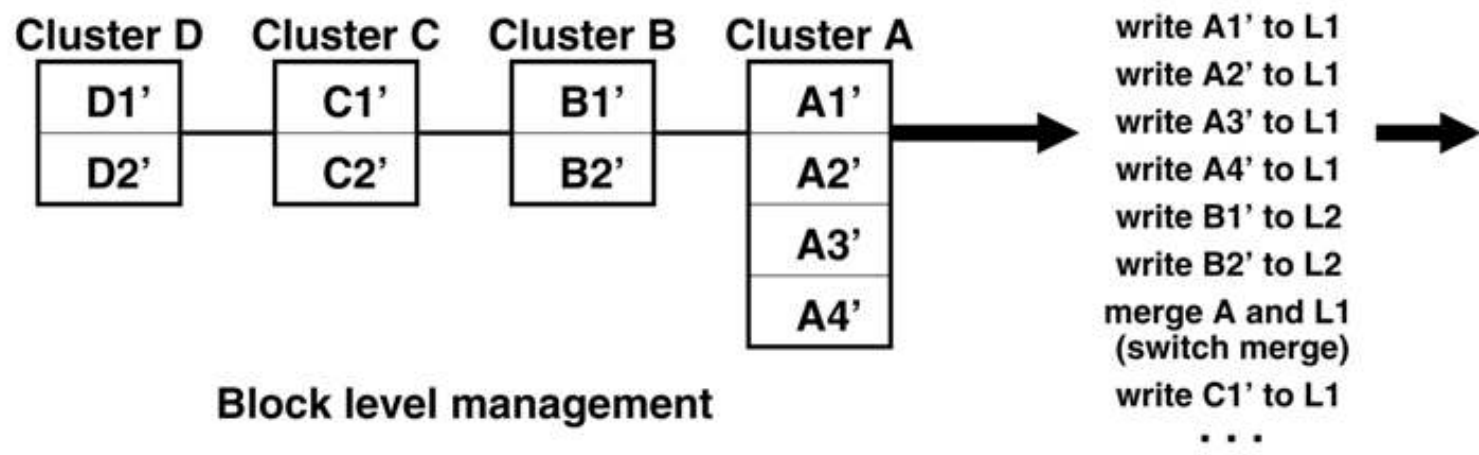
- Least Recently Used Page (LRU-P) Policy  
(page-level management)
- Least Recently Used Cluster (LRU-C) Policy
- Largest Cluster (LC) Policy
- Cold and Largest Cluster (CLC) Policy

# NVRAM write buffer management policies for flash memory

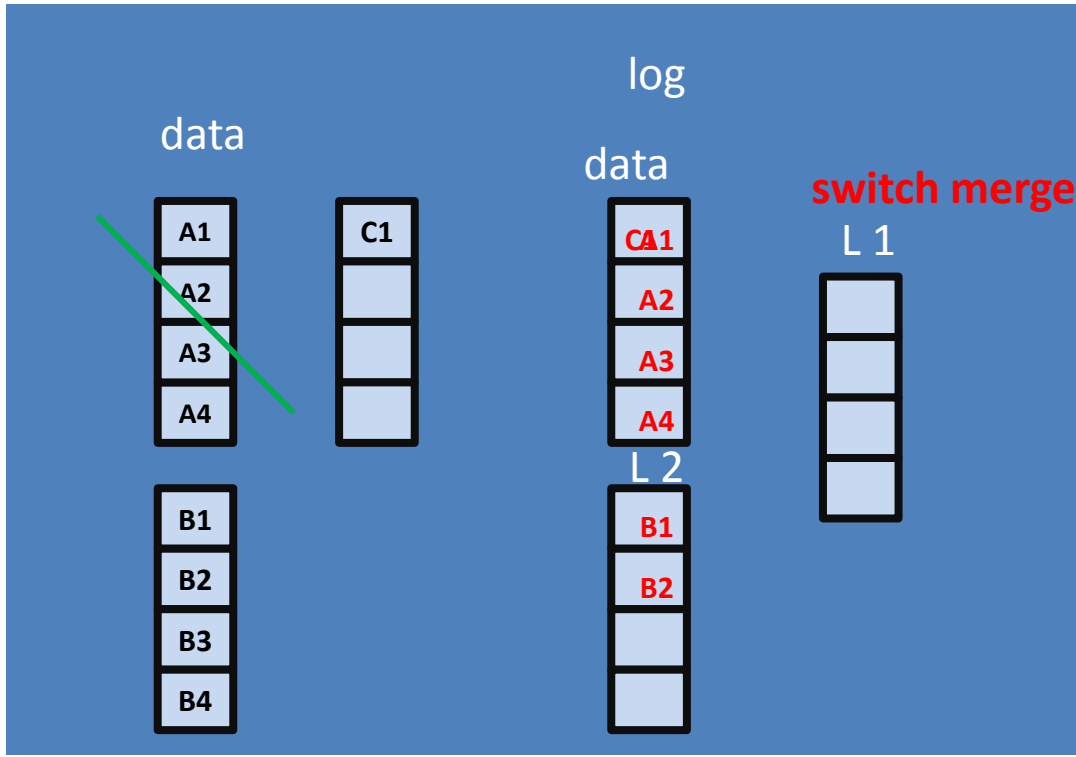




NVRAM



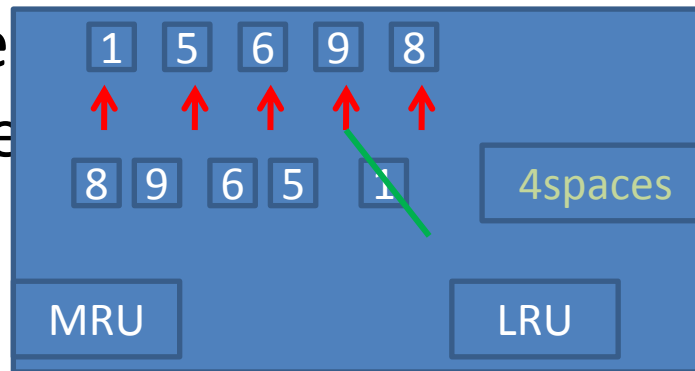
FTL(BAST)



# NVRAM write buffer management policies for flash memory

- Least Recently Used Page (LRU-P) Policy

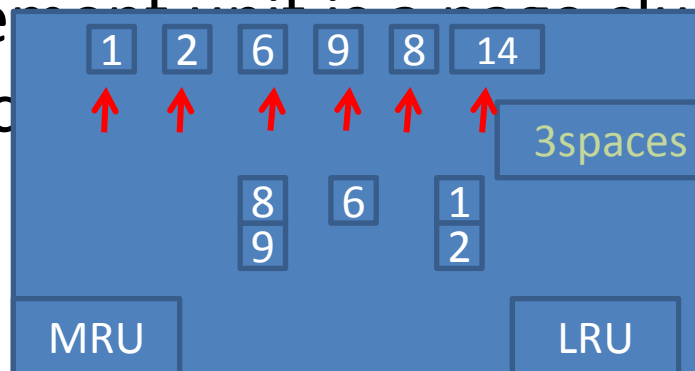
– The replacement unit is a page and the least recently used (written) page is selected as a victim.



the least recently used page is selected as a

- Least Recently Used Cluster (LRU-C) Policy

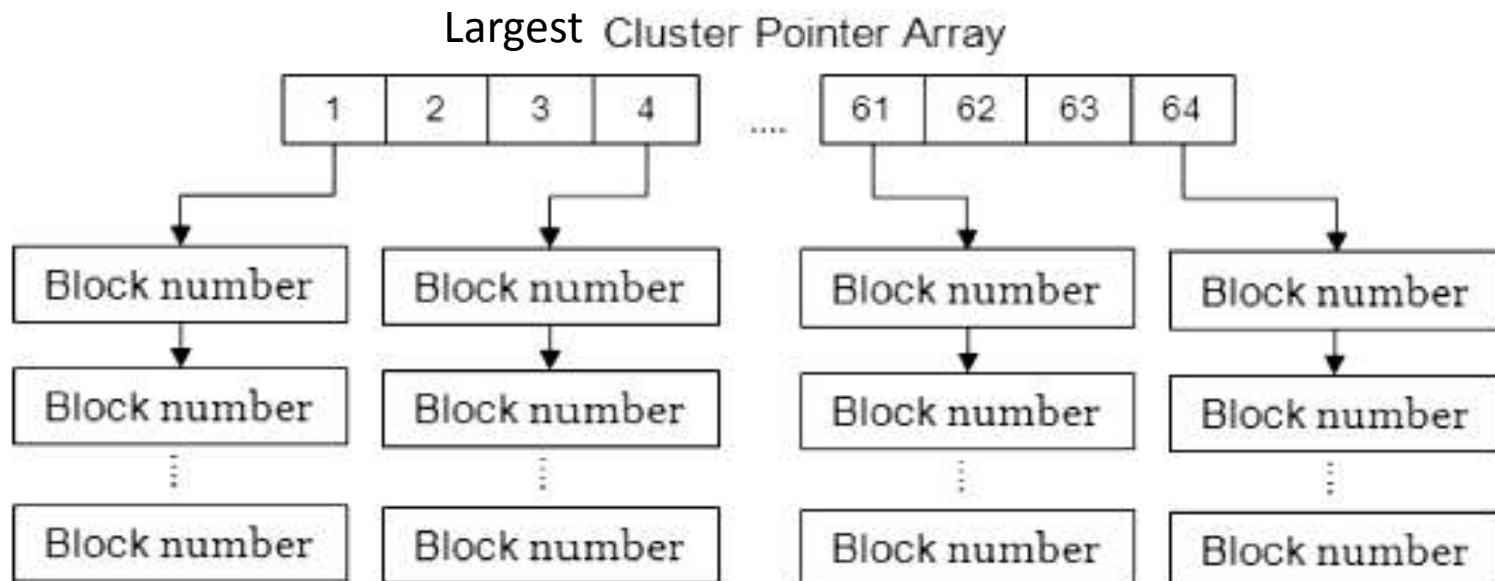
– The replacement unit is a page cluster and the least recently accessed page cluster is selected as a victim.



cluster and the least recently accessed page cluster is selected as a victim.

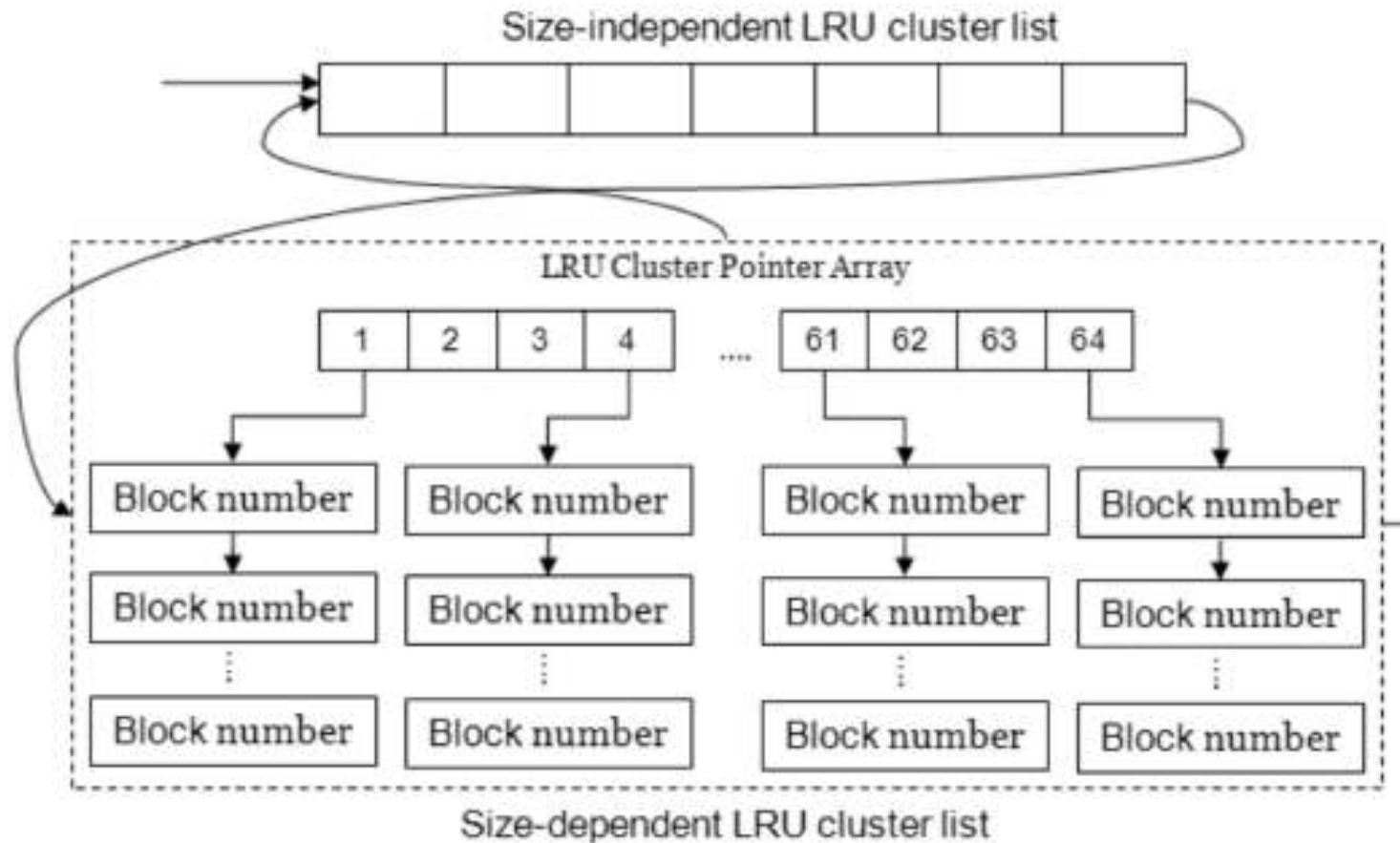
# NVRAM write buffer management policies for flash memory

- Largest Cluster (LC) Policy
  - The replacement unit is a page cluster and the page cluster with the largest cluster size is selected as a victim.



# NVRAM write buffer management policies for flash memory

- Cold and Largest Cluster (CLC) Policy



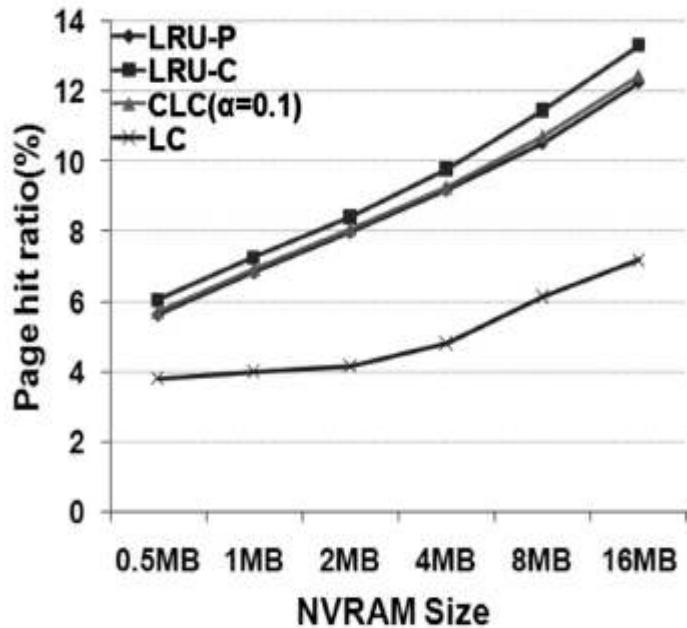
# Evaluation

## Characteristics of Disk I/O Trace

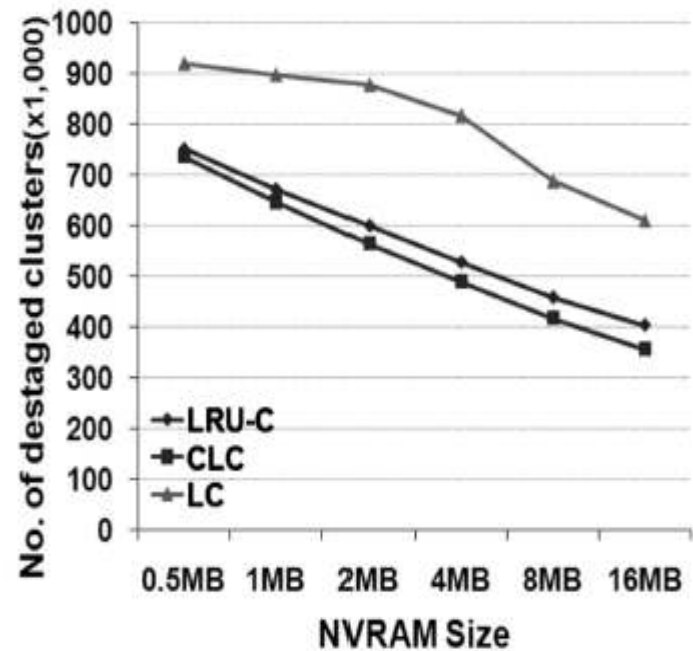
File system	FAT32, NTFS
Running application	Web surfing, sending/receiving e-mail, movie playback and downloading, document typesetting, and gaming
Sector size	512 bytes
Duration	One month
Final capacity utilization	FAT: 71% (initially empty), NTFS: N/A
Total data written	FAT: 18,284 MB, NTFS: 118,550 MB
Mean write size	FAT: 5.7 sectors, NTFS: N/A
Write locality	FAT: 65-32, NTFS: 70-17 (A-B: A% of total requests access B% of total LBA's)



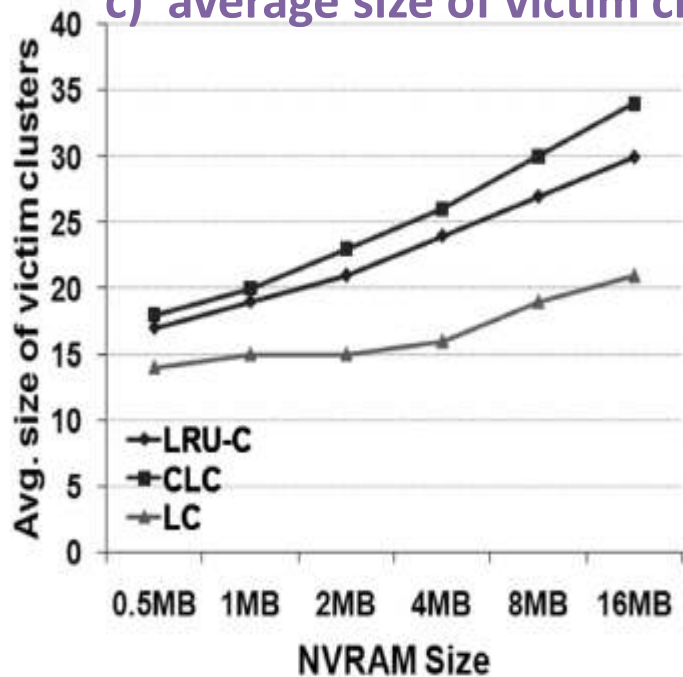
a) Page hit ratio



b) number of destaged clusters

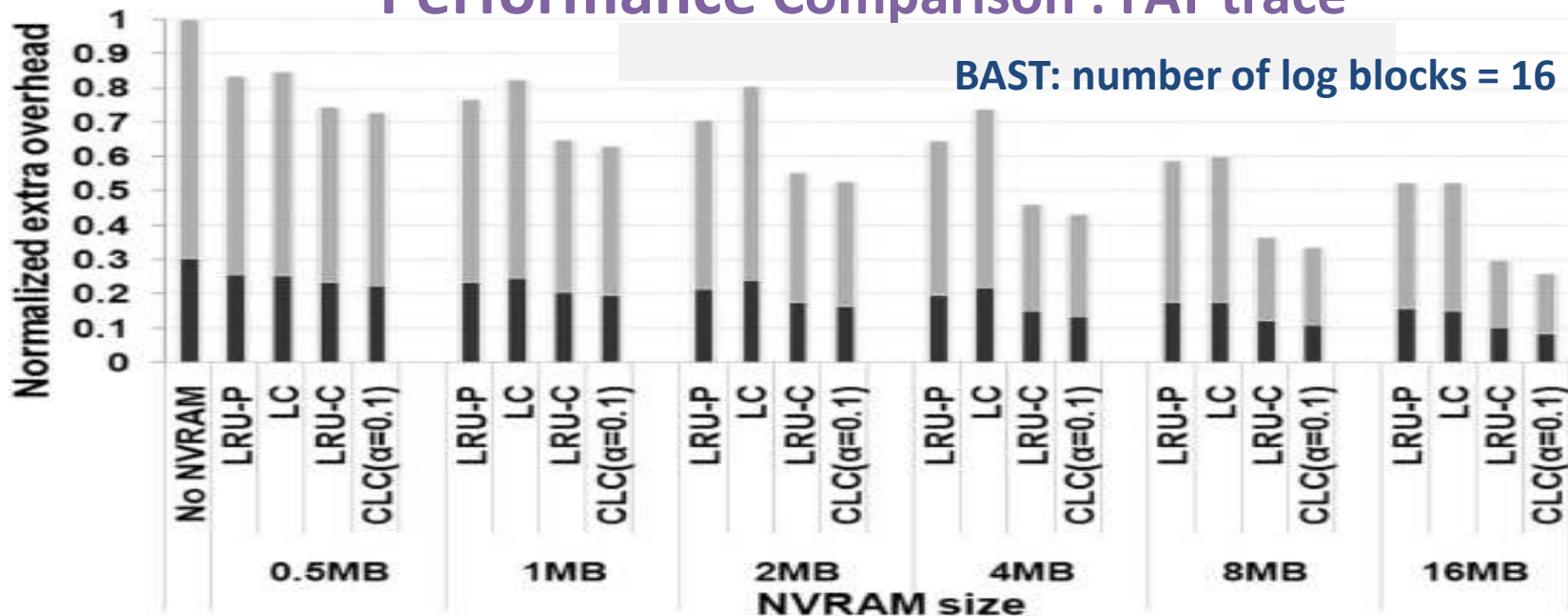


c) average size of victim clusters

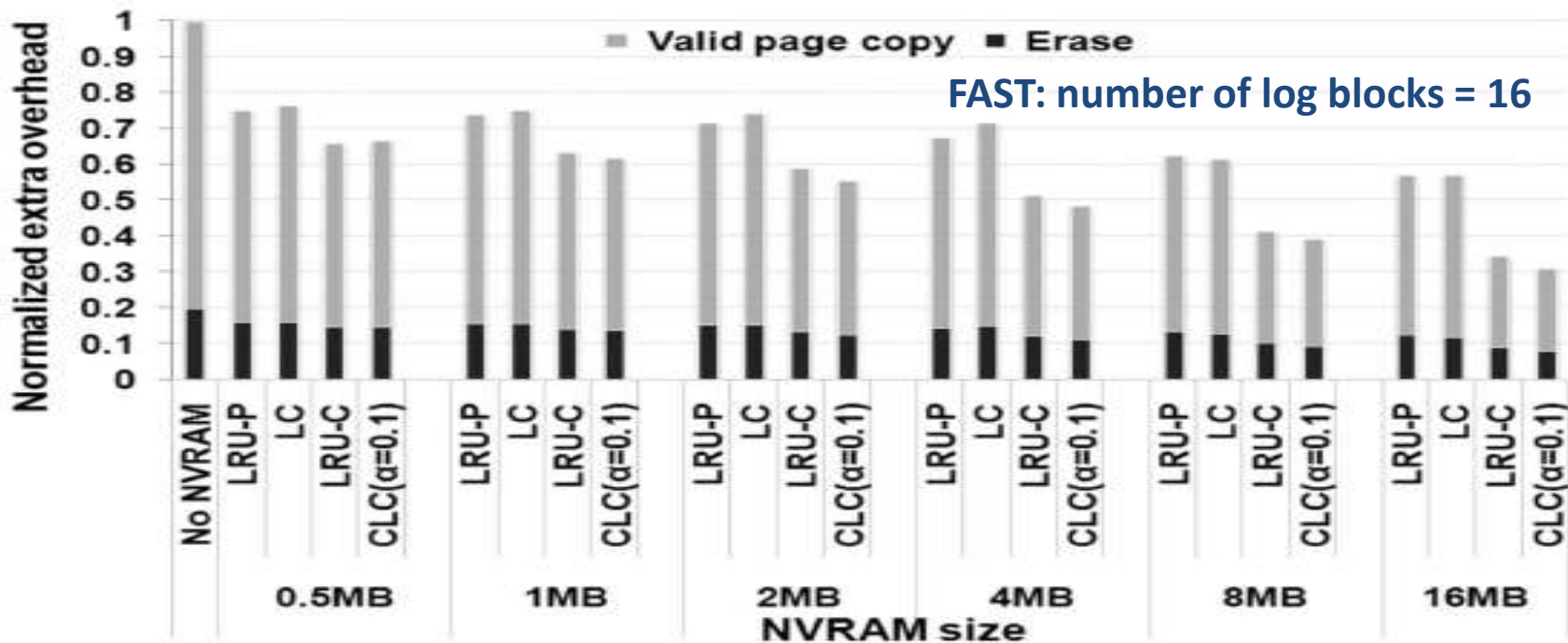


# Performance Comparison : FAT trace

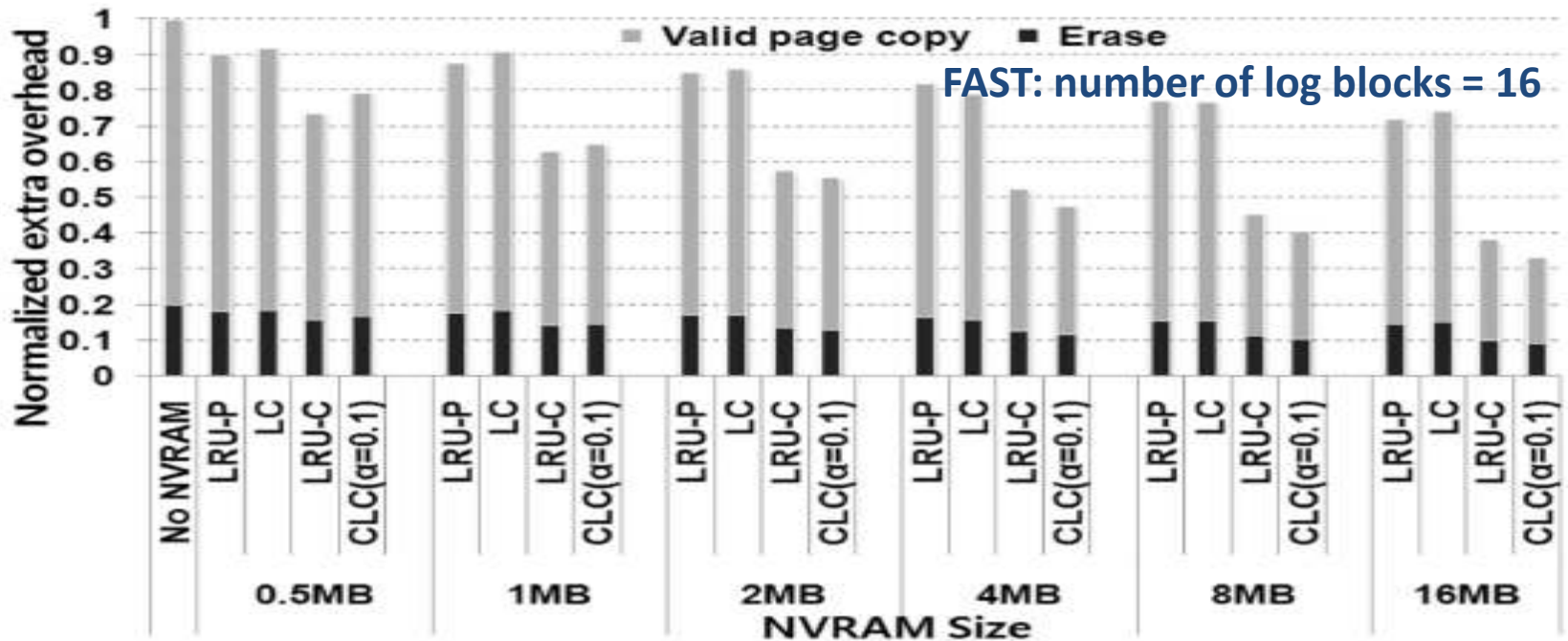
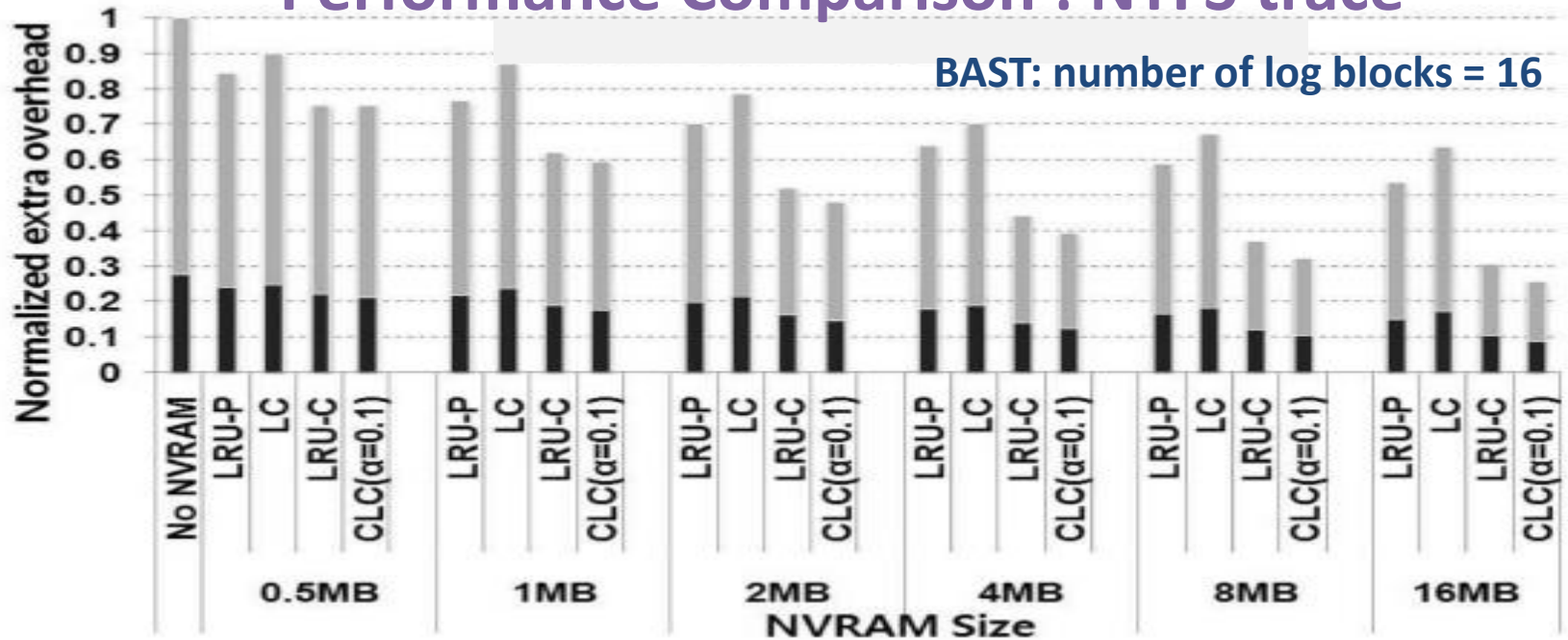
BAST: number of log blocks = 16



FAST: number of log blocks = 16

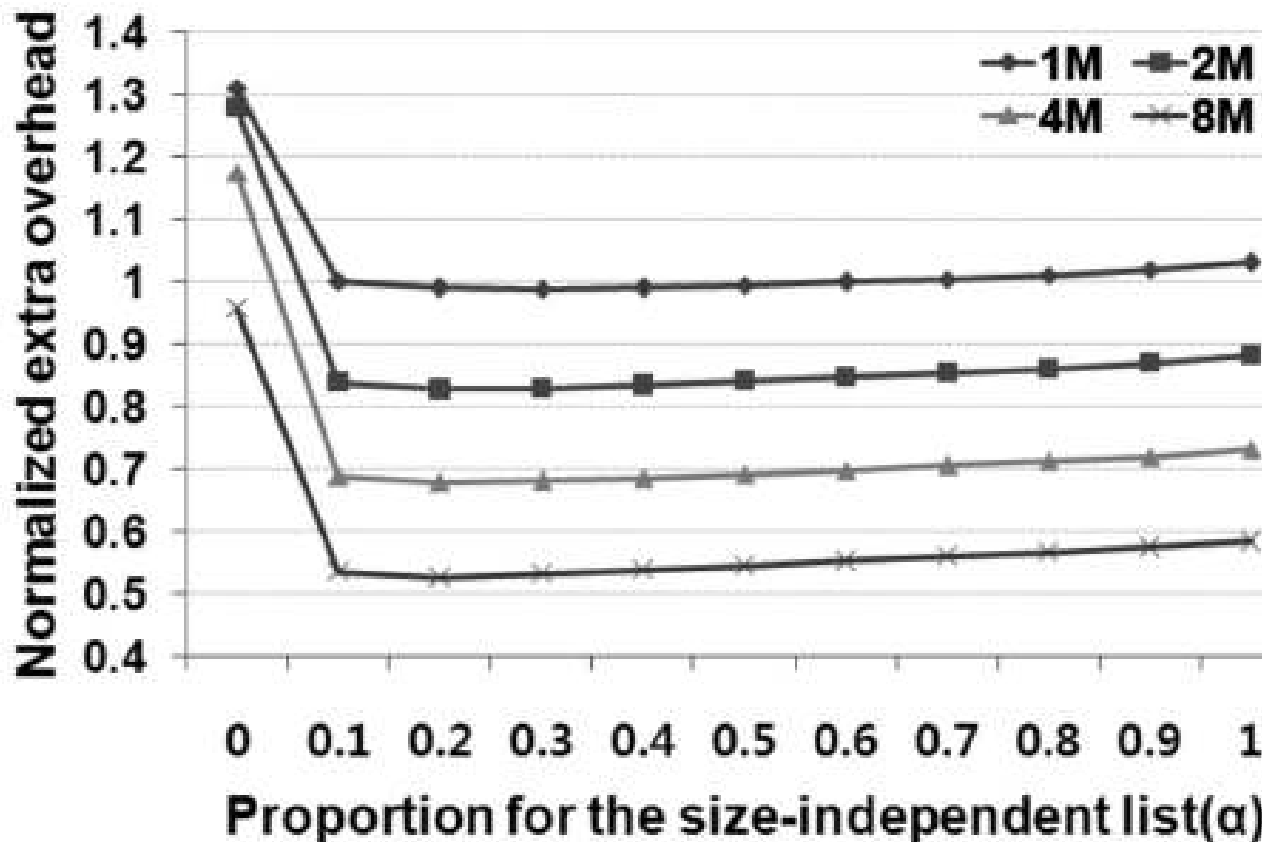


# Performance Comparison : NTFS trace



# Performance of CLC policy

$$\text{CLC} = \alpha * \text{LRUC} + (1-\alpha)\text{LC}$$



$\alpha = 0.1$

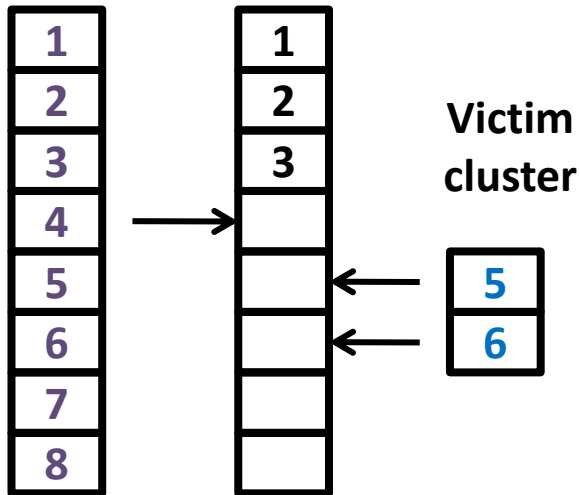
# Optimistic FTL

- Log block management :
  - a) Append operation
  - b) Data block switch operation
  - c) Log block switch operation
- $N_B$ : the block size in number of pages
- $LPI$  stores the index of the last page stored in the log block.
- $I_{min}$  and  $I_{max}$  be the smallest and largest page index in the victim cluster, respectively.

# Append operation

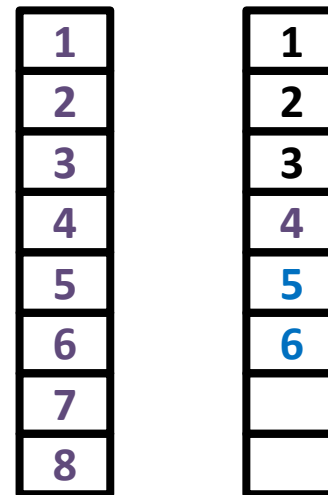
- When  $LPI < I_{\min}$  : **No erase operation is needed.**

Data block    log block



$LPI = 3$  ;  $I_{\min} = 5$  ;  $I_{\max} = 6$  ;

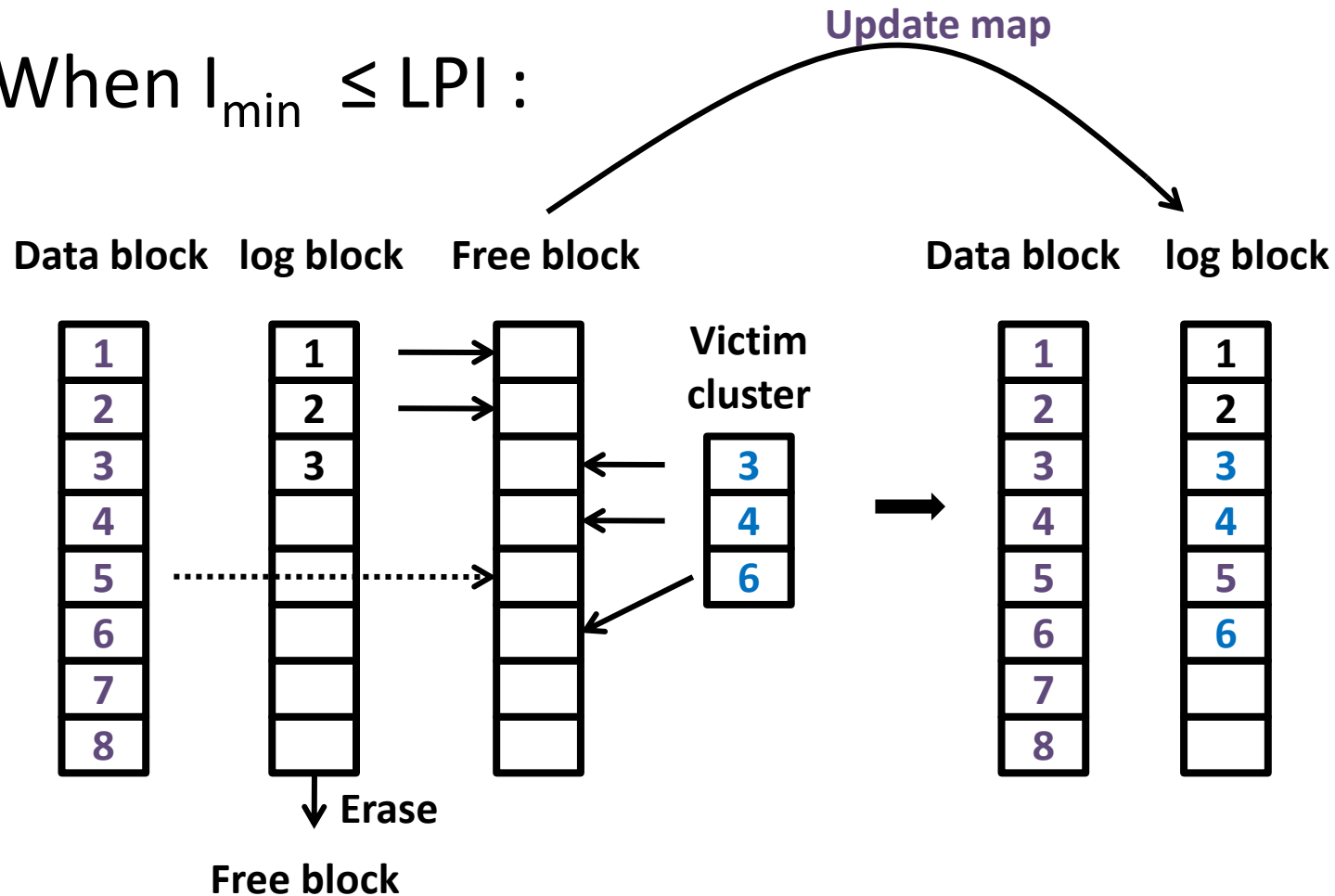
Data block    log block



$LPI = 6$  ;

# Log block switch operation(1/2)

- When  $I_{\min} \leq LPI$  :

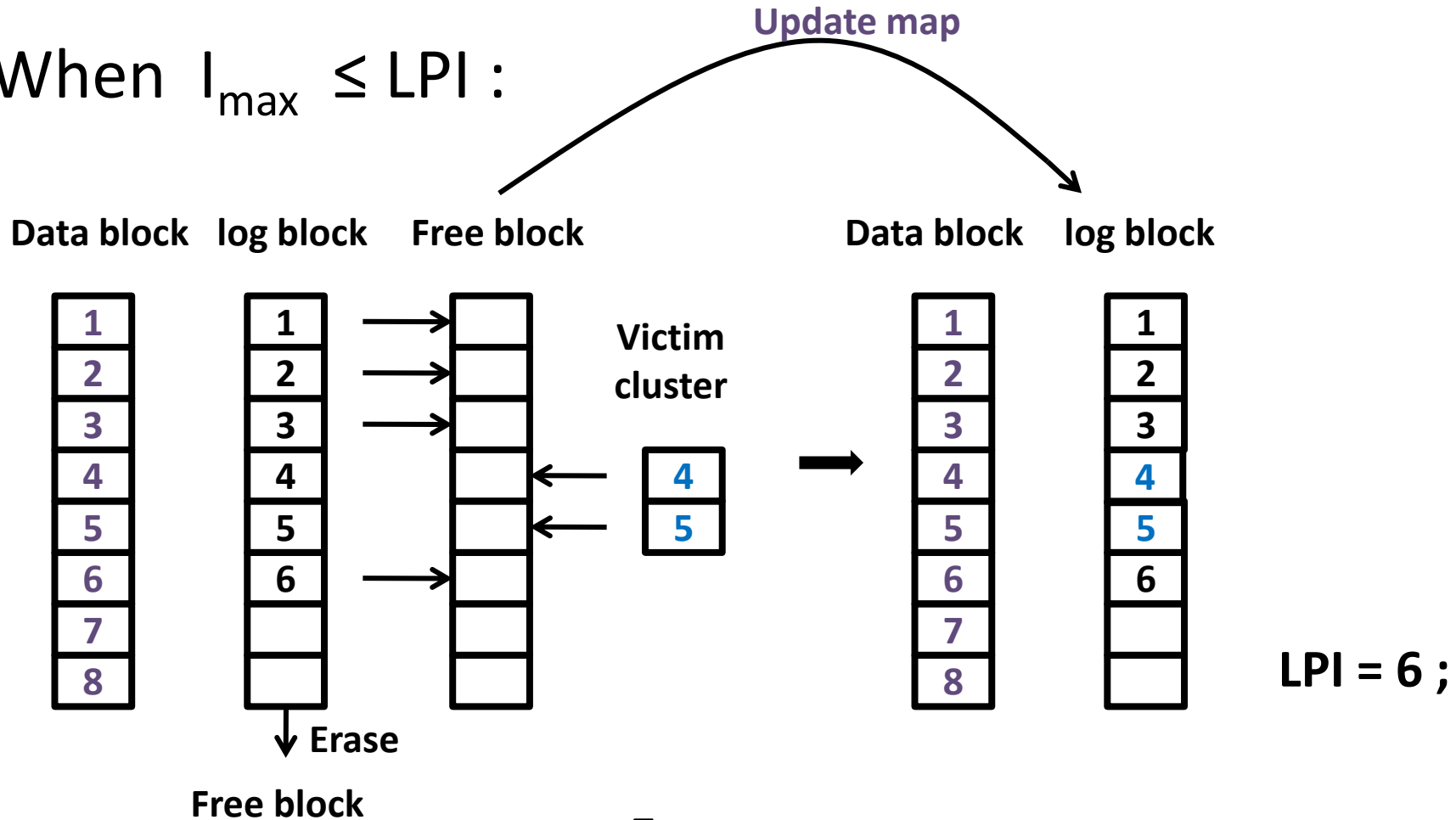


$LPI = 3 ; I_{\min} = 3 ; I_{\max} = 6 ;$

$LPI = 6 ;$

# Log block switch operation(2/2)

- When  $I_{\max} \leq \text{LPI}$  :



$$\text{LPI} = 6 ; I_{\min} = 4 ; I_{\max} = 5 ;$$

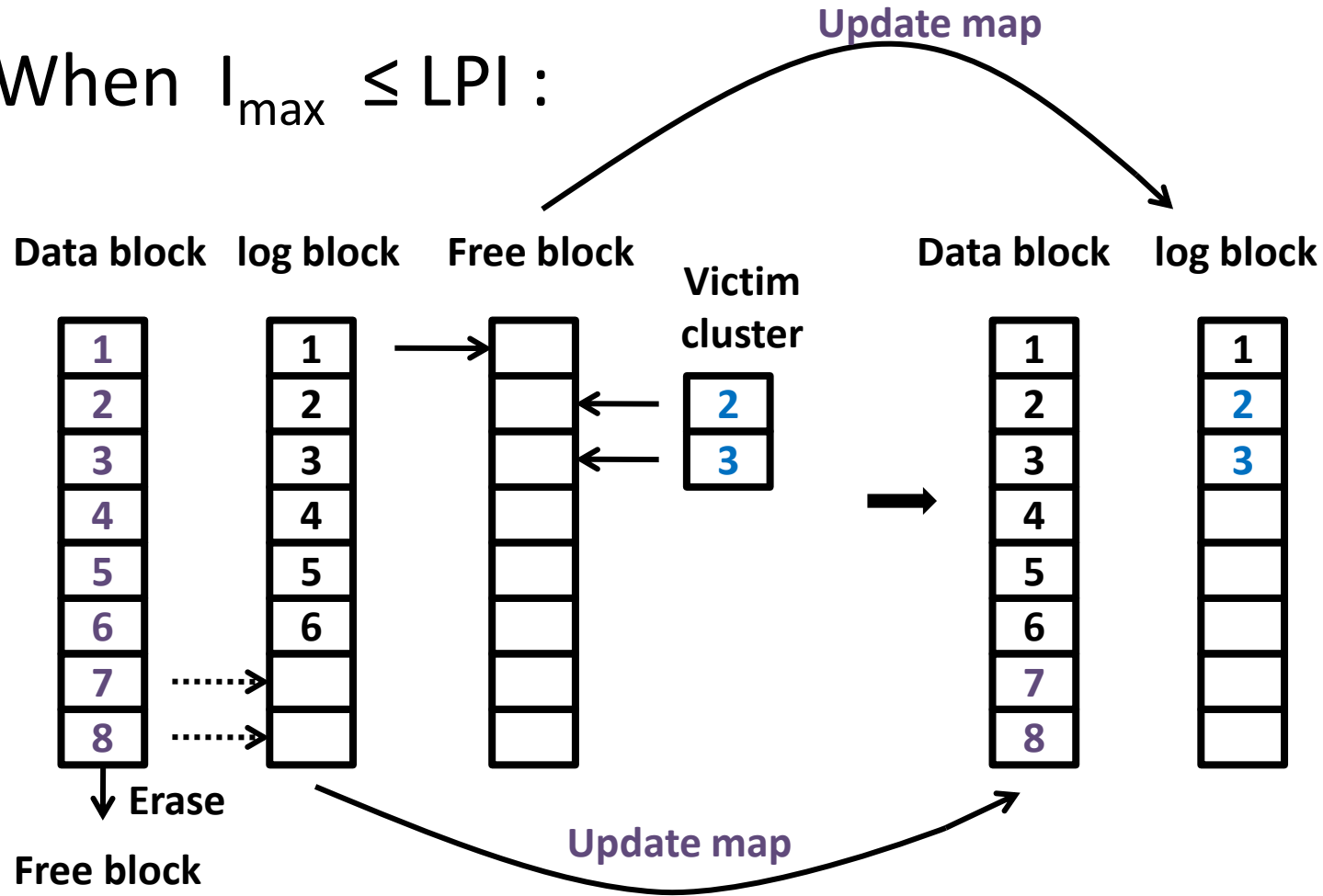
Extra cost :

$$\text{LPI} - (I_{\max} - I_{\min} + 1) = \text{LPI} - I_{\max} + I_{\min} - 1$$



# Data block switch operation

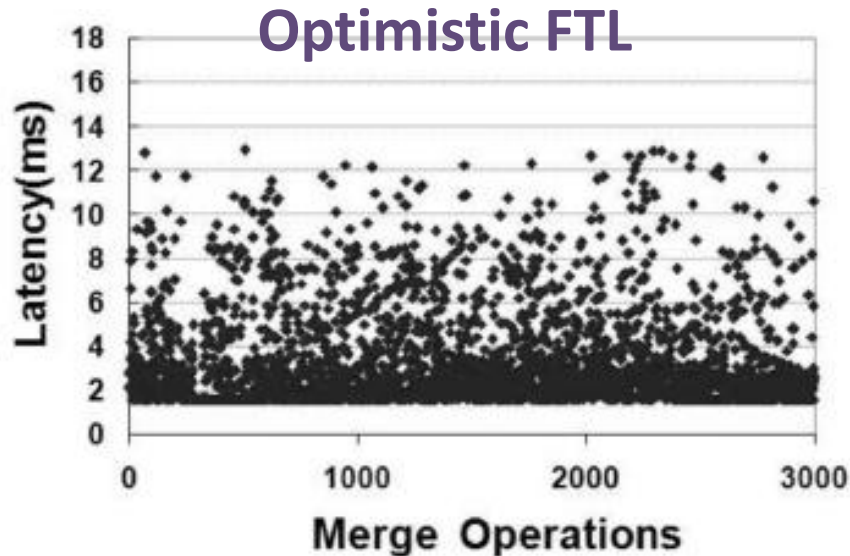
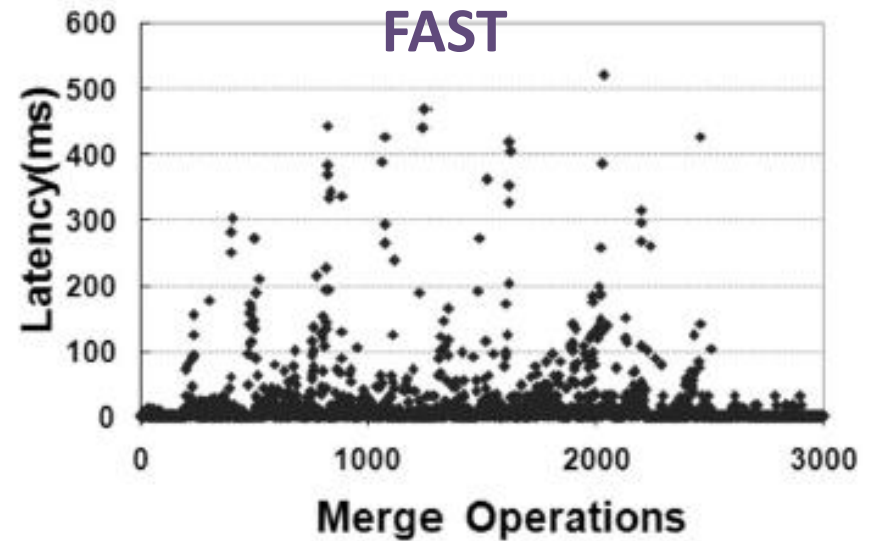
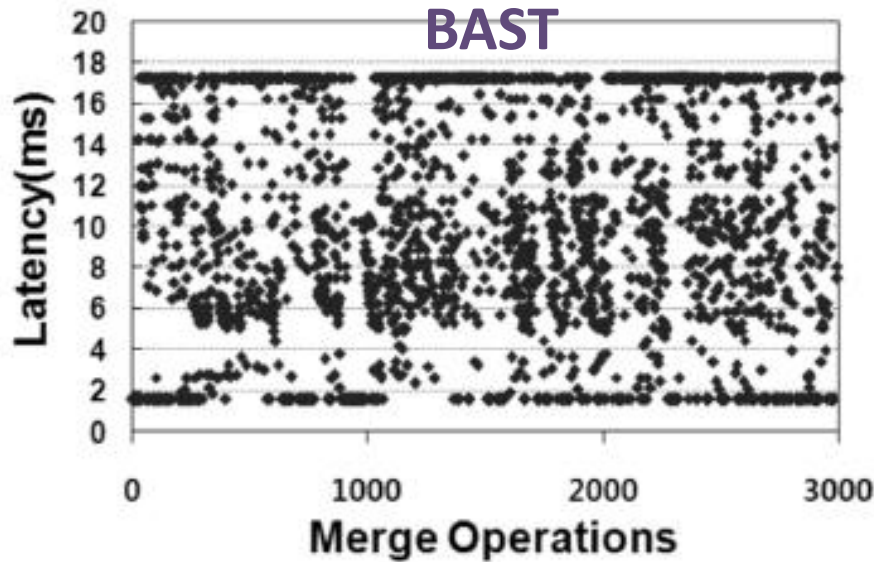
- When  $I_{\max} \leq LPI$  :



$LPI = 6 ; I_{\min} = 2 ; I_{\max} = 3 ;$

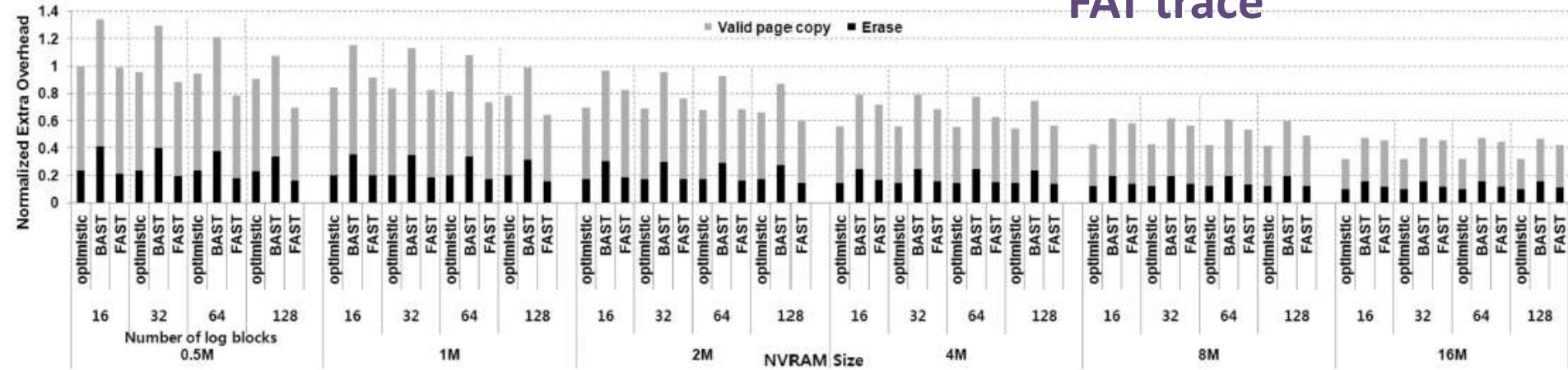
**Extra cost :**  
 $N_B - LPI + I_{\min} - 1$

# Merge latencies in each FTL algorithm

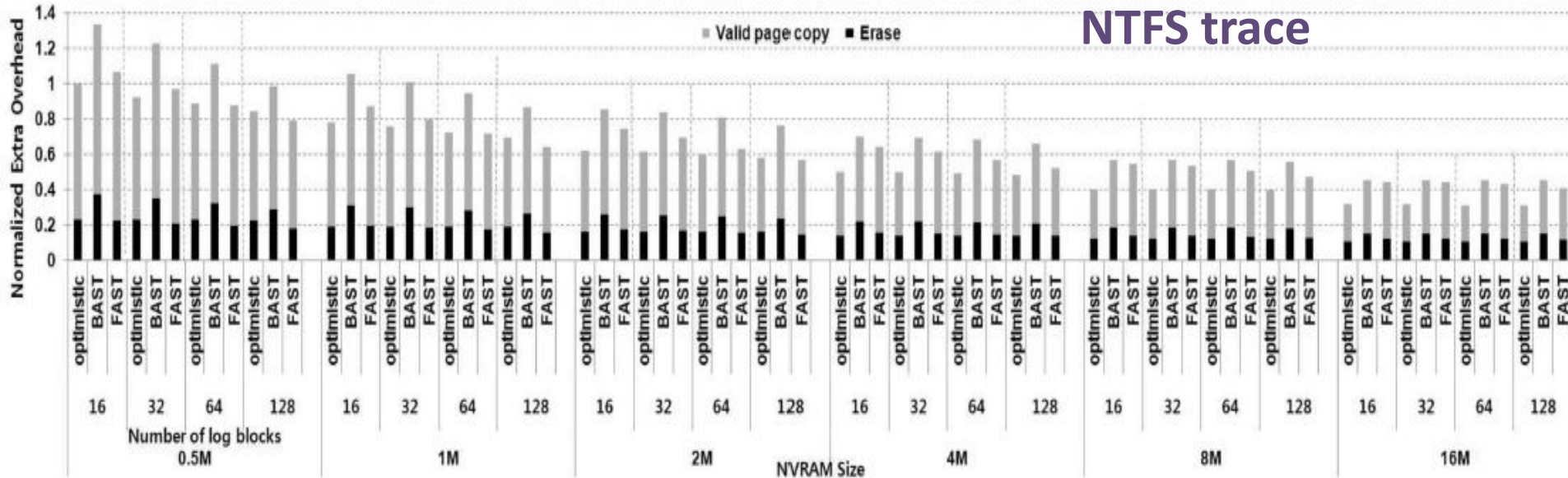


# Extra overhead in each FTL algorithm.

FAT trace



NTFS trace



# Conclusion

- The CLC policy not only exploits the temporal locality but also maximizes the number of simultaneously destaged pages.
- Simulation results have shown that the CLC policy outperforms traditional pagelevel LRU policy (LRU-P) by a maximum of 51 percent.