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### Preparation of atomic layer deposited vanadium dioxide thin films using tetrakis(ethylmethylamino) vanadium as precursor

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### Abstract

Vanadium dioxide (VO<sub>2</sub>) thin films were deposited by atomic layer deposition (ALD) using tetrakis(ethylmethylamino) vanadium precursor and H<sub>2</sub>O oxidant at a temperature of 150 °C. Optimization of post-deposition annealing results in smooth, continuous VO<sub>2</sub> films (thickness, t ~ 30 nm) with small grains, exhibiting a transition from semiconducting to metal phase, typically known as the metal-insulator transition (MIT), at ~ 72 °C with a switching ratio ~  $10^2$ . Such films were produced with high repeatability on a wafer scale and have been successfully utilized in resistively-coupled oscillators and self-selected resistive devices. Under smaller process window, thin films (t ~ 30 nm) with very large grains have also been produced, exhibiting the MIT ratio ~  $10^3$ , which is the highest achieved for the ALD VO<sub>2</sub> films deposited on SiO<sub>2</sub> substrates. Both types of films were characterized again after 120 days to access their stability in air, a property that was rarely investigated.

Keywords: Vanadium Dioxide; Atomic Layer Deposition; Metal Semiconductor Transition; Stability



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### I. INTRODUCTION

Vanadium dioxide, VO<sub>2</sub>, has attracted increasing attention recently due to its remarkable metal-insulator transition (MIT) at a transition temperature ~ 68 °C.<sup>1, 2</sup> Recently, this material has been utilized in a wide variety of devices such as the smart windows,<sup>3</sup> sensors,<sup>4</sup> steep-sloped devices,<sup>5, 6</sup> tunable capacitors,<sup>7</sup> optical limiting modulators,<sup>8</sup> memory adaptive filters,<sup>9</sup> and neuromorphic computing.<sup>10</sup> VO<sub>2</sub> has been grown by various methods, including pulsed laser deposition (PLD)<sup>11</sup> and rf magnetron sputtering.<sup>12</sup> While PLD can produce excellent films exhibiting a MIT of more than 3 orders of magnitude, this method is limited to small substrates. On the other hand, sputtering is scalable to large area, but control of film stoichiometry is not trivial.<sup>13</sup> Atomic layer deposition (ALD) is therefore of interest due to the control of stoichiometry arising from its self-limiting growth, its repeatability and conformality over large substrate areas.<sup>14</sup> Even though the deposition rate is much lower in ALD than in sputtering or PLD, the thickness requirement for devices these days are increasing smaller, making the ALD method more attractive both in research and industry.

Amongst various ALD precursors that have been developed for  $VO_2$ . tetrakis(ethylmethylamino) vanadium (TEMAV) has been increasingly used since it is a liquid precursor and the synthesized films mainly consist of +4 valency, leading to the growth of pure phase VO<sub>2</sub>.<sup>15-20</sup> Using this precursor, the window of the deposition temperatures for VO<sub>2</sub> has been reported to be between 125 and 175 °C.<sup>15</sup> The VO<sub>2</sub> thin films were successfully deposited by ALD on various substrates, such as glass, SiO<sub>2</sub>, Al<sub>2</sub>O<sub>3</sub> and GaN.<sup>17, 21-23</sup> The asdeposited ALD films are amorphous due to the low deposition temperature used, without a MIT, thus a post-deposition annealing is required. Many groups have reported the optimizations of the annealing conditions (temperature, time, ambient) and have shown that the annealing conditions are very important to achieve the desired VO<sub>2</sub> phase.<sup>16, 17, 21</sup> The



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reported for the VO<sub>2</sub> films deposited on GaN substrates.<sup>22</sup> One aspect that previous reports have not paid much attention to is the production of the VO<sub>2</sub> films on a wafer scale, which is highly desirable for industry. However, it is also desirable in research community for easier incorporation of the VO<sub>2</sub> thin films in devices, as

the typical fabrication steps, such as photolithography, are easier to process using full wafers than small pieces of substrates. However, it is difficult to achieve a good uniformity using a cross-flow reactor (for example, Savannah ALD100 system from Cambridge Nanotech), especially when using precursor like TEMAV which has a very low vapour pressure (~ 1 Torr at 107 °C).<sup>14</sup>

amount of MIT modulation, which is typically determined electrically or optically, depends

very much on the substrates; for example, a resistivity modulation ~  $10^2$  has been reported for

the VO<sub>2</sub> films deposited on SiO<sub>2</sub> substrates,<sup>17, 21</sup> but a higher modulation ~  $10^3$  has been

Therefore, we developed VO<sub>2</sub> thin films by ALD and post-deposition annealing with the emphasis on using (a) larger substrates up to a 4- or 8-inch wafer, and (b) the Si/SiO<sub>2</sub> substrates that will allow possible integration onto a CMOS platform. We have recently reported that employing TEMAV multiple pulsing mode (compare to a typical single pulsing) results in thin films with a higher deposition rate and a smaller chemical consumption without compromising the thickness uniformity.<sup>24</sup> The films we deposited have been successfully incorporated in resistively-coupled oscillators for neuromorphic application<sup>25, 26</sup> and selector devices.<sup>27</sup>

Here, we report the optimization of the post-deposition annealing conditions, employing a quartz tube furnace for annealing of small substrates and a Fiji ALD chamber for annealing of large substrates. The structural, optical and electrical properties of the VO<sub>2</sub> films that can be typically produced by the ALD are presented. Moreover, we also present the VO<sub>2</sub> thin films

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which have much larger grains and higher MIT modulation than the typical films and compare the two types of films in terms of their stability in air and potential use in devices.

### **II. EXPERIMENTAL DETAILS**

VO<sub>2</sub> thin films were deposited using a cross flow reactor system (Savannah S100 ALD from Cambridge Nanotech) on various substrates, such as Si, SiO<sub>2</sub> and quartz, with various substrate sizes up to 4-inch diameter wafer. The multiple pulsing of TEMAV is done by the following sequence: (TEMAV pulse 0.02 s -delay 2s) repeat 8 times /purge 3 s /H<sub>2</sub>O pulse 0.03 s /purge 5 s. The TEMAV cylinder was heated to 105 °C, yielding a vapor pressure ~ 1 Torr. During depositions, the chamber temperature was set to 150  $^{\circ}$ C, and the pressure is ~ 0.1 Torr which was achieved by  $N_2$  flow at 20 standard cubic centimeters per minute (sccm).

Post-deposition annealing was performed either in a 1-inch diameter quartz tube furnace (Lindberg/Blue M, Thermo Scientific) or in a Fiji F200 ALD System from Cambridge Nanotech. The following annealing parameters were varied: temperatures (450 and 550°C), pressures (between 8 and 150 Pa), ambient conditions ( $O_2$  or  $Ar/O_2$ ) and annealing times (between 0.5 and 2 hours).

The structural and optical properties of the VO<sub>2</sub> films were analyzed by room temperature Raman spectroscopy (Renishaw InVia), X-ray diffraction (Bruker D8 Advance), scanning electron microscopy (FEI Magellan 400), atomic force microscopy (Agilent 5400) and UV-Vis-NIR optical Spectrometry (Cary 7000). Wafer maps of the film thicknesses were obtained using spectroscopic ellipsometry (J.A. Woollam M-2000), integrated with an automated xy stage and a 75 W high intensity Xenon arc lamp for the spectrum of light between 0.7 to 5 eV. The film thicknesses were measured at 49 locations over a 4-inch diameter wafer. The thickness uniformity is determined by the ratio of the standard deviation over the mean value of all points taken over the wafer, multiplied by 100% and subtracted from 100%.

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To determine the MIT characteristics, the van der Pauw resistivity measurements were performed using a Keithley 4200 parameter analyzer and a prober with a home-built heater/substrate holder which temperatures were varied between 25 and 120 °C.

### **III. RESULTS**

### A. Physical properties

Raman spectroscopy is a fast, non-destructive method employed here as a preliminary test to probe the lattice vibrations in the VO<sub>2</sub>. Figure 1(a) shows the Raman scattering spectra of the VO<sub>2</sub> films under various annealing conditions in a quartz tube furnace. Thin films annealed at 450 °C under vacuum (chamber pressure at 0.3 Pa) exhibit no discernible peaks, indicating that the films are still amorphous. On the other hand, thin films annealed in air at 550 °C result in pure V<sub>2</sub>O<sub>5</sub> phase, in agreement with the previous studies.<sup>28</sup> Another source of reference for V<sup>5+</sup> peak was obtained by the analyzing V<sub>2</sub>O<sub>3</sub> powder from Alfa Aesar, before and after annealing in air at 550 °C which are also shown in Fig. 1(a). Upon annealing at 450°C at 5 Pa in O<sub>2</sub>, various peaks are observed; with the main peaks at 195, 224, 309, 399 and 616 cm<sup>-1</sup>, which can be identified as those related to VO<sub>2</sub>.<sup>29, 30</sup> The large peak at ~ 510 and the broad peak at ~ 950 cm<sup>-1</sup> are related to the silicon substrate. When the O<sub>2</sub> pressure is increased to 100 Pa, films with mixed phase (VO<sub>2</sub> and V<sub>2</sub>O<sub>5</sub>) are formed.

It appears that if a vacuum or lower O<sub>2</sub> pressure was used, a higher temperature (up to  $\sim 500 \,^{\circ}$ C) is required to produce VO<sub>2</sub>, as also shown by Zhang *et al.*<sup>18</sup> The evolution of film seems to follow from amorphous to polycrystalline phases of V<sup>4+</sup>, and then V<sup>5+</sup>. It is also noted that this evolution and the annealing parameter space are very similar among films synthesized using TEMAV.<sup>16-18</sup>

As shown in Fig. 1(b), due to the temperature gradient along the length of the quartz tube, the actual area that is suitable for annealing of VO<sub>2</sub> is rather small (the rectangular boxes measuring ~ 2 cm × 4 cm). Therefore, for higher throughput and annealing of whole wafers, a

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Fiji ALD system, with a capacity for substrate size up to an 8-inch diameter wafer, was used. As shown in Fig. 2(a), VO<sub>2</sub> samples (2 cm  $\times$  2 cm in size, total 9 pieces) were spread over the substrate holder of the ALD system, and the films were annealed at 480 °C for 2 hours under an Ar/O<sub>2</sub> flow at 200/200 sccm at a chamber pressure of 40 Pa. Figure 2(b) shows the Raman scattering spectra of the VO<sub>2</sub> thin films at various positions, and the peak intensity at 616 cm<sup>-1</sup> are shown in Fig. 2(a). Direct correlation between the peak intensity to the location of the sample is not done due to error within measurement, for example, the peak intensity will change slightly during laser warm up. However, Fig. 2 shows that the annealing was successfully performed in a Fiji ALD system for area of an 8-inch diameter wafer. The Raman peaks of VO<sub>2</sub> and V<sub>2</sub>O<sub>5</sub> from both annealing systems and those from literature are compared in Table 1, generally the Raman data between our work and others agree well.<sup>28-30</sup>

The crystallinity of the synthesized films was analyzed by XRD. In Fig. 3, both the tube furance and Fiji annealed samples show a dominant peak at  $2\theta = 27.9^{\circ}$  which is the (011) reflection. The thicker film also shows a smaller peak at  $2\theta = 39.8$  and  $57.7^{\circ}$  which are the (002) and (022) reflection (JCPDS card: 00-044-0252). These results confirm the formation of monolinic VO<sub>2</sub>(M). There are other VO<sub>2</sub> polymorphs, such as VO<sub>2</sub>(A), VO<sub>2</sub>(B) and VO<sub>2</sub>(R). The tetragonal VO<sub>2</sub>(R) is achieved upon thermal heating of VO<sub>2</sub>(M) films above 68 °C. The semi-metallic VO<sub>2</sub>(B) is an intermediate phase that is formed under narrow annealing conditions.<sup>16,17</sup> The insulating VO<sub>2</sub>(A) has rarely been reported in the ALD deposited films. However, the growth of all three polymorphs, VO<sub>2</sub>(A, B, and M) has been demonstrated by PLD on the SrTiO<sub>3</sub> substrates.<sup>36</sup>

### B. Electrical properties

The resistivity values,  $\rho$ , as a function of temperature and the corresponding derivative,  $d(\log \rho)/dT$ , of the VO<sub>2</sub> thin films after annealing in quartz tube furnace or Fiji ALD system are shown in Fig. 4(a) and (b), respectively. A clear MIT modulation by temperature, as



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shown by the low resistivity (metal) at high temperatures and high resistivity (insulator) at low temperatures, was observed. For the VO<sub>2</sub> thin films that were subjected to tube furnace annealing (Fig. 4(a)), the transition temperature during a heating cycle,  $T_{h}$ , is 72.0 °C (transition from insulator to metal), and the transition temperature during a cooling cycle,  $T_c$ , is 60.3 °C (transition from metal to insulator). For the VO<sub>2</sub> thin films that were subjected to annealing in Fiji system (Fig. 4(b)), the phase transitions occur at lower temperatures, with  $T_h$ and  $T_c$  at 60.5 °C and 47.5 °C, respectively. From the FWHM of the derivative profiles, annealing in Fiji system shows a narrower phase transition. The resistivity modulation is ~ 10<sup>2</sup> for both types of annealing, which is in the same order as that reported by Rampelberg *et al.*,<sup>17</sup> who also used the SiO<sub>2</sub> substrates. It should be noted that the resistivity modulation is dependent on the type of substrates used. An order of magnitude improvement on the resistivity modulation has been reported when the VO<sub>2</sub> is deposited on the latticed matched GaN substrates.<sup>22</sup>

### C. Optical properties

As shown in the inset of Fig. 5(a), the optical transmission spectrum of VO<sub>2</sub> with thickness ~ 30 nm exhibits a transmittance ~ 50% at wavelength,  $\lambda$ , ~ 500 nm (visible region) and up to ~ 90% at  $\lambda$  ~ 2000 nm (near infrared region), which are consistent with previous reports.<sup>31</sup> The physical appearance of the films grown on quartz substrates are greyish. The optical band gap,  $E_g$ , is extracted using Tauc's method using the relation:<sup>32</sup>

$$\alpha h v = B \left( h v - E_g \right)^n, \tag{1}$$

where  $\alpha$  is the absorption coefficient, hv is the photon energy,  $E_g$  is the band gap, B is a frequency dependent constant and n is either 0.5 or 2 (direct or indirect allowed transitions). The band gap can be deduced from the intercept of the extrapolated linear part of the plot of  $(\alpha hv)^{0.5}$  vs. hv. A smaller gap ~ 0.55 eV and a larger gap ~1.35eV can be extracted (Fig. 5a), This is the author's peer reviewed, accepted manuscript. However, the online version of record will be different from this version once it has been copyedited and typeset.

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which are consistent with the previously reported values.<sup>33</sup> The larger gap can be assigned to the onset of transitions from O 2p to V 3d states, and the smaller gap ~ 0.55 eV to the *d*-*d* transitions in the conduction band.<sup>2, 34, 35</sup> Another way to determine band gap is the E04 method. As shown in Fig. 5(b), at the absorption coefficient of  $10^4$  cm<sup>-1</sup>, the gap is 0.65 eV, broadly in agreement with the value determined by Tauc method in Fig. 5(a).

### D. Thickness uniformity and film morphology

Figure 6 shows the thickness distribution of the VO<sub>2</sub> films over a 4-inch diameter wafer. These films were deposited using multiple pulsing of TEMAV as described in a previous paper, with a GPC of  $0.06 \pm 0.02$  nm.<sup>24</sup> To improve the thickness uniformity, we rotated the wafer 180° halfway through the deposition. A deposition of 450 ALD cycles produces a film with average thickness of 25.1 ± 0.8 nm and thickness uniformity of 88.5% as shown in Fig. 6(a). Likewise, a deposition of 890 ALD cycles produces a film with average thickness of 47.7 ± 1.0 nm and a slightly better uniformity of 90.3% as shown in Fig. 6(b). The uniformity achieved within-wafer uniformity up to 98% has been previously reported for VO<sub>2</sub> deposited on a 12-inch diameter wafer.<sup>16</sup> Such a good uniformity is possible due to efficient precursor gas distribution over the substrate through a shower head in the commercial ALD tool, the EX reactor from Tokyo Electron Limited. We showed here that a modest thickness uniformity could still be achieved using a cross flow type ALD system.

Thin films deposited by ALD are typically very smooth which is very useful for large area electronics. Amorphous ALD VO<sub>2</sub> thin films show a typical root mean square (rms) roughness of only 0.27 nm for a scan area of 2 cm  $\times$  2 cm by the AFM (Fig. 7(a)). However, the required post-deposition annealing roughens the surface, with the emergence of polycrystallinity. Figure 7(b) shows a VO<sub>2</sub> thin film (t ~ 30 nm) with a roughness rms of 2.67 nm. The SEM micrograph in Fig. 7(c) shows a smooth, continuous film with a mixture of small, round grains (~ 20 – 50 nm in diameter) and larger elongated grains (~ 100 – 200 nm

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in length). The density of the film (t ~ 25 nm on Si substrate) measured by X-ray reflectivity was shown to be 4.47 gcm<sup>-3</sup>, which is close to the density of bulk VO<sub>2</sub> which is 4.57 gcm<sup>-3</sup>.

### **IV. DISCUSSION**

The smooth, continuous film with small and medium polycrystalline grains shown in Fig. 7 is of important requirement in producing VO<sub>2</sub> phase. During the annealing optimization, we found that when the annealing temperature is too high and/or too much oxygen, the grains coalesce, and the films are no longer continuous. It also coincides with the appearance of V<sub>2</sub>O<sub>5</sub> phase shown by Raman spectroscopy. On the other hand, when the temperature is too low, the film is continuous but without granular structures seen under SEM, and there is very weak or no modulation of MIT. The annealing conditions optimized for both quartz tube furnace and the Fiji ALD system are reproducible, and they consistently produce smooth VO<sub>2</sub> films with MIT modulation ~  $10^2$  (for t ~ 30 nm). As shown in Fig. 4, the typical resistivity at room temperature (below MIT) is ~ 1  $\Omega$ cm, and it reduces to ~ 0.01  $\Omega$ cm at higher temperature (above MIT).

For the annealing in quartz tube furnace, when the samples are slightly shifted towards higher temperatures from the optimized process window shown in Fig. 1(b), the annealed VO<sub>2</sub> films consist of round grains of various sizes (~ 50 to 250 nm in diameter) and very large, elongated grains (~ 400 to 1200 nm in length), as shown in Fig. 8(a). The MIT characteristics of such films with large grains (LG), along with the typical films with small grains (SG), are shown in Fig. 8(b). The resistivity below MIT is similar for both LG and SG films (~ 1  $\Omega$ cm). However, above MIT, the resistivity of LG film is much lower (~ 0.001  $\Omega$ cm) than that of SG film (~ 0.01  $\Omega$ cm). The LG film thus exhibits MIT modulation ~ 10<sup>3</sup> (for t ~ 30 nm). This is the highest that has been reported for the VO<sub>2</sub> films deposited on SiO<sub>2</sub> substrates, and on a par with films deposited on GaN substrates.<sup>22</sup>

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To access the stability of VO2 films, the resistivity modulation was measured again after 60 days in air. As shown in Fig. 8(b) the whole resistivity profile is shifted up, and the room temperature resistivity is now ~ 100  $\Omega$ cm. The resistivity of films with V<sub>2</sub>O<sub>5</sub> phase is generally higher than that of films with VO<sub>2</sub> phase, and given that V<sup>5+</sup> states are normally observed at the surface of VO<sub>2</sub> film, therefore, it seems that oxidation to higher valence state is happening in the LG film. However, it is still predominantly VO<sub>2</sub>, as evidenced by the MIT transition and the Raman shift spectra shown in Fig. 8(b) and (c).

On the other hand, the SG film shows stability even after 120 days in air. The difference of the air stability between the LG and SG films could be attributed to the morphology of these films. It clearly shows that a porous, rugged surface of the LG film is more susceptible to the ambient (moisture, oxidation, etc.) than a dense, continuous surface of the SG film, therefore an encapsulation layer would be required. Despite its smaller MIT modulation than LG films, the SG films are more attractive for incorporation in oscillator circuits due to their scalability and compatibility to CMOS platform.<sup>25, 26</sup>

### **V. CONCLUSIONS**

Optimizations of the ALD VO<sub>2</sub> thin films deposited from TEMAV and water precursors were carried out, using different annealing systems for substrates of various sizes up to 8-inch diameter wafer. The physical, optical and electrical characteristics of the films were reported. Moreover, the VO<sub>2</sub> thin films with very large, elongated grains showed the highest MIT modulation reported for films deposited on SiO<sub>2</sub> substrates. However, the films with smaller grains and modest MIT modulation have a smooth continuous surface, and they are promising for integration in devices and circuits. Moreover, these films are found to be very stable in air.



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<b>Table 1.</b> Comparison of Raman peaks of the polycrystalline VO2 and V2O5 films between	this
work and other works.	

VO2			V2O5		
This work		Pan [29]	Schilbe [30]	This work	Janicsko-
					Csathy [28]
Tube	Fiji				
furnace	system				
141	138		149	103	104
195	194	194	199	145	149
224	222	225	225	197	197
261	257	258	259	284	285
		265	265	304	305
309	304	308	313	404	405
339	338	339	339	481	482
388	391	392	392	527	528
399		395	395		628
439	438	444	444	651	652
		453	453	701	702
		489	489		804
497		503	503	995	995
583		585	595	1020	1020
616	616	618	618		
667		650	670		
824	823	825	830		

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**FIG. 1.** (a) Raman spectra of  $VO_2$  thin films annealed under various annealing conditions in a quartz tube furnace, and (b) the temperature distribution measured across furnace for different setpoints. The red squares are where the  $VO_2$  samples were placed during annealing.

**FIG. 2.** (a) The positions of the VO<sub>2</sub> samples placed across the 8-inch diameter substrate holder of a Fiji S200 ALD system for annealing at 450 °C for 2 hours in Ar/O<sub>2</sub> (100/100 sccm), and (b) Raman spectra of the VO<sub>2</sub> thin films located at various positions shown in (a). The peak intensity for the Raman shift at 616 cm<sup>-1</sup> are also shown in (a).

**FIG. 3.** Comparison of the XRD profiles of the  $VO_2$  thin films annealed in either quartz tube furnce or Fiji ALD chamber. Peaks marked with \* seem to be due to measurement artefact or substrate related.

**FIG. 4.** Resistivity,  $\rho$ , as a function of temperature and its derivative (d(log  $\rho$ )/dT) for the annealed VO<sub>2</sub> (t ~ 30 nm) thin films annealed in (a) quartz tube furnce and (b) Fiji ALD chamber. The transition temperatures during heating cycle (T<sub>h</sub>) and cooling cycle (T<sub>c</sub>) are also shown.

**FIG. 5.** (a) Transmittance (inset) and Tauc plot of absorption for a VO<sub>2</sub> film (t ~ 30 nm). The smaller gap (0.55 eV) can be assigned due to the *d*-*d* transitions, and the larger gap (1.3 eV) is due to O 2p to V 3d transitions. (b) Absorption coefficient as a function of energy for the same film.

**FIG. 6.** Thickness distribution for VO<sub>2</sub> films after (a) 450 ALD cycles, and (b) 890 ALD cycles. To achieve good uniformity, the wafers were rotated by  $180^{\circ}$  halfway through the deposition. The gas inlet and outlet of the cross flow reactor are shown in the map.

**FIG. 7.** AFM micrographs of the VO<sub>2</sub> film (t ~ 30 nm) (a) before and (b) after annealing, the rms of roughness are also shown. (c) SEM micrograph of the polycrystalline VO<sub>2</sub> film of same thickness. The scale bar in each image is 500 nm.

**FIG. 8.** (a) SEM micrograph of the VO<sub>2</sub> thin film (t ~ 30nm) with large grains (LG). The scale bar in SEM image is 500 nm. (b) Resistivity vs. temperature plots of the VO<sub>2</sub> films with LG and small grains (SG), measured as fresh sample and after 60 or 120 days. (c) Raman shift spectra of the LG film measured after 60 days.

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— 500 nm



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