

# Pushdown Multi-Agent System Verification

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## Abstract

In this paper we investigate the model-checking problem of pushdown multi-agent systems for  $ATL^*$  specifications. To this aim, we introduce *pushdown game structures* over which  $ATL^*$  formulas are interpreted. We show an algorithm that solves the addressed model-checking problem in  $3EXPTIME$ . We also provide a  $2EXPSPACE$  lower bound by showing a reduction from the word acceptance problem for deterministic Turing machines with doubly exponential space.

## 1 Introduction

*Model Checking* is a well-established method widely used to verify hardware and software systems [Clarke *et al.*, 2002]. The idea is simple and appealing: we use a mathematical model of the system we want to validate and check it over a formal specification of its desired behavior [Clarke and Emerson, 1981; Queille and Sifakis, 1981].

In the eighties, early use of model checking mainly considered *finite-state closed systems*, modeled as *Kripke structures*, and specifications given in terms of temporal-logic formulas [Pnueli, 1977]. The conceived algorithms, however, turn less appropriate in *open-system* verification as one has to take into account also the uncertainty about the agents’ behavior. As a first solution, *module checking* [Kupferman *et al.*, 2001] came out with its ability of handling the interaction between the system and an external unpredicted environment. Precisely it takes as inputs a graph partitioned in two sets (called a *module*)  $\mathcal{M}$  and a formula  $\varphi$ , and checks whether  $\mathcal{M}$  *reactively* satisfies  $\varphi$ , *i.e.*, no matter how the environment behaves.

Starting from the works on module checking, two significant directions have been taken in open-system verification. One concerns extending the framework to more sophisticated systems while maintaining the dichotomy system-environment states in modeling. In this context, worthy of mention is the work on *pushdown module checking* [Bozzelli *et al.*, 2010]. This has the merit of having handled the verification of infinite-state open systems and, thanks to the fact that the infinite

number of states is induced by a recursive structure of finite size, the problem turns out to be decidable and precisely  $3EXPTIME$ -COMPLETE for specifications in  $CTL^*$ . Another direction has instead completely redesigned the module checking approach in order to handle the more involved scenario of multi-agent (concurrent) systems. To let the temporal-logic framework working within this setting, *Alternating-Time Temporal Logic* ( $ATL^*$ , for short) [Alur *et al.*, 2002] has been introduced. This logic generalizes  $CTL^*$  by means of strategic quantifiers.  $ATL^*$  formulas are interpreted over *concurrent game structures* (CGS, for short). Given an  $ATL^*$  formula  $\langle\langle A \rangle\rangle\psi$ , with  $A$  set of agents, it is satisfied over a CGS  $\mathcal{G}$  if there exists a *strategy* for the agents in  $A$  such that, no matter which strategy is executed by agents not in  $A$ , the resulting outcome in  $\mathcal{G}$  satisfies  $\psi$ . As for finite-state  $CTL^*$  module checking, the model-checking problem for specifications in  $ATL^*$  turns out to be  $2EXPTIME$ -COMPLETE. However, the two approaches are incomparable as in module checking it is possible to use nondeterministic strategies.

Despite the undoubted utility of considering, from one hand, infinite-state open-system models induced by finite-size recursive structures and, from the other hand, multi-agent specifications, to the best of our knowledge no work has been devoted to the combination of the two.

In this paper, we consider multi-agent pushdown systems and address the related model checking problem for specifications expressed in  $ATL^*$ . To this aim, we first introduce *pushdown game structures* to properly model the infinite-state multi-agent system and formalize the model checking question. Then, by means of an automata-theoretic approach, we provide a  $3EXPTIME$  solution to the addressed problem. Precisely, we construct a doubly-exponential size *pushdown parity tree automaton* that collects all execution trees satisfying the  $ATL^*$  formula. Then by using the fact that the emptiness of this automaton can be checked in exponential time [Kupferman *et al.*, 2002], we get the desired result. We also provide a  $2EXPSPACE$  lower bound by showing a reduction from the word acceptance problem for a deterministic Turing machine with doubly exponential space.

**Related works.** In recent years, model checking of pushdown systems has received a lot of attention, largely due to the ability of these systems to capture the flow of procedure of calls and returns in programs [Alur *et al.*, 2005].

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The work in this area started with Muller and Schupp, who showed that the monadic second-order theory of graphs induced by pushdown systems is decidable [Muller and Schupp, 1985]. Walukiewicz in [Walukiewicz, 1996] showed that the model checking for pushdown systems with respect to *modal  $\mu$ -calculus* is EXPTIME-COMPLETE. The problem remains EXPTIME-COMPLETE also for CTL and LTL, while it becomes 2EXPTIME-COMPLETE for CTL\* [Walukiewicz, 2000; Bouajjani *et al.*, 1997]. In [Bozzelli *et al.*, 2010], open pushdown systems along with the module checking paradigm have been considered. This setting has been investigated under several restrictions including the imperfect-information case [Aminof *et al.*, 2013].

Literature on model checking of ATL\* is also wide. This problem has been investigated under different settings and inspired powerful formalisms for the strategic reasoning (see [Bulling, 2014], for a recent survey). Model checkers for ATL and ATL\* also exist, such as **MCMAS** [Lomuscio and Raimondi, 2006; Cermák *et al.*, 2014; 2015].

**Outline** The rest of the paper is organized as follows. In Section 2 we introduce PGSs and provide an example to help clarifying the setting. There, we also show how a PGS can be embedded into an infinite-state CGS. In Section 3 we recall the syntax and the semantics of ATL\* over CGSs and define the model-checking problem of ATL\* over PGSs. In Section 4 we show that the latter can be solved in 3EXPTIME by means of an automata-theoretic approach. There, we also show a 2EXPSpace-hard lower bound. Finally, in Section 5 we summarize the achieved results and discuss some future work.

## 2 Pushdown Game Structures

Classically, ATL\* formulas are interpreted over *Concurrent Game Structures* [Alur *et al.*, 2002]. In this paper, we instead interpret ATL\* formulas over a new semantic framework, which we call *Pushdown Game Structure*. Intuitively, this new formalism provides a concurrent game structure in which a stack is added and the labeling and transition functions depend on its content. In this section, we also show that every pushdown game structure can be transformed into a suitable concurrent game structure, so providing the required interpretation of ATL\* formulas over the former. However, note that the latter requires a infinite number of states, used to represent all the possible configurations the pushdown system can enter. We start with the definition of pushdown game structures.

**Definition 2.1 (Pushdown Game Structure)** A Pushdown Game Structure (PGS, for short) is a tuple  $\mathcal{P} = \langle \text{AP}, \text{Ag}, \text{Ac}, \text{Loc}, \Gamma, \text{tr}, \text{ap}, l_o \rangle$ , where AP, Ag, Ac, Loc and  $\Gamma$  are finite sets of atomic propositions, agents, actions, locations, and stack alphabet, respectively,  $l_o \in \text{Loc}$  is an initial location, and  $\text{ap} : \text{Loc} \times \Gamma_{\perp} \rightarrow 2^{\text{AP}}$  is a labeling function, where  $\Gamma_{\perp} = \Gamma \cup \{\perp\}$  and  $\perp$  is the special bottom stack symbol not contained in  $\Gamma$ . Let  $\text{Dc} \triangleq \text{Ac}^{\text{Ag}}$  be the set of decisions, i.e., functions from Ag to Ac representing the action choices for each agent. Then,  $\text{tr} : \text{Loc} \times \Gamma_{\perp} \times \text{Dc} \rightarrow \text{Loc} \times \Gamma_{\perp}^*$  is a

transition function mapping a location, a stack symbol, and a decision to a location and a word in the stack alphabet.

A pair  $s = (l, \alpha) \in \text{St} \triangleq \text{Loc} \times (\Gamma^* \cdot \{\perp\})$  is called *state* or *configuration*. We write  $\text{top}(\alpha)$  for the left most symbol of  $\alpha$  and call it the *top* of the stack content  $\alpha$ . The PGS moves according to the transition function. This means that, if it is in the location  $l$ , the top of the stack content is  $\gamma$ , and the agents make a decision  $d$ , then  $\text{tr}(l, \gamma, d) = (l', \alpha)$  means that the execution moves to the location  $l'$  and the symbol  $\gamma$  is replaced with  $\alpha$  on the top of the stack content. We assume that, if  $\perp$  is popped, then it is pushed right back, and that is the only case in which it is pushed. This means that  $\perp$  is always on the bottom of the stack and nowhere else. The stack containing only the symbol  $\perp$  is said to be *empty*.

As the stack has no a priori bound on its size, the set St is assumed to be possibly infinite. Saying this, it turns out that PGSs are *infinite-state multi-agent systems*.

The notion of labeling and transition can be lifted to states, as follows. For a state  $s = (l, \alpha)$ , we define  $\text{ap}(s) = \text{ap}(l, \text{top}(\alpha))$ . Moreover, for a decision  $d \in \text{Dc}$ , we define  $\text{tr}((l, \gamma \cdot \alpha), d) = (l', \beta \cdot \alpha)$ , with  $(l', \beta) = \text{tr}(l, \gamma, d)$ .

Note that for a classical *pop* we write the empty word  $\varepsilon$  on the stack. To make a classical *push* one has to first put back the read top symbol and then push the required word. The transition function also allows to perform in one step a *pop-push* operation that replaces the top stack symbol with the required word.

For our convenience, we consider also *two-player turn-based one-symbol stack games* of the form  $\mathcal{P} = \langle \text{AP}, \{E, A\}, \text{Loc}, \text{Loc}_E, \text{Loc}_A, R, \text{ap}, l_o \rangle$  where  $\text{Loc}_E$  and  $\text{Loc}_A$  are the sets of locations belonging to players E and A, respectively, and  $R \subseteq (\text{Loc} \times \{\gamma, \perp\}) \times (\text{Loc} \times \{\text{push}, \text{pop}\})$ , where  $\gamma$  is the only alphabet symbol of the stack, and push, pop, and  $\perp$  are the push, pop, and null operation on the stack. If  $(l, x, l', \text{op}) \in R$ , then, for each configuration  $(l, \alpha)$  with  $\text{top}(\alpha) = x$  we can move to the configuration  $(l', \alpha')$  with  $\alpha'$  being the string obtained from  $\alpha$  by applying the stack operation op. At each configuration  $(l, \alpha)$  of the game, the owner of the location  $l$  can pick a successor, according to the relation  $R$ . It is not hard to see that *two-player turn-based one-symbol stack games* are special cases of PGSs

To get familiar with PGSs, we give an example.

**Example 2.1 (Pushdown scheduler)** Take a system consisting of two processes **a** and **b** that may access to a common resource via the respective requests  $r_a$  and  $r_b$  and a scheduler **s** that can grant in a LIFO order the processes requests, all memorized into a stack. As model we use a PGS  $\mathcal{P} = \langle \text{AP}, \text{Ag}, \text{Ac}, \text{Loc}, \Gamma, \text{tr}, \text{ap}, l_o \rangle$ , with  $\text{AP} = \{r_a, r_b, g_a, g_b, a, b, e\}$ ,  $\text{Ag} = \{s, a, b\}$ ,  $\text{Ac} = \{a, b, \text{un}, 0, 1\}$ ,  $\text{Loc} = \{l_o, l_a, l_b, l_{\text{un}}\}$ , and  $\Gamma = \{r_a, r_b\}$ . The scheduler **s** controls the location  $l_o$  by means of the actions **a**, **b**, and **un**, standing for “**a** can make a request”, “**b** can make a request”, and “the system can unload the stack requests”, respectively. Accordingly, they lead to the locations  $l_a$ ,  $l_b$ , and  $l_{\text{un}}$ . On  $l_a$  and  $l_b$ , the agents **a** and **b**, respectively, can either make a request via action 1 or skip it with action 0. In the former case, the request is recorded into the stack by writing the symbol  $r_x$ , for  $x \in \{a, b\}$ ; otherwise, in the latter case there is no operation over the stack. Finally,

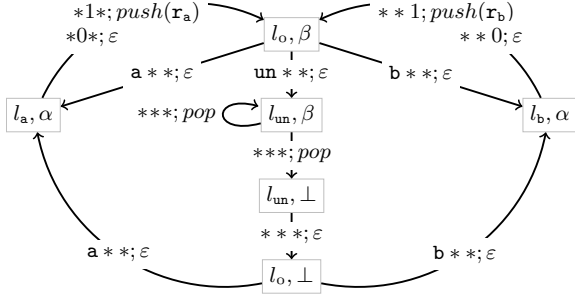


Figure 1: A Pushdown system scheduler.

the location  $l_{un}$  triggers the granting phase by emptying the stack. During this phase, neither  $a$  nor  $b$  can make any further request. This can be seen as a legitimate constraint by thinking how classical synchronizing and backup systems are designed.

The labeling function, for all  $\gamma \in \Gamma_{\perp}$  and  $x \in \{a, b\}$ , is defined as follows:  $ap(l_o, \perp) = \emptyset$ ,  $ap(l_o, r_x) = \{x\}$ ,  $ap(l_x, \gamma) = \{x\}$ ,  $ap(l_{un}, r_x) = \{g_x\}$ , and  $ap(l_{un}, \perp) = \{e\}$ . Intuitively, propositions  $a$  and  $b$  means that agents  $a$  and  $b$  are authorized to make a request, respectively. The proposition  $r_x$ , instead, occurs when the corresponding request has been just made by agent  $x$ . On the other hand, the proposition  $g_x$  occurs when the request  $r_x$  has been just granted. Finally,  $e$  indicates that the unloading phase is terminated and so the stack is empty.

The transition function  $tr$  is described directly in Figure 1. The labeling of the edges have the following meaning. First, note that it is composed of two parts separated by a semi-column. The left part represents the decision of the agents, given in the order  $s < a < b$ . The right part represents the stack operation. As an example, the label  $*1*; push(r_a)$  says that agent  $a$  is making a request  $r_a$  and the symbol  $r_a$  is pushed on the stack, where the symbol  $*$  denotes any possible action for the other agents.

The nodes represent all possible states. Note that, for the locations  $l_a$  and  $l_b$  we have collapsed the two possible configurations with  $\beta = \perp$  and  $\beta \neq \perp$  since the transition over them does not depend on the stack content.

Finally, observe that the stack is unbounded and so an execution might generate an infinite number of distinguished states. Also, observe that the stack is fundamental to keep track of the order in which the requests appear.

To correctly interpret ATL\* formulas over PGSs, we show that a PGS can be represented as an infinite-state concurrent game structure, whose definition follows. Note that we use the one reported in [Mogavero et al., 2014].

**Definition 2.2 (Concurrent Game Structures)** A concurrent game structure (CGS, for short) is a tuple  $\mathcal{G} \triangleq \langle AP, Ag, Ac, St, tr, ap, s_o \rangle$ , where  $AP$ ,  $Ag$ , and  $Ac$  are as in PGS.  $St$  is an enumerable non-empty set of states,  $s_o \in St$  is an initial state, and  $ap : St \rightarrow 2^{AP}$  is a labeling function mapping each state to a set of atomic propositions true in that state. Finally,  $tr : St \times Dc \rightarrow St$  is a transition function mapping pairs of states and decisions to states, where the set  $Dc$  is as in PGS.

Clearly, a PGS  $\mathcal{P} = \langle AP, Ag, Ac, Loc, \Gamma, tr, ap, l_o \rangle$  can be suitably turned into a CGS  $\mathcal{G}_{\mathcal{P}} = \langle AP, Ag, Ac, St, tr, ap, s_o \rangle$ , where  $St = Loc \times \Gamma_{\perp}^*$ ,  $s_o = (l_o, \perp)$ , and the functions  $ap$  and  $tr$  are the lifting on states of the corresponding functions in  $\mathcal{P}$ . Intuitively, the states of  $\mathcal{G}$  are used to implicitly represent both the current location and store the stack content. Despite this, it is important to observe that, while a PGS has a finite number of control locations, the corresponding CGS necessarily has an infinite number of control states, as the number of different stack contents is unbounded.

We conclude this section by briefly recalling the classical notions of *track*, *path*, *strategies* and *assignments*, which are required for the semantics of ATL\* (see [Mogavero et al., 2014], for more). Intuitively, tracks and paths are legal sequences of reachable states, respectively seen as partial and complete descriptions of possible outcomes over a CGS. Formally, a *track* (resp., *path*) in a CGS is a finite (resp., an infinite) sequence of states  $\rho \in St^*$  (resp.,  $\pi \in St^{\omega}$ ) such that, for all  $i \in [0, |\rho| - 1[$  (resp.,  $i \in \mathbb{N}$ ), there is a decision  $d \in Dc$  with  $(\rho)_{i+1} = tr((\rho)_i, d)$  (resp.,  $(\pi)_{i+1} = tr((\pi)_i, d)$ ). The set  $Trk \subseteq St^+$  (resp.,  $Pth \subseteq St^{\omega}$ ) contains all non-empty tracks (resp., paths). Moreover,  $Trk(s) \triangleq \{\rho \in Trk : (\rho)_0 = s\}$  (resp.,  $Pth(s) \triangleq \{\pi \in Pth : (\pi)_0 = s\}$ ) denotes the subsets of tracks (resp., paths) starting at a state  $s$ .

A *strategy* for an agent is a scheme containing all choices of actions, depending on the current outcome. Formally, a strategy in a CGS is a function  $f : Trk \rightarrow Ac$  that maps each non-empty track to an action. The set  $Str$  contains all strategies. For a given subset  $A \subseteq Ag$  of agents, an *assignment* over  $A$  is a partial function  $\chi_A : Ag \rightarrow Str$ , mapping each agent in  $A$  to a strategy. By  $Assg$  we denote the set of assignments. A path is *compatible* with an assignment  $\chi_A$  if it is obtained by agents in  $A$  using strategies in  $\chi_A$ . More formally, for a given set  $A \subseteq Ag$  and an assignment  $\chi_A$  over  $A$ , we say that a path  $\pi$  is compatible with  $\chi_A$  if, for all  $i \in \mathbb{N}$  it holds that  $(\pi)_{i+1} = tr((\pi)_i, d)$ , for some  $d \in Dc$  with  $d(a) \triangleq \chi_{Assg}(a)((\pi)_{\leq i})$ , for each  $a \in A$ . By  $play(\chi_A, s)$  we denote the set of paths starting from  $s$  that are compatible with  $\chi_A$ . Note that, for an assignment  $\chi_{Ag}$  over the full set  $Ag$  of agents, there exists only one compatible path. In this case, by abuse of notation, we denote it with  $play(\chi_{Ag}, s)$ .

### 3 ATL\*

In this section, we recall the syntax of ATL\* and introduce its semantics over PGS via its representation in terms of CGS (with infinite states). We start with the definition of ATL\* syntax.

**Definition 3.1 (ATL\* Syntax)** ATL\* formulas are built inductively from the set of atomic propositions  $AP$  and agents  $Ag$ , by using the following grammar, where  $p \in AP$  and  $A \subseteq Ag$ :

$$\varphi := p \mid \neg\varphi \mid \varphi \wedge \varphi \mid X\varphi \mid \varphi U\varphi \mid \langle\langle A \rangle\rangle\varphi.$$

As syntactic sugar we also use  $\varphi_1 \vee \varphi_2 \triangleq \neg(\neg\varphi_1 \wedge \neg\varphi_2)$ ,  $\varphi_1 \rightarrow \varphi_2 \triangleq \neg\varphi_1 \vee \varphi_2$ ,  $\varphi_1 \leftrightarrow \varphi_2 \triangleq (\varphi_1 \rightarrow \varphi_2) \wedge (\varphi_2 \rightarrow \varphi_1)$ ,  $\llbracket A \rrbracket\varphi \triangleq \neg\langle\langle A \rangle\rangle\neg\varphi$ ,  $F\varphi \triangleq tU\varphi$ , and  $G\varphi \triangleq \neg F\neg\varphi$ .

A *sentence* is a Boolean combination of ATL\* formulas of the form  $\langle\langle A \rangle\rangle\psi$ . Intuitively,  $\langle\langle A \rangle\rangle\psi$  means that each agent in

A has a strategy such that, whatever the other agents do, the resulting play satisfies  $\psi$ .

We now provide some examples of ATL<sup>\*</sup> formulas that will be useful in the sequel. Precisely, we consider  $\varphi_1 = \langle\langle\{s\}\rangle\rangle(\text{GFa} \wedge \text{GFb} \wedge \text{GFe})$ ,  $\varphi_2 = \langle\langle\emptyset\rangle\rangle(\text{GFa} \wedge \text{GFb} \wedge \text{GFe})$  and  $\varphi_3 = \langle\langle\emptyset\rangle\rangle((r_a \wedge X(\neg eUr_b)) \rightarrow (\text{Fg}_b \rightarrow X\text{Fg}_a))$  over the sets AP and Ag given in Example 2.1. The formula  $\varphi_1$  states that agent  $s$  has a way to let propositions  $a$ ,  $b$ , and  $e$  to occur infinitely often. The formula  $\varphi_2$  states that, no matters how the agents behave, propositions  $a$ ,  $b$ , and  $e$  occur infinitely often. Finally, the formula  $\varphi_3$  states that, whenever a request  $r_b$  occurs after an  $r_a$  one in the same loading phase, then, if  $r_b$  is eventually granted, then  $r_a$  is granted later on as well.

We now provide the semantics of ATL<sup>\*</sup>.

**Definition 3.2 (ATL<sup>\*</sup> Semantics)** For a CGS  $\mathcal{G} = \langle \text{AP}, \text{Ag}, \text{Ac}, \text{St}, \text{tr}, \text{ap}, s_o \rangle$  and a path  $\pi \in \text{Pth}$ , the modeling relation  $\mathcal{G}, \pi \models \varphi$  is inductively defined as follows:

- The atomic and boolean cases are defined as usual;
- $\mathcal{G}, \pi \models \langle\langle A \rangle\rangle \varphi$  if there is an assignment  $\chi_A$  such that  $\mathcal{G}, \pi' \models \varphi$ , for all  $\pi' \in \text{play}(\chi_A, (\pi)_0)$ ;
- $\mathcal{G}, \pi \models X\varphi$  if  $\mathcal{G}, (\pi)_{\geq 1} \models \varphi$ ;
- $\mathcal{G}, \pi \models \varphi_1 U \varphi_2$  if there exists  $j \in \mathbb{N}$  such that  $\mathcal{G}, (\pi)_{\geq i} \models \varphi_1$ , for all  $i < j$ , and  $\mathcal{G}, (\pi)_{\geq j} \models \varphi_2$ .

For a sentence  $\varphi$  and two paths  $\pi_1, \pi_2$  with  $(\pi_1)_0 = (\pi_2)_0$ , it holds that  $\mathcal{G}, \pi_1 \models \varphi$  iff  $\mathcal{G}, \pi_2 \models \varphi$ . Indeed, according to the semantics of the existential quantification  $\langle\langle A \rangle\rangle \psi$ , the only element of the path to take into account is the first one. For this reason, for a sentence  $\varphi$  we write  $\mathcal{G}, s \models \varphi$  if  $\mathcal{G}, \pi \models \varphi$  for some  $\pi \in \text{Pth}(s)$ . Finally, we say that  $\mathcal{G}$  satisfies  $\varphi$ , and write  $\mathcal{G} \models \varphi$ , if  $\mathcal{G}, s_o \models \varphi$ .

To get familiar with the semantics, consider the PGS  $\mathcal{P}$  given in Example 2.1 and the formulas  $\varphi_1, \varphi_2$ , and  $\varphi_3$  given above. It is easy to see that  $\mathcal{G}_{\mathcal{P}} \models \varphi_1$ . Indeed, the strategy  $f_1$  that allows the scheduler to pick infinitely often the actions  $a$ ,  $b$ , and  $un$ , makes all the generated paths to satisfy  $\text{GFa} \wedge \text{GFb} \wedge \text{GFe}$ . On the other hand, it is easy to see that  $\mathcal{G}_{\mathcal{P}} \not\models \varphi_2$ . Indeed, the strategy  $f_2$  for  $s$  such that  $f_2(\rho) = a$ , for all  $\rho \in \text{Trk}$ , never makes  $b$  and  $e$  to occur in the generated paths. Finally, we have that  $\mathcal{G}_{\mathcal{P}} \models \varphi_3$ .

**Definition 3.3 (Model-checking)** For a given PGS  $\mathcal{P}$  and an ATL<sup>\*</sup> formula  $\varphi$ , the model-checking problem is to decide whether  $\mathcal{G}_{\mathcal{P}} \models \varphi$ .

## 4 Model Checking

In this section, we provide a 3EXPTIME upper-bound and a 2EXPSpace lower-bound for the model-checking problem of ATL<sup>\*</sup> over PGS.

### 4.1 Upper-bound Complexity

For the upper bound we use an automata-theoretic approach. We start with some notation and the definition of nondeterministic pushdown automata. See [Kupferman *et al.*, 2002; 2000b] for more.

For a given set  $D \subseteq \mathbb{N}$ , a  $D$ -tree  $T$  is a prefix closed subset of  $D^*$ , *i.e.*, a set in which, if  $x \cdot d \in T$  then  $x \in T$ . The elements of  $T$  are called *nodes* and the empty word  $\varepsilon$  is called the *root*

of  $T$ . For  $x \in T$ , the set of *children* of  $x$  is  $\text{children}(T, x) \triangleq \{x \cdot i \in T : i \in D\}$ . For  $x, y \in T$ , we write  $x \prec y$  to mean that  $x$  is a proper prefix of  $y$ , *i.e.*, there exists  $z \in D^*$  such that  $x \cdot z = y$ . For  $x \in T$ , a path in  $T$  from  $x$  is a minimal set  $\pi \subseteq T$  such that  $x \in \pi$  and for each  $y \in \pi$  such that  $\text{children}(T, y) \neq \emptyset$ , there exist exactly one node in  $\text{children}(T, y)$  belonging to  $\pi$ . For an alphabet  $\Sigma$ , a  $\Sigma$ -labeled tree is a pair  $\mathcal{T} = \langle T, V \rangle$  where  $T$  is a tree and  $V : T \rightarrow \Sigma$  maps each node in  $T$  to an element in  $\Sigma$ . In the following, we mainly consider  $\Sigma$  to be the set power set  $2^{\text{AP}}$  of atomic propositions AP.

A *Nondeterministic Pushdown Tree Automata* (PD-NTA, for short), over  $\Sigma$ -labeled trees, is a tuple  $\mathcal{A} = \langle \Sigma, \Gamma, Q, q_o, \alpha_o, \delta, F \rangle$ , where  $\Sigma$  and  $\Gamma$  are *finite* input and stack alphabet sets,  $Q$  is a finite set of states,  $q_o$  is an initial state,  $\alpha_o \in \Gamma^* \cdot \perp$  is an initial stack content,  $\delta : Q \times \Sigma \times \Gamma_{\perp} \rightarrow 2^{2^{(Q \times \Gamma^*)}}$  is a transition function such that, for all  $(q, \sigma, \gamma) \in Q \times \Sigma \times \Gamma_{\perp}$ ,  $\delta(q, \sigma, \gamma)$  is a *finite* set, and  $F$  is an acceptance condition over  $Q$ .

When the automaton is in a state  $q$ , reading an input node  $x$  labeled with  $\sigma \in \Sigma$ , and the stack contains a word  $\gamma \cdot \alpha$  in  $\Gamma_{\perp}^*$ , it chooses, for some  $k \in \mathbb{N}$ , a tuple  $\langle (q_1, \beta_1), \dots, (q_k, \beta_k) \rangle \in \delta(q, \sigma, \gamma)$  and splits in  $k$  copies such that, for each  $1 \leq i \leq k$ , the  $i$ -th copy is in the state  $q_i$  and the stack content is updated by removing  $\gamma$  and pushing  $\beta_i$ . Then, it reads the node  $x \cdot i$  of the tree.

A run of a PD-NTA on a  $\Sigma$ -labeled tree  $\mathcal{T} = \langle T, V \rangle$  is a  $(Q \times \Gamma_{\perp}^*)$ -labeled tree  $\langle T, r \rangle$  such that  $r(\varepsilon) = (q_o, \alpha_o)$  and for each  $x \in T$  with  $r(x) = (q, \gamma \cdot \alpha)$ , there is a tuple  $\langle (q_1, \beta_1), \dots, (q_k, \beta_k) \rangle \in \delta(q, V(x), \gamma)$  for some  $k \in \mathbb{N}$ , such that, for all  $1 \leq i \leq k$ ,  $r(x \cdot i) = (q_i, \beta_i \cdot \alpha)$  if  $\gamma \neq \perp$ , and  $r(x \cdot i) = (q_i, \beta_i \cdot \perp)$ , otherwise.

Given a path  $\pi$  starting from  $\varepsilon$ , by  $\text{inf}_r(\pi)$  we denote the subset of states  $q$  such that there are infinitely many  $x \in \pi$  such that  $r(x) \in \{q\} \times \Gamma_{\perp}^*$ . A path satisfies a *parity* condition  $F = \{F_1, \dots, F_k\}$ , with  $F_i \subseteq F_{i+1}$ , for all  $i < k$ , and  $F_k = Q$ , if the minimum index  $1 \leq i \leq k$  such that  $\text{inf}_r(\pi) \cap F_i \neq \emptyset$  is even. A run  $\langle T, r \rangle$  is *accepting* if every path satisfies the acceptance condition. The PD-NTA  $\mathcal{A}$  accepts an input tree  $\langle T, V \rangle$  iff there is an accepting run of  $\mathcal{A}$  over it. The language of  $\mathcal{A}$ , denoted by  $\mathcal{L}(\mathcal{A})$ , contains all the trees accepted by  $\mathcal{A}$ . The emptiness problem for PD-NTA is to decide, for a given  $\mathcal{A}$ , whether  $\mathcal{L}(\mathcal{A}) = \emptyset$ . In [Kupferman *et al.*, 2002] it is reported the following.

**Theorem 4.1** *The emptiness problem for a parity PD-NTA is EXPTIME-COMplete.*

In several branching-time temporal-logic verification settings, the automata-theoretic approach has been fruitfully applied. Very close to our case are the procedures deployed for model checking pushdown systems over CTL<sup>\*</sup> specifications [Bouajjani *et al.*, 1997; Bozzelli *et al.*, 2010] and finite-state CGSs over ATL<sup>\*</sup> specifications [Alur *et al.*, 2002]. The former is a top-down procedure that first builds an automaton accepting all the trees that satisfy the formula and then checks for the membership problem of the tree unwinding of the pushdown model. Precisely, to get a tight complexity, it starts

with a single-exponential alternating<sup>1</sup> parity tree automaton and the membership problem results in a special alternating pushdown tree automaton named *one-letter*, with no blow-up in size, whose emptiness can be checked in exponential-time<sup>2</sup> resulting in an overall doubly-exponential time solution. The procedure for  $\text{ATL}^*$ , instead, uses a doubly-exponential bottom-up approach based on the idea of labeling each state of the structure with subformulas true in that state. In our setting we can neither proceed with the membership problem nor use a bottom-up procedure. Indeed, because of  $\text{ATL}^*$ , we need to consider not just the unwinding of the model but the tree execution induced by the player existentially quantified in the formula. Moreover, because of the possible infinite number of configurations induced by the PGS, a bottom-up procedure could never terminate. For this reason, we use a top-down approach that constructs a doubly exponential PD-NTA that simultaneously checks whether a tree is an execution of the structure and a model of the formula. As far as we know, this is the first top-down automata-theoretic approach exploited for  $\text{ATL}^*$ . Some details about this automata construction are reported in the following.

**Theorem 4.2** *The model-checking problem for  $\text{ATL}^*$  on PGS can be solved in 3EXPTIME.*

**Proof sketch:** We give an intuition behind the automata construction by providing some details on how to extend the one introduced in [Bouajjani *et al.*, 1997] used to solve the model-checking problem for branching-time specifications over pushdown systems. The mentioned approach starts with a tree automaton accepting all tree models of a formula  $\varphi$ , namely the formula automaton  $\mathcal{A}_\varphi$ , over which one can build a PD-NTA  $\mathcal{A}_{\mathcal{P},\varphi}$  accepting the unwinding of the pushdown structure  $\mathcal{P}$  iff it is contained in the language of the formula automaton  $\mathcal{A}_\varphi$ . To handle  $\text{ATL}^*$ , one can start with a doubly-exponential parity tree automaton as an adaptation of the one provided in [Schewe, 2008]<sup>3</sup>. Moreover, in order to correctly evaluate the formula over a PGS we need not just to consider the unwinding of the structure but rather the execution trees induced by the formula and precisely from the players existentially quantified in it. This results in selecting at each node subsets of children upon the choices of the players. As the number of these subsets is linear in the number of the decisions of the structure, the overall size of the PD-NTA we construct remains doubly-exponential. Thus, from Theorem 4.1 we derive a 3EXPTIME procedure.  $\square$

## 4.2 A Lower-bound Complexity

In this section, we show that the model-checking problem for  $\text{ATL}^*$  over PGSs is 2EXPSpace-HARD by means of a reduction from the word acceptance problem for a deterministic Turing machine with doubly exponential space. Such

<sup>1</sup>Automata having as transition relation a positive Boolean combination of states and directions [Kupferman *et al.*, 2000b].

<sup>2</sup>Recall that in general the emptiness check for alternating pushdown automata is undecidable [Kupferman *et al.*, 2002].

<sup>3</sup>In [Schewe, 2008] it is given a single-exponential alternating automaton that can be easily translated into a non-deterministic one with a single exponential-time blowup.

reduction is inspired by the one provided in [Vester, 2014] for one-counter games.

Let  $\mathcal{T} = \langle \mathbb{Q}, q_0, \Sigma, \delta, q_F \rangle$  be a Turing machine that uses at most  $2^{2^n}$  cells on an input  $w$  of length  $n$  where,  $\mathbb{Q}$  is the set of control states,  $q_0$  and  $q_F$  are the initial and final states, respectively,  $\Sigma = \{0, 1, a, r, \#\}$  is the finite alphabet set, and  $\delta : \mathbb{Q} \times \Sigma \rightarrow \mathbb{Q} \times \Sigma \times \{-1, 0, +1\}$  is the (deterministic) transition function. For our convenience, if  $\delta(q, a) = (q', a', x)$  we write  $\delta_1(q, a) = q'$ ,  $\delta_2(q, a) = a'$ , and  $\delta_3(q, a) = x$ , respectively. The input set of  $\mathcal{T}$  is given by  $\Sigma_1 = \Sigma \setminus \{\#\}$ . From this, we can construct a PGS  $\mathcal{P}_{\mathcal{T},w}$  and an  $\text{ATL}^*$  formula  $\varphi$  such that  $\mathcal{T}$  accepts  $w$  iff  $\mathcal{P}_{\mathcal{T},w} \models \varphi$ . To do this, we need some auxiliary notation. First, *w.l.o.g.*, we can assume that  $\mathcal{T}$  always accepts when the symbol  $a$  is read, and always reject when the symbol  $r$  is read. Moreover, we can assume that  $\mathcal{T}$  always halts in the position 1 of the tape and that there are two additional cells at the ends, numbered with 0 and  $2^{2^n} + 1$  containing the symbol  $a$ . Let  $\Delta = \Sigma \cup (\mathbb{Q} \times \Sigma)$ . Then a configuration is a sequence in  $\Delta^{2^{2^n}+2}$  containing exactly one element in  $\mathbb{Q} \times \Sigma$ . Since  $\mathcal{T}$  is deterministic, then there is a unique run  $C_0^w \cdot C_1^w \cdot \dots$  of computations starting from  $C_0^w = a \cdot (q_0, w_0) \cdot \dots \cdot w_n \cdot \# \cdot \dots \cdot \# \cdot a$ .  $C_i^w(j)$  denotes the  $j$ -th symbol of the  $i$ -th configuration in the computation. Observe that, given the three elements  $C_i^w(j-1)$ ,  $C_i^w(j)$ , and  $C_i^w(j+1)$ , then the symbol  $C_{i+1}^w(j)$  is uniquely determined, according to the definition of transition function. Then, for  $d \in \Delta$ , by  $\text{Pre}(d)$  we denote the set of triples  $(d_1, d_2, d_3)$  such that  $d_1 = C_i^w(j-1)$ ,  $d_2 = C_i^w(j)$ ,  $d_3 = C_i^w(j+1)$ , and  $d = C_{i+1}^w(j)$ .

At this point, we consider the auxiliary *two-player turn-based one-symbol stack game*  $\mathcal{R}_{\mathcal{T},w} = \langle \text{AP}, \{\text{E}, \text{A}\}, \text{Loc}, \text{Loc}_E, \text{Loc}_A, R, \text{ap}, l_o \rangle$  where:

- $\text{Loc} = ([0, 2^{2^n} + 1] \times (\Delta \cup \Delta^3)) \cup \{l_o, l_z, l_r, l_F\}$ ;
- $\text{Loc}_E = ([0, 2^{2^n} + 1] \times \Delta) \cup \{l_o\}$ ;
- $\text{Loc}_A = ([0, 2^{2^n} + 1] \times \Delta^3) \cup \{l_z, l_r, l_F\}$ ;
- $R$  is the smallest relation such that:
  - $(l_o, x, l_o, \text{push})$ , for  $x \in \{\perp, \gamma\}$ ;
  - $(l_o, x, (1, (q_F, a)), \text{null})$  for  $x \in \{\perp, \gamma\}$ ;
  - $((j, d), \gamma, (j, (d_1, d_2, d_3)), \text{null})$  for all  $(d_1, d_2, d_3) \in \text{Pre}(d)$  and  $j \in [1, 2^{2^n}]$ ;
  - $((j, a), x, l_F, \text{null})$  for all  $j \in [1, 2^{2^n}]$ ;
  - $((j, d), x, l_r, \text{null})$  if  $j = 0$  or  $j = 2^{2^n} + 1$ ;
  - $((j, d), x, l_z, \text{null})$  if  $C_0^w(j) = d$ ;
  - $(l_z, \gamma, l_F, \text{null})$ ;
  - $(l_z, \gamma, l_r, \text{pop})$ ;
  - $((j, (d_1, d_2, d_3)), \gamma, (j-1, d_1), \text{pop})$ , for all  $j \in [0, 2^{2^n}]$  and  $d_1, d_2, d_3 \in \Delta$ ;
  - $((j, (d_1, d_2, d_3)), \gamma, (j, d_2), \text{pop})$ , for all  $j \in [0, 2^{2^n}]$  and  $d_1, d_2, d_3 \in \Delta$ ;
  - $((j, (d_1, d_2, d_3)), \gamma, (j+1, d_3), \text{pop})$ , for all  $j \in [0, 2^{2^n}]$  and  $d_1, d_2, d_3 \in \Delta$ .

Intuitively, player E pushes the symbol  $\gamma$  into the stack a number of times that corresponds to the length of the computation accepting  $w$ . After this, the game starts from the

configuration  $(1, (q_F, a), \gamma)$  and proceeds back-way along the computation of  $\mathcal{T}$  over  $w$ . In the configurations with locations of the form  $(j, d)$ , player E selects a possible predecessor triple  $(d_1, d_2, d_3)$  of  $d$ . At this point, player A selects one of the elements in  $(d_1, d_2, d_3)$ , while a pop operation is performed on the stack. Finally, if the stack is empty and the location  $(j, d)$  is such that  $C_0^w(j) = d$ , then player E can move to configuration  $(l_z, \perp)$ , from which player A is forced to move in location  $l_F$ , since the transition  $(l_z, \gamma, l_r, \text{pop})$  on empty stacks is deactivated. It is not hard to see that  $w$  is accepted by  $\mathcal{T}$  iff player E can force the game  $\mathcal{R}_{\mathcal{T}, w}$  to reach  $l_F$ .

This reasoning allows us to reduce the accepting problem to a reachability game played on  $\mathcal{R}_{\mathcal{T}, w}$ , which is of size doubly exponential *w.r.t.* to  $\mathcal{T}$ , that can be specified by the ATL\* formula  $\langle\langle E \rangle\rangle \text{Fp}$ , where p is the proposition labeling all the configurations having  $l_F$  as location. Now, having  $\mathcal{R}_{\mathcal{T}, w}$  in mind, we can build a PGS  $\mathcal{P}_{\mathcal{T}, w}$  and an ATL\* formula  $\varphi$  such that  $\mathcal{P}_{\mathcal{T}, w} \models \varphi$  iff  $\mathcal{T}$  accepts  $w$ .

The construction of  $\mathcal{P}_{\mathcal{T}, w}$  is essentially a modification of  $\mathcal{R}_{\mathcal{T}, w}$  in which the position of the head on the tape is encoded by a suitable LTL formula  $\psi$ , rather than the set of states. This allows such a model to have polynomial size *w.r.t.*  $\mathcal{T}$  and  $w$ . The way the formula  $\psi$  works is folklore and completely described in [Bozzelli *et al.*, 2005; Kupferman *et al.*, 2000a]. We omit it here due to the lack of space.

Finally, we can prove that  $\mathcal{P}_{\mathcal{T}, w} \models \langle\langle E \rangle\rangle (\psi \wedge \text{Fp})$  iff  $\mathcal{T}$  accepts  $w$ , from which we derive the following theorem.

**Theorem 4.3** *The model-checking problem for ATL\* over PGS is in 2EXPSpace-HARD.*

## 5 Conclusion

In the last years, *open* pushdown models have received a lot of attention from the formal verification community, largely due to their ability to capture the control-flow of procedure calls and returns in reactive systems [Alur *et al.*, 2005]. In several settings, the use of pushdown models allows to verify the correctness of infinite-state systems with a decidable complexity [Piterman and Vardi, 2004; Kupferman *et al.*, 2002; Song and Touili, 2014]. As far as we know, all the work so far has concentrated on models with at most two-agents and with respect to specifications given in terms of classic temporal logics [Abdulla *et al.*, ; Chatterjee and Velner, 2012; Bozzelli *et al.*, 2010].

In this paper, we have introduced *multi-agent pushdown game structures* to model more involved infinite-state scenarios (as induced by a recursive structure) in which several agents can cooperate or act in an adversarial way in order to achieve a certain goal. As main contribution related to these structures we have introduced and studied the model checking problem with respect to the logic ATL\* and showed that this problem can be solved in 3EXPTIME. We recall that the same complexity holds also for pushdown module checking with respect to specifications given in CTL\*. The latter is a special two-player setting, where one of the player, the environment, can also use nondeterministic strategies. We also provide a non tight 2EXPSpace lower bound. Our conjecture is that the investigated problem is 3EXPTIME-COMplete. We leave this as future work.

On some extent, the high complexity of the addressed problem relies on the fact that the rich formalisms of pushdown models and ATL\* specification we combine are complex by themselves. While this allowed us to provide a result for a very general framework, the overall complexity can be easily reduced by considering opportune restrictions on both sides. Indeed, regarding the specification, by using ATL, the procedure easily reduces to 2EXPTIME. This is due the fact that it suffices to build a Büchi PD-NTA of single exponential size [Alur *et al.*, 2002]. Further, one can restrict to pushdown models with bounded-stack. In several settings, it has been shown that under such a restriction the problem has the same or slightly higher complexity than the corresponding one for finite-state systems [Alur and Yannakakis, 2001; Aminof *et al.*, 2012]. By employing techniques similar to the ones reported in [Alur and Yannakakis, 2001], we are confident that the model-checking problem of ATL specifications over bounded-stack PGS is PTIME-COMplete as it is for the case for CGS. If so, one can think of implementing an efficient model checker, as it has been done with MCMAS [Lomuscio and Raimondi, 2006; Cermák *et al.*, 2014]. This will be addressed as future work.

Another interesting setting to investigate is that of *imperfect information* under memoryless strategies. We recall that this setting is decidable in the finite-state case [Alur *et al.*, 2002]. However, moving to pushdown systems one has to distinguish whether the missing information relies in the locations, in the pushdown store, or both. We recall that in pushdown module checking only the former case is decidable for specification given in CTL and CTL\* [Aminof *et al.*, 2007; 2013].

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