Rateless Coding for Gaussian Channels

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Abstract—A rateless code—i.e., a rate-compatible family of codes—has the property that codewords of the higher rate codes are prefixes of those of the lower rate ones. A perfect family of such codes is one in which each of the codes in the family is capacity-achieving. We show by construction that perfect rateless codes with low-complexity decoding algorithms exist for additive white Gaussian noise channels. Our construction involves the use of layered encoding and successive decoding, together with a repetition and dithering technique. As an illustration of our framework, we design a practical three-rate code family. We further construct rich sets of near-perfect rateless codes within our architecture that require either significantly fewer layers or lower complexity than their perfect counterparts. Variations of the basic construction are also discussed.

Index Terms—Incremental redundancy, rate-compatible punctured codes, hybrid ARQ (H-ARQ), static broadcasting.

I. INTRODUCTION

THE design of effective "rateless" codes has received renewed strong interest in the coding community, motivated by a number of emerging applications. Such codes have a long history, and have gone by various names over time, among them incremental redundancy codes, rate-compatible punctured codes, hybrid ARQ type II codes, and static broadcast codes [3], [4], [9]–[12], [14], [18], [19], [24]. This paper focuses on the design of such codes for average power limited additive white Gaussian noise (AWGN) channels. Specifically, we develop techniques for mapping standard good single-rate codes for the AWGN channel into good rateless codes.

From a purely information theoretic perspective the problem of rateless transmission is well understood; see Shulman [23] for a comprehensive treatment. Indeed, for channels having one maximizing input distribution, a codebook drawn independently and identically distributed (i.i.d.) at random from this distribution will be good with high probability, when truncated to (a finite number of) different lengths. Phrased differently, in such cases random codes are rateless codes.

Constructing good codes that also have computationally efficient encoders and decoders requires more effort. A remarkable example of such codes for *erasure* channels are the recent Raptor codes of Shokrollahi [22], which build on the

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LT codes of Luby [2], [13]. An erasure channel model (for packets) is most appropriate for rateless coding architectures anchored at the application layer, where there is little or no access to the physical layer.

Apart from erasure channels, there is a growing interest in exploiting rateless codes closer to the physical layer, where AWGN models are more natural; see, e.g., [25] and the references therein. Surprisingly little is known about what is possible in this realm. Recent work [8], [17] applies Raptor codes to binary-input AWGN channels (among others), where it is shown that no degree distribution allows Raptor codes to approach capacity simultaneously at different signal to noise ratios (SNRs). Another line of work is based on puncturing of low-rate capacity-approaching codes such as turbo and LDPC codes [1], [9], [15], [18], [19], [25]. When iterative decoding is used, however, a balance must be struck between the performance at different rates. That is, improving performance at one rate comes at the expense of the performance at other rates. Beyond this issue, binary codes themselves may be "nearly" capacity achieving only at low SNR.

In this paper, motivated by a host of emerging wireless applications, we work at the physical layer with an associated AWGN channel model, rather than with an erasure model. And as such, our focus is on that part of the network where traditional hybrid ARQ research has been aimed. The rateless codes that result are efficient, practical, and can operate at rates of multiple b/s/Hz.

We show that the successful techniques employed to construct low-complexity codes for the standard AWGN channel—such as those arising out of turbo and low-density parity check (LDPC) codes—can be leveraged to construct rateless codes. Specifically, we develop an architecture in which a single codebook designed to operate at a single SNR is used in a straightforward manner to build a rateless codebook that operates at many SNRs.

The encoding in our architecture exploits three key ingredients: layering, dithering, and repetition. By layering, we mean the creation of a code by a linear combination of subcodes. By dithering we mean the use of multiplicative pre- and post-processing by known sequences. Finally, by repetition, we mean the use of simple linear redundancy in which each copy has a different complex gain. We show that with the appropriate combination of these ingredients, if the base codes are capacity-achieving, so will be the resulting rateless code.

In addition to achieving capacity in our architecture, we seek to ensure that if the base code can be decoded with low complexity, so can the rateless code. As we will see, this is accomplished by imposing the constraint that the layered encoding be successively decodable—i.e., that the layers can be decoded one at a time, treating as yet undecoded layers as noise.

Hence, our main result is the construction of capacity-achieving, low-complexity rateless codes, i.e., rateless codes constructed from layering, dithering, and repetition, that are successively decodable.

The paper is organized as follows. In Section II we introduce the channel and system model. In Section III we motivate and illustrate our construction with a simple special-case example. In Section IV we develop our general construction and show that within it exist perfect rateless codes for at least some ranges of interest, and in Section V we develop and analyze specific instances of our codes generated numerically. In Section VI, we show that within the constraints of our construction rateless codes for any target ceiling and range can be constructed that are arbitrarily close to perfect in an appropriate sense. In Section VII we describe some potentially useful variations on our basic construction, and their key properties. Finally, Section VIII contains some concluding remarks.

II. CHANNEL AND SYSTEM MODEL

The codes we construct are designed for a complex AWGN channel

$$\mathbf{y}_m = \alpha \mathbf{x}_m + \mathbf{z}_m, \quad m = 1, 2, \dots, \tag{1}$$

where α is a channel gain, \mathbf{x}_m is a vector of of N input symbols, \mathbf{y}_m is the vector of channel output symbols, and \mathbf{z}_m is a noise vector of N i.i.d. complex, circularly-symmetric Gaussian random variables of variance σ^2 , independent across blocks $m=1,2,\ldots$ The channel input is limited to average power P per symbol. In our model, the channel gain α and noise variance σ^2 are known a priori at the receiver but not at the transmitter.

The block length N has no important role in the analysis that follows. It is, however, the block length of the base code used in the rateless construction. As the base code performance controls the overall code performance, to approach channel capacity N must be large.

The encoder transmits a message w by generating a sequence of code blocks (incremental redundancy blocks) $\mathbf{x}_1(w)$, $\mathbf{x}_2(w)$, The receiver accumulates sufficiently many received blocks \mathbf{y}_1 , \mathbf{y}_2 , ... to recover w. The channel gain α may be viewed as a variable parameter in the model; more incremental redundancy is needed to recover w when α is small than when α is large.

An important feature of this model is that the receiver always starts receiving blocks from index m=1. It does not receive an arbitrary subsequence of blocks, as might be the case if one were modeling a broadcast channel that permits "tuning in" to an ongoing transmission; discussion of such a scenario is deferred to Section VII.

We now define some basic terminology and notation. Unless noted otherwise, all logarithms base 2, all symbols denote complex quantities, and all rates are in bits per complex symbol (channel use), i.e., b/s/Hz. We use · ^T for transpose

and \cdot^{\dagger} for Hermitian (conjugate transpose) operators. Vectors and matrices are denoted using bold face, random variables are denoted using sans-serif fonts, while sample values use regular (serif) fonts.

We define the *ceiling rate* of the rateless code as the highest rate R at which the code can operate, i.e., the effective rate if the message is decoded from the single received block \mathbf{y}_1 ; hence, a message consists of NR information bits. Associated with this rate is an SNR *threshold*, which is the minimum SNR required in the realized channel for decoding to be possible from this single block. This SNR threshold can equivalently be expressed in the form of a channel gain threshold. Similarly, if the message is decoded from $m \geq 2$ received blocks, the corresponding effective code rate is R/m, and there is a corresponding SNR (and channel gain) threshold. Thus, for a rateless encoding consisting of M blocks, there is a sequence of M associated SNR thresholds.

Finally, as in the introduction, we refer to the code out of which our rateless construction is built as the *base code*, and the associated rate of this code as simply the *base code rate*. At points in our analysis we will assume that a good base code is used in the code design, i.e., that the base code is capacity-achieving for the AWGN channel, and thus has the associated properties of such codes. This will allow us to distinguish losses due to the code architecture from those due to the choice of base code.

III. MOTIVATING EXAMPLE

To develop initial insights, we construct a simple low-complexity perfect rateless code that employs two layers of coding to support a total of two redundancy blocks.

We begin by noting that for the case of a rateless code with two redundancy blocks the channel gain $|\alpha|$ may be divided into three intervals based on the number of blocks needed for decoding. Let α_1 and α_2 denote the two associated channel gain thresholds. When $|\alpha| \geq |\alpha_1|$ decoding requires only one block. When $|\alpha_1| > |\alpha| \geq |\alpha_2|$ decoding requires two blocks. When $|\alpha_2| > |\alpha|$ decoding is not possible. The interesting cases occur when the gain is as small as possible to permit decoding. At these threshold values, for one-block decoding the decoder sees

$$\mathbf{y}_1 = \alpha_1 \mathbf{x}_1 + \mathbf{z}_1,\tag{2}$$

while for two-block decoding the decoder sees

$$\mathbf{y}_1 = \alpha_2 \mathbf{x}_1 + \mathbf{z}_1,\tag{3}$$

$$\mathbf{y}_2 = \alpha_2 \mathbf{x}_2 + \mathbf{z}_2. \tag{4}$$

In general, given any particular choice of the ceiling rate R for the code, we would like the resulting SNR thresholds to be a low as possible. To determine lower bounds on these thresholds, let

$$SNR_m = P|\alpha_m|^2/\sigma^2, (5)$$

and note that the capacity of the one-block channel is

$$I_1 = \log(1 + \mathrm{SNR}_1),\tag{6}$$

while for the two-block channel the capacity is

$$I_2 = 2\log(1 + \text{SNR}_2) \tag{7}$$

¹See Section VIII for a discussion regarding more general models for α .

²An equivalent model would be a broadcast channel in which a single encoding of a common message is being sent to a multiplicity of receivers, each experiencing a different SNR.

bits per channel use. A "channel use" in the second case consists of a pair of transmitted symbols, one from each block.

In turn, since we deliver the same message to the receiver for both the one- and two-block cases, the smallest values of $|\alpha_1|$ and $|\alpha_2|$ we can hope to achieve occur when

$$I_1 = I_2 = R.$$
 (8)

Thus, we say that the code is *perfect* if it is decodable at these limits.

We next impose that the construction be a *layered* code, and that the layers be *successively decodable*.

Our layering constraint means that we require the transmitted blocks to be linear combinations of two base codewords $\mathbf{c}_1 \in \mathcal{C}_1$ and $\mathbf{c}_2 \in \mathcal{C}_2^3$:

$$\mathbf{x}_1 = g_{11}\mathbf{c}_1 + g_{12}\mathbf{c}_2, \tag{9}$$

$$\mathbf{x}_2 = g_{21}\mathbf{c}_1 + g_{22}\mathbf{c}_2. \tag{10}$$

Base codebook \mathcal{C}_1 has rate R_1 and base codebook \mathcal{C}_2 has rate R_2 , where $R_1 + R_2 = R$, so that total rate of the two codebooks equals the ceiling rate. We assume for this example that both codebooks are capacity-achieving, so that the codeword components are i.i.d. Gaussian. Furthermore, for convenience, we scale the codebooks to have unit power, so the power constraint instead enters through the constraints

$$|g_{11}|^2 + |g_{12}|^2 = P, (11)$$

$$|g_{21}|^2 + |g_{22}|^2 = P. (12)$$

Finally, the successive decoding constraint in our system means that the layers are decoded one at a time to keep complexity low (on order of the base code complexity). Specifically, the decoder first recovers \mathbf{c}_2 while treating \mathbf{c}_1 as additive Gaussian noise, then recovers \mathbf{c}_1 using \mathbf{c}_2 as side information.

We now show that perfect rateless codes are possible within these constraints by constructing a matrix $G = [g_{ml}]$ so that the resulting code satisfies (8). Finding admissible G is simply a matter of some algebra: in the one-block case we need

$$R_1 = I_{\alpha_1}(c_1; y_1 | c_2) \tag{13}$$

$$R_2 = I_{\alpha_1}(c_2; y_1),$$
 (14)

and in the two-block case we need

$$R_1 = I_{\alpha_2}(c_1; y_1, y_2 | c_2) \tag{15}$$

$$R_2 = I_{\alpha_2}(c_2; y_1, y_2). \tag{16}$$

The subscripts α_1 and α_2 are a reminder that these mutual information expressions depend on the channel gain, and the scalar variables denote individual components from the input and output vectors.

While evaluating (13)–(15) is straightforward, calculating the more complicated (16), which corresponds to decoding \mathbf{c}_2 in the two-block case, can be circumvented by a little additional insight. In particular, while \mathbf{c}_1 causes the effective noise in the two blocks to be correlated, observe that

a capacity-achieving code requires x_1 and x_2 to be i.i.d. Gaussian. As c_1 and c_2 are Gaussian, independent, and equal in power by assumption, this occurs only if the rows of G are orthogonal. Moreover, the power constraint P ensures that these orthogonal rows have the same norm, which implies that G is a scaled unitary matrix.

The unitary constraint has an immediate important consequence: the per-layer rates R_1 and R_2 must be equal:

$$R_1 = R_2 = R/2. (17)$$

This occurs because the two-block case decomposes into two parallel orthogonal channels of equal SNR. We will see in the next section that a comparable result holds for any number of layers.

From the definitions of SNR₁ and I_1 [cf. (5) and (6)], and the equality $I_1 = R$ (8), we find that

$$P|\alpha_1|^2/\sigma^2 = 2^R - 1. (18)$$

Also, from (13) and (17), we find that

$$|g_{11}|^2 |\alpha_1|^2 / \sigma^2 = 2^{R/2} - 1.$$
 (19)

Combining (18) and (19) yields

$$|g_{11}|^2 = P \frac{2^{R/2} - 1}{2^R - 1} = \frac{P}{2^{R/2} + 1}.$$
 (20)

The constraint that G be a scaled unitary matrix, together with the power constraint P, implies

$$|g_{12}|^2 = P - |g_{11}|^2 (21)$$

$$|q_{21}|^2 = P - |q_{11}|^2 (22)$$

$$|g_{22}|^2 = |g_{11}|^2, (23)$$

which completely determines the squared modulus of the entries of G.

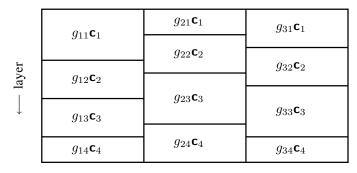
Now, the mutual information expressions (13)–(16) are unaffected by applying a common complex phase shift to any row or column of G, so without loss of generality we take the first row and first column of G to be real and positive. For G to be a scaled unitary matrix, g_{22} must then be real and negative. We have thus shown that, if a solution to (13)–(16) exists, it must have the form

$$\mathbf{G} = \begin{bmatrix} g_{11} & g_{12} \\ g_{21} & g_{22} \end{bmatrix} = \sqrt{\frac{P}{2^{R/2} + 1}} \begin{bmatrix} 1 & 2^{R/4} \\ 2^{R/4} & -1 \end{bmatrix}. \tag{24}$$

Conversely, it is straightforward to verify that (13)–(16) are satisfied with this selection. Thus (24) characterizes the (essentially) unique solution G.

In summary, we have constructed a 2-layer, 2-block perfect rateless code from linear combinations of codewords drawn from equal-rate codebooks. Moreover, decoding can proceed one layer at a time with no loss in performance, provided the decoder is cognizant of the correlated noise caused by undecoded layers. In the sequel we consider the generalization of our construction to an arbitrary number of layers and redundancy blocks.

 $^{^3}$ In practice, the codebooks \mathcal{C}_1 and \mathcal{C}_2 should not be identical, though they can for example be derived from a common base codebook via scrambling. This point is discussed further in Section VII-B.



time ---

Fig. 1. A rateless code construction with 4 layers and 3 blocks of redundancy. Each block is a weighted linear combination of the (N-element) base codewords $\mathbf{c}_1, \mathbf{c}_2, \ldots, \mathbf{c}_4$, where g_{ml} , the (m, l)th element of \mathbf{G} , denotes the weight for layer l of block m.

IV. RATELESS CODES WITH LAYERED ENCODING AND SUCCESSIVE DECODING

The rateless code construction we pursue is as follows [7]. First, we choose the range (maximum number M of redundancy blocks), the ceiling rate R, the number of layers L, and finally the associated codebooks $\mathcal{C}_1,\ldots,\mathcal{C}_L$. We assume a priori that the L base codebooks all have equal rate R/L; this assumption turns out to be necessary when constructing perfect rateless codes with M=L, and in any case has the advantage of allowing the codesbooks for each layer to be derived from a single base code.

Given codewords $\mathbf{c}_l \in \mathcal{C}_l$, l = 1, ..., L, the redundancy blocks $\mathbf{x}_1, ..., \mathbf{x}_M$ take the form

$$\begin{bmatrix} \mathbf{x}_1 \\ \vdots \\ \mathbf{x}_M \end{bmatrix} = \mathbf{G} \begin{bmatrix} \mathbf{c}_1 \\ \vdots \\ \mathbf{c}_L \end{bmatrix}, \tag{25}$$

where \mathbf{G} is an $M \times L$ matrix of complex gains and where \mathbf{x}_m for each m and \mathbf{c}_l for each l are row vectors of length N. The power constraint enters by limiting the rows of \mathbf{G} to have squared norm P and by normalizing the codebooks to have unit power. Note that with this notation, the mth row of \mathbf{G} are the weights used in constructing the mth redundancy block from the L codewords. In the sequel we use g_{ml} to denote the (m,l)th entry of \mathbf{G} and $\mathbf{G}_{m,l}$ to denote the upper-left $m \times l$ submatrix of \mathbf{G} .

An example of this layered rateless code structure is depicted in Fig. 1. Each redundancy block contains a repetition of the codewords used in the earlier blocks, but with a different complex scaling factor. The code structure may therefore be viewed as a hybrid of layering and repetition. Note that, absent assumptions on the decoder, the order of the layers is not important.

In addition to the layered code structure, there is additional decoding structure, namely that the layered code be successively decodable. Specifically, to recover the message, we first decode \mathbf{c}_L , treating $\mathbf{G} \begin{bmatrix} \mathbf{c}_1^{\mathrm{T}} & \cdots & \mathbf{c}_{L-1}^{\mathrm{T}} \end{bmatrix}^{\mathrm{T}}$ as (colored) noise, then decode \mathbf{c}_{L-1} , treating $\mathbf{G} \begin{bmatrix} \mathbf{c}_1^{\mathrm{T}} & \cdots & \mathbf{c}_{L-1}^{\mathrm{T}} \end{bmatrix}^{\mathrm{T}}$ as noise, and so on. Thus, our aim is to select \mathbf{G} so that capacity is achieved for any number $m=1,\ldots,M$ of redundancy blocks subject to the successive decoding constraint.

Both the layered repetition structure (25) and the successive decoding constraint impact the degree to which we can approach a perfect code. Accordingly, we examine the consequences of each in turn.

We begin by examining the implications of the layered repetition structure (25). When the number of layers L is at least as large as the number of redundancy blocks M, such layering does not limit code performance. But when L < M, it does. In particular, whenever the number m of redundancy blocks required by the realized channel exceeds L, there is necessarily a gap between the code performance and capacity. To see this, observe that (25) with (1), restricted to the first m blocks, defines a linear L-input m-output AWGN channel, the capacity of which is at most

$$I'_{m} = \begin{cases} m \log \left(1 + \frac{|\alpha|^{2} P}{\sigma^{2}} \right) & \text{for } m \leq L, \\ L \log \left(1 + \frac{m}{L} \frac{|\alpha|^{2} P}{\sigma^{2}} \right) & \text{for } m > L. \end{cases}$$
 (26)

Only for $m \leq L$ does this match the capacity of a general m-block AWGN channel, viz.,

$$I_m = m \log \left(1 + \frac{|\alpha|^2 P}{\sigma^2} \right). \tag{27}$$

Ultimately, for m>L the problem is that an L-fold linear combination cannot fill all degrees of freedom afforded by the m-block channel.

An additional penalty occurs when we combine the layered repetition structure with the requirement that the code be rateless. Specifically, for M>L, there is no choice of gain matrix ${\bf G}$ that permits (26) to be met with equality simultaneously for all $m=1,\ldots,M$. A necessary and sufficient condition for equality is that the rows of ${\bf G}_{m,L}$ be orthogonal for $m\leq L$ and the columns of ${\bf G}_{m,L}$ be orthogonal for m>L. This follows because reaching (26) for $m\leq L$ requires that the linear combination of L codebooks create an i.i.d. Gaussian sequence. In contrast, reaching (26) for m>L requires that the linear combination inject the L codebooks into orthogonal subspaces, so that a fraction L/m of the available degrees of freedom are occupied by i.i.d. Gaussians (the rest being empty).

Unfortunately, the columns of $\mathbf{G}_{m,L}$ cannot be orthogonal simultaneously for all m>L. That would entail the construction of orthogonal m-dimensional vectors (with nonzero entries) that remain orthogonal when truncated to their first m-1 dimensions, an obvious impossibility. Thus (26) determines only a lower bound on the loss due to the layering structure (25). Fortunately, the additional loss encountered in practice turns out to be quite small, as we demonstrate numerically as part of the next section.

Our lower bound on loss incurred by the use of insufficiently many layers is readily obtained by comparing (26) and (27). Specifically, given a choice of ceiling rate R for the rateless code, (26) implies that for rateless codes constructed using

 $^{^4}$ The lth column of ${f G}$ also has a useful interpretation. In particular, one can interpret the construction as equivalent to a "virtual" code-division multiple-access (CDMA) system with L users, each corresponding to one layer of the rateless code. With this interpretation, the signature (spreading) sequence for the lth virtual user is the lth column of ${f G}$.

TABLE I

Losses $|\alpha_m'|/|\alpha_m|$ in DB due to layered structure imposed on a rateless code of ceiling rate R=5 B/s/Hz, as a function of the number of layers L and redundancy blocks m.

	Redundancy blocks m								
	2	3	4	5	6	7	8	9	10
L = 1	5.22	6.77	7.50	7.92	8.20	8.40	8.54	8.65	8.74
L=2	0.00	1.55	2.28	2.70	2.98	3.17	3.32	3.43	3.52
L = 3	0.00	0.00	0.73	1.16	1.43	1.63	1.77	1.88	1.97
L=4	0.00	0.00	0.00	0.42	0.70	0.90	1.04	1.15	1.24
L = 5	0.00	0.00	0.00	0.00	0.28	0.47	0.62	0.73	0.82
L = 6	0.00	0.00	0.00	0.00	0.00	0.20	0.34	0.45	0.54
L = 7	0.00	0.00	0.00	0.00	0.00	0.00	0.14	0.26	0.35
L = 8	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.11	0.20
L = 9	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.09

linear combinations of L base codes, the smallest channel gain α_m' for which it's possible to decode with m blocks is

$$|\alpha'_m|^2 = \begin{cases} \left(2^{R/m} - 1\right) \frac{\sigma^2}{P} & \text{for } m \le L, \\ \left(2^{R/L} - 1\right) \frac{L}{m} \frac{\sigma^2}{P} & \text{for } m > L. \end{cases}$$
 (28)

By comparison, (27) implies that without the layering constraint the corresponding channel gain thresholds α_m are

$$|\alpha_m|^2 = \left(2^{R/m} - 1\right) \frac{\sigma^2}{P}.\tag{29}$$

The resulting performance loss $|\alpha_m'|/|\alpha_m|$ caused by the layered structure as calculated from (28) and (29) is shown in dB in Table I for a target ceiling rate of R=5 bits/symbol. For example, if an application requires M=10 redundancy blocks, a 3-layer code has a loss of less than 2 dB at m=10, while a 5-layer code has a loss of less than 0.82 dB at m=10.

As Table I reflects—and as can be readily verified—for a fixed number of layers L and a fixed base code rate R/L, the performance loss $|\alpha_m'|/|\alpha_m|$ attributable to the imposition of layered encoding grows monotonically with the number of blocks m, approaching the limit

$$\frac{|\alpha_{\infty}'|^2}{|\alpha_{\infty}|^2} = \frac{2^{R/L} - 1}{(R/L)\ln 2}.$$
 (30)

Thus, in applications where the number of incremental redundancy blocks is very large, it's advantageous to keep the base code rate small. For example, with a base code rate of 1/2 bit per complex symbol (implemented, for example, using a rate-1/4 binary code) the loss due to layering is at most 0.78 dB, while with a base code rate of 1 bit per complex symbol the loss is at most 1.6 dB.

We now determine the additional impact the successive decoding requirement has on our ability to approach capacity, and more generally what constraints it imposes on \mathbf{G} . We continue to incorporate the power constraint by taking the rate-R/L codebooks $\mathcal{C}_1,\ldots,\mathcal{C}_L$ to have unit power and the rows of \mathbf{G} to have squared norm P. Since our aim is to employ codebooks designed for (non-fading) Gaussian channels, we make the further assumption that the codebooks have constant power, i.e., that they satisfy the per-symbol energy constraint $E[|c_{l,n}(\mathbf{w})|^2] \leq 1$ for all layers l and time indices $n=1,\ldots,N$, where the expectation is taken over equiprobable

messages $w \in \{1, \dots, 2^{NR/L}\}$. Additional constraints on **G** will now follow from the requirement that the mutual information accumulated through any block m at each layer l be large enough to permit successive decoding.

Concretely, suppose we have received blocks $1, \ldots, m$. Let the optimal threshold channel gain α_m be defined as in Section III, i.e., as the solution to [cf. (27)]

$$R = m \log \left(1 + (|\alpha_m|^2 / \sigma^2)P\right). \tag{31}$$

Suppose further that layers $l+1, \ldots, L$ have been successfully decoded, and define

$$\begin{bmatrix} \mathbf{v}_1 \\ \vdots \\ \mathbf{v}_m \end{bmatrix} = \alpha_m \mathbf{G}_{m,l} \begin{bmatrix} \mathbf{c}_1 \\ \vdots \\ \mathbf{c}_l \end{bmatrix} + \begin{bmatrix} \mathbf{z}_1 \\ \vdots \\ \mathbf{z}_m \end{bmatrix}$$
(32)

as the received vectors without the contribution from layers $l+1,\ldots,L$.

Then, following standard arguments, with independent equiprobable messages for each layer, the probability of decoding error for layer l can made vanishingly small with increasing block length only if the mutual information between input and output is at least as large as the rate R/L of the code \mathcal{C}_l . That is, successive decoding requires

$$R/L \le (1/N)I(\mathbf{c}_l; \mathbf{y}_1, \dots, \mathbf{y}_m \mid \mathbf{c}_{l+1}^L)$$
(33)

$$= (1/N)I(\mathbf{c}_l; \mathbf{v}_1, \dots, \mathbf{v}_m) \tag{34}$$

$$\leq \log \frac{\det(\mathbf{I} + (|\alpha_m|^2/\sigma^2)\mathbf{G}_{m,l}\mathbf{G}_{m,l}^{\dagger})}{\det(\mathbf{I} + (|\alpha_m|^2/\sigma^2)\mathbf{G}_{m,l-1}\mathbf{G}_{m,l-1}^{\dagger})}, \quad (35)$$

where **I** is an appropriately sized $(m \times m)$ identity matrix. The inequality (35) relies on the assumption that the codebooks have constant power, and it holds with equality if the components of $\mathbf{c}_1, \ldots, \mathbf{c}_l$ are i.i.d. Gaussian.

Our ability to choose G to either exactly or approximately satisfy (35) for all $l=1,\ldots,L$ and each $m=1,\ldots,M$ determines the degree to which we can approach capacity. It is straightforward to see that there is no slack in the problem; (35) can be satisfied simultaneously for all l and m only if the inequalities are all met with equality. Beyond this observation, however, the conditions under which (35) may be satisfied are not obvious.

Characterizing the set of solutions for ${\bf G}$ when L=M=2 was done in Section III (see (24)). Characterizing the set of solutions when L=M=3 requires more work. It is shown in Appendix I that, when it exists, a solution ${\bf G}$ must have the form

$$\mathbf{G} = \sqrt{x-1} \cdot \begin{bmatrix} \sqrt{x+1} & \sqrt{x^2(x+1)} & \sqrt{x^4(x+1)} \\ \sqrt{x^3(x+1)} & e^{j\theta_1}\sqrt{x^5+1} & e^{j\theta_2}\sqrt{x(x+1)} \\ \sqrt{x^2(x^3+1)} & e^{j\theta_3}\sqrt{x(x^3+1)} & e^{j\theta_4}\sqrt{x^3+1} \end{bmatrix}$$
(36)

where $x=2^{R/6}$ and where $e^{j\theta_i}$, $i=1,\ldots,4$ are complex phasors. The desired phasors—or a proof of nonexistence—may be determined from the requirement that ${\bf G}$ be a scaled unitary matrix. Using this observation, it is shown in Appendix I that a solution ${\bf G}$ exists and is unique (up to complex

conjugate) for all $R \le 3(\log(7+3\sqrt{5})-1) \approx 8.33$ bits per complex symbol, but no choice of phasors results in a unitary G for larger values of R.

For example, using (36) with R=6 bits/symbol we find that:

$$P = 63, \quad \alpha_1 = 1, \quad \alpha_2 = \sqrt{1/9}, \quad \alpha_3 = \sqrt{1/21}$$

$$\mathbf{G} = \begin{bmatrix} \sqrt{3} & \sqrt{12} & \sqrt{48} \\ \sqrt{24} & \sqrt{33}e^{j\theta_1} & \sqrt{6}e^{j\theta_2} \\ \sqrt{36} & \sqrt{18}e^{j\theta_3} & \sqrt{9}e^{j\theta_4} \end{bmatrix}$$

where

$$\theta_1 = \arccos \frac{-5}{2\sqrt{22}},$$
 $\theta_2 = 2\pi - \arctan 3\sqrt{7},$ $\theta_3 = -\arctan \sqrt{7},$ $\theta_4 = \pi - \arctan \sqrt{7}/3.$

For M>3 the algebra becomes daunting, though we conjecture that exact solutions and hence perfect rateless codes exist for all L=M, for at least some nontrivial values of R.

For L < M perfect constructions cannot exist. As developed earlier in this section, even if we replace the optimum threshold channel gains α_m defined via (31) with suboptimal gains determined by the layering bound (26), viz.,

$$R = \begin{cases} m \log \left(1 + \frac{|\alpha_m|^2 P}{\sigma^2} \right) & \text{for } m \le L, \\ L \log \left(1 + \frac{m}{L} \frac{|\alpha_m|^2 P}{\sigma^2} \right) & \text{for } m > L, \end{cases}$$
(37)

it is still not possible to satisfy (35). However, one can come close to satisfying (35) in such cases. While the associated analysis is nontrivial, such behavior is easily demonstrated numerically, which we show as part of the next section.

V. NUMERICAL EXAMPLES

In this section, we consider numerical constructions both for the case L=M and for the case L< M. Specifically, we have experimented with numerical optimization methods to satisfy (35) for up to M=10 redundancy blocks, using the threshold channel gains α_m defined via (37) in place of those defined via (31) as appropriate when the number of blocks M exceeds the number of layers L.

For the case L=M, for each of $M=2,3,\ldots,10$, we found constructions with R/L=2 bits/symbol that come within 0.1% of satisfying (35) subject to (31), and often the solutions come within 0.01%. This provides powerful evidence that perfect rateless codes exist for a wide range of parameter choices.

For the case L < M, despite the fact that there do not exist perfect codes, in most cases of interest one can come remarkably close to satisfying (35) subject to (37). Evidently mutual information for Gaussian channels is quite insensitive to modest deviations of the noise covariance away from a scaled identity matrix.

As an example, Table II shows the rate shortfall in meeting the mutual information constraints (35) for an L=3 layer code with M=10 redundancy blocks, and a target ceiling

TABLE II

Percent shortfall in rate for a numerically-optimized rateless code with M=10 blocks, L=3 layers, and a ceiling rate of R=5 b/s/Hz.

Redundancy blocks m										
	1	2	3	4	5	6	7	8	9	10
l = 1	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.00
l=2	0.00	0.28	1.23	1.46	1.39	0.44	0.59	0.48	0.16	0.23
l = 3	0.00	0.29	1.23	1.48	1.40	0.43	0.54	0.51	0.15	0.23

rate R=5. The associated complex gain matrix is

$$\mathbf{G} = \begin{bmatrix} 1.4747 & 2.6277 & 4.6819 \\ 3.5075 & 3.7794 \, e^{j2.0510} & 2.1009 \, e^{-j1.9486} \\ 4.0648 & 3.1298 \, e^{-j0.9531} & 2.1637 \, e^{j2.5732} \\ 3.2146 & 3.1322 \, e^{j3.0765} & 3.2949 \, e^{j0.9132} \\ 3.2146 & 3.3328 \, e^{-j1.6547} & 3.0918 \, e^{-j1.4248} \\ 3.2146 & 3.1049 \, e^{j0.9409} & 3.3206 \, e^{j2.8982} \\ 3.2146 & 3.3248 \, e^{j1.2506} & 3.1004 \, e^{-j0.2027} \\ 3.2146 & 3.0980 \, e^{-j1.4196} & 3.3270 \, e^{j1.9403} \\ 3.2146 & 3.2880 \, e^{-j2.9449} & 3.1394 \, e^{-j1.9243} \\ 3.2146 & 3.1795 \, e^{j0.7839} & 3.2492 \, e^{j0.3413} \end{bmatrix}$$

The worst case loss is less than 1.5%; this example is typical in its efficiency.

The total loss of the designed code relative to a perfect rateless code is, of course, the sum of the successive decoding and layered encoding constraint losses. Hence, the losses in Table II and Table I are cumulative. As a practical matter, however, when L < M, the layered encoding constraint loss dwarfs that due to the successive decoding constraint: the overall performance loss arises almost entirely from the code's inability to occupy all available degrees of freedom in the channel. Thus, this overall loss can be estimated quite closely by comparing (27) and (26), or, equivalently, (31) and (37). Indeed this is reflected in our example, where the loss of Table I dominates over that of Table II.

VI. EXISTENCE OF NEAR-PERFECT RATELESS CODES

While the closed-form construction of *perfect* rateless codes subject to layered encoding and successive decoding becomes more challenging with increasing code range M, the contruction of codes that are at least nearly perfect is comparatively straightforward. In the preceding section, we demonstrated this numerically. In this section, we prove this analytically. In particular, we construct rateless codes that are arbitrarily close to perfect in an appropriate sense, provided enough layers are used. We term these near-perfect rateless codes. The code construction we present will be applicable to arbitrarily large M and will also allow for simpler decoding than the MMSE decoder employed in the preceding development.

A. Encoding

Our near-perfect rateless code construction [5] is a slight generalization of that used in Section IV. Specifically, as (25) indicates, in our approach to perfect constructions we made each redundancy block a linear combination of the base codewords, where the weights are the corresponding row of the combining matrix **G**. This means that each individual symbol of a particular redundancy block is, therefore, a linear combination of the corresponding symbols in the respective base codewords, with the combining matrix being the same for all such symbols.

By contrast, in this section, we allow the combining matrix to vary from symbol to symbol in the construction of each redundancy block, and use the additional degrees of freedom in the code design to simplify the analysis—at the expense of some slightly more cumbersome notation. In particular, using $c_l(n)$ and $x_m(n)$ to denote the nth elements of codeword \mathbf{c}_l and redundancy block \mathbf{x}_m , respectively, we have [cf. (25)]

$$\begin{bmatrix} x_1(n) \\ \vdots \\ x_M(n) \end{bmatrix} = \mathbf{G}(n) \begin{bmatrix} c_1(n) \\ \vdots \\ c_L(n) \end{bmatrix}, \quad n = 1, 2, \dots, N.$$
 (38)

The value of M plays no role in our development and may be taken arbitrarily large. Moreover, as before, the power constraint enters by limiting the rows of $\mathbf{G}(n)$ to have a squared norm P and by normalizing the codebooks to have unit power.

It suffices to restrict our attention to G(n) of the form

$$\mathbf{G}(n) = \mathbf{P} \odot \mathbf{D}(n), \tag{39}$$

where **P** is an $M \times L$ (deterministic) power allocation matrix with entries $\sqrt{p_{m,l}}$ that do not vary within a block,

$$\mathbf{P} = \begin{bmatrix} \sqrt{p_{1,1}} & \cdots & \sqrt{p_{1,L}} \\ \vdots & \ddots & \vdots \\ \sqrt{p_{M,1}} & \cdots & \sqrt{p_{M,L}} \end{bmatrix}, \tag{40}$$

and $\mathbf{D}(n)$ is a (random) phase-only "dither" matrix of the form

$$\mathbf{D}(n) = \begin{bmatrix} d_{1,1}(n) & \cdots & d_{1,L}(n) \\ \vdots & \ddots & \vdots \\ d_{M,1}(n) & \cdots & d_{M,L}(n) \end{bmatrix}, \tag{41}$$

with \odot denoting elementwise multiplication. In our analysis, the $d_{ij}(n)$ are all i.i.d. in i, j, and n, and are drawn independently of all other random variables, including noises, messages, and codebooks. As we shall see below, the role of the dither is to decorrelate pairs of random variables, hence it sufficies for $d_{ij}(n)$ to take values +1 and -1 with equal probability.

B. Decoding

To obtain a near-perfect rateless code, it will be sufficient to employ a successive cancellation decoder with maximal ratio combining (MRC) of the redundancy blocks. While, in principle, an MMSE-based successive cancellation decoder enables higher performance, as we will see, an MRC-based one is sufficent for our purposes, and simplifies the analysis. Indeed, although the encoding we choose creates a perlayer channel that is time-varying, the MRC-based successive cancellation decoder effectively transforms the channel back into a time-invariant one, for which any of the traditional

low-complexity capacity-approaching codes for the AWGN channel are suitable as a base code in the design.⁵

The decoder operation is as follows, assuming the SNR is such that decoding is possible from m redundancy blocks. To decode the Lth (top) layer, the dithering is first removed from the received waveform by multiplying by the conjugate dither sequence for that layer. Then, the m blocks are combined into a single block via the appropriate MRC for that layer. The message in this Lth layer is then decoded, treating the undecoded layers as noise, and its contribution subtracted from the received waveform. The L-1st layer is now the top layer, and the process is repeated, until all layers have been decoded. Note that the use of MRC in decoding is equivalent to treating the undecoded layers as white (rather than structured) noise, which is the natural approach when the dither sequence structure in those undecoded (lower) layers is ignored in decoding the current layer of interest.

We now introduce notation that allows the operation of the decoder to be expressed more precisely. We then determine the effective SNR seen by the decoder at each layer of each rendundancy block.

Since G(n) is drawn i.i.d., the overall channel is i.i.d., and thus we may express the channel model in terms of an arbitrary individual element in the block. Specifically, our received waveform can be expressed as [cf. (1) and (25)]

$$\mathbf{y} = \begin{bmatrix} y_1 \\ \vdots \\ y_M \end{bmatrix} = \alpha_m \mathbf{G} \begin{bmatrix} c_1 \\ \vdots \\ c_L \end{bmatrix} + \begin{bmatrix} z_1 \\ \vdots \\ z_M \end{bmatrix},$$

where $\mathbf{G} = \mathbf{P} \odot \mathbf{D}$, with \mathbf{G} denoting the arbitrary element in the sequence $\mathbf{G}(n)$, and where y_m is the corresponding received symbol from redundancy block m (and similarly for c_m , z_m , \mathbf{D}).

If layers $l+1, l+2, \ldots, L$ have been successively decoded from m redundancy blocks, and their effects subtracted from the received waveform, the residual waveform is denoted by

$$\mathbf{v}_{m,l} = \alpha_m \mathbf{G}_{m,l} \begin{bmatrix} c_1 \\ \vdots \\ c_l \end{bmatrix} + \begin{bmatrix} z_1 \\ \vdots \\ z_m \end{bmatrix}, \tag{42}$$

where we continue to let $\mathbf{G}_{m,l}$ denote the $m \times l$ upper-left submatrix of \mathbf{G} , and likewise for $\mathbf{D}_{m,l}$ and $\mathbf{P}_{m,l}$. As additional notation, we let $\mathbf{g}_{m,l}$ denote the m-vector formed from the upper m rows of the lth column of \mathbf{G} , whence

$$\mathbf{G}_{m,l} = \begin{bmatrix} \mathbf{g}_{m,1} & \mathbf{g}_{m,2} & \cdots & \mathbf{g}_{m,l} \end{bmatrix}, \tag{43}$$

and likewise for $\mathbf{d}_{m,l}$ and $\mathbf{p}_{m,l}$.

With such notation, the decoding can be expressed as follows. Starting with $\mathbf{v}_{m,L} = \mathbf{y}$, decoding proceeds. After layers l+1 and higher have been decoded and removed, we decode from $\mathbf{v}_{m,l}$. Writing

$$\mathbf{v}_{m,l} = \alpha_m(\mathbf{d}_{m,l} \odot \mathbf{p}_{m,l}) c_l + \mathbf{v}_{m,l-1}, \tag{44}$$

⁵More generally, the MRC-based decoder is particularly attractive for practical implementation. Indeed, as each repetition block arrives a sufficient statistic for decoding can be accumulated without the need to retain earlier repetitions in buffers. The computational cost of decoding thus grows linearly with block length while the memory requirements do not grow at all. This is much less complex than the MMSE decoder used in Section IV.

the operation of removing the dither can be expressed as

$$\mathbf{d}_{m,l}^* \odot \mathbf{v}_{m,l} = \alpha_m \mathbf{p}_{m,l} c_l + \mathbf{v}_{m,l-1}' \tag{45}$$

where

$$\mathbf{v}'_{m,l-1} = \mathbf{d}^*_{m,l} \odot \mathbf{v}_{m,l-1}. \tag{46}$$

The MRC decoder treats the dither in the same manner as noise, i.e., as a random process with known statistics but unknown realization. Because the entries of the dither matrix are chosen to be i.i.d. random phases independent of the messages, the entries of $\mathbf{D}_{m,l}$ and $\begin{bmatrix} c_1 & \cdots & c_{l-1} \end{bmatrix}$ are jointly and individually uncorrelated, and the effective noise $\mathbf{v}'_{m,l-1}$ seen by the MRC decoder has diagonal covariance $\mathbf{K}_{\mathbf{v}'_{m,l-1}} = E[\mathbf{v}'_{m,l-1}\mathbf{v}'^{\dagger}_{m,l-1}]$. The effective SNR at which this lth layer is decoded from

The effective SNR at which this lth layer is decoded from m blocks via MRC is thus

$$\sum_{m'=1}^{m} SNR_{m',l}(\alpha_m), \tag{47}$$

where

$$SNR_{m',l}(\alpha_m) = \frac{|\alpha_m|^2 p_{m',l}}{|\alpha_m|^2 (p_{m',1} + \dots + p_{m',l-1}) + \sigma^2}. \quad (48)$$

Note that we have made the dependency of these per-layer per-block SNRs on α_m explicit in the notation.

C. Efficiency

The use of random dither at the encoder and MRC at the decoder both cause some loss in performance relative to the perfect rateless codes presented earlier. In this section we show that these losses can be made small.

When a coding scheme is not perfect, its *efficiency* quantifies how close the scheme is to perfect. There are ultimately several ways one could measure efficiency that are potentially useful for engineering design. Among these, we choose the following efficiency notion:

- 1) We find the ideal thresholds $\{\alpha_m\}$ for a perfect code of rate R.
- 2) We determine the highest rate R' such that an imperfect code designed at rate R' is decodable with m redundancy blocks when the channel gain is α_m , for all $m = 1, 2, \ldots$
- 3) We measure efficiency η by the ratio R'/R, which is always less than unity.

With this notion of efficiency, we further define a coding scheme as near-perfect if the efficiency so-defined approaches unity when sufficiently many layers L are employed.

The efficiency of our scheme ultimately depends on the choice of our power allocation matrix (40). We now show the main result of this section: provided there exists a power allocation matrix such that for each l and m

$$\frac{R}{L} = \sum_{m'=1}^{m} \log(1 + \text{SNR}_{m',l}(\alpha_m)), \tag{49}$$

with $SNR_{m,l}(\alpha_m)$ as defined in (48), a near-perfect rateless coding scheme results. The existence of such a power allocation, as well as an interpretation of (49), is proved in Appendix II.

We establish our main result by finding a lower bound on the average mutual information between the input and output of the channel. Upon receiving m blocks with channel gain α_m , and assuming layers $l+1,\ldots,L$ are successfully decoded, let $I'_{m,l}$ be the mutual information between the input to the lth layer and the channel output. Then

$$I'_{l,m} = I(c_l; \mathbf{v}_{m,l} \mid \mathbf{d}_{m,l}) \tag{50}$$

$$= I(c_l; \alpha_m \mathbf{p}_{m,l} c_l + \mathbf{v}'_{m,l-1} \mid \mathbf{d}_{m,l}), \tag{51}$$

$$\geq I(c_l; \alpha_m \mathbf{p}_{m,l} c_l + \mathbf{v}'_{m,l}), \tag{52}$$

$$\geq I(c_l; \alpha_m \mathbf{p}_{m,l} c_l + \mathbf{v}_{m,l}^{"}), \tag{53}$$

$$= \log \left(1 + \sum_{m'=1}^{m} SNR_{m',l}(\alpha_m) \right)$$
 (54)

where (51) follows from (45)–(46), (52) follows from the independence of c_l and $\mathbf{d}_{m,l}$, and (53) obtains by replacing (47) $\mathbf{v}'_{m,l-1}$ with a Gaussian random vector $\mathbf{v}''_{m,l-1}$ of covariance $\mathbf{K}_{\mathbf{v}'_{m,l-1}}$. Lastly, to obtain (54) we have used (47) for the post-MRC SNR.

Now, if the assumption (49) is satisfied, then the right-hand side of (54) is further bounded for all m by

$$I'_{m,l} \ge \log\left(1 + \ln 2\frac{R}{L}\right),$$
 (55)

where we have applied the inequality $\ln(1+u) \leq u$ (valid for u>0) to (49) to conclude that $(\ln 2)R/L \leq \sum_{m'=1}^m \mathrm{SNR}_{m',l}(\alpha_m)$. Note that the lower bound (55) may be quite loose; for example, $I'_{m,l}=R/L$ when m=1.

Thus, if we design each layer of the code for a base code rate of

$$\frac{R''}{L} = \log\left(1 + \ln 2\frac{R}{L}\right),\tag{56}$$

(55) ensures decodability after m blocks are received when the channel gain is α_m , for $m = 1, 2, \ldots$

Finally, rewriting (56) as

$$\frac{R}{L} = \frac{2^{R''/L} - 1}{\ln 2},\tag{57}$$

the efficiency η of the conservatively-designed layered repetition code is bounded by

$$\eta \ge \frac{R''}{R} = \frac{(\ln 2)R''/L}{2^{R''/L} - 1} \ge 1 - \frac{\ln 2}{2} \frac{R''}{L},$$
(58)

which approaches unity as $L \to \infty$ as claimed.

In Fig. 2, the efficiency bounds (58) are plotted as a function of the base code rate R''/L. As a practical matter, our bound implies, for instance, that to obtain 90% efficiency requires a base code of rate of roughly 1/3 bits per complex symbol. Note, too, that when the number of layers is sufficiently large that the SNR per layer is low, a binary code may be used instead of a Gaussian codebook, which may be convenient for implementation. For example, a code with rate 1/3 bits per complex symbol may be implemented using a rate-1/6 LDPC code with binary antipodal signaling.

It thus remains only to show that there exists a power allocation such that (49) is satisfied, which is established in the Appendix.

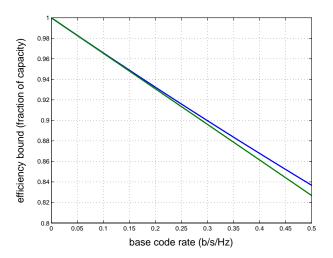


Fig. 2. Lower bound on efficiency of the near-perfect rateless code. The top and bottom curves are the middle and right-hand bounds of (58), respectively.

VII. DESIGN AND IMPLEMENTATION ISSUES

In this section, we comment on some additional issues that arise in the development and implementation of such codes.

A. Increasing Code Resolution

With an ideal rateless code, *every* prefix of the code is a capacity-achieving code. This corresponds to a maximally dense set of SNR thresholds at which decoding can occur. By contrast, our focus in the preceding sections was on rateless codes that were capacity-achieving only for prefixes whose lengths are an integer multiple of the base block length. The associated sparseness of SNR thresholds can be undesirable in some applications, since when the realized SNR is between thresholds, capacity is no longer achieved: the realized rate promised by the construction is that corresponding to the next lower SNR threshold.

On the other hand, performance may be much better than this pessimistic assessment. Attempting to decode when a partial redundancy block is available will cause the decoder for the base code to see a time-varying channel in which the symbols bolstered by the partial block have better SNR than the others. Whether the base code and decoder can be adapted to operate efficiently in this situation depends on the details of their construction, and may be hard to predict. Their performance is, however, easily assessed via simulation.

Another approach to controlling this aspect of our rateless code behavior is as follows. Suppose we are interested in a rateless code whose ceiling rate is R. Then we use the rateless construction of the preceding section to design a code of ceiling rate κR , where $1 \leq \kappa \leq M$, and have the decoder collect at least κ blocks before attempting to decode. With this approach, the associated rate thresholds are $R, R\kappa/(\kappa+1), R\kappa/(\kappa+2), \ldots, R\kappa/M$, where we note that the largest rate increment is the first, corresponding to the factor $\kappa/(\kappa+1)$. Hence, by choosing larger values of κ , one can increase the density of rate (and thus SNR) thresholds.

It should be stressed, however, that there is a price to be paid with this approach. In particular, if we keep constant the number of codeword symbols that must be accumulated before decoding at rate R is possible, then the underlying block size in our rateless construction must decrease inversely with κ . Thus, for sufficiently large κ the basic block length becomes short enough that code performance suffers, and so in practice the selection of κ involves a compromise. In addition, this approach may also increase requirements on the analog-to-digital conversion precision at the receiver front end.

B. Implementation Comments

A few additional aspects of implementation are worthy of comment.

First, one consequence of our development of perfect rateless codes for M=L is that all layers must have the same rate R/L. This does not seem to be a serious limitation, as it allows a single base codebook to serve as the template for all layers, which in turn generally decreases the implementation complexity of the encoder and decoder. The codebooks $\mathcal{C}_1,\ldots,\mathcal{C}_L$ used for the L layers should not be identical, however, for otherwise a naive successive decoder might inadvertantly swap messages from two layers or face other difficulties that increase the probability of decoding error. A simple cure to this problem is to apply pseudorandom phase scrambling to a single base codebook $\mathcal C$ to generate the different codebooks needed for each layer. Pseudorandom interleaving would have a similar effect.

Second, a layered code designed with the successive decoding constraint (35) can be decoded in a variety of ways. Because the undecoded layers act as colored noise, an optimal decoder should take this into account, for example by using a minimum mean-square error (MMSE) combiner on the received blocks $\{y_m\}$. The MMSE combining weights will change as each layer is stripped off. Alternatively, some or all of the layers could be decoded jointly; this might make sense when the decoder for the base codebook decoder is already iterative, and could potentially accelerate convergence compared to a decoder that treats the layers sequentially.

Finally, a comparatively simple receiver is possible when all M blocks have been received from a perfect rateless code in which M=L. In this special case the linear combinations applied to the layers are orthogonal, hence the optimal receiver can decode each layer independently, without successive decoding. This property is advantageous in a multicasting scenario because it allows the introduction of users with simplified receivers that function only at certain rates, in this case the lowest supported one.

Some further design and implementation issues are addressed in [21].

VIII. CONCLUDING REMARKS

There are a variety of interesting directions for further research. For example, one obvious area of future work is to incorporate time variation into the channel model (1). The rateless constructions presented in this paper are designed to operate efficiently when, e.g., for one block the channel gain is

 $[\alpha_1]$, for two blocks the gains are $[\alpha_2 \ \alpha_2]$, for three blocks the gains are $[\alpha_3 \ \alpha_3 \ \alpha_3]$, and so on. A simple extension would allow α to vary deterministically so long as the pattern of variation is known in advance. Then, for one block the code would be designed for a gain of $[\alpha_{1,1}]$, for two blocks the target gains would be $[\alpha_{2,1} \ \alpha_{2,2}]$, for three blocks the gains would be $[\alpha_{3,1}, \alpha_{3,2}, \alpha_{3,3}]$, and so on. More generally, however, the design of perfect layered rateless codes when α follows a stochastic model remains an important open problem.

Other worthwhile directions include more fully developing rateless constructions for the AWGN channel that allow decoding to begin at any received block, and/or to exploit an arbitrary subset of the subsequent blocks. Initial efforts in this direction include the faster-than-Nyquist constructions in [5], [20], and the diagonal subblock layering approach described in [20].

Beyond the single-input, single-output (SISO) channel, many multiterminal and multiuser extensions are also of considerable interest. Examples of preliminary developments along these lines include the rateless space-time code constructions in [6], the rateless codes for multiple-access channels developed in [16], and the approaches to rateless coding for parallel channels examined in [20]. Indeed, such research may lead to efficient rateless orthogonal frequency-division multiplexing (OFDM) systems and efficient rateless multi-input. multi-output (MIMO) codes with wide-ranging applications.

Finally, extending the layered approach to rateless coding developed in this paper beyond the Gaussian channel is also a potentially rich direction for further research. A notable example would be the binary symmetric channel, where good rateless solutions remain elusive.

APPENDIX I

Perfect rateless solution for L=M=3

Determining the set of solutions

$$\mathbf{G} = \begin{bmatrix} g_{11} & g_{12} & g_{13} \\ g_{21} & g_{22} & g_{23} \\ g_{31} & g_{32} & g_{33} \end{bmatrix}$$
 (59)

to (35) when L=M=3 as a function of the ceiling rate Ris a matter of lengthy if routine algebra.

We begin by observing that any row or any column of G may be multiplied by a common phasor without changing GG^{\dagger} . Without loss of generality we may therefore take the first row and first column of G to be real. Each G thus represents a set of solutions D_1GD_2 , where D_1 and D_2 are diagonal matrices in which the diagonal entries have modulus 1. The solutions in the set are equivalent for most engineering purposes and we shall therefore not distinguish them further.

We know that G must be a scaled unitary matrix, scaled so that the row and column norms are \sqrt{P} . Thus, if we somehow determine the first two rows of G, there is always a choice for the third row: it's the unique vector orthogonal to the first two rows which meets the power constraint and which has first component real and positive. Conversely, it's easy to see that any appropriately scaled unitary matrix G that satisfies (35) for m = 1 and m = 2 (and all l = 1, 2, 3) necessarily satisfies (35) for m=3. We may therefore without loss of generality restrict our attention to determining the set of solutions to the first two rows of G; the third row comes "for free" from the constraint that G be a scaled unitary matrix.

Assume, again without loss of generality, that $|\alpha_1|^2 = 1$ and $\sigma^2 = 1$. Via (35), the first row of **G** (which controls the first redundancy block) must satisfy

$$R/3 = \log(1 + g_{11}^2) \tag{60}$$

$$2R/3 = \log(1 + g_{11}^2 + g_{12}^2) \tag{61}$$

$$3R/3 = \log(1 + g_{11}^2 + g_{12}^2 + g_{13}^2) \tag{62}$$

together with the power constraint

$$P = g_{11}^2 + g_{12}^2 + g_{13}^2. (63)$$

Thus

$$P = 2^R - 1 = x^6 - 1$$

and

$$g_{11}^2 = 2^{R/3} - 1 = x^2 - 1,$$

$$g_{12}^2 = 2^{R/3}(2^{R/3} - 1) = x^2(x^2 - 1),$$
(64)

$$g_{12}^2 = 2^{R/3}(2^{R/3} - 1) = x^2(x^2 - 1),$$
 (65)

$$g_{13}^2 = 2^{2R/3}(2^{R/3} - 1) = x^4(x^2 - 1),$$
 (66)

where for convenience we have introduced the change of variables $x = 2^{R/6}$.

The first column of G (which controls the first layer of each redundancy block) is also straightforward. Via (31) with m=2 and m=3, we have

$$|\alpha_2|^2 = \frac{1}{x^3 + 1},\tag{67}$$

$$|\alpha_3|^2 = \frac{1}{x^4 + x^2 + 1}. (68)$$

Using (35) for l=1 and m=2 yields

$$R/3 = \log(1 + |\alpha_2|^2 (g_{11}^2 + g_{21}^2)). \tag{69}$$

Substituting the previously computed expressions (64) and (67) for g_{11}^2 and $|\alpha_2|^2$ into (69) and solving for g_{21} yields

$$g_{21}^2 = x^3(x^2 - 1). (70)$$

To solve for the second row of G we use (35) with m =l=2 together with the requirement that the first and second rows be orthogonal. It is useful at this stage to switch to polar coordinates, i.e., $g_{22} = |g_{22}|e^{j\theta_1}$ and $g_{23} = |g_{23}|e^{j\theta_2}$.

Orthogonality of the first and second rows means that

$$0 = g_{11}g_{21} + g_{12}|g_{22}|e^{j\theta_1} + g_{13}|g_{23}|e^{j\theta_2}.$$
 (71)

Complex conjugation is not needed here because the first row is real. Substituting the quantities (64)–(66) and (70) into (71), using the power constraint

$$|g_{23}|^2 = P - |g_{22}|^2 - g_{21}^2, (72)$$

and dividing through by $x\sqrt{x^2-1}$ yields

$$0 = \sqrt{x(x^2 - 1)} + |g_{22}|e^{j\theta_1} + x\sqrt{x^6 - x^5 + x^3 - 1 - |g_{22}|^2}e^{j\theta_2}.$$
 (73)

By isolating the rightmost of the three terms in the above equation and taking the squared modulus of both sides, we find that

$$x(x^{2}-1) + |g_{22}|^{2} + 2\cos\theta_{1}|g_{22}|\sqrt{x(x^{2}-1)}$$

$$= x^{2}(x^{6} - x^{5} + x^{3} - 1 - |g_{22}|^{2}), \quad (74)$$

so that

 $2\cos\theta_1|g_{22}|$

$$=\frac{x^8-x^7+x^5-x^3-x^2+x-(1+x^2)|g_{22}|^2}{\sqrt{x(x^2-1)}}.$$
 (75)

This ungainly expression will allow us to eliminate θ_1 from a subsequent equation.

To complete the calculation of the second row of G, we use (35) with m = 2 and l = 1, 2 to infer that

$$2^{2R/3} = x^4 = \det(\mathbf{I} + |\alpha_2|^2 \mathbf{G}_{2,2} \mathbf{G}_{2,2}^{\dagger}). \tag{76}$$

To expand the right hand side of (76), we compute

$$\mathbf{G}_{2,2}\mathbf{G}_{2,2}^{\dagger} = \begin{bmatrix} x^4 - 1 & (x^2 - 1)x^{3/2} + x\sqrt{x^2 - 1}|g_{22}|e^{-j\theta_1} \\ (*) & |g_{22}|^2 + x^3(x^2 - 1) \end{bmatrix}, \quad (77)$$

where (*) is the complex conjugate of the upper right entry, from which we find

$$\det(\mathbf{I} + \frac{1}{x^3 + 1}\mathbf{G}_{2,2}\mathbf{G}_{2,2}^{\dagger}) = \frac{x^2(x+1)}{(x^3 + 1)^2} \left(x^2(x+1) + |g_{22}|^2 + 2\cos\theta_1|g_{22}|(x-1)\sqrt{x(x^2 - 1)}\right).$$
(78)

The term $2\cos\theta_1|g_{22}|$ in (78) matches the left hand side of (75), so by combining (75), (76), and (78), solving for $|g_{22}|^2$, and simplifying terms, we arrive at

$$|g_{22}|^2 = (x^5 + 1)(x - 1).$$
(79)

The power constraint (72) then immediately yields

$$|g_{23}|^2 = x(x^2 - 1). (80)$$

The squared modulus of the entries of the last row of G follow immediately from the norm constraint on the columns:

$$g_{31}^2 = P - g_{21}^2 + g_{11}^2 = x^2(x^2 - x + 1)(x^2 - 1).$$
 (81)

$$|g_{32}|^2 = P - g_{22}^2 - g_{12}^2 = x(x^3 + 1)(x - 1)$$
 (82)

and

$$|g_{33}|^2 = P - g_{23}^2 - g_{13}^2 = (x^3 + 1)(x - 1).$$
 (83)

This completes the calculation of the squared modulus of the entries of G. In summary, we have shown that G has the form

$$\mathbf{G} = \sqrt{x-1} \cdot \begin{bmatrix} \sqrt{x+1} & \sqrt{x^2(x+1)} & \sqrt{x^4(x+1)} \\ \sqrt{x^3(x+1)} & e^{j\theta_1}\sqrt{x^5+1} & e^{j\theta_2}\sqrt{x(x+1)} \\ \sqrt{x^2(x^3+1)} & e^{j\theta_3}\sqrt{x(x^3+1)} & e^{j\theta_4}\sqrt{x^3+1} \end{bmatrix}$$
(84)

where $x = 2^{R/6}$.

We must now establish the existence of suitable θ_1,\ldots,θ_4 . To resolve this question it sufficies to consider the consequences of the orthogonality constraint (71) on θ_1 and θ_2 . As remarked at the start of this section, the last row of ${\bf G}$ and hence θ_3 and θ_4 come "for free" once we have the first two rows of ${\bf G}$.

Substituting the expressions for $|g_{ml}|^2$ determined above into (71) and canceling common terms yields

$$0 = \sqrt{x} + e^{j\theta_1} \sqrt{x^4 - x^3 + x^2 - x + 1} + e^{j\theta_2} \sqrt{x^3}.$$
 (85)

The right-hand side is a sum of three phasors of predetermined magnitude, two of which can be freely adjusted in phase. In geometric terms, the equation has a solution if we can arrange the three complex phasors into a triangle, which is possible if and only if the longest side of the triangle is no longer than the sum of the lengths of the shorter sides. The resulting triangle is unique (up to complex conjugation of all the phasors). Now, the middle term of (85) grows faster in x than the others, so for large x we cannot possibly construct the desired triangle. A necessary condition for a solution is thus

$$\sqrt{x} + \sqrt{x^3} \ge \sqrt{x^4 - x^3 + x^2 - x + 1},$$
 (86)

where equality can be shown (after some manipulation) to hold at the largest root of x^2-x+1 , i.e., at $x=(3+\sqrt{5})/2$, or equivalently $R=6\log_2 x=6\log_2(3+\sqrt{5})-6$. It becomes evident by numerically plotting the quantities involved that this necessary condition is also sufficient, i.e., a unique solution to (85) exist for all values of x in the range $1< x \le (3+\sqrt{5})/2$ and no others. Establishing this fact algebraically is an unrewarding though straightforward exercise.

A relatively compact formula for θ_1 may be found by applying the law of cosines to (85):

$$\cos(\pi - \theta_1) = \frac{x^4 - 2x^3 + x^2 + 1}{2\sqrt{x(x^4 - x^3 + x^2 - x + 1)}}.$$
 (87)

Similar formulas may be derived for θ_2 , θ_3 , and θ_4 .

APPENDIX II POWER ALLOCATION

The power allocation satisfying the property (49) can be obtained as the solution to a different but closely related rateless code optimization problem. Specifically, let us retain the block structuring and layering of the code of Section VI-A, but instead of using repetition and dithering in the construction, let us consider a code where the codebooks in a given layer are *independent* from block to block. While such a code is still successively decodable, it does not retain other characteristics that make decoding possible with low complexity. However, the complexity characteristic is not of interest. What does matter to us is that the per-layer, per-block SNRs that result from a particular power allocation will be identical to those of the code of Section VI-A for the same power allocation. Thus, in tailoring our code in this Appendix to meet (49), we simultaneously ensure our code of Section VI-A will as well.

We begin by recalling a useful property of layered codes in general that we will apply. Consider an AWGN channel with gain α and noise rvsz of variance σ^2 , and consider an L-layer

block code that is successively decodable. If the constituent codes are capacity-achieving i.i.d. Gaussian codes, and MMSE successive cancellation is used, then the overall code will be capacity achieving. More specifically, for any choice of powers p_l for layers $l=1,2,\ldots,L$ that sum to the power constraint P, the associated rates I_l for these layers will sum to the corresponding capacity $\log(1+|\alpha|^2P/\sigma^2)$. Equivalently, for any choice of rates I_l that sum to capacity, the associated powers p_l will sum to the corresponding power constraint. In this latter case, any rate allocation that yield powers that are all nonnegative is a valid one.

To see this, let the relevant codebooks for the layers be $\tilde{\mathbb{C}}_1, \dots, \tilde{\mathbb{C}}_L$, and let the overall codeword be denoted

$$\tilde{c} = \tilde{c}_1 + \dots + \tilde{c}_L, \tag{88}$$

where the $\tilde{c}_l \in \tilde{\mathbb{C}}_l$ are independently selected codewords drawn for each layer. The overall code rate is the sum of the rates of the individual codes. The overall power of the code is $P = p_1 + \cdots + p_L$.

From the mutual information decomposition

$$I(\tilde{c}; y) = \sum_{l=1}^{L} I_l \tag{89}$$

where

$$I_l = I(\tilde{c}_l; \tilde{c}_1 + \cdots + \tilde{c}_L + z \mid \tilde{c}_{l+1}^L),$$

with $\tilde{c}_{l+1}^L = (\tilde{c}_{l+1}, \tilde{c}_{l+2}, \dots, \tilde{c}_L)$, we see that the overall codebook power constraint P can be met by apportioning power to layers in any way desired, so long as $p_1 + \dots + p_L = P$. Since the undecoded layers are treated as noise, the maximum codebook rate for the lth layer is then

$$I_l = \log(1 + \text{SNR}_l) \tag{90}$$

where

$$SNR_{l} = \frac{|\alpha|^{2} p_{l}}{|\alpha|^{2} p_{1} + |\alpha|^{2} p_{2} + \dots + |\alpha|^{2} p_{l-1} + \sigma^{2}}$$
(91)

is the effective SNR when decoding the lth layer. Straightforward algebra, which amounts to a special-case recalculation of (89), confirms that $I_1 + \cdots + I_L = \log(1 + |\alpha|^2 P/\sigma^2)$ for any selection of powers $\{p_l\}$.

Alternatively, instead of selecting per-layer powers and computing corresponding rates, one can select per-layer rates and compute the corresponding powers. The rates $\{I_l\}$ for each level may be set in any way desired so long as the total rate $I_1 + \cdots + I_L$ does not exceed the channel capacity $\log(1 + |\alpha|^2 P/\sigma^2)$. The required powers $\{p_l\}$ may then be found using (90) and (91) recursively for $l = 1, \ldots, L$. There is no need to verify the power constraint: it follows from (89) that the powers computed in this way sum to P. Thus it remains only to check that the $\{p_l\}$ are all nonnegative to ensure that the rate allocation is a valid one.

We now apply this insight to our rateless context. The target ceiling rate for our rateless code is R, and, as before, α_m , $m=1,2,\ldots$, denotes the threshold channel gains as obtained via (31).

Comparing (49) with (90) and (91) reveals that (49) can be rewritten as

$$R_{l} = \sum_{m'=1}^{m} I_{m',l}(\alpha_{m}), \tag{92}$$

for all $l = 1, 2, \dots, L$ and $m = 1, 2, \dots$, where

$$R_l = R/L \tag{93}$$

and $I_{m',l}(\alpha_m)$ is the mutual information in layer l from block m' when the realized channel gain is α_m . Thus, meeting (49) is equivalent to finding powers $p_{m',l}$ for each code block m' and layer l so that for the given rate allocation R_l (a) the powers are nonnegative, (b) the power constraint is met, and (c) when the channel gain is α_m , the mutual information accumulated at the lth layer after receiving code blocks $1, 2, \ldots, m$ equals R_l .

Since the power constraint is automatically satisfied by any assignment of powers that achieves the target rates, it suffices to establish that (92) have a solution with nonnegative perlayer powers.

The solution exists and is unique, as can be established by induction on m. Specifically, for m=1 the rateless code is an ordinary layered code and the powers $p_{1,1}, \ldots, p_{1,L}$ may be computed recursively from [cf. (92)]

$$R_{l} = \sum_{m'=1}^{m} \log(1 + SNR_{m',l}(\alpha_{m})), \tag{94}$$

with $SNR_{m,l}(\alpha_m)$ as given in (48) for l = 1, ..., L.

For the induction hypothesis, assume we have a power assignment for the first m blocks that satisfies (94). To find the power assignment for the (m+1)st block, observe that when the channel gain decreases from α_m to α_{m+1} the per-layer mutual information of every block decreases. A nonnegative power must be assigned to every layer in the (m+1)st code block to compensate for the shortfall.

The mutual information shortfall in the lth layer is

$$\Delta_{m+1,l} = R_l - \sum_{m'=1}^{m} \log(1 + SNR_{m',l}(\alpha_{m+1})),$$
 (95)

and the power $p_{m+1,l}$ needed to make up for this shortfall is the solution to

$$\Delta_{m+1,l} = \log(1 + SNR_{m+1,l}(\alpha_{m+1})), \tag{96}$$

viz.,

$$p_{m+1,l} = (2^{2\Delta_{m+1,l}} - 1)$$

$$\cdot (p_{m+1,1} + \dots + p_{m+1,l-1} + \frac{\sigma_{m+1}^2}{|\alpha_{m+1}|^2}). \quad (97)$$

This completes the induction. Perhaps counter to intuition, even if the per-layer rates R_1, \ldots, R_L are set equal, the perlayer shortfalls $\Delta_{m+1,1}, \ldots, \Delta_{m+1,L}$ will not be equal. Thus, within a layer the effective SNR and mutual information will vary from block to block.

Eqs. (95) and (97) are easily evaluated numerically. An example is given in Table III.⁶

⁶If one were aiming to use a rateless code of the type described in Section VI in practice, in calculating a power allocation one should take into account the gap to capacity of the particular base code being used. This optimization is developed in [21].

TABLE III

Per-layer power assignments $p_{m,l}$ and channel gain thresholds α_m for the initial blocks of an L=4 layer rateless code with total power P=255, noise variance $\sigma^2=1$, and per-layer rate R/L=1 B/s/Hz.

	m = 1	m = 2	m = 3	m = 4	m = 5
gain (dB)	0.00	-12.30	-16.78	-19.29	-20.99
l = 1	3.00	40.80	48.98	55.77	58.79
l=2	12.00	86.70	61.21	60.58	61.65
l = 3	48.00	86.70	81.32	71.48	67.50
l=4	192.00	40.80	63.48	67.16	67.06

Finally, since this result holds regardless of the choice of the constituent R_l , it will hold for the particular choice (93), whence (49).

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