# Secure Outsourced Matrix Computation and Application to Neural Networks^ 

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#### Abstract

Homomorphic Encryption (HE) is a powerful cryptographic primitive to address privacy and security issues in outsourcing computation on sensitive data to an untrusted computation environment. Comparing to secure Multi-Party Computation (MPC), HE has advantages in supporting non-interactive operations and saving on communication costs. However, it has not come up with an optimal solution for modern learning frameworks, partially due to a lack of efficient matrix computation mechanisms. In this work, we present a practical solution to encrypt a matrix homomorphically and perform arithmetic operations on encrypted matrices. Our solution includes a novel matrix encoding method and an efficient evaluation strategy for basic matrix operations such as addition, multiplication, and transposition. We also explain how to encrypt more than one matrix in a single ciphertext, yielding better amortized performance. Our solution is generic in the sense that it can be applied to most of the existing HE schemes. It also achieves reasonable performance for practical use; for example, our implementation takes 0.6 seconds to multiply two encrypted square matrices of order 64 and 0.09 seconds to transpose a square matrix of order 64 . Our secure matrix computation mechanism has a wide applicability to our new framework E2DM, which stands for encrypted data and encrypted model. To the best of our knowledge, this is the first work that supports secure evaluation of the prediction phase based on both encrypted data and encrypted model, whereas previous work only supported applying a plain model to encrypted data. As a benchmark, we report an experimental result to classify handwritten images using convolutional neural networks (CNN). Our implementation on the MNIST dataset takes 1.69 seconds to compute ten likelihoods of 64 input images simultaneously, yielding an amortized rate of 26 milliseconds per image.


Keywords. Homomorphic encryption; matrix computation; machine learning; neural networks

## 1 Introduction

Homomorphic Encryption (HE) is an encryption scheme that allows for operations on encrypted inputs so that the decrypted result matches the outcome for the corresponding operations on the plaintext. This property makes it very attractive for secure outsourcing tasks, including financial model evaluation and genetic testing, which can ensure the privacy and security of data communication, storage, and computation [3, 46]. In biomedicine, it is extremely attractive due to the privacy concerns about patients' sensitive data [27, 47]. Recently deep neural network based models have been demonstrated to achieve great success in a number of health care applications [36], and a natural question is whether we can outsource such learned models to a third party and evaluate new samples in a secure manner?

There are several different scenarios depending on who owns the data and who provides the model. Assuming a few different roles including data owners (e.g. hospital, institution or individuals), cloud computing service providers (e.g. Amazon, Google, or Microsoft), and machine learning model providers

[^0](e.g. researchers and companies), we can imagine the following situations: (1) the data owner trains a model and makes it available on a computing service provider to be used to make predictions on encrypted inputs from other data owners; (2) model providers encrypt their trained classifier models and upload them to a cloud service provider to make predictions on encrypted inputs from various data owners; and (3) a cloud service provider trains a model on encrypted data from some data owners and uses the encrypted trained model to make predictions on new encrypted inputs. The first scenario has been previously studied in CryptoNets [23] and subsequent follow-up work [10, 7]. The second scenario was considered by Makri et al. [35] based on Multi-Party Computation (MPC) using polynomial kernel support vector machine classification. However, the second and third scenarios with an HE system have not been studied yet. In particular, classification tasks for these scenarios rely heavily on efficiency of secure matrix computation on encrypted inputs.

### 1.1 Our Contribution

In this paper, we introduce a generic method to perform arithmetic operations on encrypted matrices using an HE system. Our solution requires $O(d)$ homomorphic operations to compute a product of two encrypted matrices of size $d \times d$, compared to $O\left(d^{2}\right)$ of the previous best method. We extend basic matrix arithmetic to some advanced operations: transposition and rectangular matrix multiplication. We also describe how to encrypt multiple matrices in a single ciphertext, yielding a better amortized performance per matrix.

We apply our matrix computation mechanism to a new framework E2DM, which takes encrypted data and encrypted machine learning model to make predictions securely. This is the first HE-based solution that can be applied to the prediction phase of the second and third scenarios described above. As a benchmark of this framework, we implemented an evaluation of convolutional neural networks (CNN) model on the MNIST dataset [33] to compute ten likelihoods of handwritten images.

### 1.2 Technical Details

After Gentry's first construction of a fully HE scheme [21], there have been several attempts to improve efficiency and flexibility of HE systems. For example, the ciphertext packing technique allows multiple values to be encrypted in a single ciphertext, thereby performing parallel computation on encrypted vectors in a Single Instruction Multiple Data (SIMD) manner. In the current literature, most of practical HE schemes $[9,8,18,13]$ support their own packing methods to achieve better amortized complexity of homomorphic operations. Besides component-wise addition and multiplication on plaintext vectors, these schemes provide additional functionalities such as scalar multiplication and slot rotation. In particular, permutations on plaintext slots enable us to interact with values located in different plaintext slots.

A naive solution for secure multiplication between two matrices of size $d \times d$ is to use $d^{2}$ distinct ciphertexts to represent each input matrix (one ciphertext per one matrix entry) and apply pure SIMD

$$
\begin{aligned}
& \begin{array}{|l|l|l|}
\hline a_{0} & a_{1} & a_{2} \\
\hline a_{3} & a_{4} & a_{5} \\
\hline a_{6} & a_{7} & a_{8} \\
\hline
\end{array} \cdot \begin{array}{|l|l|l|}
\hline b_{0} & b_{1} & b_{2} \\
\hline b_{3} & b_{4} & b_{5} \\
\hline b_{6} & b_{7} & b_{8} \\
\hline
\end{array}=\begin{array}{|l|l|l|}
\hline a_{0} & a_{1} & a_{2} \\
\hline a_{4} & a_{5} & a_{3} \\
\hline a_{8} & a_{6} & a_{7} \\
\hline
\end{array} \odot \begin{array}{|l|l|l|}
\hline b_{0} & b_{4} & b_{8} \\
\hline b_{3} & b_{7} & b_{2} \\
\hline b_{6} & b_{1} & b_{5} \\
\hline
\end{array}+ \\
& \begin{array}{|l|l|l|}
\hline a_{1} & a_{2} & a_{0} \\
\hline a_{5} & a_{3} & a_{4} \\
\hline a_{6} & a_{7} & a_{8} \\
\hline
\end{array} \odot \begin{array}{|l|l|l|}
\hline b_{3} & b_{7} & b_{2} \\
\hline b_{6} & b_{1} & b_{5} \\
\hline b_{0} & b_{4} & b_{8} \\
\hline
\end{array}+\begin{array}{|l|l|l|}
\hline a_{2} & a_{0} & a_{1} \\
\hline a_{3} & a_{4} & a_{5} \\
\hline a_{7} & a_{8} & a_{6} \\
\hline
\end{array} \odot \begin{array}{|l|l|l|}
\hline b_{6} & b_{1} & b_{5} \\
\hline b_{0} & b_{4} & b_{8} \\
\hline b_{3} & b_{7} & b_{2} \\
\hline
\end{array}
\end{aligned}
$$

Fig. 1: Our matrix multiplication algorithm with $d=3$.
operations (addition and multiplication) on encrypted vectors. This method consumes one level for homomorphic multiplication, but it takes $O\left(d^{3}\right)$ multiplications. Another approach is to consider a matrix multiplication as a series of matrix-vector multiplications. Halevi and Shoup [24] introduced a matrix encoding method based on its diagonal decomposition, putting the matrix in diagonal order and mapping each of them to a single ciphertext. So it requires $d$ ciphertexts to represent the matrix and the matrixvector multiplication can be computed using $O(d)$ rotations and multiplications. Therefore, the matrix multiplication takes $O\left(d^{2}\right)$ complexity and has a depth of a single multiplication.

We propose an efficient method to perform matrix operations by combining HE-friendly operations on packed ciphertexts such as SIMD arithmetics, scalar multiplication, and slot rotation. We first define a simple encoding map that identifies an arbitrary matrix of size $d \times d$ with a vector of dimension $n=d^{2}$ having the same entries. Let $\odot$ denote the component-wise product between matrices. Then matrix multiplication can be expressed as $A \cdot B=\sum_{i=0}^{d-1} A_{i} \odot B_{i}$ for some matrices $A_{i}$ (resp. $B_{i}$ ) obtained from $A$ (resp. $B$ ) by taking specific permutations. Figure 1 describes this equality for the case of $d=3$. We remark that the initial matrix $A_{0}$ (resp. $B_{0}$ ) can be computed with $O(d)$ rotations, and that for any $1 \leq i<d$ the permuted matrix $A_{i}$ (resp. $B_{i}$ ) can be obtained by $O(1)$ rotations from the initial matrix. Thus the total computational complexity is bounded by $O(d)$ rotations and multiplications. We refer to Table 1 for comparison of our method with prior work in terms of the number of input ciphertexts for a single matrix, complexity, and the required depth for implementation. We denote homomorphic multiplication and constant multiplication by Mult and CMult, respectively.

Table 1: Comparison of secure $d$-dimensional matrix multiplication algorithms

| Methodology | Number of <br> ciphertexts | Complexity | Required depth |
| :---: | :---: | :---: | :---: |
| Naive method | $d^{2}$ | $O\left(d^{3}\right)$ | 1 Mult |
| Halevi-Shoup [26] | $d$ | $O\left(d^{2}\right)$ | 1 Mult |
| Ours | 1 | $O(d)$ | 1 Mult +2 CMult |

Our basic solution is based on the assumption that a ciphertext can encrypt $d^{2}$ plaintext slots, but it can be extended to support matrix computation of an arbitrary size. When a ciphertext has more than $d^{2}$ plaintext slots, for example, we can encrypt multiple matrices in a single ciphertext and carry out matrix operations in parallel. On the other hand, if a matrix is too large to be encoded into one ciphertext, one can partition it into several sub-matrices and encrypt them individually. An arithmetic operation over large matrices can be expressed using block-wise operations, and the computation on the sub-matrices can be securely done using our basic matrix algorithms. We use this approach to evaluate an encrypted neural networks model on encrypted data.

Our implementation is based on an HE scheme of Cheon et al. [13], which is optimized in computation over the real numbers. For example, it took 0.6 seconds to securely compute the product of two matrices of size $64 \times 64$ and 0.09 seconds to transpose a single matrix of size $64 \times 64$. For the evaluation of an encrypted CNN model, we adapted a similar network topology to CryptoNets: one convolution layer and two fully connected (FC) layers with square activation function. This model is obtained from the keras library [14] by training 60,000 images of the MNIST dataset, and used for the classification of handwriting images of size $28 \times 28$. It took 1.69 seconds to compute ten likelihoods of encrypted 64 input images using the encrypted CNN model, yielding an amortized rate of 26 milliseconds per image. This model achieves a prediction accuracy of $98.1 \%$ on the test set.

## 2 Preliminaries

The binary logarithm will be simply denoted by $\log (\cdot)$. We denote vectors in bold, e.g. a, and every vector in this paper is a row vector. For a $d_{1} \times d$ matrix $A_{1}$ and a $d_{2} \times d$ matrix $A_{2},\left(A_{1} ; A_{2}\right)$ denotes the
$\left(d_{1}+d_{2}\right) \times d$ matrix obtained by concatenating two matrices in a vertical direction. If two matrices $A_{1}$ and $A_{2}$ have the same number of rows, $\left(A_{1} \mid A_{2}\right)$ denotes a matrix formed by horizontal concatenation. We let $\lambda$ denote the security parameter throughout the paper: all known valid attacks against the cryptographic scheme under scope should take $\Omega\left(2^{\lambda}\right)$ bit operations.

### 2.1 Homomorphic Encryption

HE is a cryptographic primitive that allows us to compute on encrypted data without decryption and generate an encrypted result which matches that of operations on plaintext [ $9,18,6,13$ ]. So it enables us to securely outsource computation to a public cloud. This technology has great potentials in many real-world applications such as statistical testing, machine learning, and neural networks [40, 29, 23, 31].

Let $\mathcal{M}$ and $\mathcal{C}$ denote the spaces of plaintexts and ciphertexts, respectively. An HE scheme $\Pi=$ (KeyGen, Enc, Dec, Eval) is a quadruple of algorithms that proceeds as follows:

- KeyGen $\left(1^{\lambda}\right)$. Given the security parameter $\lambda$, this algorithm outputs a public key pk, a public evaluation key evk and a secret key sk.
- $\operatorname{Enc}_{\mathrm{pk}}(\boldsymbol{m})$. Using the public key pk, the encryption algorithm encrypts a message $\boldsymbol{m} \in \mathcal{M}$ into a ciphertext ct $\in \mathcal{C}$.
- $\operatorname{Dec}_{\text {sk }}(\mathrm{ct})$. For the secret key sk and a ciphertext ct, the decryption algorithm returns a message $\boldsymbol{m} \in \mathcal{M}$.
- $\operatorname{Eval}_{\text {evk }}\left(f ; \mathrm{ct}_{1}, \ldots, \mathrm{ct}_{k}\right)$. Using the evaluation key evk, for a circuit $f: \mathcal{M}^{k} \rightarrow \mathcal{M}$ and a tuple of ciphertexts $\left(\mathrm{ct}_{1}, \ldots, \mathrm{ct}_{k}\right)$, the evaluation algorithm outputs a ciphertext $\mathrm{ct}^{\prime} \in \mathcal{C}$.

An HE scheme $\Pi$ is called correct if the following statements are satisfied with an overwhelming probability:

- $\operatorname{Dec}_{\text {sk }}(\mathrm{ct})=\boldsymbol{m}$ for any $\boldsymbol{m} \in \mathcal{M}$ and ct $\leftarrow E n \mathrm{Enk}_{\mathrm{pk}}(\boldsymbol{m})$.
- $\operatorname{Dec}_{\text {sk }}\left(\mathrm{ct}^{\prime}\right)=f\left(\boldsymbol{m}_{1}, \ldots, \boldsymbol{m}_{k}\right)$ with an overwhelming probability if $\mathrm{ct}^{\prime} \leftarrow \mathrm{Eval}_{\mathrm{evk}}\left(f ; \mathrm{ct}_{1}, \ldots, \mathrm{ct}_{k}\right)$ for an arithmetic circuit $f: \mathcal{M}^{k} \rightarrow \mathcal{M}$ and for some ciphertexts $\mathrm{ct}_{1}, \ldots, \mathrm{ct}_{k} \in \mathcal{C}$ such that $\operatorname{Dec}_{\text {sk }}\left(\mathrm{ct}_{i}\right)=\boldsymbol{m}_{i}$.
An HE system can securely evaluate an arithmetic circuit $f$ consisting of addition and multiplication gates. Throughout this paper, we denote by $\operatorname{Add}\left(\mathrm{ct}_{1}, \mathrm{ct}_{2}\right)$ and $\operatorname{Mult} \mathrm{t}_{\mathrm{evk}}\left(\mathrm{ct}_{1}, \mathrm{ct}_{2}\right)$ the homomorphic addition and multiplication between two ciphertexts $\mathrm{ct}_{1}$ and $\mathrm{ct}_{2}$, respectively. In addition, we let CMult $\mathrm{evk}^{(\mathrm{ct} ; \boldsymbol{u})}$ denote the multiplication of ct with a scalar $\boldsymbol{u} \in \mathcal{M}$. For simplicity, we will omit the subscript of the algorithms when it is clear from the context.


### 2.2 Ciphertext Packing Technique

Ciphertext packing technique allows us to encrypt multiple values into a single ciphertext and perform computation in a SIMD manner. After Smart and Vercauteren [45] first introduced a packing technique based on polynomial-CRT, it has been one of the most important features of HE systems. This method represents a native plaintext space $\mathcal{M}$ as a set of $n$-dimensional vectors in $\mathcal{R}^{n}$ over a ring $\mathcal{R}$ using appropriate encoding/decoding methods (each factor is called a plaintext slot). One can encode and encrypt an element of $\mathcal{R}^{n}$ into a ciphertext, and perform component-wise arithmetic operations over the plaintext slots at once. It enables us to reduce the expansion rate and parallelize the computation, thus achieving better performance in terms of amortized space and time complexity.

However, the ciphertext packing technique has a limitation that it is not easy to handle a circuit with some inputs in different plaintext slots. To overcome this problem, there have been proposed some methods to move data in the slots over encryption. For example, some HE schemes [22, 13] based on the ring learning with errors (RLWE) assumption exploit the structure of Galois group to implement the rotation operation on plaintext slots. That is, such HE schemes include the rotation algorithm, denoted by $\operatorname{Rot}(\mathrm{ct} ; \ell)$, which transforms an encryption ct of $\boldsymbol{m}=\left(m_{0}, \ldots, m_{n-1}\right) \in \mathcal{M}=\mathcal{R}^{n}$ into an encryption of $\rho(\boldsymbol{m} ; \ell):=\left(m_{\ell}, \ldots, m_{n-1}, m_{0}, \ldots, m_{\ell-1}\right)$. Note that $\ell$ can be either positive or negative, and a rotation by $(-\ell)$ is the same as a rotation by $(n-\ell)$.

### 2.3 Linear Transformations

Halevi and Shoup [24] introduced a method to evaluate an arbitrary linear transformation on encrypted vectors. In general, an arbitrary linear transformation $L: \mathcal{R}^{n} \rightarrow \mathcal{R}^{n}$ over plaintext vectors can be represented as $L: \boldsymbol{m} \mapsto U \cdot \boldsymbol{m}$ for some matrix $U \in \mathcal{R}^{n \times n}$. We can express the matrix-vector multiplication by combining rotation and constant multiplication operations.

Specifically, for $0 \leq \ell<n$, we define the $\ell$-th diagonal vector of $U$ by $\boldsymbol{u}_{\ell}=\left(U_{0, \ell}, U_{1, \ell+1}, \ldots\right.$, $\left.U_{n-\ell-1, n-1}, U_{n-\ell, 0}, \ldots, U_{n-1, \ell-1}\right) \in \mathcal{R}^{n}$. Then we have

$$
\begin{equation*}
U \cdot \boldsymbol{m}=\sum_{0 \leq \ell<n}\left(\boldsymbol{u}_{\ell} \odot \rho(\boldsymbol{m} ; \ell)\right) \tag{1}
\end{equation*}
$$

where $\odot$ denotes the component-wise multiplication between vectors. Given a matrix $U \in \mathcal{R}^{n \times n}$ and an encryption ct of the vector $\boldsymbol{m}$, Algorithm 1 describes how to compute a ciphertext of the desired vector $U \cdot \boldsymbol{m}$.

```
Algorithm 1 Homomorphic linear transformation
    procedure LinTrans(ct; \(U\) )
    \(\mathrm{ct}^{\prime} \leftarrow \operatorname{CMult}\left(\mathrm{ct} ; \boldsymbol{u}_{0}\right)\)
    for \(\ell=1\) to \(n-1\) do
        \(\mathrm{ct}^{\prime} \leftarrow \operatorname{Add}\left(\mathrm{ct}^{\prime}, \operatorname{CMult}\left(\operatorname{Rot}(\mathrm{ct} ; \ell) ; \boldsymbol{u}_{\ell}\right)\right)\)
    end for
    return \(\mathrm{ct}^{\prime}\)
```

As shown in Algorithm 1, the computational cost of matrix-vector multiplication is about $n$ additions, constant multiplications, and rotations. Note that rotation operation needs to perform a key-switching operation and thus is comparably expensive than the other two operations. So we can say that the complexity is asymptotically $O(n)$ rotations. It can be reduced when the number of nonzero diagonal vectors of $U$ is relatively small.

## 3 Secure Matrix Multiplication via Homomorphic Encryption

In this section, we propose a simple encoding method to convert a matrix into a plaintext vector in a SIMD environment. Based on this encoding method, we devise an efficient algorithm to carry out basic matrix operations over encryption.

### 3.1 Permutations for Matrix Multiplication

We propose an HE-friendly expression of the matrix multiplication operation. For a $d \times d$ square matrix $A=\left(A_{i, j}\right)_{0 \leq i, j<d}$, we first define useful permutations $\sigma, \tau, \phi$, and $\psi$ on the set $\mathcal{R}^{d \times d}$. For simplicity, we identify $\mathbb{Z} \cap[0, d)$ as a representative of $\mathbb{Z}_{d}$ and write $[i]_{d}$ to denote the reduction of an integer $i$ modulo $d$ into that interval. All the indexes will be considered as integers modulo $d$.

- $\sigma(A)_{i, j}=A_{i, i+j}$.
- $\tau(A)_{i, j}=A_{i+j, j}$.
- $\phi(A)_{i, j}=A_{i, j+1}$.
- $\psi(A)_{i, j}=A_{i+1, j}$.

Note that $\phi$ and $\psi$ represent the column and row shifting functions, respectively. Then for two square matrices $A$ and $B$ of order $d$, we can express their matrix product $A B$ as follows:

$$
\begin{equation*}
A \cdot B=\sum_{k=0}^{d-1}\left(\phi^{k} \circ \sigma(A)\right) \odot\left(\psi^{k} \circ \tau(B)\right), \tag{2}
\end{equation*}
$$

where $\odot$ denotes the component-wise multiplication between matrices. The correctness is shown in the following equality by computing the matrix component of the index $(i, j)$ :

$$
\begin{aligned}
\sum_{k=0}^{d-1}\left(\phi^{k} \circ \sigma(A)\right)_{i, j} \cdot\left(\psi^{k} \circ \tau(B)\right)_{i, j} & =\sum_{k=0}^{d-1} \sigma(A)_{i, j+k} \cdot \tau(B)_{i+k, j} \\
& =\sum_{k=0}^{d-1} A_{i, i+j+k} \cdot B_{i+j+k, j} \\
& =\sum_{k=0}^{d-1} A_{i, k} \cdot B_{k, j} \\
& =(A \cdot B)_{i, j}
\end{aligned}
$$

Since Equation (2) consists of permutations on matrix entries and the Hadamard multiplication operations, we can efficiently evaluate it using an HE system with ciphertext packing method.

### 3.2 Matrix Encoding Method

We propose a row ordering encoding map to transform a vector of dimension $n=d^{2}$ into a matrix in $\mathcal{R}^{d \times d}$. For a vector $\boldsymbol{a}=\left(a_{k}\right)_{0 \leq k<n}$, we define the encoding map $\iota: \mathcal{R}^{n} \rightarrow \mathcal{R}^{d \times d}$ by

$$
\iota: \boldsymbol{a} \mapsto A=\left(a_{d \cdot i+j}\right)_{0 \leq i, j<d}
$$

i.e., $\boldsymbol{a}$ is the concatenation of row vectors of $A$. It is clear that $\iota(\cdot)$ is an isomorphism between additive groups, which implies that matrix addition can be securely computed using homomorphic addition in a SIMD manner. In addition, one can perform multiplication by scalars by adapting a constant multiplication of an HE scheme. Throughout this paper, we identify two spaces $\mathcal{R}^{n}$ and $\mathcal{R}^{d \times d}$ with respect to the $\iota(\cdot)$, so a ciphertext will be called an encryption of $A$ if it encrypts the plaintext vector $\boldsymbol{a}=\iota^{-1}(A)$.

### 3.3 Matrix Multiplication on Packed Ciphertexts

An arbitrary permutation operation on $\mathcal{R}^{d \times d}$ can be understood as a linear transformation $L: \mathcal{R}^{n} \rightarrow \mathcal{R}^{n}$ such that $n=d^{2}$. In general, its matrix representation $U \in\{0,1\}^{n \times n} \subseteq \mathcal{R}^{n \times n}$ has $n$ number of nonzero diagonal vectors. So if we directly evaluate the permutations $A \mapsto \phi^{\bar{k}} \circ \sigma(A)$ and $B \mapsto \psi^{k} \circ \tau(B)$ for $1 \leq k<d$, each of them requires $O\left(d^{2}\right)$ homomorphic operations and thus the total complexity is $O\left(d^{3}\right)$. We provide an efficient algorithm to perform the matrix multiplication on packed ciphertexts by combining Equation (2) and our matrix encoding map.

### 3.3.1 Tweaks of Permutations

We focus on the following four permutations $\sigma, \tau, \phi$, and $\psi$ described above. We let $U^{\sigma}, U^{\tau}, V$, and $W$ denote the matrix representations corresponding to these permutations, respectively. Firstly, the matrix representations $U^{\sigma}$ and $U^{\tau}$ of $\sigma$ and $\tau$ are expressed as follows:

$$
\begin{aligned}
& U_{d \cdot i+j, \ell}^{\sigma}= \begin{cases}1 & \text { if } \ell=d \cdot i+[i+j]_{d} \\
0 & \text { otherwise }\end{cases} \\
& U_{d \cdot i+j, \ell}^{\tau}= \begin{cases}1 & \text { if } \ell=d \cdot[i+j]_{d}+j \\
0 & \text { otherwise }\end{cases}
\end{aligned}
$$

for $0 \leq i, j<d$ and $0 \leq \ell<d^{2}$. Similarly, for $1 \leq k<d$, the matrix representations of $\phi^{k}$ and $\psi^{k}$ can be computed as follows:

$$
\begin{aligned}
V_{d \cdot i+j, \ell}^{k} & = \begin{cases}1 & \text { if } \ell=d \cdot i+[j+k]_{d} \\
0 & \text { otherwise }\end{cases} \\
W_{d \cdot i+j, \ell}^{k} & = \begin{cases}1 & \text { if } \ell=d \cdot[i+k]_{d}+j \\
0 & \text { otherwise },\end{cases}
\end{aligned}
$$

for $0 \leq i, j<d$ and $0 \leq \ell<d^{2}$.
As described in Equation (1), we employ the diagonal decomposition of the matrix representations for multiplications with encrypted vectors. Let us count the number of diagonal vectors to estimate the complexity. We use the same notation $\boldsymbol{u}_{\ell}$ to write the $\ell$-th diagonal vector of a matrix $U$. For simplicity, we identify $\boldsymbol{u}_{d^{2}-\ell}$ with $\boldsymbol{u}_{-\ell}$. The matrix $U^{\sigma}$ has exactly $(2 d-1)$ number of nonzero diagonal vectors, denoted by $\boldsymbol{u}_{k}^{\sigma}$ for $k \in \mathbb{Z} \cap(-d, d)$. The $\ell$-th diagonal vector of $U^{\tau}$ is nonzero if and only if $\ell$ is divisible by the integer $d$, so $U^{\tau}$ has $d$ nonzero diagonal vectors. For any $1 \leq k<d$, the matrix $V^{k}$ has two nonzero diagonal vectors $\boldsymbol{v}_{k}$ and $\boldsymbol{v}_{k-d}$. Similarly, the matrix $W^{k}$ has the only nonzero diagonal vector $\boldsymbol{w}_{d \cdot k}$. Therefore, homomorphic evaluations of the permutations $\sigma$ and $\tau$ require $O(d)$ rotations while it takes $O(1)$ rotations to compute $\psi^{k}$ or $\phi^{k}$ for any $1 \leq k<d$.

### 3.3.2 Homomorphic Matrix Multiplication

Suppose that we are given two ciphertexts ct. $A$ and ct. $B$ that encrypt matrices $A$ and $B$ of size $d \times$ $d$, respectively. In the following, we describe an efficient evaluation strategy for homomorphic matrix multiplication.
Step 1-1: This step performs the linear transformation $U^{\sigma}$ on the input ciphertext ct. $A$. As mentioned above, the matrix $U^{\sigma}$ is a sparse matrix with $(2 d-1)$ number of nonzero diagonal vectors $\boldsymbol{u}_{k}^{\sigma}$ for $k \in \mathbb{Z} \cap(-d, d)$, so we can represent the linear transformation as

$$
\begin{equation*}
U^{\sigma} \cdot \boldsymbol{a}=\sum_{-d<k<d}\left(\boldsymbol{u}_{k}^{\sigma} \odot \rho(\boldsymbol{a} ; k)\right) \tag{3}
\end{equation*}
$$

where $\boldsymbol{a}=\iota^{-1}(A) \in \mathcal{R}^{n}$ is the vector representation of $A$. If $k \geq 0$, the $k$-th diagonal vector is computed by

$$
\boldsymbol{u}_{k}^{\sigma}[\ell]= \begin{cases}1 & \text { if } 0 \leq \ell-d \cdot k<(d-k) \\ 0 & \text { otherwise }\end{cases}
$$

where $\boldsymbol{u}_{k}^{\sigma}[\ell]$ denotes the $\ell$-th component of $\boldsymbol{u}_{k}^{\sigma}$. In the other cases $k<0$, it is computed by

$$
\boldsymbol{u}_{k}^{\sigma}[\ell]= \begin{cases}1 & \text { if }-k \leq \ell-(d+k) \cdot d<d \\ 0 & \text { otherwise }\end{cases}
$$

Then Equation (3) can be securely computed as

$$
\sum_{-d<k<d} \operatorname{CMult}\left(\operatorname{Rot}(\operatorname{ct} . A ; k) ; \boldsymbol{u}_{k}^{\sigma}\right)
$$

resulting the encryption of the plaintext vector $U^{\sigma} \cdot \boldsymbol{a}$, denoted by ct. $A^{(0)}$. Thus, the computational cost is about $2 d$ additions, constant multiplications, and rotations.
Step 1-2: This step is to evaluate the linear transformation $U^{\tau}$ on the input ciphertext ct. $B$. As described above, the matrix $U^{\tau}$ has $d$ nonzero diagonal vectors so we can express this matrix-vector multiplication as

$$
\begin{equation*}
U^{\tau} \cdot \boldsymbol{b}=\sum_{0 \leq k<d}\left(\boldsymbol{u}_{d \cdot k}^{\tau} \odot \rho(\boldsymbol{b} ; d \cdot k)\right), \tag{4}
\end{equation*}
$$

where $\boldsymbol{b}=\iota^{-1}(B)$ and $\boldsymbol{u}_{d \cdot k}^{\tau}$ is the $(d \cdot k)$-th diagonal vector of the matrix $U^{\tau}$. We note that for any $0 \leq k<d$, the vector $\boldsymbol{u}_{d \cdot k}^{\tau}$ contains one in the $(k+d \cdot i)$-th component for $0 \leq i<d$ and zeros in all the other entries. Then Equation (4) can be securely computed as

$$
\sum_{0 \leq k<d} \operatorname{CMult}\left(\operatorname{Rot}(\operatorname{ct.} B ; d \cdot k) ; \boldsymbol{u}_{d \cdot k}^{\tau}\right),
$$

resulting the encryption of the plaintext vector $U^{\tau} \cdot \boldsymbol{b}$, denoted by ct. $B^{(0)}$. The complexity of this procedure is roughly half of the Step 1-1: $d$ additions, constant multiplications, and rotations.

Step 2: This step securely computes the column and row shifting operations of $\sigma(A)$ and $\tau(B)$, respectively. For $1 \leq k<d$, the column shifting matrix $V^{k}$ has two nonzero diagonal vectors $\boldsymbol{v}_{k}$ and $\boldsymbol{v}_{k-d}$ that are computed by

$$
\begin{gathered}
\boldsymbol{v}_{k}[\ell]= \begin{cases}1 & \text { if } 0 \leq[\ell]_{d}<(d-k) \\
0 & \text { otherwise }\end{cases} \\
\boldsymbol{v}_{k-d}[\ell]= \begin{cases}1 & \text { if }(d-k) \leq[\ell]_{d}<d \\
0 & \text { otherwise }\end{cases}
\end{gathered}
$$

Then we get an encryption ct. $A^{(k)}$ of the matrix $\phi^{k} \circ \sigma(A)$ by adding two ciphertexts CMult(Rot $\left.\left(\operatorname{ct} . A^{(0)} ; k\right) ; \boldsymbol{v}_{k}\right)$ and $\operatorname{CMult}\left(\operatorname{Rot}\left(\operatorname{ct} . A^{(0)} ; k-d\right) ; \boldsymbol{v}_{k-d}\right)$. In the case of the row shifting permutation, the corresponding matrix $W^{k}$ has exactly one nonzero diagonal vector $\boldsymbol{w}_{d \cdot k}$ whose entries are all one. Thus we can obtain an encryption of the matrix $\psi^{k} \circ \tau(B)$ by computing ct. $B^{(k)} \leftarrow \operatorname{Rot}\left(\mathrm{ct} . B^{(0)} ; d \cdot k\right)$. The computational cost of this procedure is about $d$ additions, $2 d$ constant multiplications, and $3 d$ rotations.
Step 3: This step computes the Hadamard multiplication between the ciphertexts ct. $A^{(k)}$ and $\mathrm{ct} . B^{(k)}$ for $0 \leq k<d$, and finally aggregates all the resulting ciphertexts. As a result, we get an encryption ct. $A B$ of the matrix $A B$. The running time of this step is $d$ homomorphic multiplications and additions.

In summary, we can perform the homomorphic matrix multiplication operation as described in Algorithm 2.

```
Algorithm 2 Homomorphic matrix multiplication
    procedure HE-MatMult(ct. \(A, \mathrm{ct} . B\) )
    [Step 1-1]
    ct. \(A^{(0)} \leftarrow \operatorname{LinTrans}\left(c t . A ; U^{\sigma}\right)\)
    [Step 1-2]
    \(\mathrm{ct} . B^{(0)} \leftarrow \operatorname{LinTrans}\left(\mathrm{ct} . B ; U^{\tau}\right)\)
    [Step 2]
    for \(k=1\) to \(d-1\) do
        \(\mathrm{ct}. A^{(k)} \leftarrow\) LinTrans \(\left(\mathrm{ct} . A^{(0)} ; V^{k}\right)\)
        ct. \(B^{(k)} \leftarrow \operatorname{LinTrans}\left(\mathrm{ct} . B^{(0)} ; W^{k}\right)\)
    end for
    [Step 3]
    \(\mathrm{ct} . A B \leftarrow \operatorname{Mult}\left(\mathrm{ct} . A^{(0)}, \mathrm{ct} . B^{(0)}\right)\)
    for \(k=1\) to \(d-1\) do
        \(\mathrm{ct} . A B \leftarrow \operatorname{Add}\left(\mathrm{ct} . A B, \operatorname{Mult}\left(\mathrm{ct} . A^{(k)}, \mathrm{ct} . B^{(k)}\right)\right)\)
    end for
    return ct. \(A B\)
```


### 3.3.3 Further Improvements

This implementation of matrix multiplication takes about $5 d$ additions, $5 d$ constant multiplications, $6 d$ rotations, and $d$ multiplications. The complexity of Steps 1-1 and 1-2 can be reduced by applying the idea of baby-step/giant-step algorithm. Given an integer $k \in(-d, d)$, we can write $k=\sqrt{d} \cdot i+j$ for some $-\sqrt{d}<i<\sqrt{d}$ and $0 \leq j<\sqrt{d}$. It follows from [25,26] that Equation (3) can be expressed as

$$
U^{\sigma} \cdot \boldsymbol{a}=\sum_{\substack{-\sqrt{d}<i<\sqrt{d} \\ 0 \leq j<\sqrt{d}}}\left(\boldsymbol{u}_{\sqrt{d} \cdot i+j}^{\sigma} \odot \rho(\boldsymbol{a} ; \sqrt{d} \cdot i+j)\right)=\sum_{-\sqrt{d}<i<\sqrt{d}} \rho\left(\sum_{0 \leq j<\sqrt{d}} \boldsymbol{a}_{i, j} ; \sqrt{d} \cdot i\right)
$$

where $\boldsymbol{a}_{i, j}=\rho\left(\boldsymbol{u}_{\sqrt{d} \cdot i+j}^{\sigma} ;-\sqrt{d} \cdot i\right) \odot \rho(\boldsymbol{a} ; j)$. We first compute encryptions of baby-step rotations $\rho(\boldsymbol{a} ; j)$ for $0 \leq j<\sqrt{d}$. We use them to compute the ciphertexts of $\boldsymbol{a}_{i, j}$ 's using only constant multiplications. After that, we perform $\sqrt{d}$ additions, $\sqrt{d}$ constant multiplications, and a single rotation for each $i$. In total, Step 1-1 can be homomorphically evaluated with $2 d$ additions, $2 d$ constant multiplications, and $3 \sqrt{d}$ rotations. Step 1-2 can be computed in a similar way using $d$ additions, $d$ constant multiplications, and $2 \sqrt{d}$ rotations.

On the other hand, we can further reduce the number of constant multiplications in Step 2 by leveraging two-input multiplexers. The sum of $\rho\left(\boldsymbol{v}_{k} ;-k\right)$ and $\rho\left(\boldsymbol{v}_{k-d} ; d-k\right)$ generates a plaintext vector that has 1's in all the slots, which implies that

$$
\begin{aligned}
\operatorname{CMult}\left(\operatorname{Rot}\left(\operatorname{ct.} A^{(0)} ; k-d\right) ; \boldsymbol{v}_{k-d}\right) & =\operatorname{Rot}\left(\operatorname{CMult}\left(\operatorname{ct.} A^{(0)} ; \rho\left(\boldsymbol{v}_{k-d} ; d-k\right)\right) ; k-d\right) \\
& =\operatorname{Rot}\left(\operatorname{ct.} . A^{(0)}-\operatorname{CMult}\left(\operatorname{ct.} A^{(0)} ; \rho\left(\boldsymbol{v}_{k},-k\right)\right) ; k-d\right) .
\end{aligned}
$$

For each $1 \leq k<d$, we compute CMult $\left(\operatorname{ct.} A^{(0)} ; \rho\left(\boldsymbol{v}_{k},-k\right)\right)$. Then, using the fact that

$$
\operatorname{CMult}\left(\operatorname{Rot}\left(\operatorname{ct.} . A^{(0)} ; k\right) ; \boldsymbol{v}_{k}\right)=\operatorname{Rot}\left(\operatorname{CMult}\left(\operatorname{ct.} A^{(0)} ; \rho\left(\boldsymbol{v}_{k},-k\right)\right) ; k\right),
$$

we obtain the desired ciphertext ct. $A^{(k)}$ with addition and rotation operations.
Table 2 summarizes the complexity and the required depth of each step of Algorithm 2 with the proposed optimization techniques.

Table 2: Complexity and required depth of Algorithm 2

| Step | Add | CMult | Rot | Mult | Depth |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathbf{1 - 1}$ | $2 d$ | $2 d$ | $3 \sqrt{d}$ | - | 1 CMult |
| $\mathbf{1 - 2}$ | $d$ | $d$ | $2 \sqrt{d}$ | - |  |
| $\mathbf{2}$ | $2 d$ | $d$ | $3 d$ | - | 1 CMult |
| $\mathbf{3}$ | $d$ | - | - | $d$ | 1 Mult |
| Total | $6 d$ | $4 d$ | $3 d+5 \sqrt{d}$ | $d$ | 1 Mult + 2 CMult |

## 4 Advanced Operations for Homomorphic Matrix Computation

In this section, we introduce a method to transpose a matrix over an HE system. We also present a faster rectangular matrix multiplication by employing the ideas from Algorithm 2. We can further extend our algorithms to parallel matrix computation without additional cost.

### 4.1 Matrix Transposition on Packed Ciphertexts

Let $U^{t}$ be the matrix representation of the transpose map $A \mapsto A^{t}$ on $\mathcal{R}^{d \times d} \cong \mathcal{R}^{n}$. For $0 \leq i, j<d$, its entries are given by

$$
U_{d \cdot i+j, k}^{t}= \begin{cases}1 & \text { if } k=d \cdot j+i \\ 0 & \text { otherwise }\end{cases}
$$

The $k$-th diagonal vector of $U^{t}$ is nonzero if and only if $k=(d-1) \cdot i$ for some $i \in \mathbb{Z} \cap(-d, d)$, so the matrix $U^{t}$ is a sparse matrix with $(2 d-1)$ nonzero diagonal vectors. We can represent this linear transformation as

$$
U^{t} \cdot \boldsymbol{a}=\sum_{-d<i<d}\left(\boldsymbol{t}_{(d-1) \cdot i} \odot \rho(\boldsymbol{a} ;(d-1) \cdot i)\right)
$$

where $\boldsymbol{t}_{(d-1) \cdot i}$ denotes the nonzero diagonal vector of $U^{t}$. The $\ell$-th component of the vector $\boldsymbol{t}_{(d-1) \cdot i}$ is computed by

$$
\boldsymbol{t}_{(d-1) \cdot i}[\ell]= \begin{cases}1 & \text { if } \ell-i=(d+1) \cdot j, 0 \leq j<d-i \\ 0 & \text { otherwise }\end{cases}
$$

if $i \geq 0$, or

$$
\boldsymbol{t}_{(d-1) \cdot i}[\ell]= \begin{cases}1 & \text { if } \ell-i=(d+1) \cdot j,-i \leq j<d \\ 0 & \text { otherwise }\end{cases}
$$

if $i<0$. The total computational cost is about $2 d$ rotations and the baby-step/giant-step approach can be used to reduce the complexity; the number of automorphism can be reduced down to $3 \sqrt{d}$.

### 4.2 Rectangular Matrix Multiplication

In this section, we design an efficient algorithm for rectangular matrix multiplication such as $\mathcal{R}^{\ell \times d} \times$ $\mathcal{R}^{d \times d} \rightarrow \mathcal{R}^{\ell \times d}$ or $\mathcal{R}^{d \times d} \times \mathcal{R}^{d \times \ell} \rightarrow \mathcal{R}^{d \times \ell}$. For convenience, let us consider the former case that $A$ has a smaller number of rows than columns (i.e., $\ell<d$ ). A naive solution is to generate a square matrix by padding zeros in the bottom of the matrix $A$ and perform the homomorphic matrix multiplication algorithm in Section 3.3, resulting in running time of $O(d)$ rotations and multiplications. However, we can further optimize the complexity by manipulating its matrix multiplication representation using a special property of permutations described in Section 3.1.

### 4.2.1 Refinements of Rectangular Matrix Multiplication

Suppose that we are given an $\ell \times d$ matrix $A$ and a $d \times d$ matrix $B$ such that $\ell$ divides $d$. Since $\sigma$ and $\phi$ are defined as row-wise operations, the restrictions to the rectangular matrix $A$ are well-defined permutations on $A$. By abuse of notation, we use the same symbols $\sigma$ and $\phi$ to denote the restrictions. We also use $C_{\ell_{1}: \ell_{2}}$ to denote the $\left(\ell_{2}-\ell_{1}\right) \times d$ submatrix of $C$ formed by extracting from $\ell_{1}$-th row to the $\left(\ell_{2}-1\right)$-th row of $C$. Then their matrix product $A B$ has shape $\ell \times d$ and it can be expressed as follows:

$$
\begin{aligned}
A \cdot B & =\sum_{0 \leq k<d}\left(\phi^{k} \circ \sigma(A)\right) \odot\left(\left(\psi^{k} \circ \tau(B)\right)_{0: \ell}\right) \\
& =\sum_{0 \leq i<\ell} \sum_{0 \leq j<d / \ell}\left(\phi^{j \cdot \ell+i} \circ \sigma(A)\right) \odot\left(\left(\psi^{j \cdot \ell+i} \circ \tau(B)\right)_{0: \ell}\right) .
\end{aligned}
$$

Our key observation is the following lemma, which gives an idea of a faster rectangular matrix multiplication algorithm.

Lemma 1. Two permutations $\sigma$ and $\phi$ are commutative. In general, we have $\phi^{k} \circ \sigma=\sigma \circ \phi^{k}$ for $k>0$. Similarly, we obtain $\psi^{k} \circ \tau=\tau \circ \psi^{k}$ for $k>0$.

Now let us define a $d \times d$ matrix $\bar{A}$ containing $(d / \ell)$ copies of $A$ in a vertical direction (i.e., $\bar{A}=$ $(A ; \ldots ; A))$. Lemma 1 implies that

$$
\begin{aligned}
\left(\phi^{i} \circ \sigma(\bar{A})\right)_{j \cdot \ell:(j+1) \cdot \ell} & =\phi^{i} \circ\left(\sigma(\bar{A})_{j \cdot \ell:(j+1) \cdot \ell}\right) \\
& =\phi^{i} \circ \sigma \circ \phi^{j \cdot \ell}(A) \\
& =\phi^{j \cdot \ell+i} \circ \sigma(A) .
\end{aligned}
$$

Similarly, using the commutative property of $\tau$ and $\psi$, it follows

$$
\left(\psi^{i} \circ \tau(B)\right)_{j \cdot \ell:(j+1) \cdot \ell}=\left(\psi^{j \cdot \ell+i} \circ \tau(B)\right)_{0: \ell}
$$

Therefore, the matrix product $A B$ is written as follows:

$$
A \cdot B=\sum_{0 \leq j<d / \ell}\left(\sum_{0 \leq i<\ell}\left(\phi^{i} \circ \sigma(\bar{A})\right) \odot\left(\psi^{i} \circ \tau(B)\right)\right)_{j \cdot \ell:(j+1) \cdot \ell} .
$$

### 4.2.2 Homomorphic Rectangular Matrix Multiplication

Suppose that we are given two ciphertexts ct. $\bar{A}$ and $\mathrm{ct} . B$ that encrypt matrices $\bar{A}$ and $B$, respectively. We first apply the baby-step/giant-step algorithm to generate the encryptions of $\sigma(\bar{A})$ and $\tau(B)$ as in Section 3.3.3. Next, we can securely compute $\sum_{i=0}^{\ell-1}\left(\phi^{i} \circ \sigma(\bar{A})\right) \odot\left(\psi^{i} \circ \tau(B)\right)$ in a similar way to Algorithm 2, say the output is ct. $\bar{A} B$. Finally, we perform aggregation and rotation operations to get the final result: $\sum_{j=0}^{d / \ell-1} \operatorname{Rot}(\operatorname{ct} . \bar{A} B ; j \cdot \ell \cdot d)$. This step can be evaluated using a repeated doubling approach, yielding a running time of $\log (d / \ell)$ additions and rotations. See Algorithm 3 for an explicit description of homomorphic rectangular matrix multiplication.

```
Algorithm 3 Homomorphic rectangular matrix multiplication
    procedure HE-RMatMult(ct. \(\bar{A}, \mathrm{ct} . B\) )
    [Step 1]
    \(\mathrm{ct}. . A^{(0)} \leftarrow\) LinTrans \(\left(\mathrm{ct} . \bar{A} ; U^{\sigma}\right)\)
    ct. \(B^{(0)} \leftarrow\) LinTrans \(\left(c t . B ; U^{\tau}\right)\)
    [Step 2]
    for \(k=1\) to \(\ell-1\) do
        \(\mathrm{ct} . A^{(k)} \leftarrow\) LinTrans \(\left(c t . A^{(0)} ; V^{k}\right)\)
        ct. \(B^{(k)} \leftarrow\) LinTrans \(\left(c t . B^{(0)} ; W^{k}\right)\)
    end for
    [Step 3]
    \(\mathrm{ct} . \bar{A} B \leftarrow \operatorname{Mult}\left(\mathrm{ct} . A^{(0)}, \mathrm{ct} . B^{(0)}\right)\)
    for \(k=1\) to \(\ell-1\) do
        \(\mathrm{ct} . \bar{A} B \leftarrow \operatorname{Add}\left(\mathrm{ct} . \bar{A} B, \operatorname{Mult}\left(\mathrm{ct} . A^{(k)}, \mathrm{ct} . B^{(k)}\right)\right)\)
    end for
    [Step 4]
    ct. \(A B \leftarrow \mathrm{ct} . \bar{A} B\)
    for \(k=0\) to \(\log (d / \ell)-1\) do
        \(\mathrm{ct} . A B \leftarrow \operatorname{Add}\left(\mathrm{ct} . A B, \operatorname{Rot}\left(\mathrm{ct} . A B ; \ell \cdot d \cdot 2^{k}\right)\right)\)
    end for
    return ct. \(A B\)
```

Table 3 summarizes the total complexity of Algorithm 3. Even though we need additional computation for Step 4, we can reduce the complexities of Step 2 and 3 to $O(\ell)$ rotations and $\ell$ multiplications, respectively. We also note that the final output ct. $A B$ encrypts a $d \times d$ matrix containing $(d / \ell)$ copies of the desired matrix product $A B$ in a vertical direction.

This resulting ciphertext has the same form as a rectangular input matrix of Algorithm 3, so it can be reusable for further matrix computation without additional cost.

Table 3: Complexity of Algorithm 3

| Step | Add | CMult | Rot | Mult |
| :---: | :---: | :---: | :---: | :---: |
| $\mathbf{1}$ | $3 d$ | $3 d$ | $5 \sqrt{d}$ | - |
| $\mathbf{2}$ | $\ell$ | $2 \ell$ | $3 \ell$ | - |
| $\mathbf{3}$ | $\ell$ | - | - | $\ell$ |
| $\mathbf{4}$ | $\log (d / \ell)$ | - | $\log (d / \ell)$ |  |
| Total | $3 d+2 \ell+\log (d / \ell)$ | $3 d+2 \ell$ | $3 \ell+5 \sqrt{d}+\log (d / \ell)$ | $\ell$ |

### 4.3 Parallel Matrix Computation

Throughout Sections 3 and 4, we have identified the message space $\mathcal{M}=\mathcal{R}^{n}$ with the set of matrices $\mathcal{R}^{d \times d}$ under the assumption that $n=d^{2}$. However, most of HE schemes $[9,18,13]$ have a quite large number of plaintext slots (e.g. thousands) compared to the matrix dimension in some real-world applications, i.e., $n \gg d^{2}$. If a ciphertext can encrypt only one matrix, most of plaintext slots would be wasted. This subsection introduces an idea that allows multiple matrices to be encrypted in a single ciphertext, thereby performing parallel matrix computation in a SIMD manner.

For simplicity, we assume that $n$ is divisible by $d^{2}$ and let $g=n / d^{2}$. We modify the encoding map in Section 3.2 to make a 1-to-1 correspondence $\iota_{g}$ between $\mathcal{R}^{n}$ and $\left(\mathcal{R}^{d \times d}\right)^{g}$, which transforms an $n$ dimensional vector into a $g$-tuple of square matrices of order $d$. Specifically, for an input vector $\boldsymbol{a}=$ $\left(a_{\ell}\right)_{0 \leq \ell<n}$, we define $\iota_{g}$ by

$$
\iota_{g}: \boldsymbol{a} \mapsto\left(A_{k}=\left(a_{g \cdot(d \cdot i+j)+k}\right)\right)_{0 \leq k<g} .
$$

The components of $\boldsymbol{a}$ with indexes congruent to $k$ modulo $g$ are corresponding to the $k$-th matrix $A_{k}$.
We note that for an integer $0 \leq \ell<d^{2}$, the rotation operation $\rho(\boldsymbol{a} ; g \cdot \ell)$ represents the matrix-wise rotation by $\ell$ positions. It can be naturally extended to the other matrix-wise operations including scalar linear transformation and matrix multiplication. For example, we can encrypt $g$ number of $d \times d$ matrices into a single ciphertext and perform the matrix multiplication operations between $g$ pairs of matrices at once by applying our matrix multiplication algorithm on two ciphertexts. The total complexity remains the same as Algorithm 2, which results in a less amortized computational complexity of $O(d / g)$ per matrix.

## 5 Implementation of Homomorphic Matrix Operations

In this section, we report the performance of our homomorphic matrix operations and analyze the performance of the implementation. For simplicity, we assume that $d$ is a power-of-two integer. In general, we can pad zeros at the end of each row to set $d$ as a power of two.

In our implementation, we employ a special encryption scheme suggested by Cheon et al. [13] (CKKS), which supports approximate computation over encrypted data. A unique property of the HE scheme is the rescaling procedure to truncate a ciphertext into a smaller modulus, which leads to rounding of the plaintext. This plays an important role in controlling the magnitude of messages, and thereby achieving efficiency of approximate computation. Recently, a significant performance improvement was made in [12] based on the Residue Number System (RNS) and Kim et al. [30] proposed a different special modulus technique to optimize the key-switching operation. We give a proof-of-concept implementation using Microsoft SEAL version 3.3.2 [43] which includes this RNS variant of the CKKS scheme. All the experiments were performed on a Macbook Pro laptop with an Intel Core i9 running with 4 cores rated at 2.3 GHz .

### 5.1 Parameter Setting

We present how to select parameters of our underlying HE scheme to support homomorphic matrix operations described in Sections 3 and 4. Our underlying HE scheme is based on the RLWE assumption over the cyclotomic ring $\mathcal{R}=\mathbb{Z}[X] /\left(X^{N}+1\right)$ for a power-of-two integer $N$. Let us denote by $[\cdot]_{q}$ the reduction modulo $q$ into the interval $(-q / 2, q / 2] \cap \mathbb{Z}$ of the integer. We write $\mathcal{R}_{q}=\mathcal{R} / q \mathcal{R}$ for the residue ring of $\mathcal{R}$ modulo an integer $q$. The native plaintext space is represented as a set of ( $N / 2$ )-dimensional complex vectors. We use an RNS by taking a ciphertext modulus $q=\prod_{i=0}^{L} q_{i}$ which is a product of distinct primes. Based on the ring isomorphism $R_{q} \rightarrow \prod_{i=0}^{L} R_{q_{i}}, a \mapsto\left(a\left(\bmod q_{i}\right)\right)_{0 \leq i \leq L}$, a polynomial with a large modulus $q$ can be represented as a tuple of polynomials with smaller coefficients modulo $q_{i}$. If needed, we raise a ciphertext modulus from $q$ to $p_{s} q$ for a prime number $p_{s}$, called the special modulus, and perform the key-switching procedure over $R_{p_{s} q}$ followed by modulus reduction back to $q$. We note that the RNS primes should be 1 modulo $2 N$ to utilize an efficient Number Theoretic Transformation (NTT) algorithm.

Suppose that all the elements are scaled by a factor of an integer $p$ and then converted into the nearest integers for quantization. If we are multiplying a ciphertext by a plaintext vector, we assume that the constant vector is scaled by a factor of an integer $p_{c}$ to maintain the precision. Thus, the rescaling procedure after homomorphic multiplication reduces a ciphertext modulus by $p$ while the rescaling procedure after a constant multiplication reduces a modulus by $p_{\mathrm{c}}$. For example, Algorithm 2 has depth of two constant multiplications for Step 1 and 2, and has additional depth of a single homomorphic multiplication for Step 3. This implies that an input ciphertext modulus is reduced by $\left(2 \log p_{\mathrm{c}}+\log p\right)$ bits after the matrix multiplication algorithm. As a result, we obtain the following lower bound on the bit length of a fresh ciphertext modulus, denoted by $\log q$ :

$$
\log q= \begin{cases}\log q_{0} & \text { for HE-MatAdd } \\ \log q_{0}+2 \log p_{\mathrm{c}}+\log p & \text { for HE-MatMult } \\ \log q_{0}+\log p_{\mathrm{c}} & \text { for HE-MatTrans }\end{cases}
$$

where $q_{0}$ is the output ciphertext modulus. The final ciphertext represents the desired vector but is scaled by a factor of $p$, which means that $\log q_{0}$ should be larger than $\log p$ for correctness of decryption. In other words, the chain of moduli for HE-MatMult can be defined via a set of primes $\left\{q_{0}, q_{1}, q_{2}, q_{3}, p_{s}\right\}$ such that $\log q_{1} \approx \log p$ and $\log q_{2} \approx \log q_{3} \approx \log p_{\mathrm{c}}$. In our implementation, we take $\log p=\log p_{\mathrm{c}}=30$ and $\log q_{0}=\log p_{s}=40$.

We use the discrete Gaussian distribution of standard deviation $\sigma=3.2$ to sample error polynomials. The secret-key polynomials were sampled from the discrete ternary uniform distribution over $\{0, \pm 1\}^{N}$. The cyclotomic ring dimension is chosen as $N=2^{13}$ to achieve at least 80-bit security level against the known attacks of the LWE problem based on the estimator of Albrecht et al. [2]. In short, we present three parameter sets and sizes of the corresponding fresh ciphertexts as follows:

$$
(N, \log q, \text { size })= \begin{cases}\left(2^{13}, 40,80 \mathrm{~KB}\right) & \text { for HE-MatAdd; } \\ \left(2^{13}, 130,260 \mathrm{~KB}\right) & \text { for HE-MatMult; } \\ \left(2^{13}, 70,140 \mathrm{~KB}\right) & \text { for HE-MatTrans }\end{cases}
$$

### 5.2 Performance of Matrix Operations

Table 4 presents timing results for matrix addition, multiplication, and transposition for various matrix sizes from 4 to 64 where the throughput means the number of matrices being processed in parallel. We provide three distinct implementation variants: single-packed, sparsely-packed, and fully-packed. The single-packed implementation is that a ciphertext represents only a single matrix; two other implementations are to encode several matrices into sparsely or fully packed plaintext slots. We use the same parameters for all variants, and thus each ciphertext can hold up to $N / 2=2^{12}$ plaintext values. For example, if we consider $4 \times 4$ matrices, we can process operations over $2^{12} /(4 \cdot 4)=256$ distinct matrices simultaneously. In the case of dimension 16 , a ciphertext can represent up to $2^{12} /(16 \cdot 16)=16$ distinct matrices.

Table 4: Benchmarks of homomorphic matrix operations

| Dim | Throughput | Message size | Expansion rate | Encoding+ <br> Encryption | Decoding+ <br> Decryption | Relative time per matrix |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | HE-MatAdd | HE-MatMult | HE-MatTrans |
| 4 | 1 | 0.06 KB | 4437 | 9.68 ms | 0.49 ms | $35 \mu \mathrm{~s}$ | 47.33 ms | 17.08 ms |
|  | 16 | 0.94 KB | 277 | 9.87 ms | 0.87 ms | $2.19 \mu \mathrm{~s}$ | 2.96 ms | 1.12 ms |
|  | 256 | 15 KB | 17.3 | 4.45 ms | 4.45 ms | $0.14 \mu \mathrm{~s}$ | 0.18 ms | 0.06 ms |
| 16 | 1 | 0.94 KB | 277 | 9.72 ms | 0.57 ms | $36 \mu \mathrm{~s}$ | 152 ms | 33.79 ms |
|  | 4 | 3.75 KB | 69.3 | 9.12 ms | 0.58 ms | $9.25 \mu \mathrm{~s}$ | 34.67 ms | 8.04 ms |
|  | 16 | 15 KB | 17.3 | 10.23 ms | 0.93 ms | $2.19 \mu \mathrm{~s}$ | 9.04 ms | 2.01 ms |
| 64 | 1 | 15 KB | 17.3 | 9.09 ms | 0.54 ms | $34 \mu \mathrm{~s}$ | 600.59 ms | 90.88 ms |

Ciphertext sizes. As mentioned above, a ciphertext could hold $2^{12}$ different plaintext slots, and thus we can encrypt one $64 \times 64$ matrix into a fully-packed ciphertext. We assumed that all the inputs had $\log p=30$ bits of precision, which means that an input matrix size is bounded by $64 \times 64 \times 30$ bits or 15 KB. Since a single ciphertext is at most 260 KB for an evaluation of matrix multiplication, the encrypted version is $260 / 15 \approx 17$ times larger than the un-encrypted version. In Table 4, the third column gives the size of input matrices and the fourth column gives an expansion ratio of the generated ciphertext to the input matrices.

Timing results. We conducted experiments over ten times and measured the average running times for all the operations. For the parameter setting in Section 5.1, the key generation takes about 60 milliseconds. In Table 4, the fifth column gives timing for encoding input matrices and encrypting the resulting plaintext slots. Since matrix multiplication requires the largest fresh ciphertext modulus and takes more time than others, we just report the encryption timing results for the case. In the sixth column, we give timing for decrypting the output ciphertext and decoding to its matrix form. Note that encryption and decryption timings are similar each other; but encoding and decoding timings depend on the throughput. The last three columns give amortized time per matrix for homomorphic matrix computation. The entire execution time, called latency, is similar between the three variant implementations, so the parallel matrix computation provides roughly a speedup as a factor of the throughput.

Performance of rectangular matrix multiplication. We present the performance of Algorithm 3 described in Section 4.2. As a concrete example, we consider the rectangular matrix multiplication $\mathbb{R}^{16 \times 64} \times$ $\mathbb{R}^{64 \times 64} \rightarrow \mathbb{R}^{16 \times 64}$. As we described above, our optimized method has a better performance than a simple method exploiting Algorithm 2 for the multiplication between $64 \times 64$ matrices.

To be precise, the first step of Algorithms 2 or 3 generates two ciphertexts ct. $A^{(0)}$ and ct. $B^{(0)}$ by applying the linear transformations of $U^{\sigma}$ and $U^{\tau}$, thus both approaches have almost the same computational complexity. Next, in the second and third steps, two algorithms apply the same operations to the resulting ciphertexts but with different numbers: Algorithm 2 requires approximately four times more operations compared to Algorithm 3. As a result, Step 2 turns out to be the most time consuming part in Algorithm 2, whereas it is not the dominant procedure in Algorithm 3. Finally, Algorithm 3 requires some additional operations for Step 4 , but we need only $\log (64 / 16)=2$ automorphisms.

Table 5 shows more detailed experimental results of HE-MatMult and HE-RMatMult based on the same parameter as in the above section. The total running times of two algorithms are 600 milliseconds and 283 milliseconds, respectively; therefore, Algorithm 3 achieves a speedup of $2 \times$ compared to Algorithm 2.

## 6 E2DM: Making Prediction based on Encrypted Data and Model

In this section, we propose a novel framework E2DM to test encrypted convolutional neural networks model on encrypted data. We consider a new service paradigm where model providers offer encrypted

Table 5: Performance comparison of homomorphic square and rectangular matrix multiplications

| Algorithm | Step 1 | Step 2 | Step 3 | Step 4 | Total |
| :---: | :---: | :---: | :---: | :---: | :---: |
| HE-MatMult | 161.09 ms | 418.20 ms | 20.99 ms | - | 600.28 ms |
|  |  | 114.15 ms | 5.44 ms | 1.91 ms | 282.59 ms |
| HE-RMatMult |  | $3.66 \times$ | $3.86 \times$ | - | $2.12 \times$ |
| Speedup | - |  |  |  |  |

trained classifier models to a public cloud and the cloud server provides on-line prediction service to data owners who uploaded their encrypted data. In this inference, the cloud should learn nothing about private data of the data owners, nor about the trained models of the model providers.

### 6.1 Neural Networks Architecture

The first viable example of CNN on image classification was AlexNet by Krizhevsky et al. [32] and it was dramatically improved by Simonyan et al. [44]. It consists of a stack of linear and non-linear layers. The linear layers can be convolution layers or FC layers. The non-linear layers can be max pooling (i.e., compute the maximal value of some components of the feeding layer), mean pooling (i.e., compute the average value of some components of the feeding layer), ReLu functions, or sigmoid functions.

Specifically, the convolution operator forms the fundamental basis of the convolutional layer. The convolution has kernels, or windows, of size $k \times k$, a stride of $(s, s)$, and a mapcount of $h$. Given an image $I \in \mathbb{R}^{w \times w}$ and a kernel $K \in \mathbb{R}^{k \times k}$, we compute the convolved image $\operatorname{Conv}(I, K) \in \mathbb{R}^{d_{K} \times d_{K}}$ by

$$
\operatorname{Conv}(I, K)_{i^{\prime}, j^{\prime}}=\sum_{0 \leq i, j<k} K_{i, j} \cdot I_{s \cdot i^{\prime}+i, s \cdot j^{\prime}+j}
$$

for $0 \leq i^{\prime}, j^{\prime}<d_{K}=\lceil(w-k) / s\rceil+1$. Here $\lceil\cdot\rceil$ returns the least integer greater than or equal to the input. It can be extended to multiple kernels $\mathcal{K}=\left\{K^{(k)}\right\}_{0 \leq k<h}$ as

$$
\operatorname{Conv}(I, \mathcal{K})=\left(\operatorname{Conv}\left(I, K^{(0)}\right), \cdots, \operatorname{Conv}\left(I, K^{(h-1)}\right)\right) \in \mathbb{R}^{d_{K} \times d_{K} \times h}
$$

On the other hand, FC layer connects $n_{I}$ nodes to $n_{O}$ nodes, or equivalently, it can be specified by the matrix-vector multiplication of an $n_{O} \times n_{I}$ matrix. Note that the output of convolution layer has a form of tensor so it should be flatten before FC layer. Throughout this paper, we concatenate the rows of the tensor one by one and output a column vector, denoted by FL(•).

### 6.2 Homomorphic Evaluation of CNN

We present an efficient strategy to evaluate CNN prediction model on the MNIST dataset. Each image is a $28 \times 28$ pixel array, where the value of each pixel represents a level of gray. After an arbitrary number of hidden layers, each image is labeled with 10 possible digits. The training set has 60,000 images and the test set has 10,000 images. We assume that a neural network is trained with the plaintext dataset in the clear. We adapted a similar network topology to CryptoNets: one convolution layer and two FC layers with square activation function. Table 6 describes our neural networks to the MNIST dataset and summarizes the hyperparameters.

The final step of neural networks is usually to apply the softmax activation function for a purpose of probabilistic classification. We note that it is enough to obtain an index of maximum values of outputs in a prediction phase.

In the following, we explain how to securely test encrypted model on encrypted multiple data. In our implementation, we take $N=2^{13}$ as a cyclotomic ring dimension so each plaintext vector is allowed to have dimension less than $2^{12}$ and one can predict 64 images simultaneously in a SIMD manner. We describe the parameter selection in more detail below.

Table 6: Description of our CNN on the MNIST dataset

| Layer | Description |
| :---: | :--- |
| Convolution | Input image $28 \times 28$, kernel size $7 \times 7$, stride size of 3, number of output channels 4 |
| $1^{\text {st }}$ square | Squaring 256 input values |
| FC- 1 | Fully connecting with 256 inputs and 64 outputs: $\mathbb{R}^{64 \times 256} \times \mathbb{R}^{256 \times 1} \rightarrow \mathbb{R}^{64 \times 1}$ |
| $2^{\text {nd }}$ square | Squaring 64 input values |
| FC- 2 | Fully connecting with 64 inputs and 10 outputs: $\mathbb{R}^{10 \times 64} \times \mathbb{R}^{64 \times 1} \rightarrow \mathbb{R}^{10 \times 1}$ |

### 6.2.1 Encryption of Images

At the encryption phase, the data owner encrypts the data using the public key of an HE scheme. Suppose that the data owner has a two-dimensional image $I \in \mathbb{R}^{28 \times 28}$. For $0 \leq i^{\prime}, j^{\prime}<d_{K}=8$, let us define an extracted image feature $I\left[i^{\prime}, j^{\prime}\right]$ formed by taking the elements $I_{3 \cdot i^{\prime}+i, 3 \cdot j^{\prime}+j}$ for $0 \leq i, j<7$. That is, a single image can be represented as the 64 image features of size $7 \times 7$. It can be extended to multiple images $\mathcal{I}=\left\{I^{(k)}\right\}_{0 \leq k<64}$. For each $0 \leq i, j<7$, the dataset is encoded into a matrix consisting of the $(i, j)$-th components of 64 features over 64 images and it is encrypted as follows:

$$
\text { ct. } I_{i, j}=\operatorname{Enc}\left[\begin{array}{cccc}
I^{(0)}[0,0]_{i, j} & I^{(1)}[0,0]_{i, j} & \cdots & I^{(63)}[0,0]_{i, j} \\
I^{(0)}[0,1]_{i, j} & I^{(1)}[0,1]_{i, j} & \cdots & I^{(63)}[0,1]_{i, j} \\
\vdots & \vdots & \ddots & \vdots \\
I^{(0)}[7,7]_{i, j} & I^{(1)}[7,7]_{i, j} & \cdots & I^{(63)}[7,7]_{i, j}
\end{array}\right]
$$

The resulting ciphertexts $\left\{\text { ct. } I_{i, j}\right\}_{0 \leq i, j<7}$ are sent to the public cloud and stored in their encrypted form.

### 6.2.2 Encryption of Trained Model

The model provider encrypts the trained prediction model values such as multiple convolution kernels' values $\mathcal{K}=\left\{K^{(k)}\right\}_{0 \leq k<4}$ and weights (matrices) of FC layers. The provider begins with a procedure for encrypting each of the convolution kernels separately. For $0 \leq i, j<7$ and $0 \leq k<4$, the $(i, j)$-th component of the kernel matrix $K^{(k)}$ is copied into plaintext slots and the model provider encrypts the plaintext vector into a ciphertext, denoted by ct. $K_{i, j}^{(k)}$.

Next, the first FC layer is specified by a $64 \times 256$ matrix and it can be divided into four square sub-matrices of size $64 \times 64$. For $0 \leq k<4$, we write $W_{k}$ to denote the $k$-th sub-matrix. Each matrix is encrypted into a single ciphertext using the matrix encoding method in Section 3.2, say the output ciphertext ct. $W_{k}$.

For the second FC layer, it can be expressed by a $10 \times 64$ matrix. The model provider pads zeros in the bottom to obtain a matrix $V$ of size $16 \times 64$ and then generates a $64 \times 64$ matrix $\bar{V}$ containing four copies of $V$ vertically, say the output ciphertext ct. $V$. Finally, the model provider transmits three distinct types of ciphertexts to the cloud: ct. $K_{i, j}^{(k)}, \mathrm{ct} . W_{k}$, and ct. $V$.

### 6.2.3 Homomorphic Evaluation of Neural Networks

At the prediction phase, the public cloud takes ciphertexts of the images from the data owner and the neural network prediction model from the model provider. Since the data owner uses a SIMD technique to batch 64 different images, the first FC layer is specified as a matrix multiplication: $\mathbb{R}^{64 \times 256} \times \mathbb{R}^{256 \times 64} \rightarrow$ $\mathbb{R}^{64 \times 64}$. Similarly, the second FC layer is represented as a matrix multiplication: $\mathbb{R}^{10 \times 64} \times \mathbb{R}^{64 \times 64} \rightarrow$ $\mathbb{R}^{10 \times 64}$.

Homomorphic convolution layer. The public cloud takes the ciphertexts ct. $I_{i, j}$ and ct. $K_{i, j}^{(k)}$ for $0 \leq i, j<$ 7 and $0 \leq k<4$. We apply pure SIMD operations to efficiently compute dot-products between the
kernel matrices and the extracted image features. For each $0 \leq k<4$, the cloud performs the following computation on ciphertexts:

$$
\mathrm{ct} . C_{k} \leftarrow \sum_{0 \leq i, j<7} \operatorname{Mult}\left(\mathrm{ct} . I_{i, j}, \mathrm{ct} . K_{i, j}^{(k)}\right) .
$$

By the definition of the convolution, the resulting ciphertext $\mathrm{ct}_{k}$ represents a square matrix $C_{k}$ of order 64 such that

$$
C_{k}=\left[\begin{array}{cc}
\vdots & \vdots \\
\mathrm{FL}\left(\operatorname{Conv}\left(I^{(0)}, K^{(k)}\right)\right) & \cdots \mathrm{FL}\left(\operatorname{Conv}\left(I^{(63)}, K^{(k)}\right)\right) \\
\vdots & \vdots
\end{array}\right]
$$

That is, it is an encryption of the matrix $C_{k}$ having the $i$-th column as the flatten convolved result between the the $i$-th image $I^{(i)}$ and the $k$-th kernel $K^{(k)}$.

The first square layer. This step applies the square activation function to all the encrypted output images of the convolution in a SIMD manner. That is, for each $0 \leq k<4$, the cloud computes as follows:

$$
\mathrm{ct} . S_{k}^{(1)} \leftarrow \mathrm{SQR}\left(\mathrm{ct} . C_{k}\right)
$$

where $\operatorname{SQR}(\cdot)$ denotes the squaring operation of an HE scheme. Note that $c t . S_{k}^{(1)}$ is an encryption of the matrix $C_{k} \odot C_{k}$.

The FC-1 layer. This procedure requires a matrix multiplication between a $64 \times 256$ weight matrix $W=\left(W_{0}\left|W_{1}\right| W_{2} \mid W_{3}\right)$ and a $256 \times 64$ input matrix $C=\left(C_{0} \odot C_{0} ; C_{1} \odot C_{1} ; C_{2} \odot C_{2} ; C_{3} \odot C_{3}\right)$. The matrix product $W \cdot C$ is formed by combining the blocks in the same way, that is,

$$
W \cdot C=\sum_{0 \leq k<4}\left(W_{k} \cdot\left(C_{k} \odot C_{k}\right)\right) .
$$

Thus the cloud performs the following computation:

$$
\mathrm{ct} . F \leftarrow \sum_{0 \leq k<4} \text { HE-MatMult }\left(\mathrm{ct} . W_{k}, \mathrm{ct} . S_{k}^{(1)}\right)
$$

The second square layer. This step applies the square activation function to all the output nodes of the first FC layer:

$$
\mathrm{ct} . S^{(2)} \leftarrow \mathrm{SQR}(\mathrm{ct} . F)
$$

The FC-2 layer. This step performs the rectangular multiplication algorithm between the weight ciphertext ct. $V$ and the output ciphertext ct. $S^{(2)}$ of the second square layer:

$$
\text { ct.out } \leftarrow \text { HE-RMatMult }\left(\mathrm{ct} . V, \text { ct. } S^{(2)}\right)
$$

### 6.2.4 The Threat Model

Suppose that one can ensure the IND-CPA security of an underlying HE scheme, which means that ciphertexts of any two messages are computationally indistinguishable. Since all the computations on the public cloud are performed over encryption, the cloud learns nothing from the encrypted data so we can ensure the confidentiality of the data against such a semi-honest server.

### 6.3 Performance Evaluation of E2DM

We evaluated our E2DM framework to classify encrypted handwritten images of the MNIST dataset. We used the library keras [14] with Tensorflow [1] to train the CNN model from 60,000 images of the dataset by applying the ADADELTA optimization algorithm [50].

### 6.3.1 Optimization Techniques

Suppose that we are given an encryption ct. $A$ of a $d \times d$ matrix $A$. Recall from Section 3 that we apply homomorphic liner transformations to generate the encryption ct. $A^{(\ell)}$ of a matrix $\phi^{\ell} \circ \sigma(A)$ for $0 \leq \ell<d$. Sometimes one can pre-compute $\phi^{\ell} \circ \sigma(A)$ in the clear and generate the corresponding ciphertexts for free. Thus this approach gives us a space/time trade-off: although it requires more space for $d$ ciphertexts rather than a single ciphertext, it reduces the overhead of rotation operations from $(3 d+5 \sqrt{d})$ to $(d+2 \sqrt{d})$, achieving a better performance. This method has another advantage, in that an input ciphertext modulus is reduced by $\left(\log p+\log p_{c}\right)$ bits after matrix multiplication while $\left(\log p+2 \log p_{c}\right)$ in the original method. This is because the encryptions of $\phi^{k} \circ \sigma(A)$ are given as fresh ciphertexts and it only requires additional depths to generate the encryptions of $\psi^{k} \circ \tau(B)$.

We can apply this idea to the FC layers. For each $0 \leq k<4$ and $0 \leq \ell<64$, the model provider generates a ciphertext ct. $W_{k}^{(\ell)}$ representing the matrix $\phi^{\ell} \circ \sigma\left(W_{k}\right)$ of the first FC layer. For the second FC layer, the provider generates an encryption ct. $V^{(\ell)}$ of the matrix $\phi^{\ell} \circ \sigma(\bar{V})$ for $0 \leq \ell<16$.

### 6.3.2 Parameters

The convolution layer and the square activation layers have a depth of one homomorphic multiplication. As discussed before, the FC layers have depth of one homomorphic multiplication and one constant multiplication by applying the pre-computation optimization technique. Therefore, the lower bound on the bit length of a fresh ciphertext modulus is $5 \log p+2 \log p_{\mathrm{c}}+\log q_{0}$. In our implementation, we assume that all the inputs had $\log p=30$ bits of precision and we set $\log p_{\mathrm{c}}=30$ for the bit precision of constant values. We set the bit lengths of the output ciphertext modulus and the special modulus as $\log q_{0}=\log p_{s}=\log p+10$. We could actually obtain the bit length of the largest fresh ciphertext modulus about $\log q \approx 250$ and took the ring dimension $N=2^{13}$ to ensure at least 80 bits of security. This security was chosen to be consistent with other performance number reported from CryptoNets. Note that a fresh ciphertext has 0.488 MB under this parameter setting.

### 6.3.3 Ciphertext Sizes

Each image is a $28 \times 28$ pixel array, where each pixel is in the range from 0 to 255 . The data owner first chooses 64 images in the MNIST dataset, normalizes the data by dividing by the maximum value 255 , and generates the ciphertexts $\left\{\text { ct. } I_{i, j}\right\}_{0 \leq i, j<7}$. The total size of ciphertexts is $0.488 \times 49 \approx 23.926 \mathrm{MB}$ and a single ciphertext contains information of 64 images, and therefore the total ciphertext size per image is $23.926 / 64 \approx 0.374 \mathrm{MB}$ or 383 KB . Since each image has approximately $28 \times 28 \times 30$ bits, it is 133 times smaller than the encrypted one. Meanwhile, the model provider generates three distinct types of ciphertexts:

- ct. $K_{i, j}^{(k)}$ for $0 \leq i, j<7$ and $0 \leq k<4$;
- ct. $W_{k}^{(\ell)}$ for $0 \leq k<4$ and $0 \leq \ell<64$;
- ct. $V^{(\ell)}$ for $0 \leq \ell<16$.

The total size of ciphertexts is $0.488 \times 468 \approx 228.516 \mathrm{MB}$. After the homomorphic evaluation of E2DM, the cloud sends only a single ciphertext to an authority who is the legitimate owner of the secret key of the HE scheme. The ciphertext size is around 0.078 MB and the size per image is $0.078 / 64 \mathrm{MB} \approx 1.25$ KB. Table 7 summarizes the numbers in the second and third columns.

Table 7: Total ciphertext sizes of E2DM

|  | Ciphertext size | Size per instance |
| :--- | ---: | ---: |
| Data owner $\rightarrow$ Cloud | 23.926 MB | 383 KB |
| Model provider $\rightarrow$ Cloud | 228.516 MB | - |
| Cloud $\rightarrow$ Authority | 0.078 MB | 1.25 KB |

### 6.3.4 Implementation Details

The key generation takes about 0.87 seconds for the parameters setting in Section 6.3.2. The data owner takes about 121 milliseconds to encrypt 64 different number of images. Meanwhile, the model provider takes about 1.06 seconds to generate the encrypted prediction models. This procedure takes more time than the naive method but it is an one-time process before data outsourcing and so it is a negligible overhead.

In Table 8, we report timing results for the evaluation of E2DM. The third column gives timings for each step and the fourth column gives the relative time per image (if applicable). The dominant cost of evaluating the framework is that of performing the first FC layer. This step requires four matrix multiplication operations over encrypted $64 \times 64$ matrices so it expects to take about $0.6 \times 4 \approx 2.4$ seconds from the result of Table 4 . We further take advantage of the pre-computation method described in Section 6.3.1, and thereby it only took about 1.36 seconds to evaluate the layer ( 1.8 times faster). Similarly, we could apply this approach to the second FC layer, which leads to 0.09 seconds for the evaluation. In total, it took about 1.69 seconds to classify encrypted images from the encrypted training model, yielding an amortized rate of 26 milliseconds per image.

Table 8: Experimental results of E2DM for MNIST

|  | Stage | Latency | Amortized time <br> per image |
| :---: | :---: | ---: | ---: |
|  | Encoding + Encryption | 120.91 ms | 1.89 ms |
| Model provider | Encoding + Encryption | 1059.14 ms | - |
| Cloud | Convolution | 219.34 ms | 3.42 ms |
|  | $1^{\text {st }}$ square | 16.99 ms | 0.27 ms |
|  | FC-1 | 1355.19 ms | 21.17 ms |
|  | $2^{\text {nd }}$ square | 6.41 ms | 0.10 ms |
|  | FC-2 | 90.41 ms | 1.41 ms |
|  | Total evaluation | $\mathbf{1 6 8 8 . 3 4} \mathbf{m s}$ | $\mathbf{2 6 . 3 8} \mathbf{~ m s}$ |
| Authority | Decoding + Decryption | 0.72 ms | 0.01 ms |

After the evaluation, the cloud returns only a single packed ciphertext that is transmitted the authority. Then the output can be decrypted with the secret key and the authority computes the argmax of 10 scores for each image to obtain the prediction. These procedures take around 0.72 milliseconds, yielding an amortized time of 0.01 milliseconds per image. In the end, the data owner gets the results from the authority.

This model achieves an accuracy of $98.1 \%$ on the test set. The accuracy is the same as the one obtained by the evaluation of the model in the clear, which implies that there is no precision loss from the approximate homomorphic encryption.

### 6.4 Comparison with Previous Work

Table 9 compares our result for the MNIST dataset with the state-of-the-art frameworks: CryptoNets [23], MiniONN [34], and GAZELLE [28]. The first column indicates the framework and the second column denotes the method used for preserving privacy. The last columns give running time and communication costs required for image classification.

HE-based frameworks. We used a similar network topology to CryptoNets (only different numbers of nodes in the hidden layers) but considered different scenario and underlying cryptographic primitive. CryptoNets took 570 seconds to perform a single prediction, yielding in an amortized rate of 70 milliseconds. In our case, data is represented in a matrix form and applied to the evaluation of neural networks using
homomorphic matrix operations. As a result, E2DM achieves 340 -fold reduction in latency and 34 -fold reduction in message sizes. CryptoNets allows more SIMD parallelism, so it could give better amortized running time. However, this implies that CryptoNets requires a very large number of prediction to yield better amortized complexity, so its framework turns out to be less competitive in practice.

Mixed protocol frameworks. Liu et al. [34] presented MiniONN framework of privacy-preserving neural networks by employing a ciphertext packing technique as a pre-processing tool. Recently, Juvekar et al. [28] presented GAZELLE that deploys automorphism structure of an underlying HE scheme to perform matrix-vector multiplication, thereby improving the performance significantly. It took 30 milliseconds to classify one image from the MNIST dataset and has an online bandwidth cost of 0.5 MB . Even though these mixed protocols achieve relatively fast run-time, they require interaction between protocol participants, resulting in high bandwidth usage.

Table 9: Privacy-preserving neural network frameworks for MNIST

| Framework | Method | Runtime |  |  |  | Communication cost |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | ---: | ---: | ---: |
|  |  | Offline | Online | Total | Amortized | Offline | Online | Total | Amortized |
| CryptoNets | HE | - | - | 570 s | 70 ms | - | - | 595.5 MB | 0.07 MB |
| MiniONN | HE, MPC | 0.88 s | 0.40 s | 1.28 s | 1280 ms | 3.6 MB | 44 MB | 47.6 MB | 47.6 MB |
| GAZELLE | HE, MPC | 0 | 0.03 s | 0.03 s | 30 ms | 0 | 0.5 MB | 0.5 MB | 0.5 MB |
| E2DM | HE | - |  | 1.69 s | 26 ms | - | - | 23.93 MB | 0.37 MB |

## 7 Related Works

### 7.1 Secure Outsourced Matrix Computation

Matrix multiplication can be performed using a series of inner products. Wu and Haven [48] suggested the first secure inner product method in a SIMD environment. Their approach is to encrypt each row or column of a matrix into an encrypted vector and obtain component-wise product of two input vectors by performing a single homomorphic multiplication. However, it should aggregate all the elements over the plaintext slots in order to get the desired result and this procedure requires at least $\log d$ automorphisms. Since one can apply this solution to each row of $A$ and each column of $B$, the total complexity of secure matrix multiplication is about $d^{2}$ multiplications and $d^{2} \log d$ automorphisms.

Recently, several other approaches have been considered by applying the encoding methods of Lauter et al. [40] and Yasuda et al. [49] on an RLWE-based HE scheme. Duong et al. [17] proposed a method to encode a matrix as a constant polynomial in the native plaintext space. Then secure matrix multiplication requires only one homomorphic multiplication over packed ciphertexts. This method was later improved in [37]. However, this solution has a serious drawback for practical use: the resulting ciphertext has nonmeaningful terms in its coefficients, so for more computation, it should be decrypted and re-encoded by removing the terms in the plaintext polynomial.

Most of related works focus on verifiable secure outsourcing of matrix computation [11, 4, 38, 19]. In their protocols, a client delegates a task to an untrusted server and the server returns the computation result with a proof of the correctness of the computation. There are general results $[20,15,19]$ of verifiable secure computation outsourcing by applying a fully HE scheme with Yao's Garbled circuit or pseudorandom functions. However, it is still far from practical to apply these theoretical approaches to real-world applications.

### 7.2 Privacy-preserving Neural Networks Predictions

Privacy-preserving deep learning prediction models were firstly considered by Gilad-Bachrach et al. [23]. The authors presented the private evaluation protocol CryptoNets for CNN. A number of subsequent
works have improved it by normalizing weighted sums prior to applying the approximate activation function [10], or by employing a fully HE to apply an evaluation of an arbitrary deep neural networks [7].

There are other approaches for privacy-preserving deep learning prediction based on MPC [5, 41] or its combination with (additively) HE. The idea behind such hybrid protocols is to evaluate scalar products using HE and compute activation functions (e.g. threshold or sigmoid) using MPC technique. Mohassel and Zhang [39] applied the mixed-protocol framework of [16] to implement neural networks training and evaluation in a two-party computation setting. Liu et al. [34] presented MiniONN to transform an existing neural network to an oblivious neural network by applying a SIMD batching technique. Riazi et al. [42] designed Chameleon, which relies on a trusted third-party. Their frameworks were later improved in [28] by leveraging effective use of packed ciphertexts. Even though these hybrid protocols could improve efficiency, they result in high bandwidth and long network latency.

## 8 Conclusion and Future Work

In this paper, we presented a practical solution for secure outsourced matrix computation. We did demonstrate its applicability by presenting a novel framework E2DM for secure evaluation of encrypted neural networks on encrypted data. Our experimentation shows that E2DM achieves lower size of encrypted messages and latency compared to CryptoNets.

Our secure matrix computation primitive can be applied to various computing applications such as genetic testing and machine learning. In particular, we can investigate financial model evaluation based on our E2DM framework. Our another future work is to extend the matrix computation mechanism for more advanced operations.

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[^0]:    * An early version of this work was published in CCS 2018. In this version, we present better experimental results in Sections 5 and 6 based on our more recent implementation.

