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# SEMI-INSULATING TRANSITION METAL-DOPED III–V MATERIALS

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This review surveys the properties of transition metal (TM) doped semiinsulating (SI) III-V semiconductors. After a general definition of a SI material, a simple model of a SI crystal with a midgap donor and shallow impurities is discussed. A short history, main properties, and thermal stability problems of SI Cr-doped GaAs are presented. The puzzling problem of SI V-doped GaAs is explained. Several dopants (Cr, Fe, Co, and Ti) in SI InP are discussed in terms of the resistivities obtained, as well as thermal stability. Finally, GaP and GaInAs high resistivity systems are considered.

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#### 1. Introduction

Transition metal (TM) impurities in III-V compounds have been investigated for a quarter of a century. These omnipresent, multicharge, and multilevel deep centers, acting as donors, acceptors and sometimes double acceptors, have very significant impact on optical, transport, magnetic, and other properties of III-V semiconductors. Several review articles have recently been devoted to this subject (see, for example, [1-4]).

The first, and still the most important, application of transition metals impurities in III-V compounds is their ability to compensate shallow impurities and thus produce semi-insulating (SI) materials.

The term semi-insulating material has been used for many years, being introduced into the *Physics Abstracts* in 1969. However, in the late seventies, their importance has grown seriously, and since 1980, a series of conferences devoted exclusively to SI III-V materials has been organized [5-9]. The sixth of these will take place three weeks from now, in Toronto.

#### 2. General definition

A semi-insulated material may be defined as a high resistivity semiconductor crystal with the Fermi level firmly located in a midgap position. However, it cannot be an intrinsic semiconductor, because the Fermi level position should not change after standard thermal annealings which may generate shallow crystal defects. Furthermore, for many intentionally undoped crystals, concentrations of electrically active impurities cannot be made much smaller than  $10^{15}$  cm<sup>-3</sup>.

A semi-insulated material cannot also be a crystal which possesses only one dominant deep defect. In such a case, the Fermi level lies between this level and one of the band edges. Therefore, to fulfill our definition, a semi-insulating material must possess a deep defect partly compensated by shallow impurities.

Let us consider a model SI semiconductor with  $N_D$  midgap donors,  $N_{AS}$  shallow acceptors, and  $N_{DS}$  shallow donors. To calculate the free electron and free hole concentration (according to [10] and [11]) we take the following parameters, corresponding to GaAs at the room temperature [12]: the effective state density  $N_c = [2(2\pi m_e^* k)^{3/2}/h^3]T^3/2 = 4.21 \times 10^{17} \text{ cm}^{-3}$ ; the intrinsic carrier pair density  $n_i = 2.25 \times 10^6 \text{ cm}^{-3}$ ; the energy gap  $E_g = 1.42 \text{ eV}$ ; the deep donor midgap position  $E_D = E_g/2 = 0.71 \text{ eV}$ , their concentration  $N_D = 10^{14} \div 10^{17} \text{ cm}^{-3}$ ; and identical degeneracies of the occupied and unoccupied donor states  $(g_{D^0} = g_{D^+})$ .



Fig.1. Free carrier concentration in a model SI crystal as a function of the midgap donor concentration. The two crossing curves correspond to  $N_{AS} - N_{DS} = 10^{15}$  cm<sup>-3</sup>, the straight line corresponds to  $N_{AS} - N_{DS} = 0$ .

Figure 1 presents results of our calculations of  $n(N_D)$  and  $p(N_D)$  for  $N_{AS} - N_{DS}$  equal to zero and  $10^{15}$  cm<sup>-3</sup>, respectively. One can see that for  $N_{AS} - N_{DS} = 0$ ,  $n = 10^{10} \div 10^{11}$  cm<sup>-3</sup>, which corresponds to  $\rho$  of only  $10^3 \div 10^4\Omega$ cm. However, in the latter case, the free electron and free hole concentrations

obtained for the midgap donor concentration, remain within an order of magnitude of the intrinsic carrier concentration, even though  $N_D$  varies from  $1.5 \times 10^{15}$  to around  $5 \times 10^{16}$  cm<sup>-3</sup>. Such a material is sometimes called a semi-intrinsic crystal.

Figure 2 presents results of calculations of the resistivity of our model semiconductor as a function of  $N_D$ , when the difference  $N_{AS} - N_{DS}$  is again equal to  $10^{15}$  cm<sup>-3</sup>, and the concentration of ionized centers is of the order of  $10^{16}$  cm<sup>-3</sup>.



Fig.2. The resistivity of a model SI crystal as a function of the midgap donor concentration for  $N_{AS} - N_{DS} = 10^{15}$  cm<sup>-3</sup>.

Free carrier room temperature mobilities were thus estimated as  $\mu_e = 5000 \text{ cm}^2/\text{Vs}$  for electrons and  $\mu_h = 390 \text{ cm}^2/\text{Vs}$  for holes [13]. The resistivity values obtained remain above  $10^7 \Omega \text{cm}$  for more than two orders of magnitude change of  $N_D$ . The  $\rho(N_D)$  curve reaches a maximum at resistivity

$$\rho_{max} = \left[2en_i(\mu_e\mu_h)^{\frac{1}{2}}\right]^{-1} = 10^9 \,\Omega \text{cm}$$

which corresponds to the ratio  $p/n = \mu_e/\mu_h = 13$ , i.e.  $p = 8.1 \times 10^7$  cm<sup>-3</sup> and  $n = 6.3 \times 10^5$  cm<sup>-3</sup>.

## 3. TM-doped SI GaAs crystals

To obtain an ideal semi-insulating TM-doped semiconductor, one should choose a TM impurity with the following properties: a) a mid-gap acceptor or mid-gap donor level, b) good thermal stability. It is not easy to fulfill both these conditions. The first TM-doped SI material was Cr-doped GaAs. Chromium acts as a deep, mid-gap acceptor in GaAs (Fig. 3), as was shown by Haisty and Cronin



Fig.3. The energy levels of the chromium impurity in GaAs, GaP, and InP. The dashed lines represent the levels which are predicted but not yet observed.

from Texas Instruments in 1964 [14] (see Fig. 4). Thus, chromium impurities are able to compensate the shallow donors (Si, S, Se and Te) typically present in the concentration range of  $10^{15} - 10^{17}$  cm<sup>-3</sup> in horizontal Bridgman (HB) and liquid encapsulated Czochralski (LEC) GaAs crystals grown in silica crucibles. For concentration of chromium impurities  $N_{\rm CR} > N_{DS} - N_{AS}$ , the Fermi level is firmly located in the mid-gap position. This property of chromium was the basis of U.S. Patent #3344071 (1967), due to G.R.Cronin. The patent concerns growth of SI GaAs crystals with resistivity values above  $10^8 \Omega$ cm by doping with 0.2–0.5 ppm of chromium. One of the examples presented in this patent is a 100 g GaAs crystal, grown in an aluminum crucible, doped with 100 mg of chromium. The crystal had resistivity equal to  $3.5 \times 10^8 \Omega$ cm.

A detailed analysis of the LEC Cr-doped GaAs resistivity performed about two decades later on 1-4 kg crystals is shown in Fig. 5 [15]. The Cr concentration was measured with the help of a radiotracer <sup>51</sup>Cr technique. Two regions of almost constant resistivity are evident, with an apparent threshold at the Cr concentration of about 1 ppm. The lower resistivity regime  $(1 \div 2 \times 10^8 \Omega \text{cm})$  corresponds to



Fig.4. Variation of resistivity as a function of temperature for TM-doped GaAs [14].

nominally undoped SI GaAs with the Fermi level located probably at the deep donor EL2 (see, for example, [16]). In the higher resistivity regime  $(5 \div 9 \times 10^8 \Omega \text{cm})$ , the Fermi level is really pinned to the chromium  $\text{Cr}^{3+/2+}$  acceptor level.

The only serious technological problem with Cr-doped GaAs crystals was the poor thermal stability of chromium. In GaAs wafers annealed at temperatures in excess of about 700°C, changes in conductivity type are likely to occur [17]. These effects are not always reproducible. The most frequent effect was conversion to p-type conductivity, but conversion to n-type was sometimes also observed. All these effects were surface phenomena taking place in a few micron thick layers. Furthermore, during epitaxial crystal growth on SI Cr-doped wafers, the chromium atoms diffuse out of the substrates, and the first few microns of a nominally undoped epitaxial layer have a resistivity that is much higher than expected. Moreover, accumulation of chromium was observed at surfaces after post-implantation annealing, or at substrate-layer interfaces after epitaxial growth (see [17] and [18]). This outdiffusion process was found even at relatively low temperatures ( $500 - 700^{\circ}$ C) (see Fig. 6), and may have serious impact on electronic device properties.

In the early eighties, the above mentioned thermal stability problems, as well



Fig.5. SI GaAs resistivity as a function of Cr concentration, using samples taken from four separate crystals [15].

as successful growth of undoped SI GaAs LEC material grown in PBN crucibles, reduced the role of SI Cr-doped GaAs. However, about 50% of production of Sumitomo Electric Industries (the first GaAs world producer) is still 3-inch HB Cr-doped GaAs grown in 1 meter long quartz crucibles.

During the last few years, serious improvements in HB growth technology have made growth of thermally stable Cr-doped GaAs possible. The general idea is to reduce the dislocation density and, as a result, to eliminate the mechanism of chromium diffusion through extended crystal defects. One of the methods of dislocation density reduction is strong  $(5 \div 50 \times 10^{19} \text{ cm}^{-3})$  doping with indium (patented in 1970 [20]). Indium (and also other isoelectronic impurities) does not affect the electrical properties of SI GaAs, but is hardening the GaAs matrix and, as a result, strongly reduces its dislocation density. In 1985, a co-doping of GaAs with Cr and In was proposed in two patents [21-22], as a method of obtaining thermally stable SI material. Recently, thermally stable HB Cr-doped SI GaAs crystals were reported by Sumitomo Electric Industries [23-24]. These low dislocation density  $(1 \div 5 \times 10^3 \text{ cm}^{-2})$ , 2- and 3-inch, Cr-doped SI GaAs crystals were grown in a state-of-the-art three zone (3T-HB) Bridgman furnace. A second group of crystals was additionally co-doped with indium at a relatively low concentration level  $(1.5 \div 4 \times 10^{19} \text{ cm}^{-3})$ . The latter crystals have drastically lower dislocation densities  $(< 10^3 \text{ cm}^{-2})$  and much smaller radial variation of resistivity than those



Fig.6. Chromium profile in an MBE layer, measured by SIMS [19]. The substrate was heated to 530°C for 30 minutes before growth.

Fig.7. Free electron concentration and their mobility as a function of vanadium concentration for several samples from two HB GaAs crystals [30].

of the first group. Crystals from both the groups remained stable after 30 minute annealing at 850°C in AsH<sub>3</sub> atmosphere. Thus, the SI HB GaAs:Cr,In material is already considered as one of the four major SI GaAs substrates for the nineties.

Some years ago, several V-doped SI GaAs crystals and layers with resistivities of around  $10^7 - 10^8 \Omega$ cm were also reported – LEC [25], VPE [26], MOCVD [27], and HB [28]. This material could be a very promising one, since the diffusivity of V in GaAs was shown to be one order of the magnitude lower than that of Cr [25]. However, detailed optical and electrical investigations of a series of V-doped crystals have clearly shown [28-29] that the vanadium  $V^{3+/2+}$  acceptor level is located 0.15 eV below the bottom of the conduction band and the mid-gap level responsible for the high resistivity of these materials is the EL2 deep donor. The role of vanadium in the crystal growth process may be explained with the help of Fig. 7 [30]. Acting purely as an acceptor, vanadium should compensate shallow donors and decrease the free electron mobility. The observed mobility enhancement as a function of V concentration, strongly suggests that a purification process is taking place. SIMS measurements have confirmed this hypothesis. Silicon and sulphur concentrations, which normally increase from the seed to the tail end of a HB crystal, strongly decrease in the presence of vanadium. Thus, vanadium plays a mainly chemical role (gettering of donors) during the crystal growth process [30]. Recent MOVPE growth of V-doped GaAs [31] has confirmed this interpretation. It was shown that only less than 1% of vanadium atoms in GaAs MOVPE layers are electrically active. Furthermore, attempts to create a V-related complex (suggested by many authors) by implantation into GaAs of V, O, V+O, Si+V+O, and S+V+O, for variety of doses and at different energies, have led to an unequivocally negative result [31].

## 4. TM-doped SI InP crystals

The first SI InP reported was a Cr-doped material analogous to GaAs [32]. However, the chromium  $Cr^{3+/2+}$  acceptor level is located in the upper part of the energy gap in InP (see Fig. 3) and only  $10^3 - 10^5 \Omega cm$  resistivity range could be achieved at the room temperature. In 1975, SI Fe-doped InP crystals were obtained at Nippon Electric [33]. The mid- gap position of the Fe<sup>3+/2+</sup> acceptor level (see Fig. 8) makes it possible to obtain SI InP with resistivities of around  $10^7 - 10^8 \Omega cm$ .



Fig.8. The energy levels of the iron impurity in GaAs, GaP, and InP. The dashed lines represent levels which are predicted but not yet observed.

Up to now, this material remains the main SI InP substrate. State-of-the-art 2-inch crystals with dislocation densities of around  $1 \div 4 \times 10^3$  cm<sup>-2</sup> are grown by the Electro-Dynamic Gradient (EDG) (also called "Electronic Bridgman") technique at the AT&T Bell Laboratories [34] or by the multi-zone LEC technique at

Sumitomo Electric [35]. Unfortunately, outdiffusion properties similar to those of Cr in GaAs were reported for Fe in InP [17], [36] (see Fig. 9).



Fig.9. A comparison of the Fe distribution in InP:Fe and InP:Fe + Ga wafers before and after annealing under phosphorus vapor pressure [37].

Other TM acceptors, such as Co, were also used to obtain SI InP (see, for example, [38]), but due to the  $\text{Co}^{3+/2+}$  level being in the lower part of the energy gap (at about  $E_v + 0.3$  eV [4]), the resistivity obtained is only around  $10^5 \Omega \text{cm}$ . Furthermore, the Co impurity diffusion coefficient is larger than that for Fe and

one cannot expect thermal stability better than that for Fe-doped InP for this material.

In the mid-eighties, this situation stimulated serious activity in search of new, thermally stable SI InP TM-doped substrates [39].

Fe-doped InP material can also be improved by co-doping with the isoelectronic Ga impurity [37], or Ga and As impurities [40], which reduces the dislocation density. Thermal stability of Fe and Ga-doped InP was shown to be much better than that resulting with no Ga co-doping [37] (see Fig. 9).

Another possibility, developed recently for InP, is co-doping with deep TM donors and shallow acceptors. Such an idea was proposed for two deep mid-gap TM donors in InP (Ti and Cr) [41-42] (see Fig. 3 and 10). SI crystals of Cr-doped



Fig.10. The energy levels of the titanium impurity in GaAs, GaP, and InP. The dashed line represents a level which is predicted but not yet observed.

In P, co-doped with Cd or Hg, reached resistivities of up to  $10^6 \Omega \text{cm}$  [43]. However, the thermal stability of Cr was found to be similar to that of Fe in InP.

The titanium  $Ti^{4+/3+}$  donor level has been much more promising, because diffusivity trends of TM in III-V crystals predict [44] that it will have a much - lower diffusivity than is the case for iron. There are several values of the titanium donor ionization energy reported in the literature, the most reasonable one being  $E_c = 0.5 \text{ eV}$ , obtained with the help of high pressure measurements [45]. SI crystals of Ti-doped InP, co-doped with Be, Cd, Hg, and Zn, with resistivities of up to  $5 \times 10^6 \Omega$ cm, were grown by several groups (see Table I). To check their thermal stability, Ti profiles were measured in Ti + Hg-doped InP [37], as well as Ti and Zn profiles in Ti + Zn-doped InP [48] (see Fig. 11). All these measurements, performed in the 800 - 975°C temperature range, showed the very high thermal stability of



Fig.11. SIMS profiles of Ti and Zn dopants in an InP wafer after annealing at 900°C for 30 min. with 3 atm of phosphorus overpressure [48].

the Ti and Zn profiles.

The last possibility which should be mentioned is MOVPE growth of SI InP layers co-doped with Fe and Ti [50-51]. The material obtained, with resistivities higher than  $10^7 \Omega$ cm, remains SI in the presence of an excess of shallow donors, as well as an excess of shallow acceptors. This material was already used in AT&T Bell Laboratories for development of low-threshold low-leakage buried lasers, without the use of additional dielectric layers [50].

#### 5. TM-doped SI GaP crystals

SI GaP material may be useful for high-temperature electronic applications. SI crystals doped with Fe [52], and V [53], and also SI layers doped with Cr [54] were reported in the literature. For Fe- and Cr-doped materials, one may, in spite of their high resistivities (related to the deep positions of these acceptors in the GaP energy gap (see Figs.3 and 8)), expect thermal stability problems like similar to those found for GaAs and InP. In the case of V-doped GaP, the resistivity reported is  $3 \times 10^8 \Omega$ cm (the vanadium  $V^{3+/2+}$  acceptor level is located 0.58 eV below the bottom of the conduction band [53]). This material is expected to be thermally stable. A good method of obtaining even higher resistivity GaP seems to be co-dopping with Ti and shallow acceptors. The titanium Ti<sup>4+/3+</sup> donor level lies about 1 eV above the valence band (see Fig. 10). Another possibility may be co-dopping with Ti and Fe.

#### TABLE I.

Growth	Acceptor	ρ <sub>300</sub>	$n_{300}$	$\mu_{300}$	Reference
techn.		$(\Omega \text{ cm})$	(cm_v)	$(cm^2/vs)$	
GF	Hg	$4.7 \times 10^{4}$	$7.7 \times 10^{10}$	1700	[42]
$\mathbf{GF}$	Hg	$5.2 \times 10^{4}$	$9 \times 10^{10}$	1350	[42]
$\mathbf{GF}$	Hg	$7.5 \times 10^{4}$	$6.3 \times 10^{10}$	1300	[42]
GF	Hg	$> 5 \times 10^{5}$			[37]
LEC	Be	$1 \times 10^{6}$	$2.5 \times 10^{9}$	2500	[46]
LEC	Cd	$1.1 \times 10^{6}$	$2.2 \times 10^{9}$	2600	[46]
LEC	Zn	$3.3 \times 10^{6}$	$7.3 \times 10^{8}$	2600	[46]
LEC	Zn	$1.6 \times 10^{5}$	$1.5 \times 10^{10}$	2600	[47]
LEC	Zn	$5 \times 10^{6}$	$6 \times 10^{8}$	2100	[47]
LEC	Zn	$1.6 \times 10^{6}$	$2.1 \times 10^{9}$	1830	[48]
LEC	Zn	$5.7 \times 10^{6}$	$7.8 \times 10^{8}$	1400	[48]
LPE	Zn	$2.3 \times 10^{3}$			[49]
LPE	Zn	$2.7 \times 10^{4}$			[49]

Reported SI Ti-doped InP crystals.

#### 6. TM-doped ternary and quaternary compounds

It should also be noted that the variety of the TM at our disposal gives us a possibility of looking for midgap levels in ternary and quaternary III-V layers. Several examples of such doping may be found in the literature. The most important of these compounds is  $In_{0.53}Ga_{0.47}As/InP$ . Its doping with Fe [55], or Ti [56], or both of these [51], were recently reported. Both impurities form mid-gap levels in this compound. The titanium position (at  $E_c = 0.37 \pm 0.02$  eV) is nearly ideal, because  $E_g/2 = 0.75/2$  eV = 0.375 eV. The resistivities obtained are of the order of  $10^3 \Omega$ cm, close to the intrinsic limit.

# 7.Conclusions

In conclusion, one can predict further investigations of new SI materials such as layers of ternary and quaternary compounds doped with  $3d^n$  TM impurities, as well as bulk III-V crystals doped with  $4d^n$  and  $5d^n$  TM impurities. We may also expect further improvements of the SI systems already known. There is no "ideal" SI material, and it is still wanted...

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