



THE UNIVERSITY *of* EDINBURGH

Edinburgh Research Explorer

Single Photon Counting Performance and Noise Analysis of CMOS SPAD-based Image Sensors

Citation for published version:

Dutton, N, Gyongy, I, Luca, P & Henderson, R 2016, 'Single Photon Counting Performance and Noise Analysis of CMOS SPAD-based Image Sensors', *Sensors*, vol. 16, no. 1122, pp. 1-17.
<https://doi.org/10.3390/s16071122>

Digital Object Identifier (DOI):

[10.3390/s16071122](https://doi.org/10.3390/s16071122)

Link:

[Link to publication record in Edinburgh Research Explorer](#)

Document Version:

Peer reviewed version

Published In:

Sensors

General rights

Copyright for the publications made accessible via the Edinburgh Research Explorer is retained by the author(s) and / or other copyright owners and it is a condition of accessing these publications that users recognise and abide by the legal requirements associated with these rights.

Take down policy

The University of Edinburgh has made every reasonable effort to ensure that Edinburgh Research Explorer content complies with UK legislation. If you believe that the public display of this file breaches copyright please contact openaccess@ed.ac.uk providing details, and we will remove access to the work immediately and investigate your claim.



1 Article

2 **Single Photon Counting Performance and Noise**
3 **Analysis of CMOS SPAD-based Image Sensors**4 Neale A.W. Dutton ^{1,*}, Istvan Gyongy ², Luca Parmesan and Robert K. Henderson ²5 ¹ STMicroelectronics Imaging Division, Pinkhill, Edinburgh, UK, EH12 7BF; neale.dutton@st.com6 ² CMOS Sensors and Systems Group, School of Engineering, The University of Edinburgh, Edinburgh, UK,
7 EH9 3JL; robert.henderson@ed.ac.uk

8 * Correspondence: neale.dutton@st.com

9 Academic Editor: Prof Eric Fossum

10 Received: 21st January 2016; Accepted: date; Published: date

11 **Abstract:** SPAD-based solid state CMOS image sensors utilising analogue integrators have attained
12 deep sub electron read noise (DSERN) permitting single photon counting (SPC) imaging. A new
13 method is proposed to determine the read noise in DSERN image sensors by evaluating the peak
14 separation and width (PSW) of single photon peaks in a photon counting histogram (PCH). The
15 technique is used to identify and analyse cumulative noise in analogue integrating SPC SPAD-based
16 pixels. The DSERN of our SPAD image sensor is exploited to confirm recent multi-photon threshold
17 quanta image sensor (QIS) theory. Finally, various single and multiple photon spatio-temporal
18 oversampling techniques are reviewed.

19 **Keywords:** Single Photon Avalanche Diode, SPAD, CMOS Image Sensor, CIS, Single Photon
20 Counting, SPC, Quanta Image Sensor, QIS, Spatio-Temporal Oversampling.

22 **1. Introduction**

23 Imaging a few photons per pixel, per frame, demands pixels operating in the single photon
24 counting regime. This challenge is encountered in either low-light or high-speed imaging; at long
25 integration times (ms to s) and low photon flux, or short integration times (μ s or less) and high photon
26 flux, respectively. Examples are high-speed cameras for engine and exhaust combustion analysis,
27 low-light or night-vision cameras for defence [1], staring applications in astronomy and many
28 scientific applications such as, spectroscopy, fluorescence lifetime imaging microscopy (FLIM) [2][3],
29 positron emission tomography (PET) [4], fluorescence correlation spectroscopy (FCS) [5], Forster
30 Resonance Emission Tomography (FRET) [6], and in automotive applications for LIDAR [7].

31 For true photoelectron (or photon) counting to be reached, the ratio of the input sensitivity or
32 signal to the noise of the imaging system must be sufficiently high to allow discrete and resolvable
33 signal levels for each photoelectron to be discriminated. Referring the readout noise to the input
34 sensitivity in photoelectrons, the single photon counting regime is theoretically entered below 0.5e-
35 input referred read noise (RN)[8], but practically there is a 90% accuracy of determining the number
36 of photoelectrons at 0.3e- RN, and approaching 100% accuracy at 0.15e- RN [9]. These probability
37 figures, assume RN is Gaussian distributed and the discrimination thresholds between one
38 photoelectron signal, to the next, are set precisely mid-way and do not take into account fixed pattern
39 noise (FPN) or gain variations in photo-response non-uniformity (PRNU). Such sensors in this
40 photon-counting regime with approximately $<0.3e-$ RN may be referred to as deep sub-electron read
41 noise (DSERN) image sensors [10].

42 With high charge to voltage factor (CVF) sensitivity (or conversion gain (CG)), DSERN pixels
43 have limited photoelectron or photon counting capability (full well capacity), and therefore restricted

44 dynamic range (DR). DR may be extended by a range of techniques: exposure control with the capture
45 of multiple sequential images [11], pixel design with dual integrations (e.g. lateral overflow
46 integration capacitors (LOFIC) [12]), or by combining multiple pixel samples through spatio-
47 temporal oversampling [13], [14]. In the latter the number of oversampled frames is traded off against
48 the frame rate.

49 This paper evaluates the single photon counting and noise characteristics of our recent work on
50 SPAD-based image sensors [15]–[17] and analyses the benefits, tradeoffs and noise performance of
51 various spatio-temporal oversampling techniques [18], [19]. A new method of determining RN, CVF
52 and other imaging measurements of DSERN image sensors is described.

53

54 2. Solid-state Single Photon Counting Imaging Background

55 Since the late 1980's, single photon counting (SPC) and time-gated imaging have been
56 dominated by photo-cathode based intensifier techniques achieving high signal amplification
57 through the 'photo-intensification' of the generated electron cascade through the photo-electric effect
58 using existing charge-coupled device (CCD) and CMOS image sensors (CIS) [1]. However, there are
59 a number of drawbacks which limit their usage dependent on the application. Namely, the
60 wavelength (colour) and spin properties of the photons are lost. Systems have high cost and are
61 physically bulky due to the requirement of operation in a vacuum. Furthermore, photo-cathodes are
62 sensitive to magnetic fields, they have high (kV) operating voltage and also cannot be used in-vivo.
63 Solid-state photon counting image sensor technologies, developed over the last 16 years, address
64 some of these issues.

65 The electron-multiplying CCD (EMCCD) was first demonstrated in 2001 [20], and has recently
66 achieved $0.45e^-$ RN [21]. However, dark current is amplified through the electron multiplication
67 process, and therefore external cooling is employed [22]. The first solid-state CIS pixel array with
68 DSERN appeared in 2015, achieving best-case $0.22e^-$ RN in a remarkable $1.4\mu\text{m}$ pixel pitch (PP) with
69 $403\mu\text{V}/e^-$ CVF [10]. Later, the first photon-counting CMOS imager achieved $0.27e^-$ RN, by external
70 cooling and a high CVF of $220\mu\text{V}/e^-$ was realised by removing the reset transistor [23]. Oversampling
71 ADCs have been employed in CIS to reduce all sources of readout noise ($1/f$, systematic temporal,
72 source follower thermal, etc.) by correlated multiple sampling (CMS). The lowest published CIS RN
73 in voltage (estimated by the author as CVF multiplied by RN) through four sample CMS is $31.7\mu\text{V}$
74 RMS [24]. Therefore, with CVF surpassing $400\mu\text{V}/e^-$ and RN as low as $31.7\mu\text{V}$ RMS, CIS with sub
75 $0.15e^-$ RN appears not an unreasonable assumption in the near future.

76 Single photon avalanche diode (SPAD) image sensors emerged in 2002 with bump-bonded
77 SPADs [25] onto a digital counter or time-to-digital converter (TDC) per SPAD device recording the
78 time of arrival of single photons. High temporal resolution ($\approx 50\text{ps}$ [26]) permits time resolved
79 imaging such as capturing light-in-flight [27], and seeing round corners [28]. These time correlated
80 single photon counting (TCSPC) sensors have favoured the temporal precision of the photon's arrival
81 over spatial resolution ($>44\mu\text{m}$) and fill-factor ($<4\%$) which has, so far, restricted the wider adoption
82 of these sensors. The digital circuit providing photon counting or timing occupies the majority of the
83 pixel area to the detriment of photon detection. Chip stacking technology and the use of advanced
84 digital CMOS process technologies are two methods that pitch reduction and fill factor increase will
85 be achieved for SPAD-based image sensors in the future. Regardless of the technology, to realise high
86 fill factor SPAD pixels, a trade-off is made between optical efficiency versus in-pixel functionality or
87 the number of in-pixel transistors; low-transistor count analogue circuits will always be more
88 compact than digital circuits. Our recent research has focused on time resolved photon counting
89 applications using alternative analogue pixel designs that achieve higher fill factor and smaller pixel
90 pitch, namely analogue counters [15], time-to-amplitude converters (TAC) [29], [30] and single bit
91 binary memories [17].

92 Binary SPAD-based imagers, with the capability of recording one SPAD avalanche within an
 93 integration time, were first published in 2011 [31] and have recently been published at 65k binary
 94 pixels [32] and in our work at 77k binary pixels [17]. Binary black and white imaging is not inherently
 95 practical for many imaging applications, therefore spatio-temporal oversampling is employed to
 96 create gray levels [14], [19]. SPAD-based image sensors based on analogue counting techniques first
 97 appeared in [33] and have recently been demonstrated with 8 to 15µm PP commensurate with CCD,
 98 EMCCD and sCMOS image sensors, and fill factor (FF) as high as 26.8% [15], [16], [34]. These sensors
 99 achieve time-gating comparable to gated photo-cathodes in the nanosecond [18] and sub nanosecond
 100 range [34]. Analogue-based SPAD imagers employ conventional CIS readout techniques and so, to
 101 aid comparison with CCD and CIS, equivalent metrics may be applied such as:

- 102 • Sensitivity, of the counter circuit to one SPAD avalanche event in mV/SPAD event,
 103 equivalent to CVF (or CG).
- 104 • Maximum number of SPAD events equivalent to full well.
- 105 • Input referred RN normalising voltage RMS RN to one SPAD event instead of one
 106 photoelectron.

107
 108 These equivalencies are used throughout this paper. SPAD-based image sensors are the first
 109 solid-state imaging technology to have demonstrated sub 0.15e- RN, and as such provide a look-
 110 ahead to the signal and noise characteristics of DSERN image sensors in CMOS and other
 111 technologies.

112

113 3. Single Photon Counting Noise Modelling and Analysis

114 The first part of this section details a model of read noise and sensitivity (or CVF) developed to
 115 characterise our recent work in SPAD-based imaging. The second part discusses three noise
 116 measurement methods for DSERN image sensors based on the photon counting histogram (PCH).
 117 The use of single photon counting histograms are not new to the imaging community but the analysis
 118 presented here seeks to model and quantify the noise measurements that may be obtained from the
 119 PCH. A discrete Poisson probability density function (PDF) may represent photoelectrons (or
 120 photons) either from multiple reads of a single pixel or a single read of multiple pixels. For a single
 121 pixel 'i', the PDF for the captured photoelectrons k may be represented as:

$$P(i, k) = \frac{\lambda^k \exp(-\lambda)}{k!} \quad : \quad k \in \mathbb{Z} \quad (1)$$

124 Where λ = mean number of photoelectrons in the integration period. PRNU may be modelled
 125 to first order as a normal distribution with mean CVF μ_{CVF} and variance σ_{CVF}^2 . For each electron k, the
 126 ideal voltage domain input signal S_{IN} is created with the signal from each electrons at a separation
 127 $v_{(i,k)}$ equal to the CVF for that pixel 'i':

$$v(i, k) = k \cdot CVF(i) \quad 129 \quad (2)$$

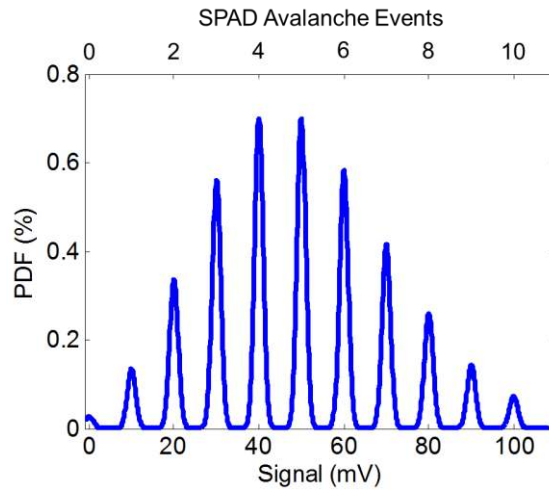
$$S_{IN}(v_{k,i}) = P(i, k) \quad 131 \quad (3)$$

157 For each electron k , assuming the read noise is dominated by thermal noise it follows a Gaussian
 158 distribution. Read noise σ_{RN} is applied on each electron's output signal S_k for the range $v=0$ to $(n.CVF)$:
 159 where n is the maximum number of electrons in the Poissonian PDF in Eqn.(1):

$$S_k(v) = \frac{1}{\sigma_{RN}\sqrt{2\pi}} \cdot \exp\left(-\frac{(v - S_{IN}(v_k))^2}{2\sigma_{RN}^2}\right) \quad (4)$$

160

161



162

163 **Figure 1.** Photon counting histogram (PCH) generated by the read noise model with CVF equivalent of 10mV/e,
 164 mean $\lambda = 5e^-$ exposure and 0.1e- equivalent RN.

165

166 The voltage domain output signal is then represented as the summation of each of the
 167 constituent signals for each electron within the PDF:

$$S_{OUT}(v) = \sum_{k=0}^n S_k(v) \quad (5)$$

168

169 Figure 1 provides a photon counting histogram (PCH) example of the output of the model given
 170 by Eqn. 5 with 10mV/SPAD event (or 10mV/e- equivalent) and 0.1e- equivalent RN. As seen in the
 171 figure, discrete peaks are visible in the PCH. The RN distribution around each photon counting peak
 172 can be determined using three recent methods:
 173

174 3.1. Valley to Peak Ratio Method

175 Fossum et al. proposed the Valley to Peak ratio Method (VPM) detailed in [10], [35]. This
 176 measures the peak height and the neighbouring valley height (or dip between photon peaks) in the
 177 PCH. The VPM has an upper and lower RN measurement limit. Although theoretically possible, it is
 178 difficult in practice to obtain peaks and valleys in PCHs in the region of 0.5e- to $\approx 0.45e^-$ RN giving an
 179 upper limit to VPM. At the lower limit, below 0.15e- RN, the VPM is inherently restricted as the valley
 180 has reached the 'floor' of the PCH (zero counts in more than one adjacent bin), and a companion
 181 method is needed.

227 3.2. Peak Separation and Width Method

228 The Peak Separation and Width (PSW) method is proposed in this paper, and has been used in
 229 this paper to measure the SPAD-based image sensors in our recent work [15], [16]. The previous VPM
 230 measurement evaluates vertically in the PCH, whereas this PSW method operates in the voltage
 231 domain or horizontally in the PCH. By determining, the centroid of each single photon counting peak
 232 (whether by taking the peak position, or using a centroid weight algorithm, or similar), the peak
 233 separation data may provide a number of measurements:

- 234 • The sensitivity or CVF per pixel ('i') is established by mean peak separation in a per-pixel
 235 PCH.
- 236 • The PRNU and the average CVF of the sensor are evaluated through a histogram of the
 237 compiled peak separation data from step 1 above, taking RMS and mean respectively.
- 238 • Vertical, horizontal and pixel to pixel FPN (VPFN, HFPN, PPFPN) are exhibited as horizontal
 239 offsets to the peaks, in the set of per pixel PCHs.

240
 241 The width of each peak is measured to deduce the noise characteristics of the sensor. The full
 242 width half maximum (FWHM) of each peak is captured (preferably using interpolative fitting
 243 between PCH bins to lessen errors from quantisation and non-linearity in calculations). Assuming
 244 the noise around each peak is normally distributed, the FWHM may be converted to standard
 245 deviation using the conventional expression:

$$246 \quad \sigma = \frac{FWHM}{2\sqrt{2\ln 2}} \rightarrow \sigma \approx \frac{FWHM}{2.3548} \quad (6)$$

247 The interested reader may create a more complete noise model by expanding equations (4) and
 248 (6) to take into account other read noise sources (reset, flicker, etc.). Ideally the peak width remains
 249 constant across the full signal range, and RN is determined by the mean of the peak width data.
 250 However, if a signal dependent noise source is present then the peak widths will increase (and peak
 251 heights decrease) for increasing signal. There is no lower limit to the PSW method. However, the
 252 upper limit is set by the height of the valley between two peaks: by definition this valley must be
 253 lower than half of the two adjacent peak heights which evaluates at $< 0.3e \cdot RN$ approximately.

254

255 3.3. Regressive Modelling and Fitting Method

256 The third method fits and scales the noise model described above, against a PCH (whether a
 257 single exposures of a full sensor or multiple exposures per pixel). This method has been used in [23]
 258 to graphically confirm the correct evaluation of RN and mean exposure. This method is expanded
 259 here to encompass the previous two methods. First the VPM and PSW are used (as appropriate given
 260 their respective limits) to obtain an estimate of RN and CVF to restrict the scaling and fitting 'search'
 261 domain. Next the iterative process begins, recording the goodness of fit of the recorded PCH to the
 262 modelled PCH and continuing the regression analysis (by whichever chosen fitting method).

263 Like the PSW method, this regression analysis should be performed per pixel to obtain the CVF,
 264 PRNU and FPN distributions of the image sensor. Furthermore, as in PSW, ADC non-linearity will
 265 affect the regression analysis so some method of interpolation between PCH bins may be necessary.
 266 The downside to this method, is its computationally intensive nature and the requirement to have a
 267 consistent mean number of photons for exact fitting. The Poisson distribution in Eqn. (1) assumes a
 268 constant mean number of photoelectrons (i.e. constant light level) through successive reads of a single
 269 pixel, and a constant light level across the array with equal sensitivity (0% PRNU). The advantage of
 270 the method is that the model can be expanded to account for known converter non-linearity or other
 271 noise sources, such as described in the following sections.

293 4. Analogue Counter and Photon Counting Performance

294 Single photon counting is achieved in the analogue domain with a SPAD avalanche pulse
 295 triggering an integrator circuit based on the principle of the charge transfer amplifier (CTA) whose
 296 operation is briefly described here, and in further detail in [15], [16].

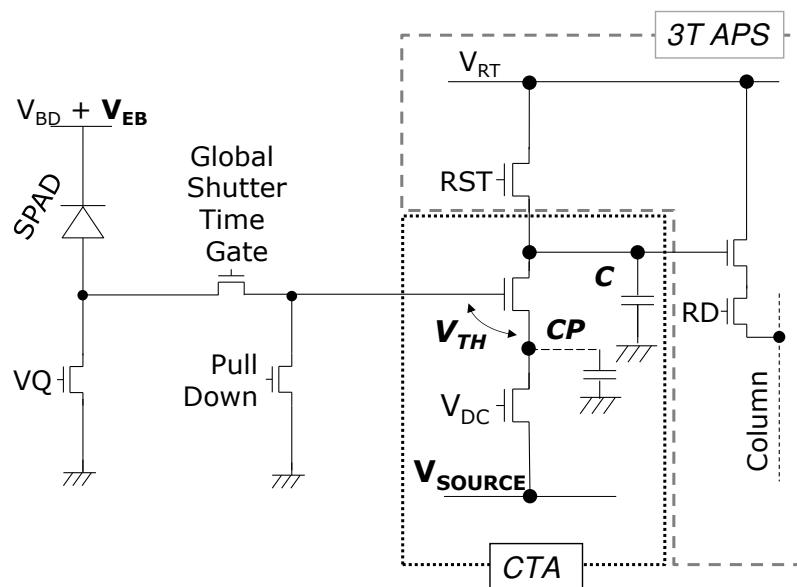
297 In reference to Figure 2, the CTA is reset by pulling the main capacitor 'C' to the high reset
 298 voltage V_{RT} . The CTA operates by the input gate voltage (in this case the SPAD anode voltage)
 299 increasing above the threshold voltage of the input source follower. Charge flows from the main
 300 capacitor 'C' to the parasitic capacitor 'C_P' and the voltage rises on the parasitic node. The rising
 301 voltage pushes the source follower into the cut-off region and the charge flow halts, causing a discrete
 302 charge packet to be transferred from the main capacitor for each input pulse. The voltage step
 303 sensitivity (CVF equivalent) of CTA pixels is determined by the fixed capacitor ratio (parasitic
 304 capacitance 'C_P' divided by integration capacitor 'C') scaling down the input voltage spike. The CTA
 305 voltage step (ΔV_{CTA}) is bias controllable by ' V_{SOURCE} ' and given to a first order by the equation:

$$306 \quad \Delta V_{CTA} = \left(\frac{C_P}{C}\right) \cdot (V_{EB} - V_{SOURCE} - V_{TH}) \quad (7)$$

307 Where V_{EB} is the excess bias of the SPAD above the breakdown voltage V_{BD} , V_{SOURCE} is the global
 308 CTA source bias voltage, and V_{TH} is the threshold voltage of the CTA input transistor.

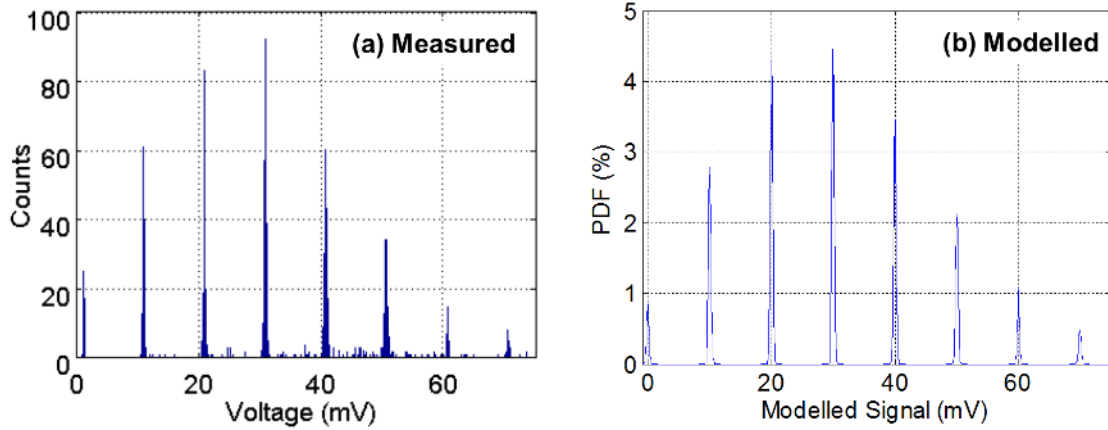
309

310



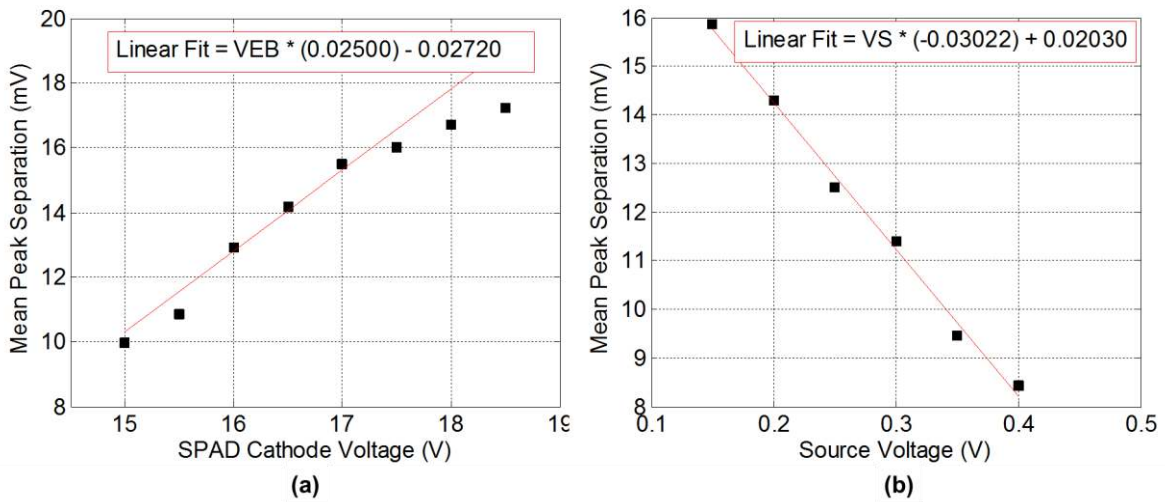
311

312 **Figure 2.** Charge transfer amplifier (CTA) analogue integrator pixel with active pixel sensor (APS)
 313 readout for global shutter or time-gated SPAD-based photon counting imaging.



314
315
316
317

Figure 3. (a) Measured PCH of the analogue counting pixel test structure in [15], (b) Modelled PCH with mean $\lambda = 3$ SPAD events, CVF equivalent of 10mV/SPAD event and equivalent 0.02e- RN.



318
319
320
321
322

Figure 4. Measured mean peak separation from a set of PCHs, (a) The relationship of counter sensitivity to SPAD operating voltage, (b) The relationship to CTA V_{SOURCE} voltage.

V_{SOURCE} Bias Voltage (mV)	Linear Full Well Voltage (mV)	Sensitivity from Linear Fit (mV / SPAD Event)	Input Referred Read Noise (SPAD events)	Equivalent Linear Full Well (SPAD Events)
200	802.8	14.26	0.064	56
300	722.1	11.23	0.082	64
400	651.4	8.21	0.113	79
500	648.3	5.19	0.178	125

323
324

Table 1. Photon counting performance of 320x240 SPAD-based image sensor [16].

325 Figure 3 (a) illustrates an example of the output of one test structure pixel recorded with 1,000
 326 repetitions of 30 μ s integration time and ADC conversion from [15]. 1,000 repetitions was chosen to
 327 give an adequate number of data samples versus experimental time. The SPAD is biased at 2.7V V_{EB}
 328 above breakdown voltage $V_{BD} \approx 13.4V$. The discrete peaks under a classical Poisson distribution are
 329 clearly evident indicating the photon counting in this example is shot noise limited. Fig 3(b) is the
 330 side-by-side modelled PCH from a manual regressive modelling and fitting method analysis. The
 331 parameters were chosen for the closest found fit, although an offset in the x-axis is still present. In
 332 Fig.3 (a), there is a slight 'in-filling' of some data values between the peaks. This is attributed to a
 333 distortion mechanism in the passively operated CTA circuit due to the imperfect reset, or incomplete
 334 discharge, of the parasitic capacitance C_P for short inter-arrival times of two SPAD avalanche events
 335 less than 100 ns apart.

336 The PSW method is performed for the image sensor in [16] to determine the response of the
 337 analogue counter to the SPAD excess bias and the source bias voltage. Fig.4 illustrates the relationship
 338 of the mean peak separation or image sensor sensitivity to both the SPAD excess bias and the CTA
 339 source voltage. The absolute value of the linear gradient fitting parameter indicates the capacitor ratio
 340 whilst the offset parameter indicates the other terms in the CTA equation.

341 The linear full well (defined as a deviation of 3% in sensor output from an ideal linear response) is
 342 measured against the CTA V_{SOURCE} bias, and the data are presented in Table 1. This demonstrates the
 343 trade-off of increasing full well against lower sensitivity and increasing RN.

344

345 4. Analogue Counter Cumulative Noise

346 Through noise measurement and iterative modelling, it is established that the analogue
 347 integrator circuits employed in SPAD-based counting pixels suffer from cumulative noise. For each
 348 SPAD event, noise affecting the counter circuit modulates the circuit sensitivity, and as the pixel
 349 integrates, the noise cumulates. Although the passive CTA pixel suffers from the 'in-filling' distortion
 350 mechanism described in the previous section, all analogue integrator structures such as CTAs or
 351 switched current sources (SCS) [36] [37] circuits will suffer from cumulative noise to a certain degree.
 352 The two main sources of cumulative noise are thermal noise through the switched path (which
 353 exhibits as a kT/C noise on the in-pixel capacitor, with the SPAD dead time, or counter switch time,
 354 controlling the thermal noise bandwidth) and systematic temporal noise on the common supplies. Of
 355 course, for long integration times, $1/f$ noise in the counter circuit and low frequency temporal noise
 356 on the common supplies will also modulate the integrator sensitivity and contribute cumulative
 357 noise.

358 The PSW method is employed on one pixel in the test array in [15] to evaluate for this cumulative
 359 and signal dependent noise source. Multiple experiments were captured (each with an individual
 360 PCH as seen in Figure 3), and for each experiment the integration time (from 1 μ s to 100 μ s) was
 361 increased to obtain greater number of SPAD events. An example of the combined PCH is modelled
 362 in Figure 5(a). Figure 5(b) extracts the increasing peak width indicating the presence of a cumulative
 363 noise source (σ_C) from measured data. A linear fit (solid black line) identifies an $\sigma_C = 86.9 \mu V$ RMS
 364 noise increase per SPAD event. The model shown in Fig.5(a) is matched with 86.9 μV RMS noise per
 365 counter step and the modelled FWHM response is shown alongside (dashed red line) in Fig 5.(b).

366 The cumulative noise modelled response S_N after N steps can be modelled to first order by
 367 expanding equation 4 into an iterative expression assuming the cumulative noise is Gaussian. The
 368 initial reset level S_0 ($N=0$) is assumed constant with no FPN and no noise terms (a Dirac function).
 369 The first modelled counter step S_1 has σ_C cumulative noise applied. The second step S_2 is the
 370 convolved response of the first counter step with the same Gaussian cumulative noise, and so on, as
 371 an iterative convolution for subsequent counter steps as shown in Equation 8.

$$S_N(v_N) = \frac{1}{\sigma_C \sqrt{2\pi}} \cdot \exp\left(-\frac{(v_N - S_{N-1}(v_{N-1}))^2}{2\sigma_C^2}\right) \quad (8)$$

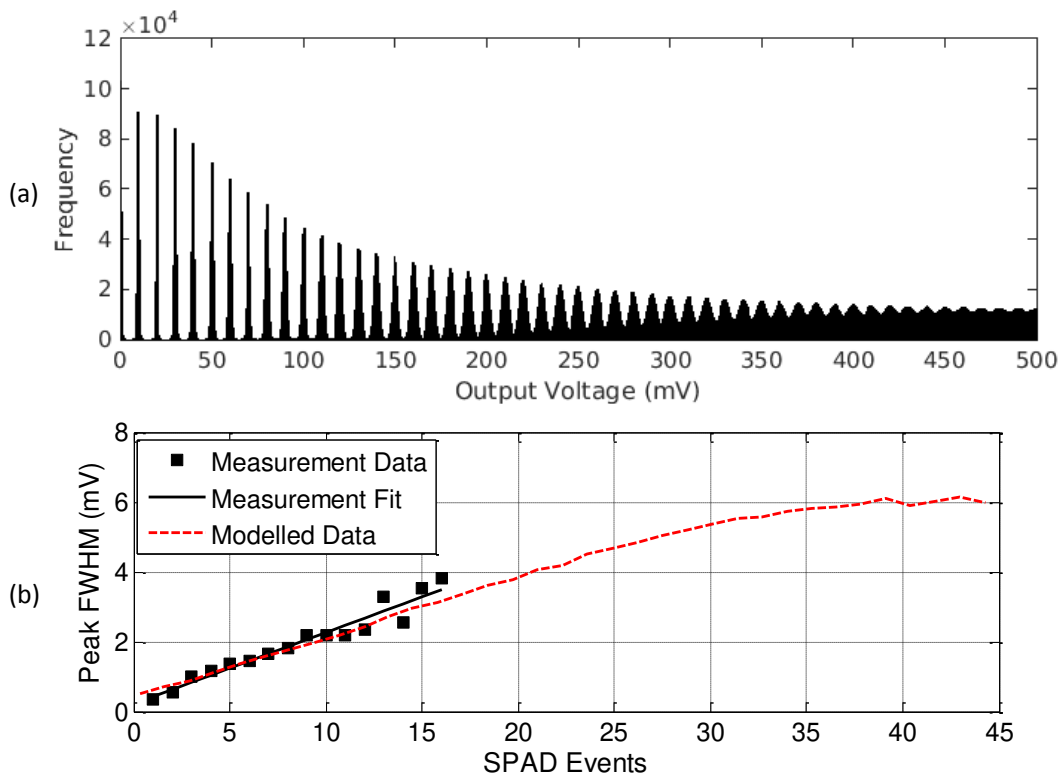
394

395 Where the v_N represents the voltage range of interest.

396 The same PSW procedure is performed for the full 320x240 image sensor in [16]. The imager has
 397 700 μV RMS noise per SPAD event, an increase of approximately 8 times. This is attributed to an
 398 increase of kT/C noise due to both the main and parasitic capacitors decreasing in size between the
 399 sensors, the capacitance ratio increasing from approximately doubling from 0.013 to 0.03, and an
 400 increase in temporal noise due to many more pixels active on the same supplies. Although it is noted,
 401 that some fraction of the increase may also be attributed to $\approx 1\%$ PRNU which would manifest
 402 similarly with a $\approx 100\mu\text{V}$ RMS broadening of the peaks per counted photon.

403 Figure 6(a) gives an example PCH from the imager. Fig. 6 (b) is the PCHs of the noise model
 404 applying 700 μV RMS cumulative noise and 0.06e- RN, and Fig. 6 (c) applying only RN. Fig. 6(b) has
 405 a much closer fit to the captured PCH, whereas Fig.6(c) indicates the shape of a PCH that a CIS
 406 DSERN sensor with 0.06e- RN should achieve. With such a cumulative noise source, the equivalent
 407 input referred read noise increases depending on exposure. Table 2 presents the signal against the
 408 equivalent input referred noise figures for both the imager and test structure.

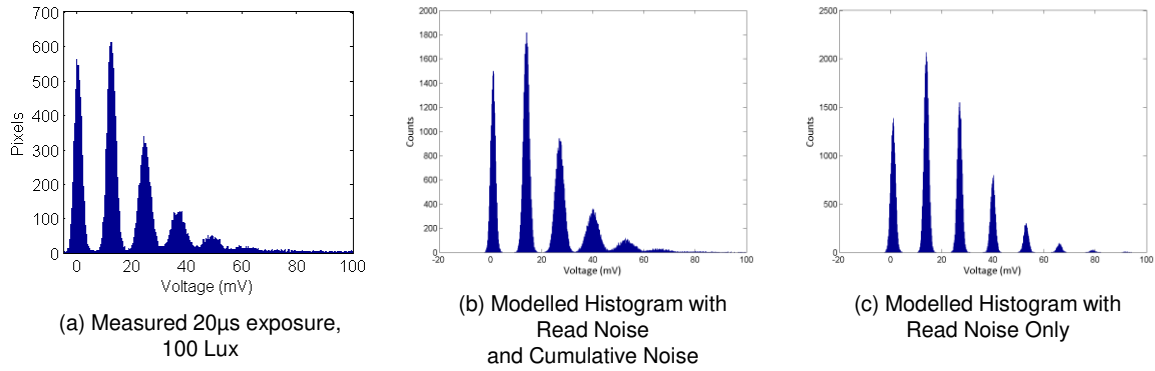
409



410

411 **Figure 5.** (a) Modelled multiple exposure PCH of a signal dependent cumulative noise source in the
 412 SPAD-based analogue counter structure [15]. (b) Measured and modelled peak FWHM, the first order
 413 linear fit has parameters: offset 204.7 μV with cumulative noise FWHM 225.9 μV / SPAD event =
 414 86.9 μV / SPAD event RMS. The modelled data has cumulative noise 86.9 μV / SPAD event applied.

415



416

417

418

419

420

Figure 6. (a) Measured PCH for all pixels in the 320x240 image sensor in [16]. (b) Modelled PCH (mean $\lambda = 1.5e^-$) accounting for both cumulative noise and read noise showing close fit to the measured PCH (c) Modelled PCH with read noise only showing a different response.

Equivalent Input Referred Total Noise	No. of SPAD Avalanche Events	
	Image Sensor [16]	Test Structure [15]
0.15e-	2	19
0.3e-	5	45
1e-	19	160

421

Table 2. Equivalent noise at a range of SPAD events.

422

423 **5. Spatio-Temporal Oversampling of Photon Counting Pixels**

424

425

426

427

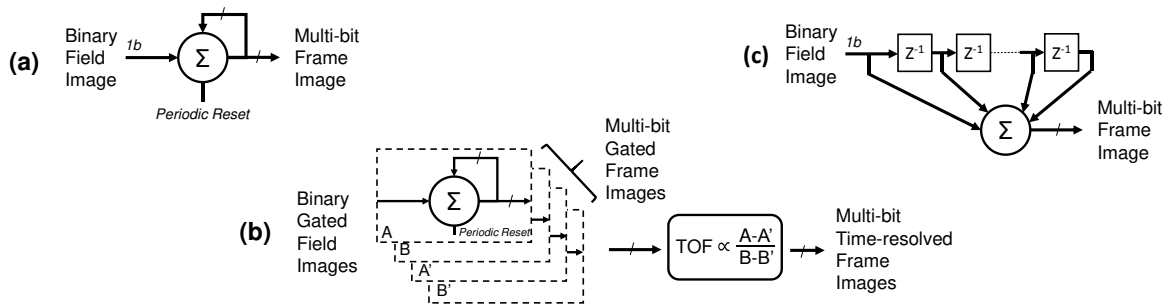
428

429

430

431

As analogue SPAD pixels suffer from increasing cumulative noise at higher photon counts and the effective full well is restricted, oversampling individual frames at low photon counts provides a means to create an image of high dynamic range with low overall noise. This section addresses trade-offs, and details different methods, of spatio-temporal oversampling of photon counting pixels. The Quanta Image Sensor (QIS) framework proposed by Fossum [38], extrapolates the imaging trends of pixel shrink, increasing CVF, decreasing RN, decreasing full well and spatio-temporal oversampling to a concept of a SPC image sensor where a ‘pixel’ is the spatio-temporal sum of multiple integrations of multiple sub-pixels (‘jots’).



432

433

434

435

436

Figure 7. Per-pixel spatio-temporal oversampling techniques. (a) Intensity image using IIR with periodic reset [17]. (b) Time-resolved image: four IIR per pixel [18]. (c) High frame rate intensity image using first-order FIR per pixel [19].

437

438 The small full well of photon counting pixels, in the order of magnitude of 100's of
439 photoelectrons or photons, limits a sensor's dynamic range. Spatio-temporal oversampling of
440 multiple pixels may be performed to increase the full well past a single pixel's limit. Furthermore, for
441 DSERN photon counting pixels with cumulative noise such as the SPAD-based analogue pixels
442 described in this paper, the level of the photon counting oversampling threshold (i.e. if pixel output
443 >1 photon or if > 2 photons, etc.) sets the noise of the oversampled output image; a higher
444 oversampling threshold induces greater noise in the output frame image. However, this threshold is
445 traded off against the frame rate and the oversampled full well. A signal level of N photoelectrons
446 can be reached with less oversampled frames (and greater output frame rate) with a higher
447 oversampling threshold of the pixel signal. By setting the threshold above the thermal and $1/f$ noise
448 floor, the oversampled is truly shot noise limited as little or no thermal and $1/f$ noise accumulates.

449

450 5.1. Single Photon Binary Quanta Imaging

451 Using SPAD-based single photon image sensors with binary response, a variety of oversampling
452 techniques have been evaluated in our recent work [16][17][18][19] and in the work of others [14],
453 [39]. 'Field' images are individual reads from the image sensor and the oversampled frame is a
454 summation of fields. The simplest technique in order to oversample a set of binary single photon field
455 images, is to temporally or spatially sum a set of input binary pixel (or 'jot') values, to create an output
456 'macro' pixel with grey levels. This is the equivalent operation of a first-order low-pass infinite
457 impulse response (IIR) filter with a periodic filter reset operation as shown in Figure 7 (a).
458 Considering temporal oversampling only (as demonstrated in [17]), to achieve a certain output frame
459 rate in FPS, with oversampled ratio OSR and input binary field rate f , the output rate is: $FPS = f/OSR$
460 and inversely the IIR reset period = OSR/f , thus attaining an output bit depth of $B = \log_2(OSR)$,
461 increasing the image bit depth by a factor of OSR or 2^B . It is clear that to attain frame rates $>30FPS$, at
462 bit depths $B > 5$ bit, a high field rate $f > 1k$ fields/s is required from the sensor. In [17], we demonstrated
463 7b bit depth at 40FPS, and 8b at 20FPS with 5.12k global shutter fields per second.

464 SPADs with picosecond temporal precision enable Indirect Time of Flight (ITOF) imaging to be
465 performed. Previous examples are pulsed ITOF using analogue pixels [33] and continuous wave
466 ITOF using digital pixels [40]. However, both approaches had very large pixel pitch and low fill
467 factor. A similar oversampling technique was applied in [18] with compact binary SPAD pixels, to
468 investigate time-gated binary image oversampling to produce a high resolution QVGA Indirect Time
469 of Flight (ITOF) output image as shown in Fig.7(b). Two primary gated field images (A & B) are
470 sequentially captured in interleaved fashion synchronous to a pulsed laser. Two secondary gated
471 images (A' & B') are set with the same time-gate without the laser for background removal. With four
472 field images, the output time-resolved frame rate is therefore a quarter of the previous intensity-only
473 technique (assuming a pipelined division operation).

474 A third technique in [19], addresses the low frame rate, and evaluates a continuous-time moving
475 average operation by applying a first-order low-pass finite impulse response (FIR) filter. As shown
476 in Fig.7(c), the FIR is implemented as a shift-register of length equal to the over-sampling ratio (i.e. a
477 FIR with number of taps = OSR) and a tracking counter. The benefit of this technique is the output
478 frame rate has no relationship with the OSR and is equal to the input field rate of the sensor. The
479 frame rate increase over the IIR technique is at the cost of the shift register per pixel. Longer
480 integration time increases temporal blur, therefore, higher OSR increases image lag of fast moving
481 scene elements. On the other hand, an increased bit depth (from greater OSR) decreases quantisation
482 noise in areas of slow movement in an imaged scene.

483

484

Reference	[41]	[42]	[16], [17]
Sensor Name	QIS Pathfinder	SwissSPAD	SPC Imager
Process Technology	180nm CMOS	0.35µm HV CMOS	130nm Imaging CMOS
Array Size	1376x768	512x128	320 x 240
Photo-detector	'Pump-gate Jot' PD	SPAD	SPAD
NMOS Pixel Transistors	3	11	9
Fill Factor (%)	45	5	26.8
Pixel Pitch (µm)	3.6	24	8
Microlensing	N	Y (12x concentration factor)	N
Shuttering	Rolling	Global	Global
CDS	True CDS	None	None
Parallel Data Channels	32	128	16
Max. Field Rate (FPS)	1,000	150,000	20,000
Sensor Data Rate	1Gbps	10.24Gbps	1.54Gbps
Pixel CVF or Equivalent	120µV / e-	>1V per SPAD Event	>1V per SPAD Event
Bit Error Rate	Not Reported	Not Reported	1.7 x 10 ⁻³ BER
Read Noise (e-) or Equivalent	Not Reported	Not Reported	0.168e-
Power During Operation	20mW	1650mW	40.8mW
Power FOM†	2.5pJ/b (ADC only) 19pJ/b (Full Sensor)	168pJ/b (Full Sensor + SPADs)	104pJ/b (Full Sensor + SPADs)

485 **Table 3.** Binary capture, oversampled output, quanta image sensor comparison table. †FOM=Sensor
486 power/ (No. of Pixel x FPS x N), where N = ADC resolution = 1b for these sensors.

487

488 We compare our recent work in this area, to two others demonstrating high binary field rates
489 with column parallel single bit flash ADCs for single bit QIS in Table 3. In a 3T CIS implementation
490 [41], amplification and CDS is employed and suitable for pixels with low signal swing (i.e. CVF ≤
491 input-referred offset and read noise). In our work [16] and another SPAD-based example [42], no
492 CDS or column amplifier circuits are required as the pixel sensitivity is >1V/SPAD event which is
493 much greater than offsets and RN. The RN and non-linear exposure characteristics of such
494 oversampled binary imagers are theoretically described in [9] and experimentally confirmed in our
495 work in [16], [18]. The measured bit error rate is 0.0017 providing an equivalent DSERN of 0.168e-.
496 Without CDS timing and increased column current, the field rate more than doubles [16].

497

498 5.2. Multi-photon Binary Quanta Imaging

499 As previously discussed, setting the oversampling threshold greater than a single counted
500 photon provides a benefit to output frame rate assuming the sensor output data rate remains the
501 same. By setting the oversampling threshold at two photons rather than one, half number of field
502 readouts are required to reach a certain oversampled signal level as each binary bit now represents
503 more than one photon. However, for the SPAD-based analogue counting pixel this is at the cost of
504 oversampling greater cumulative noise, FPN or PRNU with each successive field image.

505 An experiment is performed on the image sensor [16] recording the 'bit density' (the number of
506 pixels outputting a logical high indicating the multi-photon counting threshold is reached) against

507 increasing integration time for a fixed light level. The pixel array in configured in analogue counting
508 CTA mode with $V_{SOURCE} = 0.15V$. Figure 8 highlights the normalized bit density (D) to normalized
509 exposure (H), where $1.0H=5\mu s$ integration time, for an incrementing comparator threshold capturing
510 2 to 8 photons. The theoretical curves from [9] are plotted alongside for comparison. As no CDS is
511 implemented, the high FPN due to column comparator mismatch and source follower threshold
512 variation will effectively induce a PRNU in the measured data for all pixels which is seen as the
513 discrepancy between ideal and measured data particularly in the plotted line for the 4 photon
514 threshold. The closest fit in terms of photon number (2 to 8) is listed in the legend alongside.

515 Figure 9 is the measured normalized RMS noise which has the characteristic shape from Fossum's
516 theoretical Quanta Image Sensor paper in [9]. There are a number of remarkable characteristics of
517 multi-photon threshold binary imaging that are experimentally verified in this noise plot. The rising
518 slope of each of the noise plots indicates the shot-noise dominant region. The 2-photon line
519 demonstrates the "soft-knee" shot noise compression with a smooth roll-off after the peak after $H=1.0$
520 as expected in 1-photon or 2-photon threshold QIS. The subsequent increasing thresholds show a
521 horizontal shift in the exposure x-axis as a higher number of photons (or equivalent SPAD events)
522 are required to trigger the binary output. This can also be observed in the horizontal shift in the D-
523 LogH plot in Fig.8. The maximum noise in the 8-photon threshold is measured as 1.52 times higher
524 than the 2-photon threshold where the theory [9] suggests it should be no more than square root of
525 two higher (1.412 times).
526

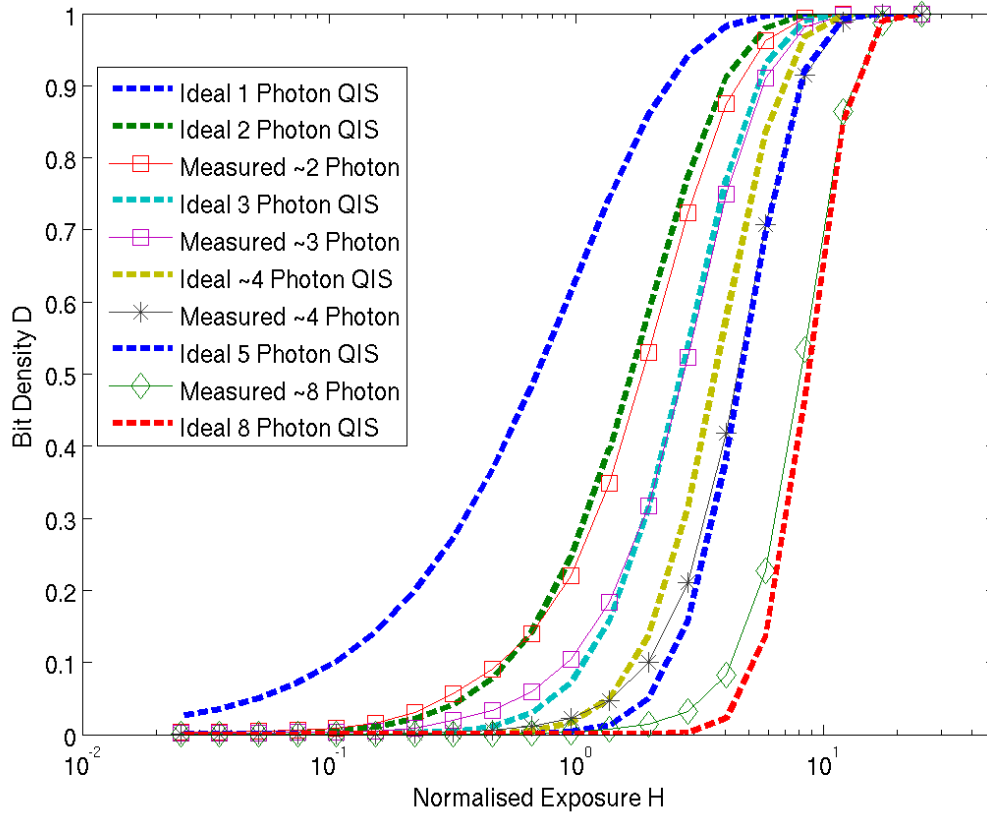
527 6. Discussion

528 Table 4 provides a comparison table highlighting a selection of state of the art solid-state photon
529 counting image sensors in the three different technologies (CIS, EMCCD and SPAD). This section
530 discusses and compares the performance of SPAD-based image sensors based on analogue
531 integration. SPAD based image sensors have the highest CVF of solid-state SPC image sensors.
532 Moreover, the pixel size of the SPAD analogue-based imagers is commensurate with EMCCD and
533 sCMOS scientific imagers, although FF is lower. With the exception of the LOFIC pixel which has
534 dual CVF's, like the recent CIS DSERN pixels, the increase in CVF of SPAD pixels yields a reduced
535 full well in the order of 100's photo-electrons or integrated SPAD events.

536 SPADs have the advantage of picosecond temporal resolution. Analogue pixels with low
537 transistor counts permit nanosecond and sub-nanosecond time-gated SPC imaging to be realized
538 where digital pixels further permit TCSPC imaging with 10's ps time resolution at the cost of low
539 spatial resolution.

540 In terms of RN, SPAD analogue integrators share a similar noise characteristic with 3 transistor
541 (3T) CIS pixels in that the integration node is not fully depleted and so suffers from kT/C noise. Our
542 test structure [15] cancels the kT/C noise by implementing 3T-pixel true CDS timing and furthermore
543 implemented 4,096 sample CMS to yield $<0.01e-$ equivalent RN in the best case. However, both 3T
544 timing and $>1k$ sample CMS is very restrictive in an image sensor design preventing, for example,
545 the global shutter or global time-gated operation that our recent work and [37] implements. Therefore
546 delta-reset sampling CDS [43] is implemented in our SPAD analogue counter image sensor which
547 adds a noise component of 100's μV RMS kT/C to the RN. However, the equivalent CVF of the SPAD-
548 based analogue pixels in the 10mV range is high enough to compensate, as demonstrated by the
549 $0.06e-$ RN figure which is the lowest in the published SPC image sensor literature.

550 In comparison to other works, analogue integrators suffer from cumulative noise limiting the
551 photon number resolution. Spatio-temporal oversampling, at a few photons per pixel level, mitigates
552 the noise integration whilst extending the photon number resolution although high frame rates are
553 required.



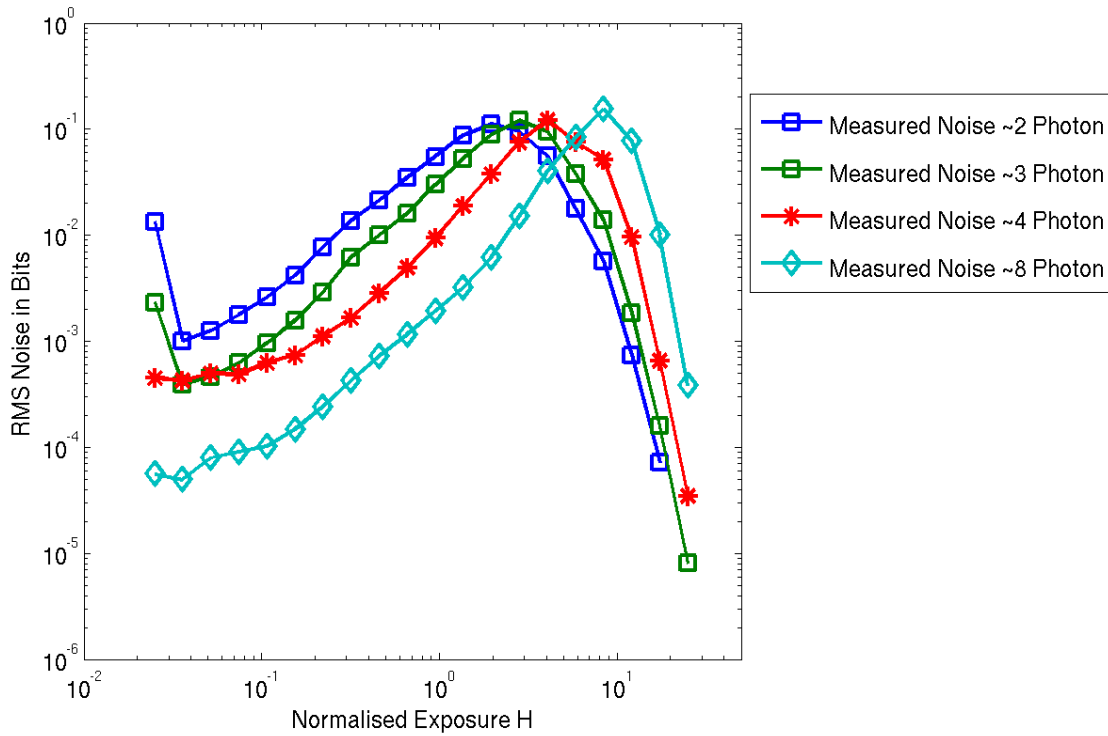
554

555

556

Figure 8. Multi-photon threshold oversampled binary imaging normalised bit density to exposure. Ideal curves from [9] are presented alongside measured results.

557



558

559

560

Figure 9. Measured RMS noise in multi-photon threshold oversampled binary imaging.

Reference	[12]	[10]	[23]	[21]	[44]	[15]	[16]	[26]
Photodetector	PIN PD + LOFIC	'Pump-gate Jot' PIN PD	PIN PD	EMCCD	SPAD	SPAD	SPAD	SPAD
Pixel Circuit	5T + LOFIC	4T	4T	CCD	Active CTA 8T	Passive CTA 11T	Passive CTA 9T	7b Counter >100T
Array Size	1280x960	1	35x512	1920x1080	160x120	3x3	320x240	32x32
Pixel Size (μm)	5.6	1.4	11.2 x 5.6	5.5	15	9.8	8	50
Fill Factor (%)	30.4	-	-	50	21	3.12	26.8	1
Pixel CVF or Equivalent	240 $\mu\text{V}/e^-$	403 $\mu\text{V}/e^-$	220	Gain dependent from 44 $\mu\text{V}/e^-$	16.5mV/SPAD event	13.1 to 2mV /SPAD Event	17.4mV to 8.4mV /SPAD event	1 DN / SPAD Event
Full Well or Equivalent	200ke-	210e-	1500e-	20ke- to 160e-	41	80 to 360	56 to 125	127
Read Noise (or Equivalent)	0.41e-	0.22e-	0.27e-	0.45e-	0.08e-	<0.01e- to 0.22e-	0.06e- to 0.18e-	0
Excess Noise	-	-	-	Y	-	-	-	-
Cumulative Noise	-	-	-	-	Y*	Y	Y	-
Measured Cumulative Noise	-	-	-	-	Not Measured	86.9 μV RMS / SPAD Event	700 μV RMS / SPAD Event	-
Time Gating Width or Temporal Resolution	-	-	-	-	0.75 ns	100 ns	30 ns	52ps

561 **Table 4.** Solid state single photon counting image sensor comparison table. *As based on a CTA analogue
562 integrator structure, the presence of cumulative noise is assumed by the author.

563

564 7. Conclusion

565 Our recent work on SPAD-based photon counting image sensors is analysed for photon
566 counting performance and deep sub electron equivalent noise characteristics. A noise model is
567 developed to include both CIS RN and the cumulative noise specific to analogue integrator circuits.
568 When combined, the three new methods (VPM, PSW and regressive analysis) of determining RN
569 form a new powerful set of tools for the measurement of most SPC and DSERN image sensor
570 characteristics alongside the existing techniques such as photon transfer curve analysis.

571 These single-photon and multi-photon methods of binary image capture have the attractive
572 quality of similar noise and signal characteristics of photographic film. Future development of these
573 binary photon-counting image sensors is an interesting and new avenue of research. The tradeoff
574 between in-pixel cumulative and spatio-temporal oversampling is examined. Analogue SPC pixels
575 have DSERN but exhibit cumulative noise limiting photon number resolution. As a result they are
576 best operated at low photon number in combination with digital oversampling. A very large dynamic
577 range is conceivably possible, combining the multi-photon counting with an oversampled frame
578 store, which would extend the limited dynamic range of the analogue counter. Furthermore, the
579 frame rate penalty of oversampling is addressed by a continuous-time moving average technique.

580 The capability of an image sensor to capture the arrival of a single photon, is the fundamental
581 limit to the detection of quantised electromagnetic radiation. Each of the three solid-state SPC image
582 sensor technologies, CMOS SPAD, EMCCD and DSERN CIS have specific advantages that will
583 individually serve a variety of photon counting applications.

584

585 **Acknowledgments:** The following people are gratefully acknowledged for their support in this work: Lindsay
586 Grant, Bruce Rae, Sara Pellegrini, Tarek Al Abbas, Graeme Storm, Kevin Moore, Pascal Mellot, Salvatore
587 Gnechchi, and all our co-authors in our recent works. Thanks to ST Crolles for silicon fabrication. This work is
588 primarily supported by STMicroelectronics Imaging Division and the research leading to these results has
589 received funding from the European Research Council under the EU's Seventh Framework Programme
590 (FP/2007-2013) / ERC Grant Agreement n.339747.

591 **Author Contributions:** N.D., I.G. and R.H. conceived and designed the experiments; N.D. and I.G. performed
592 the experiments; N.D. modelled and analyzed the data; N.D. and R.H conceived and designed the pixel test
593 structure, N.D., L.P. and R.H. conceived and designed the image sensor. N.D wrote the paper; N.D., L.P., and
594 R.H contributed edits to the paper.

595 **Conflicts of Interest:** The authors declare no conflict of interest. The founding sponsors had no role in the design
596 of the study; in the collection, analyses, or interpretation of data; in the writing of the manuscript, and in the
597 decision to publish the results.

598 Abbreviations

599 The following abbreviations are used in this manuscript:

600 CG: Conversion Gain
601 CIS: CMOS Image Sensor
602 CTA: Charge Transfer Amplifier
603 CVF: Charge to Voltage Conversion Factor
604 DSERN: Deep Sub Electron Read Noise
605 EMCCD: Electron Multiplied Charge Coupled Device
606 FIR: Finite Impulse Response Filter
607 IIR: Infinite Impulse Response Filter
608 PCH: Photon Counting Histogram
609 PSW: Peak Separation and Width method
610 QIS: Quanta Image Sensor
611 RN: Read Noise
612 SPAD: Single Photon Avalanche Diode
613 SPC: Single Photon Counting
614 TCSPC: Time Correlated Single Photon Counting
615 VPM: Valley to Peak method

616
617

618 References

- 619 [1] P. Seitz and A. Theuwissen, *Single Photon Imaging*, 1st ed. Springer, 2011.
- 620 [2] N. A. W. Dutton, S. Gnechchi, L. Parmesan, A. J. Holmes, B. Rae, L. A. Grant, and R. K. Henderson, "A
621 Time Correlated Single Photon Counting Sensor with 14GS/s Histogramming Time to Digital
622 Converter," in *IEEE International Solid-State Circuits Conference - ISSCC Digest of Technical Papers*, 2015.
- 623 [3] D.-U. Li, R. Walker, J. Richardson, B. Rae, A. Buts, D. Renshaw, and R. Henderson, "FPGA
624 implementation of a video-rate fluorescence lifetime imaging system with a 32×32 CMOS single-photon
625 avalanche diode array," in *2009 IEEE International Symposium on Circuits and Systems*, 2009, pp. 3082–
626 3085.

- 627 [4] L. H. C. Braga, L. Gasparini, L. Grant, R. K. Henderson, N. Massari, M. Perenzoni, D. Stoppa, and R.
628 Walker, "A Fully Digital 8x16 SiPM Array for PET Applications With Per-Pixel TDCs and Real-Time
629 Energy Output," *IEEE J. Solid-State Circuits*, vol. PP, no. 99, pp. 1–14, 2014.
- 630 [5] S. Burri, F. Powolny, C. E. Bruschini, X. Michalet, F. Regazzoni, and E. Charbon, "65K pixel, 150k frames-
631 per-second camera with global gating and micro-lenses suitable for life-time imaging," in *Proc. of SPIE*
632 *Photonics Europe*, 2014.
- 633 [6] S. P. Poland, N. Krstajić, J. Monypenny, S. Coelho, D. Tyndall, R. J. Walker, V. Devauges, J. Richardson,
634 N. Dutton, P. Barber, D. D.-U. Li, K. Suhling, T. Ng, R. K. Henderson, and S. M. Ameer-Beg, "A high
635 speed multifocal multiphoton fluorescence lifetime imaging microscope for live-cell FRET imaging,"
636 *Biomed. Opt. Express*, vol. 6, no. 2, p. 277, Jan. 2015.
- 637 [7] C. Niclass, K. Ito, M. Soga, H. Matsubara, I. Aoyagi, S. Kato, and M. Kagami, "Design and
638 characterization of a 256 × 64-pixel single-photon imager in CMOS for a MEMS-based laser scanning
639 time-of-flight sensor.," *Opt. Express*, vol. 20, no. 11, pp. 11863–81, May 2012.
- 640 [8] N. Teranishi, "Required Conditions for Photon-Counting Image Sensors," *IEEE Trans. Electron Devices*,
641 vol. 59, no. 8, pp. 2199–2205, Aug. 2012.
- 642 [9] E. R. Fossum, "Modeling the Performance of Single-Bit and Multi-Bit Quanta Image Sensors," *IEEE J.*
643 *Electron Devices Soc.*, vol. 1, no. 9, pp. 166–174, Sep. 2013.
- 644 [10] J. Ma and E. Fossum, "Quanta Image Sensor Jot with Sub 0.3e- r.m.s. Read Noise and Photon Counting
645 Capability," *IEEE Electron Device Lett.*, vol. PP, no. 99, pp. 1–1, 2015.
- 646 [11] C. S. Bamji, P. O'Connor, T. Elkhatib, S. Mehta, B. Thompson, L. A. Prather, D. Snow, O. C. Akkaya, A.
647 Daniel, A. D. Payne, T. Perry, M. Fenton, and V.-H. Chan, "A 0.13 μm CMOS System-on-Chip for a 512
648 × 424 Time-of-Flight Image Sensor With Multi-Frequency Photo-Demodulation up to 130 MHz and 2
649 GS/s ADC," *IEEE J. Solid-State Circuits*, vol. 50, no. 1, pp. 303–319, Jan. 2015.
- 650 [12] S. Nasuno, S. Wakashima, F. Kusuhara, R. Kuroda, and S. Sugawa, "A CMOS Image Sensor with
651 240μV/eConversion Gain, 200keFull Well Capacity and 190-1000nm Spectral Response," in *International*
652 *Image Sensor Workshop*, 2015.
- 653 [13] T. Vogelsang, M. Guidash, and S. Xue, "Overcoming the Full Well Capacity Limit: High Dynamic Range
654 Imaging Using Multi-Bit Temporal Oversampling and Conditional Reset," in *International Image Sensor*
655 *Workshop*, 2013.
- 656 [14] F. Yang, "Bits from Photons: Oversampled Binary Image Acquisition," T.U. Delft, 2012.
- 657 [15] N. A. W. Dutton, L. A. Grant, and R. K. Henderson, "9.8μm SPAD-based Analogue Single Photon
658 Counting Pixel with Bias Controlled Sensitivity," in *International Image Sensors Workshop*, 2013.
- 659 [16] N. A. W. Dutton, I. Gyongy, L. Parmesan, S. Gnechchi, N. Calder, B. R. Rae, S. Pellegrini, L. A. Grant, and
660 R. K. Henderson, "A SPAD-Based QVGA Image Sensor for Single-Photon Counting and Quanta
661 Imaging," *IEEE Trans. Electron Devices*, vol. Pre-Public, no. 99, pp. 1–8, 2015.
- 662 [17] N. A. W. Dutton, L. Parmesan, A. J. Holmes, L. A. Grant, and R. K. Henderson, "320x240 Oversampled
663 Digital Single Photon Counting Image Sensor," in *VLSI Symposium*, 2014.
- 664 [18] N. A. W. Dutton, L. Parmesan, S. Gnechchi, I. Gyongy, N. J. Calder, B. R. Rae, L. A. Grant, and R. K.
665 Henderson, "Oversampled ITOF Imaging Techniques using SPAD-based Quanta Image Sensors," in
666 *International Image Sensor Workshop*, 2015.
- 667 [19] I. Gyongy, N. A. W. Dutton, L. Parmesan, A. Davies, R. Saleeb, R. Duncan, C. Rickman, P. Dalgarno, and
668 R. K. Henderson, "Bit-plane Processing Techniques for Low-Light, High Speed Imaging with a SPAD-
669 based QIS," in *International Image Sensor Workshop*, 2015.

- 670 [20] J. Hynccek, "Impactron-a new solid state image intensifier," *IEEE Trans. Electron Devices*, vol. 48, no. 10,
671 pp. 2238–2241, 2001.
- 672 [21] C. Parks, S. Kosman, E. Nelson, N. Roberts, and S. Yaniga, "A 30 fps 1920 × 1080 pixel Electron
673 Multiplying CCD Image Sensor with Per-Pixel Switchable Gain," in *International Image Sensor Workshop*,
674 2015.
- 675 [22] M. S. Robbins and B. J. Hadwen, "The noise performance of electron multiplying charge-coupled
676 devices," *IEEE Trans. Electron Devices*, vol. 50, no. 5, pp. 1227–1232, May 2003.
- 677 [23] M.-W. Seo, S. Kawahito, K. Kagawa, and K. Yasutomi, "A 0.27e⁻ Read Noise 220-uV Conversion Gain
678 Reset-Gate-Less CMOS Image Sensor," *IEEE Electron Device Lett.*, vol. 36, no. 12, pp. 1344–1347, Dec.
679 2015.
- 680 [24] Y. Chen, Y. Xu, Y. Chae, A. Mierop, X. Wang, and A. Theuwissen, "A 0.7e⁻_{rms}-temporal-
681 readout-noise CMOS image sensor for low-light-level imaging," in *2012 IEEE International Solid-State
682 Circuits Conference*, 2012, pp. 384–386.
- 683 [25] B. F. Aull, A. H. Loomis, D. J. Young, R. M. Heinrichs, B. J. Felton, P. J. Daniels, and Deborah J. Landers,
684 "Geiger-Mode Avalanche Photodiodes for Three Dimensional Imaging," *Lincoln Lab. J.*, vol. 13, no. 2,
685 pp. 335–350, 2002.
- 686 [26] J. Richardson, R. Walker, L. Grant, D. Stoppa, F. Borghetti, E. Charbon, M. Gersbach, and R. K.
687 Henderson, "A 32x32 50ps Resolution 10 bit Time to Digital Converter Array in 130nm CMOS for Time
688 Correlated Imaging," in *IEEE Custom Integrated Circuits Conference*, 2009, pp. 77–80.
- 689 [27] G. Gariepy, N. Krstajić, R. Henderson, C. Li, R. R. Thomson, G. S. Buller, B. Heshmat, R. Raskar, J. Leach,
690 and D. Faccio, "Single-photon sensitive light-in-flight imaging," *Nat. Commun.*, vol. 6, p. 6021, Jan. 2015.
- 691 [28] G. Gariepy, F. Tonolini, R. Henderson, J. Leach, and D. Faccio, "Detection and tracking of moving objects
692 hidden from view," *Nat. Photonics*, vol. 10, no. 1, pp. 23–26, Dec. 2015.
- 693 [29] L. Parmesan, N. A. W. Dutton, N. J. Calder, A. J. Holmes, L. A. Grant, and R. K. Henderson, "A 9.8 μm
694 Sample and Hold Time to Amplitude Converter CMOS SPAD Pixel," in *ESSDERC 2014*, 2014.
- 695 [30] L. Parmesan, N. A. W. Dutton, N. J. Calder, L. A. Grant, and R. K. Henderson, "A 256x256 SPAD array
696 with in-pixel Time to Amplitude Conversion for Fluorescence Lifetime Imaging Microscopy," in
697 *International Image Sensor Workshop*, 2015.
- 698 [31] Y. Maruyama and E. Charbon, "A Time-Gated 128X128 CMOS SPAD Array for On-Chip Fluorescence
699 Detection," in *2011 International Image Sensors Workshop*, 2011.
- 700 [32] J. M. Pavia, M. Wolf, and E. Charbon, "Measurement and modeling of microlenses fabricated on single-
701 photon avalanche diode arrays for fill factor recovery.," *Opt. Express*, vol. 22, no. 4, pp. 4202–13, Feb.
702 2014.
- 703 [33] D. Stoppa, L. Pancheri, M. Scandiuzzo, L. Gonzo, G.-F. Dalla Betta, and A. Simoni, "A CMOS 3-D Imager
704 Based on Single Photon Avalanche Diode," *IEEE Trans. Circuits Syst. I Regul. Pap.*, vol. 54, no. 1, pp. 4–
705 12, Jan. 2007.
- 706 [34] M. Perenzoni, N. Massari, D. Perenzoni, L. Gasparini, and D. Stoppa, "A 160 × 120 Pixel Analog-
707 Counting Single-Photon Imager With Time-Gating and Self-Referenced Column-Parallel A/D
708 Conversion for Fluorescence Lifetime Imaging," *IEEE J. Solid-State Circuits*, vol. PP, no. 99, pp. 1–13, 2015.
- 709 [35] J. Ma, D. Starkey, A. Rao, K. Odame, and E. R. Fossum, "Characterization of Quanta Image Sensor Pump-
710 Gate Jots With Deep Sub-Electron Read Noise," *IEEE J. Electron Devices Soc.*, vol. 3, no. 6, pp. 472–480,
711 Nov. 2015.
- 712 [36] L. Pancheri, N. Massari, and D. Stoppa, "SPAD Image Sensor With Analog Counting Pixel for Time-

- 713 Resolved Fluorescence Detection," *IEEE Trans. Electron Devices*, vol. 60, no. 10, pp. 3442–3449, Oct. 2013.
- 714 [37] M. Perenzoni, N. Massari, D. Perenzoni, L. Gasparini, and D. Stoppa, "A 160×120-pixel analog-counting
715 single-photon imager with Sub-ns time-gating and self-referenced column-parallel A/D conversion for
716 fluorescence lifetime imaging," in *IEEE International Solid-State Circuits Conference - ISSCC Digest of
717 Technical Papers*, 2015, pp. 1–3.
- 718 [38] E. R. Fossum, "Gigapixel Digital Film Sensor (DFS) Proposal," in *Nanospace Manipulation of Photons and
719 Electrons for Nanovision Systems, Proceedings of.*, 2005.
- 720 [39] F. Yang, L. Sbaiz, E. Charbon, S. Susstrunk, and M. Vetterli, "Image reconstruction in the gigavision
721 camera," *Comput. Vis. Work. (ICCV Work. 2009 IEEE 12th Int. Conf.*, pp. 2212–2219, 2009.
- 722 [40] C. Niclass, C. Favi, T. Kluter, F. Monnier, and E. Charbon, "Single-Photon Synchronous Detection," *IEEE
723 J. Solid-State Circuits*, vol. 44, no. 7, pp. 1977–1989, Jul. 2009.
- 724 [41] S. Masoodian, A. Rao, J. Ma, K. Odame, and E. R. Fossum, "A 2.5 pJ/b Binary Image Sensor as a
725 Pathfinder for Quanta Image Sensors," *IEEE Trans. Electron Devices*, vol. 63, no. 1, pp. 100–105, Jan. 2016.
- 726 [42] S. Burri, Y. Maruyama, X. Michalet, F. Regazzoni, C. Bruschini, and E. Charbon, "Architecture and
727 applications of a high resolution gated SPAD image sensor," *Opt. Express*, vol. 22, no. 14, pp. 17573–89,
728 Jul. 2014.
- 729 [43] J. Nakamura, Ed., *Image Sensors and Signal Processing for Digital Still Cameras*, First. CRC Press, 2006.
- 730 [44] M. Perenzoni, N. Massari, D. Perenzoni, L. Gasparini, and D. Stoppa, "A 160x120 Pixel Analog-Counting
731 Single-Photon Imager with Sub-ns Time Gating and Self-Referenced Column-Parallel A/D Conversion
732 for Fluorescence Lifetime Imaging," in *IEEE International Solid-State Circuits Conference - ISSCC Digest of
733 Technical Papers*, 2015, p. 11.3.
- 734