# Size-Time Complexity of Boolean Networks for Prefix Computations 

G. BILARDI<br>Cornell University, Ithaca, New York

AND<br>F. P. PREPARATA<br>University of Illinois at Urbana-Champaign, Urbana, Illinois


#### Abstract

The prefix problem consists of computing all the products $x_{0} x_{1} \cdots x_{J}(j=0, \ldots, N-1)$, given a sequence $\mathbf{x}=\left(x_{0}, x_{1}, \ldots, x_{N_{-1}}\right)$ of elements in a semigroup. In this paper we completely characterize the size-time complexity of computing prefixes with Boolean networks, which are synchronized interconnections of Boolean gates and one-bit storage devices. This complexity crucially depends upon two properties of the underlying semigroup, which we call cycle-freedom (no cycle of length greater than one in the Cayley graph of the semigroup), and memory-induciveness (arbitrarily long products of semigroup elements are true functions of all their factors). A nontrivial characterization is given of non-memory-inducive semigroups as those whose recurrent subsemigroup (formed by the elements with self-loops in the Cayley graph) is the direct product of a left-zero semigroup and a rightzero semigroup. Denoting by $S$ and $T$ size and computation time, respectively, we have $S=$ $\Theta((N / T) \log (N / T))$ for memory-inducive non-cycle-free semigroups, and $S=\Theta(N / T)$ for all other semigroups. We have $T \in[\Omega(\log N), O(N)]$ for all semigroups, with the exception of those whose recurrent subsemigroup is a right-zero semigroup, for which $T \in[\Omega(1), O(N)]$. The preceding results are also extended to the VLSI model of computation. Area-time optimal circuits are obtained for both boundary and nonboundary I/O protocols.

Categories and Subject Descriptors: F.1.1 [Computation by Abstract Devices]: Models of Computation-unbounded-action devices; F.2.2 [Analysis of Algorithms and Problem Complexity]: Nonnumerical Algorithms and Problems-computations on discrete structures; F.2.3 [Analysis of Algorithms and Problem Complexity]: Trade-off among Complexity Measures

General Terms: Algorithms, Theory Additional Key Words and Phrases: Boolean networks, parallel computation, prefix computation, semigroups, size-time trade-offs


[^0](c) 1989 ACM 0004-5411/89/0400-0362 \$01.50

## 1. Introduction

The prefix problem consists of computing all the products of $x_{0} x_{1} \cdots x_{j}(j=0$, $\ldots, N-1)$, given a sequence $\mathbf{x}=\left(x_{0}, x_{1}, \ldots, x_{N-1}\right)$ of elements in a semigroup. Prefix computations occur in the solution of several significant problems such as carry-look-ahead addition [7, 21], the evolution of finite-state machines [19], linear recurrences [18], digital filtering [5], various graph problems [17], sorting in bitmodels of computation [3, 11], scheduling [13], and others.

The prefix problem has been extensively investigated in the circuit model, where the computation is carried out by an acyclic network of gates. Various complexity measures such as size, depth, width, and their trade-offs have been studied in [15], [19], and [22] for circuits whose gates are semigroup multipliers, and in [8] and [9] for circuits of Boolean gates. Algorithms for the EREW-PRAM model have been proposed in [17]. Implementations on a tree-connected network are discussed in [13].
In this paper we study the complexity of computing prefixes with Boolean networks, which are synchronized interconnections of Boolean gates and one-bit storage devices. Relevant measures are computation time $T$ and size $S$, defined as the total number of components (combinational and sequential) in the network. Our model of computation is essentially the same as the aggregate of [14], from which it differs only in the input/output conventions. Both models afford the study of the role of sequential logic in circuits, and allow the consideration of circuits of size sublinear in the input size. Results in Boolean networks have also interesting implications for other models of parallel computation such as fixed interconnections of processors and VLSI circuits [23].

We have found that the size-time complexity of the prefix problem is determined by two properties of the underlying semigroup, which we call cycle-freedom and memory-induciveness. We call a semigroup cycle-free if its Cayley graph has no cycle of length greater than one and non-cycle-free otherwise. We call a semigroup memory-inducive if products of arbitrary length are true functions of all their factors, and non-memory-inducive otherwise. Our results, which completely characterize the size-time complexity of the prefix problem, are encompassed by the following theorem, which summarizes Theorems 4 to 9.

Theorem 1. The size-time complexity of the prefix problem on a Boolean network is $S=\Theta((N / T) \log (N / T))$, for memory-inducive non-cycle-free semigroups, and is $S=\Theta(N / T)$, for all other semigroups. The bounds hold for $T \in[\Omega(\log N)$, $O(N)]$ for all semigroups, with the exception of those whose recurrent subsemigroup is a right-zero semigroup, for which $T \in[\Omega(1), O(N)]$.

For memory-inducive non-cycle-free semigroups, the upper bound can be achieved by known constructions based on binary-tree networks [13], or twisted-reflected-tree networks [3, 7, 19, 21], whereas the lower bound (Sect. 3.4, Theorem 4) is less obvious, and is based on arguments of computational friction [4]. We also give a nontrivial characterization of non-memory-inducive semigroups (Sect. 3.3, Theorem 3).

For the remaining semigroups, the lower bound is based on a trivial input/output argument, while the upper bound is achieved by nontrivial algorithms (Section 4, Theorems 7 and 9) executed by tree-connected networks.

It may be interesting to contrast Theorem 1 with the result of [8] that there are constant-depth, polynomial-size (unbounded fan-in) Boolean circuits to compute prefixes for a semigroup, if and only if the semigroup is group-free, an attribute weaker than cycle-free.

In Section 5, we extend our results to the area-time complexity of the prefix problem in the VLSI model of computation [23].

We conclude in Section 6 by considering some open problems.

## 2. Definitions and Problem Staternent

A finite semigroup is a pair $\langle A, \cdot\rangle$ where $A=\left\{a_{1}, a_{2}, \ldots, a_{s}\right\}$ is a set of size $s$ and $\cdot$ is an associative binary operation on $A$, which we call product. We denote by $x y$ the product of elements $x, y \in A$. A finite monoid is a finite semigroup with a distinguished element $e$, called the identity, such that $x e=e x=x$, for all $x \in A$. Any semigroup can be easily transformed into a monoid by the addition of an element with the properties of the identity. Perhaps surprisingly, the addition of the identity may increase the complexity of computing prefixes.

For a sequence $\mathbf{x}=\left(x_{0}, x_{1}, \ldots, x_{N-1}\right) \in A^{N}$, the sequence of prefixes of $\mathbf{x}$ is defined as $\mathbf{y}=\left(y_{0}, y_{1}, \ldots, y_{N-1}\right)$, with $y_{j}=x_{0} x_{1} \cdots x_{j}$. The prefix problem consists in computing $y$ from $x$.

In the study of the complexity of the prefix problem, an important role is played by the Cayley graph $G(A)=(A, E)$ of $A$, containing for each ordered pair $(x, y)$ an arc of the form ( $x, x y$ ), labeled by $y$. It is easy to see that each node of $G(A)$ has out-degree $s$, that the labels of the self-loops of a given node form a subsemigroup of $A$, and that $G(A)$ is transitively closed.

An element $a$ of $A$ is said to be recurrent if $G(A)$ contains a self-loop at $a$, that is, if there is an element $b$ of $A$, not necessarily distinct from $a$, such that $a b=a$. We call $\operatorname{recur}(A)$ the set of recurrent elements of $A$. It is easy to see that $\operatorname{recur}(A)$ is a subsemigroup.

Two elements $a$ and $b$ of $A$ are equivalent, if either $a=b$ or there exist elements $c$ and $d$ such that $a c=b$, and $b d=a$. We observe that the relation "equivalent" is a true equivalence, and that its equivalence classes are the strongly connected components of $G(A)$.

We call a semigroup cycle-free ( $C F$ ) if the only cycles in its Cayley graph are self-loops, and non-cycle-free ( $N C F$ ), otherwise. We shall see that cycle-freedom is a crucial property of a semigroup in determining the complexity of the prefix problem.

It is well known (and also easy to prove) that for each element $a$ in a finite semigroup there are two positive integers $k$ and $p$ such that $a, a^{2}, \ldots, a^{k+p-1}$ are all distinct, and $a^{k+p}=a^{k}$. Moreover, if the period $p$ of $a$ is larger than 1 , then $\left\{a^{k}\right.$, $\left.a^{k+1}, \ldots, a^{k+p-1}\right\}$ from a group. If $p=1$ for all the elements, then the semigroup is called group-free [20].

We now give examples of semigroups that belong to the various classes introduced above. If any element $x \in A$ different from the identity has an inverse $x^{-1}$ such that $x x^{-1}=e$, then ( $e, x, e$ ) forms a nontrivial cycle in $G(A)$ and $A$ is NCF. As a corollary, all groups are NCF, and cycle-freedom implies group-freedom.

An example of CF semigroup is the left-zero semigroup $\left\langle L_{p},{ }^{\circ}\right\rangle$, where $L_{p}=$ $\left\{l_{1}, l_{2}, \ldots, l_{p}\right\}$ and $l_{i} \circ l_{j}=l_{i}$, for all $l_{i}$ and $l_{j}$. An example of semigroup that is NCF but group-free is the right-zero semigroup $\left\langle R_{q}, *\right\rangle$, where $R_{q}=\left\{r_{1}, r_{2}, \ldots, r_{q}\right\}$ and $q \geq 2$, and $r_{i} * r_{j}=r_{j}$, for all $r_{i}$ and $r_{j}$. This semigroup with $q=2$, and with the adjunction of the identity, becomes the monoid that models the function "carry" in binary addition (the identity representing carry propagation and the two zeros representing carry setting and carry resetting, respectively).

Among CF semigroups, of particular interest are insertion semigroups, characterized by the following property: For all $x, y, z, w \in A$,

$$
\begin{equation*}
x y z=x y \Longrightarrow x w y z=x w y \tag{1}
\end{equation*}
$$

All abelian CF semigroups are also insertion semigroups. An instance of abelian CF semigroup is given by a set $A=\{0,1, \ldots, s-1\}$ with respect to the operation threshold- $(s-1)$ addition defined as $x y=\min (x+y, s-1)$. The prefix operation on this semigroup represents the cumulative sum of the sequence $\mathbf{x}$ with the value ( $s-1$ ) replacing each larger value.

Further examples of insertion semigroups are all semilattices, where the semigroup operation is commutative and idempotent. Examples of semilattices are the set of the $0-1$ vectors of length $n$ with respect to component-wise OR (AND), and the set of the first $s$ nonnegative integers with the MINIMUM (MAXIMUM) operation.

An interesting insertion semigroup that is not abelian is the set of the rankings of $n$ items with respect to the operation of rank concatenation. The computation of prefixes in this semigroup is used in the construction of optimal VLSI-sorting circuits [3, 4, 11]. Identifying the $n$ items with the integers from 1 to $n$, a ranking is an ordered partition of the set $\{1,2, \ldots, n\}$, that is, a sequence of disjoint sets whose union equals $\{1,2, \ldots, n\}$. Intuitively, all the elements in a given set have the same rank, and have rank higher than those in the next set. The concatenation of two rankings $u=\left(u_{1}, u_{2}, \ldots, u_{p}\right)$ and $v=\left(v_{1}, v_{2}, \ldots, v_{q}\right)$ is $u v=\left(w_{1}, w_{2}, \ldots, w_{p}\right)$ with $w_{j}$ equal to the subsequence of the nonempty terms of $\left(u_{j} \cap v_{1}, u_{j} \cap v_{2}, \ldots, u_{j} \cap v_{q}\right)$.

## 3. Lower Bounds

3.1 Model. A Boolean network is a directed graph with the following types of nodes: (1) input nodes, with in-degree zero and out-degree one; (2) output nodes, with in-degree one and out-degree zero; (3) combinational nodes, each labeled by a Boolean function of one- or two-input variables, with in-degree equal to the number of input variables, and out-degree one or two (to allow fan-out); (4) onebit storage nodes, with in-degree one and out-degree one or two.

The notions of computation of, and of function computed by, a Boolean network can be formalized as done in [14]. Here we appeal to the intuitive meaning of these notions, and just discuss the input/output protocol, since it differs slightly from that of [14]. We assume that each input (output) variable of the problem is assigned one input (output) node and one input (output) time. Two variables can be assigned the same node, but only at different times. Only one node and one time are assigned to a given variable (unilocal, semellective protocol), and this node and time are independent of the input value (placc-determinatc, time-determinate protocol).

Clearly, when solving the prefix problem by a Boolean network, a specific binary encoding of the semigroup elements must be chosen. Since our present aim is to study the dependence of the complexity of the prefix problem upon the length $N$ of the input sequence, and not its dependence on semigroup size or representation, we assume that the bits that encode a given semigroup variable are input (output) all at the same time. We call an input/output protocol with this property wordinstantaneous, in analogy with the term word-local introduced in [23].
3.2 Computational Friction. Our lower bound for the prefix problem is based on the mechanism of computational friction developed in [4] as a generalization of arguments previously applied to binary addition in [1] and [16]. Computational friction, so denoted in the context of a fluidodynamic analogy for VLSI computations, is a phenomenon that slows down the flow of information from input to output nodes below the rate allowed by the number of I/O nodes, and
therefore, when present, yields lower bounds stronger than the trivial $S T=\Omega(N)$ bound. As noted in [4], computational friction rests crucially on the property that both fan-in and fan-out of logical elements are bounded, and it can be analyzedin perfectly symmetrical ways-with respect to either fan-in or fan-out. With respect to fan-in, two phenomena contribute to the appearance of friction: (i) A substantial fraction of the information carried by each wavefront of input variables is transferred to the output variables, and (ii) this information must be stored within the network for a time logarithmic in the input wavefront size hence, for bounded fan-in, functional dependence imposes a delay between reading the input and computing an output depending upon that input. Symmetrically (with respect to fan-out), we have: (i) each wavefront of output variables carries a substantial fraction of the information contained in the input variables, and (ii) this information must be stored within the network for a time logarithmic in the output wavefront size since, for bounded fan-out, functional dependence imposes a delay between reading an input and computing the outputs depending upon it. For the sake of the ensuing discussion, we precisely analyze the latter phenomenon (fanout constraint) in Theorem 2 below, an earlier version of which is proved in [4].

We begin with the following definition of functional dependence:
Definition 1. Let $X=\left\{x_{0}, \ldots, x_{n-1}\right\}$ and $Y=\left\{y_{0}, \ldots, y_{m-1}\right\}$ be sets of input and output variables, respectively; each variable ranges over a finite alphabet. All elements of $X$ and $Y$ are encoded in binary, and the binary encoding of a variable will be referred to as a word. Let $\mathbf{x}=\left(x_{0}, \ldots, x_{n-1}\right)$ and $\mathbf{y}=\left(y_{1}, \ldots, y_{m-1}\right)$ denote vectors and let $f: \mathbf{x} \mapsto \mathbf{y}$ be a function. We say that $y_{j}$ is functionally dependent upon $x_{i}$ (and that $x_{i}$ functionally affects $y_{j}$ ) if there exist two vectors $\mathbf{x}^{\prime}$ and $\mathbf{x}^{\prime \prime}$ that differ only in their $x_{i}$-component and such that $f\left(\mathbf{x}^{\prime}\right)$ and $f\left(\mathbf{x}^{\prime \prime}\right)$ differ in their $y_{j}$-component.

Next, we introduce the notion of friction set, which formally captures the features of computational friction intuitively described above.

Definition 2. Let $f, X$, and $Y$ be as introduced in Definition 1. Let $\alpha$ and $\beta$ be nondecreasing functions. Set $U \subseteq Y$ is called an ( $\alpha, \beta$ )-friction set for $f$ if for any subset $W$ of $U$ there exists a subset $V$ of $X$ with the two following properties:
(1) Each variable of $V$ functionally affects at least $\alpha(|W|)$ variables of $W$.
(2) There is an assignment of values to the variables of $X-V$ such that, in the resulting restriction of $f$, the variables in $W$ assume at least $2^{\beta(|W|)}$ distinct configurations.

The significance of friction sets rests on the following theorem.
Theorem 2. Let $f, X$, and $Y$ be as introduced in Definition 1 , and let $U \subseteq Y$ be an ( $\alpha, \beta$ )-friction set for $f$. Then for any time-determinate, semellective, wordinstantaneous Boolean network computing $f$, size $S$ and time $T$ satisfy the bound

$$
\begin{equation*}
S=\Omega\left(\beta\left(\frac{|U|}{T} \log \alpha\left(\frac{|U|}{T}\right)\right)\right) \tag{2}
\end{equation*}
$$

In the proof of Theorem 2 we shall use the simple combinatorial lemma stated below without proof.

Lemma 1. Let $\left(u_{1}, u_{2}, \ldots, u_{T}\right)$ be a sequence of integers, and let $m=\left(u_{1}+u_{2}\right.$ $\left.+\cdots+u_{T}\right) / T$ be their average. Then, for every integer $\tau \in[1, T]$, there are $\tau$
consecutive terms of the sequence whose average is at least $m / 2$. More formally, there is an integer $t_{0} \in[1, T-\tau+1]$ such that $\left(u_{t_{0}}+u_{t_{0}+1}+\cdots+u_{t_{0}+\tau-1}\right) / \tau \geq$ $m / 2$.

Proof of Theorem 2. Let $C$ be a Boolean network that computes $f$, and let the origin of time be chosen so that the computation takes place in the interval [ $1, T$ ]. Since $C$ is word-instantaneous, we can define the set $U_{i}$ of the variables in $U$ that are output exactly at time $t \in[1, T]$. ( $U_{1}$ may be empty for some $t$.) Since $C$ is time-determinate, $U_{1}$ is independent of the input values, and since $C$ is semellective (in the output), the $U_{t}$ 's are pairwise disjoint. Finally, the union of the $U_{t}$ 's equals $U$.

From Lemma 1, with $u_{t}=\left|U_{t}\right|$, and $m=|U| / T$, we have that, for any $\tau \in$ $[1, T]$, there is a $t_{0} \in[1, T-\tau+1]$ such that $\left(\left|U_{t_{0}}\right|+\left|U_{t_{0}+1}\right|+\cdots+\right.$ $\left.\left|U_{t_{0}+\tau-1}\right|\right) \geq \tau|U| /(2 T)$. Then, the set $W \triangleq \bigcup_{\substack{t_{i=t_{0}}+\tau-1}} U_{\text {, satisfies }}$ the bound

$$
\begin{equation*}
|W| \geq \frac{\tau|U|}{2 T} \tag{3}
\end{equation*}
$$

Since $W$ is a subset of a friction set, there is by hypothesis a subset $V$ of $X$ satisfying properties (1) and (2) of Definition 2.

We claim that any choice $\tau \leq \log \alpha(|U| / 2 T)$ guarantees the separation of the time interval [ $\left.t_{0}, t_{0}+\tau-1\right]$ (during which $W$ is output) and the time interval during which the set $V$ of the variables affecting $W$ is input.

Indeed, noting that $\tau \geq 1$, that $\alpha$ is nondecreasing, and that $|W|$ satisfies (3), we can write

$$
\tau \leq \log \alpha\left(\frac{|U|}{2 T}\right) \leq \log \alpha\left(\frac{\tau|U|}{2 T}\right) \leq \log \alpha(|W|)
$$

All variables of $W$ are output no earlier than $t_{0}$, and no later than $t_{0}+\tau-1$. Since, by Property 1 , each variable of $V$ functionally affects at least $\alpha(|W|)$ variables of $W$, no variable of $V$ can be input later than $\left(t_{0}+\tau-1\right)-\log \alpha(|W|) \leq t_{0}-1$. This establishes the claim.

We therefore choose $\tau=\operatorname{Llog} \alpha(|U| / 2 T)\rfloor$. In order to use Property 2 of Definition 1, the values of $X-V$ are held fixed. This implies that the variables of $W$ become function solely of the variables in $V$. By the claim just established, the values of the variables of $W$ must be encoded in the network at time $t_{0}-1$. Property 2 ensures that the assignment of $X-V$ can be chosen so that $W$ assumes at least $2^{\beta\left(\left|h^{\prime}\right|\right)}$ distinct configurations by varying the values of $V$. Thus, the number of binary storage devices in the network is at least $\beta(|W|)$. We conclude that

$$
S \geq \beta(|W|) \geq \beta\left(\frac{|U|}{2 T} \log \alpha\left(\frac{|U|}{2 T}\right)\right) .
$$

The latter bound implies (2).
Corollary 1. If, in Theorem 2, $\alpha(n)=\Omega(n)$, and $\beta(n)=\Omega(n)$, then

$$
\begin{equation*}
S=\Omega\left(\left(\frac{|U|}{T}\right) \log \left(\frac{|U|}{T}\right)\right) . \tag{4}
\end{equation*}
$$

The above discussion indicates that the two crucial ingredients of computational friction are functional dependence of the output upon the input and the transfer of information from input to output (I/O transfer). In the next section we shall investigate the dependence of semigroup products upon their factors.
3.3 Functional Dependence in Semigroup Products. We now specialize the set $X$ and $Y$ introduced above so that each of their elements is a variable ranging over a finite alphabet $A$, where $\langle A, \cdot\rangle$ is a semigroup. Moreover, the elements of $Y$ are defined as semigroup products of elements of $A$, that is:

$$
\begin{equation*}
y_{j}=x_{0} x_{1} \cdots x_{j} \quad j=0,1, \ldots, N-1 . \tag{5}
\end{equation*}
$$

The term $x_{i}$-factor denotes the value of the $(i+1)$ st term of product (5).
It is convenient to introduce the following notion.
Definition 3. A semigroup is called memory-inducive (MI) if, for every $j \geq 0$, and for every $0 \leq i \leq j, y_{j}=x_{0} x_{1} \cdots x_{j}$ is functionally dependent upon $x_{i}$. A semigroup is called non-memory-inducive (NMI) otherwise.
We shall see that most semigroups are MI, and we shall give an exact characterization of those that are NMI. All proofs of memory-induciveness consider the generic product $y_{j}$ and, for each $i$ in $[0, j]$, exhibit two distinct selections of factors differing exactly for their $x_{i}$-factor and producing different values for $y_{j}$. First we need to define one important class of semigroups:
Definition 4. Let $\left\langle L_{p},{ }^{\circ}\right\rangle$ be a left-zero semigroup and let $\left\langle R_{4}, *\right\rangle$ be a rightzero semigroup, as defined in Section 2. Define $Z_{p, 4}=\left\langle L_{p} \times R_{q}, \cdot\right\rangle$ as the directproduct semigroup [10] of $L_{p}$ and $R_{q}$, that is, $\left(l_{h}, r_{k}\right) \cdot\left(l_{l^{\prime}}, r_{k^{\prime}}\right)=\left(l_{l_{k}}, r_{k^{\prime}}\right)$.

From this definition, it follows that $\left(l_{h_{0}}, r_{k_{0}}\right) \cdot\left(l_{h_{1}}, r_{k_{1}}\right) \cdots\left(l_{h_{1}}, r_{k_{k}}\right)=\left(l_{h_{0}}, r_{k_{k}}\right)$. Thus, $y_{j}=x_{0} x_{1} \cdots x_{j}=x_{0} x_{j}$ depends only on the first and last factor, so that $Z_{p, q}$ is NMI. The significance of the semigroups $Z_{p .,}$ 's rests on the role they play in the following theorem.

Theorem 3. A semigroup $\langle A, \cdot\rangle$ is NMI if and only if the subsemigroup $\langle\operatorname{recur}(A), \cdot\rangle$ is isomorphic to $Z_{p, q}$, for some positive $p$ and $q$.

Theorem 3 is crucial for obtaining a complete classification of semigroups based on the complexity of the prefix problem. The proof is somewhat lengthy and technical, and is given in the Appendix.

Theorem 3 has the following interesting consequence, which could also be derived directly from the argument used to prove Claim 2 (see the Appendix).

Corollary 2. an NMI semigroup is monoid-free (none of its subsemigroups is a monoid with at least two elements) and, a fortiori, group-free.
3.4 I/O Information Transfer in Prefix Computations. We now apply the general results embodied by Theorems 2 and 3 to the set of MI-NCF semigroups.

Theorem 4. For any time-determinate, semellective, word-instantaneous Boolean network that solves the prefix problem of size $N$ for a MI-NCF semigroup $\langle A, \cdot\rangle$, size and time satisfy the bound

$$
\begin{equation*}
S=\Omega\left(\left(\frac{N}{T}\right) \log \left(\frac{N}{T}\right)\right) . \tag{6}
\end{equation*}
$$

Proof. We show that Theorem 2 can be applied, with $X=\left\{x_{0}, x_{1}, \ldots, x_{N-1}\right\}$, $Y=\left\{y_{0}, y_{1}, \ldots, y_{N-1}\right\}$, and function $f$ defined by (5). We select $U=\left\{y_{1}, y_{3}, \ldots\right.$, $\left.y_{2 i+1}, \ldots\right\} \subset Y$ and claim that $U$ is an $(\alpha, \beta)$-friction set for $f$, with $\alpha(n)=\Omega(n)$ and $\beta(n)=\Omega(n)$. Indeed, let $W$ be an arbitrary set of $U$, and let $J$ be such that $W=\left\{y_{j}: j \in J\right\}$. Let $I$ be the set of the $\lfloor|W| / 2\rfloor$ smallest indices in $J$, and
select $V$ as the set $\left\{x_{i}, x_{i+1}: i \in I\right\}$. We now observe:
(1) Since $\langle A, \cdot\rangle$ is MI, each variable of $V$ affects each $y_{j}$ with $j \in(J-I)$, that is, at least $L|W| / 2\rfloor$ variables of $W$. (This means that $\alpha(|W|) \geq\lfloor|W| / 2\rfloor$.)
(2) Since $\langle A, \cdot\rangle$ is NCF, then its Cayley diagram has a cycle of length $\geq 2$, and hence a cycle of length exactly 2 , that is, there are four elements $a, b, c, d \in A$ (not necessarily all distinct, but with $a \neq b$ ), such that $a c=b$ and $b d=a$. Consider the input sequences for which $x_{0}=a$, and $x_{i}=(c d)$ for $x_{i} \in(X-V)$ and $i \neq 0$ (this is the fixed assignment of the variables in $X-V$ ). For any partition $I=\left(I_{1}, I_{2}\right)$ of $I$, selecting the variables in $V$ as $x_{i}=x_{i+1}=(c d)$, for $i \in I_{1}$, and $x_{i}=c$ and $x_{i+1}=d$, for $i \in I_{2}$, results in $y_{i}=a$ for $i \in I_{1}$, and $y_{i}=b$ for $i \in I_{2}$. Therefore, there are at least $2^{|/|}=2^{| | w^{1 / 2 J}}$ output configurations for the variables of $W$. (This means that $\beta(|W|) \geq l|W| / 2\rfloor$.)

Since both conditions of Definition 2 are met, Theorem 2 applies; moreover, since both $\alpha$ and $\beta$ are linear functions, Corollary 2 applies with $|U|=\lfloor N / 2\rfloor$, thereby establishing (6).

The previous argument cannot be extended to cover either CF or NMI-NCF semigroups; in fact, in the next section we shall describe networks for prefix computation for such semigroups that violate bound (6). To gain some intuition on this phenomenon, we observe that a cycle in the Cayley diagram-as illustrated in the proof of Theorem 4-is the machinery necessary for sustained information transfer, while memory-induciveness forces temporary storage of this information in the network. The absence of cycles drastically reduces the information that can be transferred: Indeed the (generally nonsimple) path of $G(A)$ corresponding to a sequence $\mathbf{y}=\left(y_{0}, \ldots, y_{N-1}\right)$ of prefixes can be described with $O(\log N)$ bits, rather than the $\Omega(N)$ necessary for NCF semigroups. On the other hand, the absence of memory-induciveness means that the semigroup $\langle A, \cdot\rangle$ satisfies the condition in the statement of Theorem 3. This means that the path corresponding to $\mathbf{y}$ reaches $\operatorname{recur}(A)$ in at most $m_{A}$ steps, and thereafter each output variable becomes a function of a fixed state and of a constant number of input variables, thus requiring no essential temporary storage in the network.

Thus, Theorem 4 precisely partitions the semigroups into two classes: the MINCF semigroups, briefly referred to as friction semigroups, form one class, while the CF and the NMI-NCF form the other, correspondingly referred to as frictionless semigroups.
3.5 Computation Time. Based on the results of Section 3.4, we derive the following lower bound on computation time of prefix networks.

Theorem 5. Let $\langle A, \cdot\rangle$ be a semigroup such that recur $(A)$ is not a right-zero semigroup. Then, the computation time of any Boolean network that solves the prefix problem of size $N$ satisfies the bound $T=\Omega(\log N)$.

Proof. A simple consequence of bounded fan-out is that, if a given input variable functionally affects $N$ output variables, then $T=\Omega(\log N)$.

If $\operatorname{recur}(A)$ is not a right-zero semigroup, then either $\langle A, \cdot\rangle$ is MI , or $\operatorname{recur}(A)$ is isomorphic to some $Z_{p ; q}$, with $p \geq 2$. We claim that in either case $x_{0}$ functionally affects $y_{0}, y_{1}, \ldots, y_{N-1}$. If $\langle A, \cdot\rangle$ is MI, the claim is an obvious consequence of Definition 3. If, on the other hand, $\operatorname{recur}(A)$ has at least two strongly connected components, there are two elements $a$ and $b$ that belong to different components.

To see that $x_{0}$ affects $y_{j}$, for all $j \geq 0$, we need only to consider that $b=b b^{j} \neq a b^{j}$, since $a b^{j}$ belongs to the same component as $a$.

## 4. Upper Bounds

In this section we present size-time optimal network implementations of prefix algorithms. A network for general semigroups, with size $S=\Theta(N / T \log (N / T))$, is considered in Section 4.1. This network is optimal for MI-NCF (friction) semigroups. More efficient networks, with size $S=\Theta(N / T)$, are possible for both CF and NMI semigroups.

For CF semigroups, the reduction in size is achieved by reducing the amount of information relative to the input that the network stores while computing the output. The details of the design are presented in Section 4.2. In the same section we present an alternative scheme suited for CF insertion semigroups, which represents a simplification of the network for general semigroups.

For NMI semigroups, the reduction in size is achieved by reducing the delay between the input time of a variable $x_{i}$, and the output time of the corresponding output $y_{i}$. The network for NMI semigroups is illustrated in Section 4.3.

The constructions of both Section 4.2 and Section 4.3 apply to NMI-CF semigroups. Indeed, the prefix problem for these semigroups degenerates: for $j \geq m_{A}$, output $y_{j}$ is guaranteed to be constant with $j$, and equal to some left-zero of $A$.
4.1 General Semigroups. The algorithms are executed by a network having the structure of a binary tree $K$ with $w$ leaves. Tree realizations of prefix circuits were reported earlier in [13], and can be viewed as emulations of the well-known prefix network described in [5], [7], [19], and [21] and called twisted-reflected-tree in [5].

In tree $K$, leaf nodes perform input/output operations, while internal nodes perform data processing. Each node is bidirectionally connected to its parent and its offsprings.

The input sequence $\mathbf{x}=\left(x_{0}, \ldots, x_{N-1}\right)$ is segmented into $N / w$ wavefronts of width $w$, where $1 \leq w \leq N / \log N$ (for ease of discussion, we assume that $N$ is a power of two). The $i$ th wavefront is denoted $\mathbf{x}_{i}=\left(x_{i w}, x_{i w+1}, \ldots, x_{(i+1) w-1}\right)$, where $i=0,1, \ldots, N / w-1$. The wavefronts are sequentially fed to the network, with $x_{i w+j}$ input at the $j$ th leaf (see Figure 1). A fixed wavefront is processed by the network in two phases: an ascending phase (consisting of one input step and $\log w$ processing steps), when information flows from leaves to root, and a descending phase (consisting of $\log w$ processing steps and one output step), when the direction of the flow is reversed.

Let the level of a node $V$, denoted level $(V)$, be the number of edges on the path between $V$ and the root of $K$. For each of the algorithms described below, a step takes time $O(1)$ (independent of $N$ ). Moreover, a given step on a given wavefront is carried out by a single level of nodes, so that the network can be pipelined at a constant rate. Clearly, processing of the $N$-term sequence is completed in $N / w+2$ $\log w+2$ steps, and hence in $T=O(N / w)$.
More subtle is the use of storage at each node, which determines the global size of the network. A fixed wavefront is processed by a given level twice: once during the ascending phase, and then again-2 level $(V)+1$ steps later-during the descending phase. (For uniformity of presentation, we assume that the root too processes a wavefront in two (contiguous) steps, although these actions are obviously combinable into a single step.) In the interval of time between the two steps (one in the ascending phase, the other in the descending phase) performed by nodes


Fig. 1. Input protocol for prefix computation on a binary-tree network.
of a given level on the same wavefront, some information relative to that wavefront must be stored at the nodes.

A given internal node $V$ of $K$ determines a segmentation of the input sequence $\mathbf{x}$ as $\mathbf{x}=\alpha_{0} \beta_{0} \alpha_{1} \beta_{1} \cdots \alpha_{N / w-1} \beta_{N / w-1} \alpha_{N / w}$ where $\beta_{0} \beta_{1} \cdots$ is a subsequence of $\mathbf{x}$ that is input by the successive wavefronts to the leaves of the subtree rooted at $V$ ( $\alpha_{0}$ and $\alpha_{N / w}$ may be empty). For a given sequence $\mathbf{x}$, let $x$ denote the product of its terms. Each $\beta_{j}$ is further segmented as $\beta_{j}=\beta_{j}^{\prime} \beta_{j}^{\prime \prime}$, where $\beta_{j}^{\prime}$ and $\beta_{j}^{\prime \prime}$ are input at the left and right subtrees of $V$, respectively. For convenience, we adjoin to $\langle A, \cdot\rangle$ an identity $e$, if $\langle A, \cdot\rangle$ is not a monoid. This is simply a technical device to initialize the states of the processing modules, and in no way affects the input/output transformation.

Referring to the $j$ th wavefront, during the ascending phase internal node $V$ computes $\beta_{j}=\beta_{j}^{\prime} \beta_{j}^{\prime \prime}$ from the values $\beta_{j}^{\prime}$ and $\beta_{j}^{\prime \prime}$ received from its offsprings. In addition, the root (for which all the $\alpha$ 's are empty) maintains a state $\sigma$ initialized to $e$ (the identity introduced above) and updated as $\sigma:=\sigma \beta_{j}$. During the descending phase, nonroot node $V$ must receive from its parent the prefix $\gamma=\alpha_{1} \beta_{1} \ldots$ $\alpha_{j-1} \beta_{j-1} \alpha_{j}$; if $V$ has stored $\beta_{j}^{\prime}$, then it can provide the correct prefixes $\gamma$ and $\gamma \beta_{j}^{\prime}$ to its offsprings.

Below we describe in detail the actions of each node. We use a comma to separate concurrently executable actions, and a semicolon to separate actions to be sequentially executed. The ascending phase substep below is thought of as preceding the descending phase substep, although various degrees of concurrency are realizable. Note that, for correct synchronization, each internal
node $V$ uses a queue（called $\beta^{\prime}$－queue）capable of storing 2 level $(V)+1$ semigroup elements（the $\beta_{j}^{\prime}$ ）．In addition，each nonroot node has cells to store the elements to be forwarded in the next step；note that，for the root，one of these elements，$\beta_{j}$ ， is＂forwarded＂to the root itself．The content of all cells are initialized to the identity $e$ ．In summary，the generic step runs as follows：

Generic Step

```
ASCENDING PHASE
    begin forward \beta}\mathrm{ to parent, [root: }\sigma:=\sigma\beta
        \beta
        \mp@subsup{\beta}{}{\prime\prime}}:=\mathrm{ term received from right child;
        insert }\mp@subsup{\beta}{}{\prime}\mathrm{ into 的'-queue;
        \beta:= 尔啋;
    end
DESCENDING PHASE
    begin forward }\gamma\mathrm{ to left child and }\gamma\mp@subsup{\delta}{}{\prime}\mathrm{ to right child;
        \gamma:= term received from parent; [root: \gamma:=\sigma;]
        extract }\mp@subsup{\delta}{}{\prime}\mathrm{ from }\mp@subsup{\beta}{}{\prime}\mathrm{ -queue;
        compute }\gamma\mp@subsup{\delta}{}{\prime}\mathrm{ ;
    end
```

The algorithm is readily implemented by endowing the module of a node with a semigroup multiplier and a queue capable to store $(2 \operatorname{level}(V)+1)=O(\log w)$ semigroup elements．Thus，the total size of the network is $S=O(w \log w)$（ignoring the dependence upon the semigroup size and operation）．Therefore we have：

Theorem 6．The size－time complexity of the prefix problem on a Boolean network is $S=O((N / T) \log (N / T))$ ，for $T \in[\Omega(\log N), O(N)]$ ．

For friction semigroups the bound of Theorem 6 is optimal，as shown by Theorem 4．Since the recurrent subsemigroup of a friction semigroup is not a right－ zero semigroup，by Theorem 5 we have $T=\Omega(\log N)$ ．For $T=\Theta(N)$ ，the obvious $S=\Omega(1)$ lower bound is achieved．

4．2 Cycle－Free Semigroups．As we have already noted in the concluding remarks of Section 3．4，the information content of a sequence of prefixes in a CF semigroup is only logarithmic in the length of the sequence．This fact indicates the possibility of reducing the amount of information relative to a given wavefront that the network must process．
We shall represent a sequence $\mathbf{y}=\left(y_{0}, y_{1}, \ldots, y_{m_{p}-1}\right) \in A^{m_{p}}$ of semigroup elements by the sequence of pairs $\left(\left(a_{1}, m_{1}\right),\left(a_{2}, m_{2}\right), \ldots,\left(a_{p}, m_{p}\right)\right)$ that is uniquely determined by the conditions：$a_{i} \neq a_{i+1},(i=1, \ldots, p-1) ; y_{j}=a_{1}$ ，for $0 \leq j<m_{1}$ and $y_{j}=a_{i}$ for $m_{i-1} \leq j<m_{i},(i=2, \ldots, p)$ ．This representation is particularly efficient when $\mathbf{y}$ is a prefix sequence for a CF semigroup since $p \leq l_{A}$ ，where $l_{A}$ is the length of the longest simple path in the Cayley graph $G(A)$ ．

We then consider a recursive scheme for the prefix problem whereby：
（1）The input sequence（whose length $N$ is assumed to be a power of two）is split into a left and a right half of identical size．
（2）The prefix sequences of the two halves are recursively computed（in parallel）， and
（3）The results are combincd to form the prefix scquence of the entire input．

With the selected representation, let the two sequences computed by Step (2) be

$$
\left(\left(a_{1}, m_{1}\right), \ldots,\left(a_{p}, m_{p}=\frac{N}{2}\right)\right)
$$

and

$$
\left(\left(b_{1}, n_{1}\right), \ldots,\left(b_{q}, n_{q}=\frac{N}{2}\right)\right)
$$

where both $p$ and $q$ are no greater than $l_{A}$. The representation of the output sequence to be produced by Step (3) is obtained by first forming the sequence of pairs

$$
\left(\left(a_{1}, m_{1}\right), \ldots,\left(a_{p}, m_{p}\right),\left(a_{p} b_{1}, m_{p}+m_{1}\right), \ldots,\left(a_{p} b_{q}, m_{p}+m_{q}\right)\right)
$$

and by subsequently deleting all but the last pair from any maximal run of pairs with identical first term. At the bottom of the recursion, an individual element $x \in A$ is represented as the pair $(x, 1)$.

The outlined scheme is naturally implemented by a tree-connected network $K$ whose generic node is designed to perform the concatenation of two prefix sequences. Such node will be equipped with a storage array of $2 l_{A}$ cells, a semigroup multiplier, and some other simple logic. A cell of the array will be used to store a pair of type ( $a_{i}, m_{i}$ ). To determine the bit capacity of a cell, we note that:
(i) The term $a_{i}$ can always be encoded by $\lceil\log s 7$ bits, (where $s=|A|$ ).
(ii) For a node $V$ of $K$ with $w$ leaves, the term $m_{i}$ has value at most $w / 2^{\operatorname{leve} /(v)}$, so that $\log w-\operatorname{level}(V)+1$ bits are sufficient to represent it. Thus, the capacity of a cell at node $V$ is $\lceil\log s\rceil+\log w-\operatorname{level}(V)+1$.

The basic operation of node $V$ consists of loading into the first and last $l_{A}$ cells of the array the representations of the prefix sequences produced by the left and right children, respectively (some of the cells may actually be unused). The steps required to concatenate the two representations are fairly straightforward if the rows of the array are organized as shift registers. The update of the second term of each pair is done by appending a single bit, which is 0 for the pairs originating from the left child and 1 for those originating from the right child, since $m_{p}$ is a power of 2 . Notice that, since $\lceil\log s\rceil+\log w-\operatorname{level}(V)$ is the number of bits for the pairs of the children, the array at $V$ has the correct capacity.

We now describe how to obtain the ordinary form of the prefix sequence from its representation as a sequence of pairs $\left(\left(a_{1}, m_{1}\right), \ldots,\left(a_{p}, m_{p}\right)\right)$. Again, a recursive approach is appropriate. Indeed the first and the second half of $Y$ have respective representations

$$
\left(\left(a_{1}, m_{1}\right), \ldots,\left(a_{i}, \frac{m_{p}}{2}\right)\right)
$$

and

$$
\left(\left(a_{i}^{\prime}, m_{i}^{\prime}\right), \ldots,\left(a_{p}, m_{p}\right)\right)
$$

where $i$ is such that $m_{i-1}<m_{p} / 2 \leq m_{i}$ and $i^{\prime}=i$ if $m_{i} \neq m_{p} / 2$ and $i^{\prime}=i+1$ if $m_{i}=m_{p} / 2$. (Note that $i$ can be determined in time $O\left(l_{A}\right)$.)

The conversion of representation is again performed by tree $K$, with each wavefront starting at the root and propagating toward the leaves. Pair $\left(y_{j}, j\right)$ is correctly delivered at leaf $j$.

The preceding description indicates that, referring to a single wavefront $\mathbf{x}$, tree $K$ is used in an ascending phase to compute the pair representation of the prefix sequence $\mathbf{y}$ of $\mathbf{x}$, and that a subsequent descending phase obtains the ordinary form of the latter. Note, however, that in marked contrast to the interaction between ascending and descending phases of the general algorithm of Section 4.1, the two phases are now completely decoupled. It is also simple to see that the operation of tree $K$ can be pipelined by just combining at the root the results of successive wavefronts.

Finally we note that $K$ consists of $\log w$ levels, and that the $2^{j}$ modules at level $j$ have each $l_{A} \times(\log w-j+\lceil\log s 1)$ bits of storage and fixed-size Boolean circuitry. Thus, the total size of $K$ is given by $O\left(\sum_{j=0}^{\log _{0}{ }^{w-1}} 2^{j}(\log w-j)\right)=O(w)$. Computation is completed after $O(N / w)$ steps both for the input phase and the output phase, and again, the time used by each step is independent of $N$. The above construction yields:

Theorem 7. For a CF semigroup, the prefix computation for an $N$-term sequence can be done in time $T$ and size $S$, with $S=O(N / T)$, for $T \in[\Omega(\log N), O(N)]$.

Remark. The above result holds for any CF semigroup and is clearly optimal. However, for the very important case of insertion monoids, there exists an alternative method of the same flavor as the general technique outlined in Section 4.1. Each internal node $V$ of $K$ still contains a semigroup multiplier, and a queue with $2 l_{A}$ cells, each capable of storing a semigroup element; an additional cell stores the node state state $(V)$.

Initially, for each $V$ in $K$, state $(V):=e$. Nonroot internal node $V$ performs the following actions:

## Generic Step

```
ASCENDING PHASE
    begin forward }\beta\mathrm{ to parent,
        \beta
        \beta"}:=\mathrm{ term received from right child;
        if state(V)\beta\not= state(V) then
            begin state(V):= state(V)\beta;
                insert ( }\mp@subsup{\beta}{}{\prime},\mp@subsup{\beta}{}{\prime\prime})\mathrm{ into queue
            end;
        \beta:=\mp@subsup{\beta}{}{\prime}}\mp@subsup{\beta}{}{\prime\prime
    end
DESCENDING PHASE
begin forward ( \(\gamma_{I}, \beta_{I}\) ) to left child and ( \(\gamma_{R}, \beta_{R}\) ) to right child;
        (\gamma,\beta):= term received from parent;
        ( }\mp@subsup{\beta}{}{\prime},\mp@subsup{\beta}{}{\prime\prime}):= next pair in queue
        if }\gamma\mp@subsup{\beta}{}{\prime},\mp@subsup{\beta}{}{\prime\prime}=\gamma
            begin (\mp@subsup{\gamma}{t}{\prime},\mp@subsup{\beta}{t}{\prime}):=(\gamma,\mp@subsup{\beta}{}{\prime}),(\mp@subsup{\gamma}{R}{},\mp@subsup{\beta}{R}{}):=(\gamma\mp@subsup{\beta}{}{\prime},\mp@subsup{\beta}{}{\prime\prime});
                extract ( }\mp@subsup{\beta}{}{\prime},\mp@subsup{\beta}{}{\prime\prime})\mathrm{ from queue
            end
        else (\mp@subsup{\gamma}{L}{},\mp@subsup{\beta}{L}{}):=(\mp@subsup{\gamma}{R}{},\mp@subsup{\beta}{R}{}):=(\gamma,e)
    end
```

Lemma 2. The above scheme correctly computes the prefix sequence for insertion monoids.

Proof. We say that $\beta_{i}$ is a local transition of $\beta_{0} \beta_{1} \cdots \beta_{j-1} \beta_{j} \neq \beta_{0} \beta_{1} \cdots \beta_{j-1}$, and a global transition if $\alpha_{0} \beta_{0} \cdots \alpha_{j} \beta_{j} \neq \alpha_{0} \beta_{0} \cdots \alpha_{j}$. Let $e$ denote the monoid identity.

Claim 1. A global transition is also a local transition. Indeed $\beta_{0} \cdots \beta_{j-1} \beta_{j}=$ $\beta_{0} \cdots \beta_{j-1}$ implies $\beta_{0} \alpha_{1} \cdots \beta_{j-1} \alpha_{j} \beta_{j}=\beta_{0} \alpha_{1} \cdots \beta_{j-1} \alpha_{j} e \beta_{j}=\beta_{0} \alpha_{1} \cdots \beta_{j-1} \alpha_{j} e=\beta_{0} \alpha_{1}$ $\cdots \beta_{j-1} \alpha_{j}$ by the insertion property (1), and therefore $\alpha_{0} \beta_{0} \cdots \alpha_{j} \beta_{j}=\alpha_{0} \beta_{0} \cdots \alpha_{j}$. This means that the global transitions form a subset of the pairs of the form $\left(\beta^{\prime}, \beta^{\prime \prime}\right)$ inserted into the queues of the nodes of the network during the ascending phase.

A local transition $\beta_{j}$ is not global because we may have $\alpha_{0} \beta_{0} \cdots \alpha_{j} \beta_{j}=$ $\alpha_{0} \beta_{0} \cdots \beta_{j-1} \alpha_{j}$, even if $\beta_{0} \cdots \beta_{j} \neq \beta_{0} \cdots \beta_{0-1}$. Let $\gamma=\alpha_{0} \beta_{0} \cdots \alpha_{j}(\gamma$ is called the prefix associated with $\beta_{j}$ ).

Claim 2. If $\gamma_{1} \beta_{j}=\gamma_{1}$ for a left factor $\gamma_{1}$ of $\gamma$, then $\gamma \beta_{j}=\gamma$. Indeed, let $\gamma=$ $\gamma_{1} \gamma_{2}$. Then, $\gamma_{1} \beta_{j}=\gamma_{1}$ trivially implies $\gamma_{1} e \beta_{j}=\gamma_{1} e$, which in turn, by (1), implies $\gamma_{1} \gamma_{2} e \beta_{j}=\gamma_{1} \gamma_{2} e$, whence $\gamma \beta_{j}=\gamma$.

Suppose now that $\left(\beta_{j}^{\prime}, \beta_{j}^{\prime \prime}\right)$ is a local transition at the head of the queue at $V$. When $V$ receives the pair $(\gamma, \beta)$, with $\beta=\beta_{j}$, we have $\gamma \beta=\gamma$ and $\gamma \beta_{j}^{\prime} \beta_{j}^{\prime \prime}=\gamma \beta$, that is, $\left(\beta_{j}^{\prime}, \beta_{j}^{\prime \prime}\right)$ is correctly eliminated from the queue and the pairs $\left(\gamma \beta_{j}^{\prime}\right)$ and $\left(\gamma \beta_{j}^{\prime}, \beta_{j}^{\prime \prime}\right)$ are passed on to the left and right children of $V$. This is correct, since $\gamma \beta_{j}^{\prime}=\gamma$ and $\gamma \beta_{j}^{\prime} \beta_{j}^{\prime \prime}=\gamma$. Thus, $\left(\beta_{j}^{\prime}, \beta_{j}^{\prime \prime}\right)$ is certainly eliminated from the queue upon arrival of its associated prefix, but it may be eliminated sooner by a left factor $\gamma_{1}$ of it such that $\gamma_{1} \beta_{j}^{\prime} \beta_{j}^{\prime \prime}=\gamma_{1}$. This shows that a local transition is always eliminated before any subsequent global transition residing in the queue, which is correctly detected by the condition ( $\gamma \beta^{\prime} \beta^{\prime \prime}=\gamma \beta$ ) of the algorithm. In the majority of cases the action $\left(\gamma_{L}, \beta_{L}\right)=\left(\gamma_{R}, \beta_{R}\right):=(\gamma, e)$ occurs, corresponding to no transition, either local or global.

We can therefore conclude:
Theorem 8. For an insertion monoid $A$, the prefix computation for an $N$-term sequence can be done in time $T$ and size $S$ with $S=O(N / T)$ for $T \in[\Omega(\log N)$, $O(N)]$. The memory used by each nonroot module is $O\left(s l_{A} \cdot \log s\right)$ bits, where $s=|A|$ and $l_{A}$ is the height of the Cayley graph of $A$.

Proof. Indeed, the result $S=O(N / T)$ follows from Theorem 7. Each nonroot module has a queue of $2 l_{A}$ cells, each with $\lceil\log s 7$ bits.
4.3 Non-Memory-Inducive Semigroups. As we noted earlier, for this class of semigroups we have

$$
y_{j}=\left\{\begin{array}{lll}
x_{0} \cdots x_{j} & \text { for } 0 \leq j \leq 2 m_{A}-1 \\
x_{0} \cdots x_{m_{A}-1} x_{j-m_{A}+1} & \cdots x_{j} & \text { for } \quad 2 m_{A} \leq j \leq N-1
\end{array}\right.
$$

We assume, without loss of generality, that $m_{A}$ is a power of 2 (else, we replace $m_{A}$ with $\left.2^{\left\lceil\log m_{2} m_{A}\right\urcorner}\right)$. Note, also, that for $j \geq 2 m_{A} y_{j}=x_{0} \cdots x_{m_{A}-1} x_{h} \cdots x_{j}$ for any $m_{A} \leq$ $h \leq j-m_{A}$.

As usual, the sequence $\mathbf{x}$ is supplied in $N / w$ wavefronts each of width $w$, a divisor of $N$ and therefore a power of 2 . We assume, for simplicity, that $w \geq m_{A}$ and leave to the reader the simple task of developing the details for the opposite case.

The network consists of a binary tree $K$ of depth $\log w$. The lower $\log m_{A}$ levels realize a collection of $w / m_{A}$ prefix trees as those described in Section 4.1, whereas the upper $\log w-\log m_{f}$ levels realize a straightforward semigroup multiplier tree. The root of each of the lower prefix trees, except the rightmost one, has an arc to the root of the adjacent tree on the right.

The operation of this network is as follows: The first wavefront, $\left(x_{0}, \ldots, x_{w-1}\right)$ is supplied in an isolated fashion and stored at the leaves, while in time $O(\log w)$ the tree computes the prefix $y_{m_{A^{-1}}}$ and broadcasts it to all leaves, where it is stored (broadcast to the leaves with indices $0, \ldots, 2 m_{A}-1$ is in reality redundant).

Once this initial step is completed, the usual pipeline operation takes place, starting with the first wavefront that was temporarily stored earlier. The delay between the first wavefront and the subsequent ones is in compliance with the stated I/O protocol. During this phase only the lower trees are active, and each nonroot node operates as described in Section 4.1. A slight modification is in order for the operation of the root. Referring to the Generic Step algorithm of Section 4.1 (ASCENDING PHASE), $\beta$ is now passed the root of the tree adjacent to the right. The term simultaneously received from the root of the tree adjacent on the left is called $\beta^{*}$, and the root state is updated as $\sigma:=\beta^{*}$.

Note that this implementation actually computes prefix $y_{j}$ as $x_{0} \ldots$ $x_{m_{A}-1} x_{(i-1) m_{A}} \cdots x_{i m_{A}-1} x_{i m_{A}} \cdots x_{j}$ where $i=\left\lfloor j / m_{A}\right\rfloor$, for $j \geq m_{A}$. This is obviously correct since $(i-1) m_{A}<j-m_{A}+1$.

It is easily realized that $K$ consist of $O(w)$ modules, each independent of $N$, and that computation is completed in $2 \log w+2 \log m_{A}+N / w+4$ steps, that is, in time $T=O(N / w)$. Thus we have:

Theorem 9. For an NMI semigroup $\langle A, \cdot\rangle$, the prefix computation for an $N$-term sequence can be done in time $T$ and size $S$, with $S=O(N / T)$, for $T \in$ [ $\Omega(\log N), O(N)]$.

Note that if $\operatorname{recur}\langle A, \cdot\rangle$ is a right-zero semigroup, then for $j \geq m_{A}, y_{j}=$ $x_{j-m_{A}+1} \cdots x_{j}$. This shows that the broadcast of $y_{m_{A}-1}$ is no longer necessary and that the network becomes a collection of $w$ combinational machines each computing $a$ product of at most $m_{A}$ terms. In this case, the time range in Theorem 9 can be extended as $T \in[\Omega(1), O(N)]$.

## 5. Area-Time Complexity

The results of the preceding sections have some consequences on the area-time complexity of synchronous VLSI circuits for the prefix problem. These consequences are summarized below by Theorem 10 for nonboundary circuits (those for which the position of the I/O ports is unconstrained), and by Theorem 11 for boundary circuits (those for which I/O ports are constrained to lie on the boundary of a convex chip).

Theorem 10. The area-time complexity of nonboundary VLSI circuits for the prefix problem is $A=\Theta((N / T) \log (N / T))$, for MI-NCF semigroups, and is $A=$ $\Theta(N / T)$, for all other semigroups. The bounds hold for $T \in[\Omega(\log N), O(N)]$ for all semigroups, with the exception of those whose recurrent subsemigroup is a rightzero semigroup, for which $T \in[\Omega(1), O(N)]$.

Proof. A VLSI circuit is the layout of a Boolean network. Since our prefix networks are balanced trees, they can be laid out (according to the H-scheme [6]) in area proportional to the number of nodes times the area of a node, that is, in area proportional to their size. Thus, Theorem 10 follows from Theorem 1, and from the fact that size is a lower bound for area.

The fact that our prefix networks admit of a nonboundary layout whose area is proportional to the size is accidental. In general, layout area is larger than size, owing to the space occupied by wires.

We now consider boundary layouts. We begin by establishing the following lemma:

Lemma 3. Let $\langle A, \cdot\rangle$ be such that recur $(A)$ is not a right-zero semigroup. Any boundary circuit $C$ that solves the prefix problem of size $N$ for $A$ in time $T$ (where $T=\Omega(\log N)$ ) contains a binary tree of height $O(T)$ with $\Omega(N / T)$ leaves on the boundary.

Proof. In order to generate $N$ output symbols in time $T$, circuit $C$ must contain $w \geq(N / T)$ output ports. Since each output $y_{j}, 0 \leq j \leq N-1$, is functionally dependent upon input $x_{0}$ (see proof of Theorem 5), there must be a path in $C$ from the input port reading $x_{0}$ to each output port. The union of the $w$ paths can be viewed as an undirected graph $G$, whose edges are wires each carrying a semigroup symbol. Since each output port is connected to the exterior by one such wire, we may correctly assume that the output port is itself on the boundary of the layout (by transplanting it on the boundary if necessary). Moreover, we may assume that each output port is a degree-1 node of $G$; otherwise, we create one such node on the layout boundary and connect it to the port, with an insignificant increase in wire area. Let $H$ be a minimum-height spanning tree of $G$ rooted at the input port of $x_{0}$. Owing to the hypothesis of bounded fan-in and fan-out, we may assume without loss of generality that $H$ is a rooted binary tree. Tree $H$ has $w$, that is, $\Omega(N / T)$, leaves. Since $T$ is the computation time of $C$, the height of $H$ is $O(T)$.

Lemma 3 establishes a crucial connection between binary trees and prefix circuits. Our problem is then reduced to the boundary layout of binary trees, a topic that has been adequately investigated [6, 12, 24]. The results most relevant to our discussion are expressed by the following three lemmas:

Lemma 4 [6]. A complete binary tree with $w$ leaves can be laid out with the leaves on the boundary of a rectangular region of height $O(\log w)$ and width $O(w)$. The resulting $O(w \log w)$ area is optimal.

The constructive proof of this lemma yields a layout of a complete binary tree with $w$ leaves in a rectangular region consisting of $2 w-1$ vertical tracks (one per node) and $\log w+1$ horizontal tracks (one per level of nodes in the tree). More specifically, nodes appear from left to right according to the in-order traversal of the tree, and consecutive levels appear in consecutive horizontal tracks, with the root at the top, and the leaves at the bottom.

Lemma 5 [24]. Any layout of a binary tree of height $h$ with $w$ leaves placed on the boundary has area $A=\Omega((w \log w) / \log (2 h / \log w))$.

Lemma 6 [12]. There exist binary trees with $w$ leaves, height $h$, and boundary layout area $A=O((w \log w) / \log (2 h / \log w))$, which can emulate a balanced binary tree of $w$ leaves in time $O(h)$.

With these premises, we can now give tight area-time bounds for boundary prefix circuits.

Theorem 11. For boundary VLSI circuits for the prefix problem we have the following area-time complexity results.
(1) MI-NCF Scmigroups: $A=\Theta((N / T) \log (N / T))$, with $T \in[\Omega(\log N), O(N)]$;
(2) CF Semigroups: $A=\Theta((N / T) \log (N / T))$, and $A=\Omega((N / T \log (N / T))$ $/ \log (T / \log N)$ ), with $T \in[\Omega(\log N), O(N)]$;
(3) CF-Insertion and NMI Semigroups, Except for Those with Right-Zero Recurrent Subsemigroup: $A=\Theta((N / T \log (N / T)) / \log (T / \log N))$, with $T \in$ [ $\Omega(\log N), O(N)] ;$
(4) Semigroups with Right-Zero Recurrent Subsemigroup: $A=\Theta(N / T)$, with $T \in$ [ $\Omega(1), O(N)]$.
Proof. First of all we note that the bounds on computation time are not affected by layout issues, and hence are as in Theorem 1. As regards the wire area, we distinguish the following four cases:
(1) MI-NCF Semigroups. These semigroups are processed by the networks of Section 4.1 , which are complete binary trees with $w=\Theta(N / T)$ leaves, where each node is equipped with a queue of size $O(\log w)$ and logic circuitry of constant size. These networks can be laid out in area $A=O(w \log w)$ according to the scheme provided by Lemma 4. The queue of each node can be easily accommodated in the vertical track of that node. In conclusion, we obtain $A=O((N / T) \log (N / T))$. The area is of the same order as the optimal size, and is therefore optimal.
(2) CF Semigroups. The upper bound for this case is obtained, as in case (1), by the network of Section 4.1. It turns out that the obvious layout for the network given in Section 4.2 for CF semigroups (a balanced binary tree with edges of bandwidth proportional to their height) would result in a larger area, in spite of a smaller size. The boundary layout of this network needs further investigation. The lower bound follows from Lemmas 3 and 5, with $w=\Omega(N / T)$ and $h=O(T)$.
(3) CF-Insertion and NMI Semigroups, Except for Those with Right-Zero Recurrent Subsemigroup. These semigroups (see Remark at the end of Section 4.2 and Section 4.3) are processed by complete binary trees with constant-size nodes and $w=\Theta(N / T)$ leaves. The upper bounds stated in the theorem can be achieved by emulating the complete binary trees of our prefix networks with the trees of Lemma 6 with $h=\Theta(T)$. The lower bound follows from Lemmas 3 and 5, with $w=\Omega(N / T)$ and $h=O(T)$.
(4) Semigroups with Right-Zero Recurrent Subsemigroup. As observed in Section 4.3, the prefix networks for these semigroups consist of $w=O(N / T)$ combinational circuits of constant size. A simple layout having the shape of an array of constant height and $O(w)$ width yields a boundary circuit of area $A=O(N / T)$, which is trivially optimal.

## 6. Remarks and Open Problems

In this paper, we have considered the computation of the prefixes of an N -term sequence of semigroup elements on Boolean networks. We have completely characterized the size-time and the area-time complexity of the networks as a function of $N$, under some assumptions on the network's I/O protocol.

A problem intimately related to the computation of prefixes is the computation of suffixes $z_{j}=x_{j} x_{j+1} \cdots x_{N-1}$, for $j=0,1, \ldots, N-1$. It is readily observed that the suffixes for a semigroup $\langle A, \cdot\rangle$ are the prefixes of its reverse semigroup $\langle A, *\rangle$, where $x * y=y \cdot x$. It must be noted, however, that a semigroup and its reverse are not necessarily in the same class, as characterized in this paper. A notable case in point is the semigroup of the carry function in binary addition mentioned in Section 2, which is a friction semigroup, where its reverse semigroup (a left-zero two-element semigroup with adjoined identity) is frictionless because it is CF. The latter is easily seen to model the comparison-exchange operation of two binary
integers. Nevertheless, the operation of comparison-exchange exhibits computational friction for reasons unrelated to the underlying prefix computation (see [2]).

It would be interesting to investigate to which extent the results of this paper depend on the I/O protocol assumptions.

In this direction, it is appropriate to observe that, for some semigroups, non-word-instantaneous circuits are more efficient than word-instantaneous ones. A simple example is provided by the direct product of the OR semigroup ( $\{0,1\}$, OR) (which is CF ) with the right-zero semigroup of two elements $R_{2}$ (which is NMI). It is straightforward to verify that this direct product is a friction semigroup. Now let $C_{1}$ be the network of Section 4.2 specialized for the OR semigroup, and let $C_{2}$ be the network of Section 4.3 specialized for $R_{2}$. Both $C_{1}$ and $C_{2}$ have size $S$ $=O(N / T)$. Taken as a pair, they clearly compute the direct product of their respective semigroups with size $S=O(N / T)$, seemingly contradicting Theorem 4. A closer look at $C_{1}$ and $C_{2}$ reveals the delay between the input time of $x_{i}$ and the output time of $y_{i}$ is constant in the latter and is logarithmic in the former. There is simply no way to combine the two networks so that the protocol of the result is word-instantaneous both in the input and in the output. Therefore, Theorem 4 does not apply to the pair ( $C_{1}, C_{2}$ ), and there is no contradiction.

The major outstanding problem is the investigation of the dependence of network complexity upon semigroup size and operation. For example, in Theorem 8 we have shown that, for the important case of insertion semigroups, the upper bounds of Theorem 7 can be considerably improved. However, the construction of prefix Boolean networks that are optimal also with reference to semigroup size remains an open problem.

## Appendix. Proof of Theorem 3

Sufficiency. (If $\langle\operatorname{recur}(A), \cdot\rangle$ is isomorphic to $Z_{p .4}$, then $\langle A, \cdot\rangle$ is NMI.) We begin by observing that every $b \in A$ has a recurrent power $b^{i}$. In fact, each nonrecurrent element may appear at most once in the sequence $b, b^{2}, b^{3}, \ldots$

Next, we claim that if $a \in \operatorname{recur}(A)$, then $(a b) \in \operatorname{recur}(A)$, for any $b \in A$. Let $i$ be such that $b^{i} \in \operatorname{recur}(A)$. By the isomorphism between $\operatorname{recur}(A)$ and $Z_{p, q}, a b^{i}$ and $a$ belong to the same strongly connected component of $G(A)$. Therefore, for some $z, a b^{i} z=a$, whence $(a b)\left(b^{i-1} z b\right)=(a b)$, which implies that $(a b)$ is recurrent.

Each nonrecurrent element may appear at most once in any sequence of prefixes and, by the preceding claim, if prefix $y_{i}$ is recurrent then $y_{j}$ will also be recurrent for every $j>i$. We conclude that there is a (minimum) integer $m_{A}$ such that ( $x_{0} x_{1}$ $\left.\cdots x_{m_{A^{-1}}}\right) \in \operatorname{recur}(A)$ for any choice of $x_{0}, x_{1}, \ldots, x_{m_{A}-1}$. It follows that for $j \geq$ $2 m_{A}-1$ both $\left(x_{0} \cdots x_{m_{A}-1}\right)$ and $\left(x_{j-m_{A}+1} \cdots x_{j}\right)$ belong to $\operatorname{recur}(A)$. Since $\operatorname{recur}(A)$ is isomorphic to some $Z_{p .4}$, by Definition $4, y_{j}=x_{0} \cdots x_{n_{A}-1} x_{j-m_{A}+1} \cdots x_{j}$ for any $j \geq 2 m_{A}-1$. This shows that for $j>2 m_{A}-1, y_{j}$ does not functionally depend upon $x_{m_{A}}, x_{m_{A}+1}, \ldots, x_{j-m_{A}}$, and therefore $\langle A, \cdot\rangle$ is NMI.

Necessity. (If $\langle A, \cdot\rangle$ is NMI, then $\langle\operatorname{recur}(A), \cdot\rangle$ is isomorphic to $Z_{p, q}$.) This part of the proof is based on a number of claims stated and proved below. Let recur $(A)$ be the union of strongly connected components $C_{1}, C_{2}, \ldots, C_{p}$.

Claim A1. The components $C_{1}, C_{2}, \ldots, C_{p}$ of recur $(A)$ have no outgoing arcs in $G(A)$, that is, if $a \in C_{r}$, then $(a c) \in C_{r}$ for all $c \in A$.
Proof of Claim A1. Assume, for a contradiction, that $a \in C_{r}$ and that, for some $c \in A, a c \in C_{s}$ with $s \neq r$. Since $a \in \operatorname{recur}(A)$, there is a $b \in A$ such
that $a b=a$. We now show that $y_{j}$ functionally depends upon $x_{i}$ for $i=0, \ldots, j$, that is, $\langle A, \cdot\rangle$ is MI. To see that $y_{j}$ depends upon $x_{0}$, consider the products $a b^{j}$ and $(a c) b^{j}$ that differ only for the $x_{0}$-factor. Clearly, $a b^{j}=a$. On the other hand, since $a c \in C_{s}$, then $(a b) b^{j} \notin C_{r}$, otherwise $C_{r}$ and $C_{s}$ would not be distinct strongly connected components of $\operatorname{recur}(A)$; thus $(a c) b^{j} \neq a$. To see that $y_{j}$ depends on $x_{i}$ for $1 \leq i \leq j$, consider the products $a b^{i-1} b b^{j-i}=a b^{j}=a$ and $a b^{i-1} c b^{j-i}=$ $(a c) b^{j-1} \notin C_{r}$ which differ only in the $x_{i}$-factor and have different values. We have a contradiction, which establishes Claim A1.

Claim A2. If $\langle A, \cdot\rangle$ is NMI, then each element of $A$ has period 1.
Proof of Claim A2. Assume, for a contradiction, that there is an element $a$ with period $\geq 2$. Then $\langle A, \cdot\rangle$ has a subsemigroup $A^{\prime}$ with an identity $e$ and at least another element, say $a$. Then, for every $j \geq 0$, and $0 \leq i \leq j, e=e^{j+1}=e^{i} e e^{j-i} \neq$ $e^{i} a e^{j-i}=a$. Thus $\langle A, \cdot\rangle$ is MI, a contradiction.

Below, we make use of the following notation: If $C \subseteq A$ and $a \in A$ are such that, for $u \in C, u a$ is independent of $u$, then $u a$ is denoted by $C a$.

Claim A3. For $r=1,2, \ldots, p$, if $a$ and $b$ are in $C_{r}$, then $b a=a$. Thus, each $C_{r}$ is a right-zero subsemigroup, and for $a \in C_{r}$, we can write $C_{r} a=a$.

Proof of Claim A3. Let $u \in C_{r}$. Since $\langle A, \cdot\rangle$ is not MI, by Claim A2, $u$ has period 1 and hence, for some $k, u^{k+1}=u^{k}$. Let $a=u^{k}$. Clearly, $a \in C_{r}$, and $a^{2}=a$. Thus, if $C_{r}=\{a\}$, then Claim A3 is established. Assume then that $C_{r}$ contains also $b$, with $b \neq a$. By definition of strongly connected component, there is a $c \in A$ such that $a c=b$. We claim that $b a=a$. We prove the claim by contradiction by showing that if $b a \neq a$, then for $j \geq 0$ and $0 \leq i \leq j, y_{j}$ functionally depends upon $x_{i}$, and therefore $\langle A, \cdot\rangle$ is MI.

We consider four cases. For $j=0, y_{0}=x_{0}$ obviously depends upon $x_{0}$. For $j=1, y_{1}=x_{0} x_{1}$ depends upon both factors as shown by the inequalities $b a \neq a a$ and $a a \neq a c$. For $j \geq 2$ and $i<j$, the dependence is established considering the products $a=a^{j+1}=a^{i} a a^{j-i}$, and $a^{i} c a^{j-i}=a c a=b a$. For $i=j$, the dependence follows from the fact that $a^{j}=a^{j-1} a \neq a^{j-1} c$.

Summarizing, for every $b \in C_{r}, b a=a$, that is, $C_{r} a=a$. Moreover, $b^{2}=$ $b(a c)=(b a) c=a c=b$, so that all the elements of $C_{r}$ are idempotent. Since the argument to show that $C_{r} a=a$ is based solely on the idempotence of $a$, it can be extended to all elements of $C_{r}$, yielding the desired conclusion.

Claim A4. If $a \in \operatorname{recur}(A)$ and $b \in C_{s}$, then ba is independent of $b$, and can be written as $C_{s}$ a.

Proof of Claim A4. For a contradiction, let $b_{1}$ and $b_{2}$ be elements of $C_{s}$ such that $b_{1} a \neq b_{2} a$. Since $a \in \operatorname{recur}(A)$, then $a^{p}=a$ for any $p \geq 1$. Then, for $j \geq 2$ and $i<j, b_{1} a=b_{1}^{i} b_{1} a^{j-1} \neq b_{1}^{i} b_{2} a^{j-1}=b_{2} a$, so that $y_{j}$ depends upon $x_{i}$, and the semigroup is MI.

Claim A5. Let $a \in C_{r}$ and $b \in C_{s}$, with $r \neq s$. If $C_{s} a=b$, then $C_{r} b=a$, and for no other $c \in C_{r}$ we have $C_{s} c=b$.

Proof of Claim A5. Since, by hypothesis, $C_{s} a=b$, then in particular $b a=b$. Then $C_{r} b=C_{r}(b a)=\left(C_{r} b\right) a$. But $C_{r} b \in C_{r}$, and, by Claim A3, $\left(C_{r} b\right) a=a$. This establishes $C_{r} b=a$. Assume now, for a contradiction, that for some $c \in C_{r}, c \neq a$, $C_{s} c=b$. This implies $C_{r} b=c$, a contradiction.

Claim A5 shows that, for any $r$ and $s$ in $\{1, \ldots, p\}$, there is a bijection between $C_{r}$ and $C_{s}$. Thus, $C_{1}, C_{2}, \ldots, C_{p}$ have identical cardinality $q$. If we let $C_{1}=$ $\left\{a_{1}, a_{2}, \ldots, a_{q}\right\}$, then an arbitrary element of $C_{h}$ can be written in a unique way as $C_{h} a_{k}$. Therefore $\operatorname{recur}(A)=\left\{C_{h} a_{k}: 1 \leq h \leq p, 1 \leq k \leq q\right\}$.

To establish that $\operatorname{recur}(A)$ is isomorphic to $Z_{p, q}$, it remains to show that $\left(C_{h} a_{k}\right)\left(C_{h^{\prime}} a_{k^{\prime}}\right)=C_{h a_{k}}$. Indeed, $\left(C_{h} a_{k^{\prime}}\right)\left(C_{h^{\prime}} a_{k^{\prime}}\right)=C_{h \prime}\left(a_{k} C_{h^{\prime}}\right) a_{k^{\prime}}$. Since $a_{k} \in C_{1}$, by Claim A1, $a_{k} C_{h^{\prime}} \in C_{1}$, whence, by Claim A3, $\left(a_{k} C_{h^{\prime}}\right) a_{k^{\prime}}=a_{k^{\prime}}$.
acknowledgments. We are indebted to D. E. Muller and to N. Pippenger for their valuable suggestions. We are also grateful to the referees whose constructive criticisms have led to considerable improvements over an earlier version of the manuscript.

## REFERENCES

1. Balidft, G. M. On the area required by VISI circuits. In VLSI Systems and Computations, H. T. Kung, R. Sproull, and G. Steele, Eds. Computer Science Press, Rockville, Md., 1981, pp. 100-107.
2. Bilardi, G. The area-time complexity of sorting. Ph.D. dissertation TR-R-1024, Univ. Illinois at Urbana-Champaign, Urbana, IIl., Dec. 1984.
3. Bilardi, G., and Preparata, F. P. The influence of key length on the area-time complexity of sorting. In Proceedings of the International Colloquium on Automata, Languages, and Programming, W. Brauer, Ed. (Nafplion, Greece, July). Springer-Verlag, New York, 1985, pp. 53-62.
4. Bllardi, G., and Preparata, F. P. Area-time lower-bound techniques with applications to sorting. Algorithmica 1,1 (1986), 65-91.
5. Bilardi, G., and Preparata, F. P. Digital filtering in VLSI. In Proceedings of Aegean Workshop on Computing. F. Makedon, K. Mehlhorn, T. Papathedorou, and P. Spirakis, Eds. (Loutraki, Greece, July). Springer-Verlag, New York, 1986, pp. 1-11.
6. Brent, R. P., and Kung, H. T. On the area of binary tree layouts. Inf. Proc. Lett. 11, 1 (Aug. 1980), 46-48.
7. Brent, R. P., and Kung, H. T. A regular layout for parallel adders. IEEE Trans. Comput. C-31, 3 (Mar. 1982), 260-264.
8. Chandra, A. K., Fortune, S., and Lipton, R. Unbounded fan-in circuits and associative functions. In Proceedings of the 15th Annual Symposium on Theory of Computing (Boston, Mass., Apr. 25-27). ACM, New York, 1983, pp. 52-60.
9. Chandra, A. K., Fortune, S., and Lipton, R. Lower bounds for constant depth circuits for prefix problems. In Proceedings of the International Colloquium on Automata, Languages, and Programining. Springer-Verlag, New York, 1983, pp. 109-117.
10. Clifford, A. H., and Preston, G. B. The Algebraic Theory of Semigroups, vol. 1. American Mathematical Society, Providence, R.I., 1961.
11. Cole, R., And Siegel, A. On information flow and sorting: New upper and lower bounds for VLSI circuits. In Proceedings of the 26th Annual Symposium on the Foundations of Computer Science (Portland, Ore., Oct.). IEEE, New York, 1985, pp. 208-221.
12. Cole, R., and Siegel., A. Optimal VLSI circuits for sorting. J. ACM 35, 4 (Oct. 1988), 777-810.
13. Dekel, E., and Sahni, S. Binary trees and parallel scheduling algorithms. IEEE Trans. Comput. C-32, 3 (Mar. 1982), 307-315.
14. Dymond, P. W., And Cook, S. A. Hardware complexity and parallel computation. In Proceedings of the 21st Annual Symposium on the Foundations of Computer Science (Syracuse, N.Y., Oct.). IEEE, New York, 1980, pp. 360-372.
15. Fitch, F. F. New bounds for parallel prefix circuits. In Proceedings of the 15 th Annual ACM Symposium on Theory of Computing (Boston, Mass., Apr. 25-27). ACM, New York, 1983, pp. 100-109.
16. Johnson. R. B. The complexity of a VLSI adder. Infor. Process. Lett. 11, 2 (Oct. 1980), 92-93.
17. Kruskal, C. P., Rudolph, L., and Snir, M. The power of parallel prefix. IEEE Trans. Comput. C-34, 10 (Oct. 1985), 965-968.
18. Kuck, D. J. The Structure of Computers and Computations. Wiley, New York, 1978.
19. Ladner, R. E., and Fischer, M. J. Parallel prefix computation. J. ACM 27, 4 (Oct. 1980), 831-838.
20. McNaughton, R., and Papert, S. Counter-Free Altomata. M.I.T. Press, Cambridge, Mass., 1971.
21. Ofman, Yu. On the algorithmic complexity of discrete functions. Sov. Phys. Dokl. 7, 7 (Jan. 1963), 589-591.
22. Snir, M. Depth-size trade-offs for parallel prefix computations. J. Algorithms 7, 2 (June 1986), 185-201.
23. Thompson, C. D. A complexity theory for VLSI. Ph.D. dissertation, Dept. Comput. Sci., CarnegieMellon Univ., Aug. 1980.
24. Yao, A. C. The entropic limitations on VLSI computations. In Proceedings of the $13 t h$ Annual ACM Symposium on Theory of Computing (Milwaukee, Wis., May 11-13). ACM, New York, 1981, pp. 308-311.
received january 1987; Revised october 1987 and September 1988; aCCEPTED SEPTEMBER 1988

[^0]:    A preliminary version of this work appears in Proceedings of the 19th Annual ACM Symposium on Theory of Computing (New York, N.Y., May 25-27). ACM, New York, 1987, pp. 436-442.
    The research of G. Bilardi was supported in part by National Science Foundation grant DCI 86-02256. The research of F. P. Preparata was supported in part by National Science Foundation grant ECS 8410902.

    Authors' present addresses: G. Bilardi, Department of Computer Science, Cornell University, Ithaca, NY 14853. F. P. Preparata, Coordinated Science Laboratory, Departments of Electrical and Computer Engineering and of Computer Science, University of Illinois at Urbana-Champaign, Urbana-Champaign, IL 61801.
    Permission to copy without fee all or part of this material is granted provided that the copies are not made or distributed for direct commercial advantage, the ACM copyright notice and the title of the publication and its date appear, and notice is given that copying is by permission of the Association for Computing Machinery. To copy otherwise, or to republish, requires a fee and/or specific permission.

