# Small-Signal Analysis of DC–DC Converters with Sliding Mode Control

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Abstract—This paper deals with small-signal analysis of dc-dc converters with sliding mode control. A suitable small-signal model is developed which allows selection of control coefficients, analysis of parameter variation effects, characterization of the closed-loop behavior in terms of audiosusceptibility, output and input impedances, and reference to output transfer function. Unlike previous analyses, the model includes effects of the filters used to evaluate state variable errors. Simulated and experimental results demonstrate model potentialities.

Index Terms—Control techniques, sliding mode control, small-signal analysis

#### I. INTRODUCTION

CONTROL technique suitable for dc-dc converters must cope with their intrinsic nonlinearity and wide input voltage and load variations, ensuring stability in any operating condition while providing fast transient response. Among the various control techniques proposed in literature, sliding mode control [1], [2] offers several advantages, namely, large-signal stability, robustness, good dynamic response, and simple implementation [3], [4].

In spite of these positive aspects, sliding mode control is not yet popular, probably because its theoretical complexity which can make selection of controller parameters difficult. In fact, these parameters must be chosen so as to satisfy existence, hitting, and stability conditions. The analysis is easily carried out only for second-order converters which permit a phase-plane description of the system.

Another limitation is that sliding mode control requires, in theory, sensing of all state variables and generation of suitable references for each of them.

In practice, generation of reference signals for all state variables is not needed in dc–dc converters. In fact, since only error signals are required for the control, they can be achieved by high-pass filtering the state variable signals in the assumption that their dc component is automatically adjusted by the converter according to the input–output power balance condition. Of course, these high-pass filters, not considered in previous analyses, increase the system order and can heavily affect the converter dynamic.

Moreover, converter control can be done effectively by sensing only one inductor current in addition to the output

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voltage [4], even for high-order converters (e.g., Cuk and Sepic), resulting in a control complexity similar to that of standard current-mode controllers. However, a comparison between the two solutions (full-order control and reduced-order control) is not easy to obtain.

In summary, there is a lack of models able to describe the effects of circuit and controller parameter variations and to allow a comparison between the sliding mode approach and other popular control techniques like current-mode control, pulse width modulation (PWM) control, etc., in terms of converter transfer functions (audiosusceptibility, output and input impedances, and reference to output transfer ratio).

This paper presents a small-signal model of sliding mode-controlled dc-dc converters operating in a continuous conduction mode which also includes the effects of the filters used to determine state variable references. Simulated results of a Sepic converter are reported which show the model potentialities. Experimental results of a Boost converter are also given.

# II. BASIC SLIDING MODE CONTROLLER

Fig. 1 shows the general sliding mode-control scheme of dc-dc converters. Although not used in practice, this scheme emphasizes the properties and operation mechanism of this control.  $U_g$  and  $u_{CN}$  are input and output voltages, respectively, while  $i_{Li}$  and  $u_{Cj}$   $(i=1 \div r, j=r+1 \div N-1)$  are the internal state variables of the converter (r) inductor currents and N-r-1 capacitor voltages), and N is the system order.

For the sake of generality, the state variables will be indicated as components of state vector  $\tilde{x}$ . According to the general sliding mode-control theory, all state variables are sensed, and the corresponding errors (defined by difference to the reference values  $\tilde{x}^*$ ) are multiplied by proper gains  $K = [k_1, k_2, \cdots k_N]^T$  and added together to form sliding function  $\psi$ . Then, hysteretic block HC keeps this function near zero by gating the power switch S on and off. We can therefore assume

$$\psi = K^T(\tilde{x} - \tilde{x}^*) \cong 0. \tag{1}$$

This means that the control forces the system to evolve on the hyperplane (sliding surface) defined by (1).

As discussed in [4], selection of coefficients K must be done in order to satisfy some basic requirements: 1) the state trajectories starting from points near the hyperplane must be directed toward the sliding surface (1) for both possible states of the converter switch (*existence condition*); 2) the system

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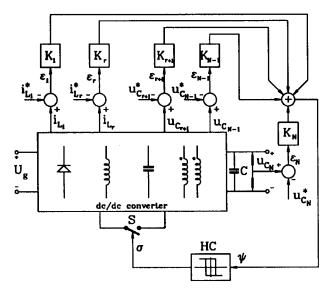


Fig. 1. Basic scheme of a sliding mode controller for dc-dc converters.

trajectories must encounter the sliding surface irrespective of their starting point in the phase space (*hitting condition*); and 3) the system motion on the sliding surface must reach a stable point corresponding to the desired voltages and currents (*stability condition*).

Although the feasibility of a sliding mode controller of reduced order (where only one inductor current is sensed in addition to the output voltage) has been already demonstrated [4], in the following we will consider the case of Fig. 1 for the sake of generality. Results for reduced-order controllers are easily obtainable by setting some sliding coefficients,  $k_i$ , to zero.

## III. SMALL-SIGNAL MODEL DERIVATION

A generic dc-dc converter, working in a continuous conduction mode, can be characterized by the following two sets of equations:

$$\dot{\tilde{x}} = A_1 \tilde{x} + B_1 \tilde{u} \quad \text{switch on} \tag{2}$$

$$\dot{\tilde{x}} = A_0 \tilde{x} + B_0 \tilde{u} \quad \text{switch off} \tag{3}$$

where  $\tilde{x}$  is the vector of state variables, and  $\tilde{u}$  is the vector of the input variables (input voltage  $\tilde{u}_g$  and possibly load current  $\tilde{i}_o$ ). According to the *state space averaging method* [5], the average system behavior is described by the equation

$$\dot{x} = Ax + Bu \tag{4a}$$

where

$$A = A_1 \delta + A_0 (1 - \delta)$$
  

$$B = B_1 \delta + B_0 (1 - \delta)$$
(4b)

where  $\delta$  is the converter duty cycle in the steady state, and x and y are the averaged state variables and input variables, respectively. After linearization (small-signal assumption), we can derive the following small-signal equation in which the hat

means perturbation from a steady-state working point (X, U)

$$\dot{\hat{x}} = A\hat{x} + B\hat{u} + C\hat{\delta},\tag{5}$$

Matrices A and B are the same as (4b), and matrix C is given by

$$C = (A_1 - A_0)X + (B_1 - B_0)U.$$
 (6)

In general, only some of the reference values  $\tilde{x}^*$  indicated in (1) are fed from the external control (often, only as output voltage reference is assigned). The other references are derived internally to the controller, normally by low-pass filtering the corresponding state variables. For these latter references we can define M additional state variables  $x^*_{\text{int}}$  whose dynamic is described by

$$\dot{x}_{i_{\text{int}}}^* = -\frac{1}{\tau_i} x_{i_{\text{int}}}^* + \frac{1}{\tau_i} x_i \quad i = 1 \div M \tag{7}$$

where  $\tau_i$  are the corresponding filter time constants. Thus, the system order increases by M. The remaining N-M state variable references are additional inputs. Note that linear (7) holds also for perturbed variables.

We call  $\hat{x}'$  the vector of all state variables, including the additional state variables of the filters

$$\hat{x}' = \begin{bmatrix} \hat{x} \\ \hat{x}_{\text{int}}^* \end{bmatrix}. \tag{8}$$

Now we can exploit condition (1) which holds whenever the converter operates in the sliding mode. Since the hysteretic block HC forces the instantaneous state variables close to their references (the closeness depends on the hysteresis amplitude and thus on the switching frequency), constraint (1) can be used also for averaged variables. Thus, taking the derivative of (1), i.e.,  $\dot{\psi} = 0$ , and using (5) and (7), we can express the converter duty cycle as a function of complete state vector  $\hat{x}'$ , derivative of external references  $\dot{\hat{x}}^*_{\rm ext}$ , and input variables

$$\hat{\delta} = D_1 \hat{x}' + D_2 \hat{u} + D_3 \dot{\hat{x}}_{\text{ext}}^* \tag{9}$$

where matrices  $D_i$   $(i=1 \div 3)$  are given in Appendix I. Substituting (9) in (5) and using (7) we obtain the linear system equation

$$\dot{\hat{x}}' = A_c \hat{x}' + B_c \hat{u} + D_c \dot{\hat{x}}_{\text{ext}}^*. \tag{10}$$

Taking now into account constraint (1), which holds even for perturbed variables, the system order is reduced by one. The final small-signal model has order N+M-1 and is given by

$$\dot{\hat{x}}'' = A_T \hat{x}'' + B_T \hat{u} + D_{T1} \hat{x}_{\text{ext}}^* + D_T \dot{\hat{x}}_{\text{ext}}^*. \tag{11}$$

Expressions for matrices  $A_c$ ,  $B_c$ ,  $D_c$ ,  $A_T$ ,  $B_T$ ,  $D_{T1}$ , and  $D_{T2}$  are given in Appendix I.

From (11) we are now able to compute all closed-loop transfer functions. In particular, the input–output voltage transfer function (audiosusceptibility), external reference to output voltage, and input admittance are directly derived from (11).

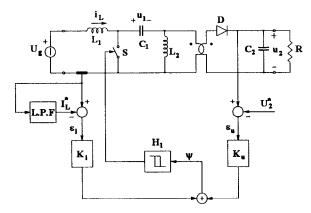


Fig. 2. Sepic converter with sliding mode control.

Instead, derivation of converter output impedance requires definition of the load current as an external input in (5).

As shown above, the model derivation involves only knowledge of the small-signal model derived from the *state space averaging method* and condition (1) which holds whenever the system moves on the sliding surface. Thus, a similar procedure can be applied for the derivation of the model for converters operating in discontinuous conduction mode (DCM) by simply using the corresponding small-signal model for the power stage [5] and thus changing (4b). However, the validity of the model is guaranteed only if a proper sliding surface is chosen so as to ensure a stable sliding mode motion for converters operating in DCM.

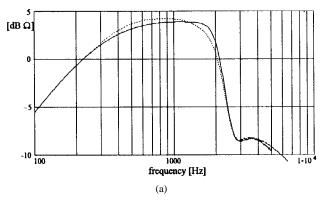
It is noticeable that the above model derivation involves the same approximations of state space-averaged modeling [5]. Thus, its validity is limited to those frequencies for which switching frequency effects can be neglected. In this view, the hysteresis block HC is modeled as constraint (1) on the average values without considering the effects due to the switching frequency.

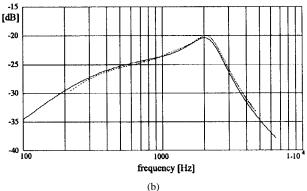
#### IV. MODEL VERIFICATION

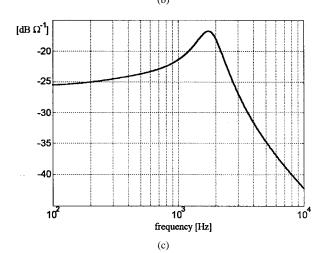
In order to test the validity of the model, a Sepic converter (whose parameters are given in Appendix IV) with a sliding mode control was analyzed. According to [6], only two state variables are sensed: one is the output voltage, and the other is the input inductor current as shown in Fig. 2.

Output impedance, audiosusceptibility, input admittance, and reference to output transfer function were computed from the above small-signal model. The corresponding results are shown in Fig. 3. For the output impedance [Fig. 3(a)] and audiosusceptibility [Fig. 3(b)], the results of the model simulation (continuous line) are reported together with the results of the converter simulation (dotted line) for the purpose of comparison. As we can see, the maximum error is about 1 dB in the whole range of operating frequencies. Fig. 3(c) shows the converter input admittance, and Fig. 3(d) shows the reference to output transfer function obtained from our model.

As already explained, the proposed small-signal model can be used both for full and reduced-order control, thus showing how much the latter influences the system performances.







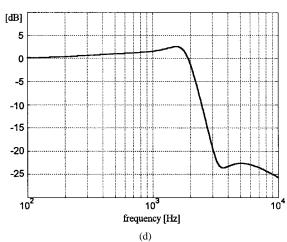


Fig. 3. (a) Output impedance, (b) audiosusceptibility (continuous line—model simulation and dotted line—circuit simulation), (c) input admittance, and (d) reference to output transfer function.

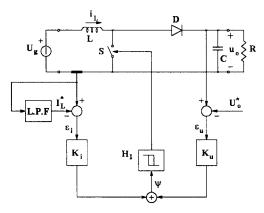


Fig. 4. Boost converter with sliding mode control.

Although a full-order control generally results in better performances, this is not true in general. For example, the audiosusceptibility of the converter in Fig. 2 is considerably better in terms of peak value and low-frequency behavior.

# V. EFFECTS OF CONTROL PARAMETER VARIATIONS

An important advantage of the derived small-signal model is the possibility to analyze the dynamic of the controlled system as a function of all control parameters, i.e., filter time constants and sliding coefficients  $k_i$ . This makes the design process straightforward unlike previous analyses which disregard filter time constants and do not give information on the dependence of the system dynamic on sliding coefficients.

Let us consider, for example, the Boost converter with sliding mode control shown in Fig. 4. The order of the overall system is two because we have two state variables (N=2) and one low-pass filter (M=1). Following the procedure given in Appendix I, we compute matrix  $A_T$  which is given by

$$A_{T} = \begin{bmatrix} -\frac{2}{RC} + g\frac{\delta'}{C} \left(1 - \frac{L_{\text{eq}}}{R\tau}\right) & \frac{\delta'}{C} \\ 1 - g\frac{L_{\text{eq}}}{RC}\delta' & 1 - g\frac{L_{\text{eq}}}{RC}\delta' \\ -\frac{g}{\tau} & 0 \end{bmatrix}$$
(12)

where  $K^T = [k_i \ k_u], \ g = k_u/k_i, \ \delta' = 1 - \delta, \ \tau$  is the filter time constant, and  $L_{\rm eq} = L/\delta'^2$ . Note that coefficients  $k_i$  and  $k_u$  were chosen in order to make the value of  $\delta$  adimensional so that g becomes a conductance. The characteristic polynomial of matrix  $A_T$  is given by the determinant of matrix  $(sI - A_T)$ 

$$\Delta = s^{2} + s \cdot \left[ \frac{\frac{2}{RC} + g \cdot \frac{\delta'}{C} \left( 1 - \frac{L_{\text{eq}}}{R\tau} \right)}{1 - g \frac{\delta'}{C} \frac{L_{\text{eq}}}{R}} \right] + \frac{g\delta'}{\tau C} \frac{1}{1 - g \frac{\delta'}{C} \frac{L_{\text{eq}}}{R}}.$$
 (13)

From the analysis of the coefficients of polynomial  $\Delta$  (see Appendix II), we recognize that if we choose

$$0 < g < g_{\text{crit}} = \frac{RC\delta'}{L} \tag{14}$$

TABLE I Values of  $\tau_{\rm crit}$  and  $g_{\rm crit}$  for Basic Converter Topologies

|                   | Buck | Boost                                 | Buck-boost  |
|-------------------|------|---------------------------------------|---|
| gcrit             | ∞    | $\frac{RC\delta'}{L}$                 | $\frac{RC\delta'}{L\delta}$   |
| <sup>7</sup> crit | 0    | $\frac{L_{eq}}{R+2\frac{L_{eq}}{RC}}$ | $\frac{L_{eq}}{\frac{R}{\delta} + (2 - \delta') \frac{L_{eq}}{RC}}$ |

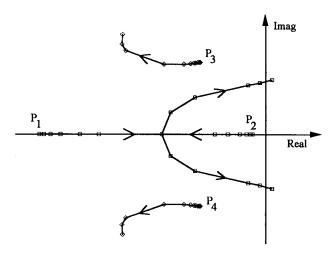


Fig. 5. Root locus of closed-loop system for variation of low-pass filter time constant of the Sepic converter in Fig. 2.

the system stability is ensured provided that

$$\tau > \tau_{\text{crit}} = \frac{L_{\text{eq}}}{R + 2\frac{L_{\text{eq}}}{RC}}.$$
 (15)

It can be demonstrated that condition (14), derived under small-signal time-averaging approximation, is the same as the existence condition of the sliding mode for a Boost converter in the steady state. This is easily derived from a converter trajectory analysis (see Appendix III). The same result can be found for other basic converter topologies for which values of  $\tau_{\rm crit}$  and  $g_{\rm crit}$  are reported in Table I.

For higher order systems, for which it is not easy to derive simple conditions as (14) and (15), (11) can be used directly to observe the effects of the controller parameters on the system dynamic. For example, Fig. 5 shows the root locus resulting from different values of the filter time constant (0.06 ms  $<\tau<2$  ms) of the Sepic converter in Fig. 2. Too low values cause real poles  $P_1$  and  $P_2$  to become complex or even to cross the imaginary axis.

## VI. EXPERIMENTAL RESULTS

A Boost converter prototype, according to the scheme of Fig. 4, was used to test the validity of the above theoretical results. Converter parameters are listed in Table II.

Converter audiosusceptibility and output impedance were measured and compared with those predicted by the model. For

TABLE II EXPERIMENTAL PROTOTYPE PARAMETERS

| $U_g = 24 \text{ V}$ | $U_0 = 48 \text{ V}$ | $P_0 = 50 \text{ W}$    | $f_S = 50 \text{ kHz}$ |
|----------------------|----------------------|-------------------------|------------------------|
| $L = 570  \mu H$     | $C = 22 \mu F$       | $\tau = 0.4 \text{ ms}$ | g = 0.35               |

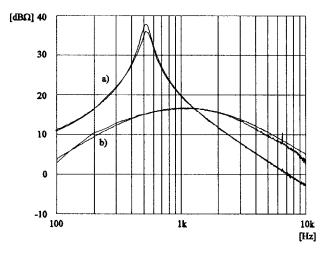


Fig. 6. Comparison between model forecast and experimental results: output impedance a) open loop and b) closed loop.

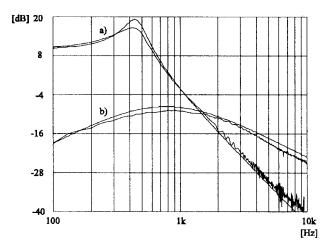


Fig. 7. Comparison between model forecast and experimental results: audiosusceptibility a) open loop and b) closed loop.

this purpose, the parameters used in the model were measured directly on the prototype, and the inductor series resistance was taken into account.

In Fig. 6, the open- and closed-loop output impedance is reported. As we can see, theoretical and experimental results agree pretty well. The sliding mode control reduces the peak in the output impedance diagram by almost 20 dB. Fig. 7 shows the audiosusceptibility behavior in the same conditions. Once again, simulated and experimental curves look very similar, while the improvement in the audiosusceptibility peak is about 30 dB.

Note that control causes a worsening of the dynamic characteristics at high frequencies as compared to the open-loop case. This behavior agrees with the multiloop nature of sliding mode control as explained in [7].

#### VII. CONCLUSION

A small-signal model of dc-dc converters with sliding mode control is derived. It allows evaluation of closed-loop performances like audiosusceptibility, output and input impedances and reference to output transfer function.

With this model, stability as well as effects of controller parameter variations can be deeply investigated, and control parameters can be carefully selected. Simulated results of a Sepic converter and experimental results of a Boost converter are reported, showing the validity of the approach.

#### APPENDIX I

In order to derive the expressions of matrices in (9)–(11), it is convenient to split the vector of state variable references between internal and external variables as follows:

$$x^* = \begin{bmatrix} x_{\text{int}}^* \\ x_{\text{ext}}^* \end{bmatrix}. \tag{A1.1}$$

From (1), it follows that the derivative of sliding function  $\psi$ is also zero. Thus, considering perturbed signals, we can write

$$\dot{\hat{\psi}} = K^T (\dot{\hat{x}} - \dot{\hat{x}}^*) = K^T \dot{\hat{x}} - K_{\text{int}}^T \dot{\hat{x}}_{\text{int}}^* - K_{\text{ext}}^T \dot{\hat{x}}_{\text{ext}}^* = 0$$
 (A1.2)

in which the vector of sliding coefficients K is divided in two terms  $K_{\text{int}}$  and  $K_{\text{ext}}$ .

The dynamic description of the internal references is given by (7) which can be rewritten as

$$\dot{\hat{x}}_{\text{int}}^* = T_1 \hat{x}_{\text{int}}^* - T_2 \hat{x} \tag{A1.3}$$

where  $T_1$  is a diagonal  $M \times M$  matrix, while  $T_2$  is a  $M \times N$ matrix given by

$$T_2 = [T_1 \quad 0]$$
 (A1.4)

which holds in the hypothesis that the state variables in vector x are ordered as in vector  $x^*$ .

Substituting (A1.3) and (5) into (A1.2) and calculating the duty cycle, we obtain (9) whose matrices are given by

$$D_1 = [D_{11} \quad D_{22}] \tag{A1.5a}$$

$$D_2 = -(K^T C)^{-1} \cdot K^T B$$
 (A1.5b)

$$D_3 = (K^T C)^{-1} \cdot K_{\text{ext}}^T$$
 (A.15c)

where

$$D_{11} = -(K^T C)^{-1} \cdot (K^T A + K_{\text{int}}^T T_2)$$
  

$$D_{22} = (K^T C)^{-1} \cdot K_{\text{int}}^T T_1.$$
 (A1.6)

Note that term  $K^TC$  is scalar.

The complete description of the system is given by (10) which is obtained by substituting (9) in (5) and using (A1.3) and (8). Matrices  $A_c$ ,  $B_c$ , and  $D_c$  result

$$A_{c} = \begin{bmatrix} A + CD_{11} & CD_{22} \\ -T_{2} & T_{1} \end{bmatrix}$$
 (A1.7a)

$$A_{c} = \begin{bmatrix} A + CD_{11} & CD_{22} \\ -T_{2} & T_{1} \end{bmatrix}$$
 (A1.7a)  

$$B_{c} = \begin{bmatrix} B + CD_{2} \\ 0 \end{bmatrix}$$
 (A1.7b)

$$D_c = \begin{bmatrix} CD_3 \\ 0 \end{bmatrix}. \tag{A1.7c}$$

TABLE III

| $U_g = 15 \text{ V}$ | $L_1 = 700 \mu\text{H}$ | $L_2 = 360 \mu\text{H}$ | $C_1 = 6.8 \mu\text{F}$ | $C_2 = 250 \mu\text{F}$ |
|----------------------|-------------------------|-------------------------|-------------------------|-------------------------|
| n = 1.5              | $U_2 = 20 \text{ V}$    | $R = 20 - 200 \Omega$   | g = 1                   | $\tau = 0.6 \text{ ms}$ |

Lastly, in order to derive the matrices of the reduced order system (11), let us consider nonzero the first element  $k_1$  of vector K. Thus, from (1) and (A1.1) we can write

$$K^{T}(\hat{x} - \hat{x}^{*}) = [K^{T} - K_{\text{int}}^{T}] \cdot \hat{x}' - K_{\text{ext}}^{T} \cdot \hat{x}_{\text{ext}}^{*} = 0.$$
 (A1.8)

Solving for the first state variable and substituting in (10), we obtain (11) in which we have

$$\hat{x}'' = \hat{x}'(2:N+M)$$

$$A_T = A_c(2:N+M,2:N+M) + A_c(2:N+M,1) \cdot F_1$$

$$B_T = B_c(2:N+M,:)$$

$$D_{T1} = A_c(2:N+M,1) \cdot F_2$$

$$D_{T2} = D_c(2:N+M,:)$$
(A1.9)

where

$$F_{1} = -\frac{1}{k_{1}} \cdot [K^{T}(2:N) - K_{\text{int}}^{T}]$$

$$F_{2} = \frac{1}{k_{1}} \cdot K_{\text{ext}}^{T}$$
(A1.10)

and the notation A(n:m,j:k) means the submatrix obtained from matrix A taking rows from index n-m and columns from index j-k, while the symbol : alone means all.

For example, for a Boost converter the previous matrices are given by the following expressions:

$$A = \begin{bmatrix} 0 & -\frac{\delta'}{L} \\ \frac{\delta'}{C} & -\frac{1}{RC} \end{bmatrix} \quad B = \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} \quad C = \begin{bmatrix} \frac{U_g}{\delta'L} \\ -\frac{U_g}{\delta'^2RC} \end{bmatrix}$$

$$T_1 = \begin{bmatrix} -\frac{1}{\tau} \end{bmatrix} \qquad T_2 = \begin{bmatrix} -\frac{1}{\tau} & 0 \end{bmatrix}$$

$$A_c = \begin{bmatrix} \frac{\eta}{\tau} \left( 1 - \frac{\delta'\tau g}{C} \right) & \frac{2\eta g}{RC} & -\frac{\eta}{\tau} \\ \frac{\eta\delta'}{C} \left( 1 - \frac{L_{\text{eq}}}{\tau R} \right) & -\frac{2\eta}{RC} & \frac{\eta\delta'L_{\text{eq}}}{\tau RC} \\ \frac{1}{\tau} & 0 & -\frac{1}{\tau} \end{bmatrix}$$

$$B_c = \frac{\eta}{\delta'RC} \begin{bmatrix} -g \\ 1 \\ 0 \end{bmatrix} \quad D_c = \eta g \begin{bmatrix} \delta' \frac{L_{\text{eq}}}{RC} \\ 0 \end{bmatrix}$$

$$B_T = \frac{\eta}{\delta'RC} \begin{bmatrix} 1 \\ 0 \end{bmatrix} \quad D_{T1} = \begin{bmatrix} \frac{\eta\delta'g}{C} \left( 1 - \frac{L_{\text{eq}}}{\tau R} \right) \\ \frac{g}{\tau} \end{bmatrix}$$

$$D_{T2} = g\eta \begin{bmatrix} \delta' \frac{L_{\text{eq}}}{RC} \\ 0 \end{bmatrix}$$

where

$$\eta = \frac{1}{1 - g \frac{L_{\text{eq}}}{RC} \delta'}$$

and  $A_T$  is given by (12).

#### APPENDIX II

In order to derive conditions (14) and (15), which ensure system stability for a Boost converter with sliding mode control, we analyze the sign of coefficients of the characteristic polynomial  $\Delta$  (13). Calling  $r_1$  and  $r_2$  the roots of  $\Delta$ , we can write

$$\Delta = s^2 - s \cdot (r_1 + r_2) + r_1 \cdot r_2. \tag{A2.1}$$

The system is stable if both  $r_1$  and  $r_2$  have a negative real part, i.e., if

$$r_1 \cdot r_2 > 0 \tag{A2.2}$$

and

$$r_1 + r_2 < 0.$$
 (A2.3)

From (13), (A2.2), and (A2.3) the following inequalities result:

$$\frac{g}{\tau} \frac{\delta'}{C} \frac{1}{1 - g\frac{\delta'}{C} \frac{L_{\text{eq}}}{R}} > 0 \tag{A2.4}$$

$$\frac{\frac{2}{RC} + g\frac{\delta'}{C} \left(1 - \frac{L_{\text{eq}}}{R\tau}\right)}{1 - g\frac{\delta'}{C} \frac{L_{\text{eq}}}{R}} > 0.$$
(A2.5)

From (A2.4) it results

$$0 < g < g_{\text{crit}} = \frac{RC}{\delta' L_{\text{eq}}} = \frac{RC\delta'}{L}$$
 (A2.6)

which is condition (14). From (A2.5), taking into account (A2.6), it results

$$\tau > \frac{L_{\text{eq}}}{R} \frac{1}{1 + \frac{2}{R\delta' a}} \tag{A2.7}$$

where, substituting the maximum value  $g_{\text{crit}}$  for g given by (A2.6), the minimum value  $\tau_{\text{crit}}$  as given by (15) results.

## APPENDIX III

As already mentioned, the existence condition requires that the phase trajectories are directed toward the sliding surface in a small volume around the surface itself. This statement translates into conditions [1], [2]

$$\lim_{\psi \to 0^{+}} \frac{d\psi}{dt} < 0$$

$$\lim_{\psi \to 0^{-}} \frac{d\psi}{dt} > 0. \tag{A3.1}$$

Now, in order to satisfy existence conditions [1], [4], we choose the control strategy that closes the switch when the sliding function  $\psi$  is negative and opens the switch when  $\psi$ 

is positive. Thus, writing the expression of  $\dot{\psi} = K^T \dot{x}$  for the Boost converter in the two situations corresponding to the different values of the switch status (open for the first one and closed for the second one), the above inequalities (A.3.1) give

$$\frac{k_u}{k_i} < \frac{RC}{L} \cdot \frac{U_g}{u_C}$$
(A3.2a)
$$\frac{k_u}{k_i} < \frac{RC}{L} \cdot \frac{u_C - U_g}{Ri_L - u_C}$$
(A3.2b)

$$\frac{k_u}{k_i} < \frac{RC}{L} \cdot \frac{u_C - U_g}{Ri_L - u_C} \tag{A3.2b}$$

where  $u_C$  is the instantaneous voltage of the output filter capacitor, and  $i_L$  is the input current. Now, if we consider a small volume around the operating point in the phase plane (small-signal approximation), we can use the nominal capacitor voltage in (A3.2a) and (A3.2b) which is equal to the output voltage. With this approximation, simple calculations show that (A.3.2b) is equivalent to (A.3.2a), thus obtaining condition (14).

#### APPENDIX IV

The parameter values of the Sepic converter of Fig. 2 are given in Table III on the previous page.

### REFERENCES

- [1] V. I. Utkin, Sliding Mode and Their Application in Variable Structure System. Moscow: MIR, 1974.
- U. Itkis, Control Systems of Variable Structure. New York: Wiley,

- [3] R. Venkataramanan, A. Sabanovic, and S. Cuk, "Sliding-mode control of dc-to-dc converters," IECON, 1985.
- [4] P. Mattavelli, L. Rossetto, G. Spiazzi, and P. Tenti, "General-purpose sliding-mode controller for dc-dc converter applications," in PESC Conf. Proc., 1993, pp. 609-615.
- [5] R. D. Middlebrook and S. Cuk, Advances in Switched-Mode Power Conversion. Pasadena: Teslaco, 1981, pp. 73-89.
- [6] P. Mattavelli, L. Rossetto, G. Spiazzi, and P. Tenti, "Sliding mode control of SEPIC converters," in Proc. European Space Power Conf. (ESPC), Graz, Austria, 1993, pp. 173-178.
- [7] R. B. Redley, B. H. Cho, and F. C. Lee, "Analysis and interpretation of loop gains of multiloop-controlled switching regulators," IEEE Trans. Power Electron., vol. 3, no. 4, pp. 489-498, Oct. 1988.

Paolo Mattavelli (S'92-M'96), for a photograph and a biography, see this issue, p. 86.

Leopoldo Rossetto (M'95), for a photograph and a biography, see this issue,

Giorgio Spiazzi (S'92-M'94), for a photograph and a biography, see this issue, p. 86.