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Spatial atomic layer deposition: A route towards further industrialization of atomic layer deposition

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Atomic layer deposition (ALD) is a technique capable of producing ultrathin conformal films with atomic level control over thickness. A major drawback of ALD is its low deposition rate, making ALD less attractive for applications that require high throughput processing. An approach to overcome this drawback is spatial ALD, i.e., an ALD mode where the half-reactions are separated spatially instead of through the use of purge steps. This allows for high deposition rate and high throughput ALD without compromising the typical ALD assets. This paper gives a perspective of past and current developments in spatial ALD. The technology is discussed and the main players are identified. Furthermore, this overview highlights current as well as new applications for spatial ALD, with a focus on photovoltaics and flexible electronics. © 2012 American Vacuum Society.

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I. INTRODUCTION

Atomic layer deposition (ALD) is a thin film growth technique based on the sequential exposure of a substrate to self-limiting surface half-reactions.¹ ALD is currently being used commercially by the semiconductor industry to form metal/high-k gate oxide stacks for field effect transistors, capacitors for dynamic random access memory devices, as well as in the thin-film magnetic head industry to form gap dielectrics.² Recently, ALD has raised interest from new application areas, such as photovoltaics and organic electronics. Applications where ALD has displayed added value include

surface passivation layers in crystalline silicon solar cells,³ buffer layers in CuInGa(Se,S) (CIGS) as an alternative to chemical bath deposition (CBD) CdS,³ and moisture diffusion barrier layers for OLEDs and thin film photovoltaics.⁴ These applications; however, require high-throughput and low-cost production techniques to make them economically viable. Unfortunately, these prerequisites are considered to be the weak points of ALD. One way to increase the throughput of an ALD system is to do batch processing,⁵ but this is not always compatible with the in-line processes often used in the before mentioned applications. A significant reduction of the cost of ownership of ALD processes and equipment must be realized to make ALD economically viable for these new applications.

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Spurred by these new opportunities for ALD, several “new” approaches to ALD have been recently proposed and developed to meet this challenge. Many of these approaches share a common feature: spatial separation of the half-reactions, and can be summarized under the term “spatial ALD.” This paper overviews the development of spatial ALD, the approaches and progress to date. Furthermore an overview of potential new applications of spatial ALD will be given.

II. SPATIAL ALD

The difference between conventional temporal ALD and spatial ALD lies in the manner that the precursors are delivered to the substrate (see Fig. 1). In conventional ALD the precursors are dosed into a chamber sequentially, separated in time by a purge step [Fig. 1(a)]. In spatial ALD, the precursors are supplied continuously, but in different physical locations [Fig. 1(b)]. Thus there are (at least) two zones where, in the presence of a substrate, a half-reaction can take place. If the substrate is present in such a half-reaction zone for a sufficiently long period of time, a saturated monolayer will form from precursor A. Then, the substrate is moved to the sec-

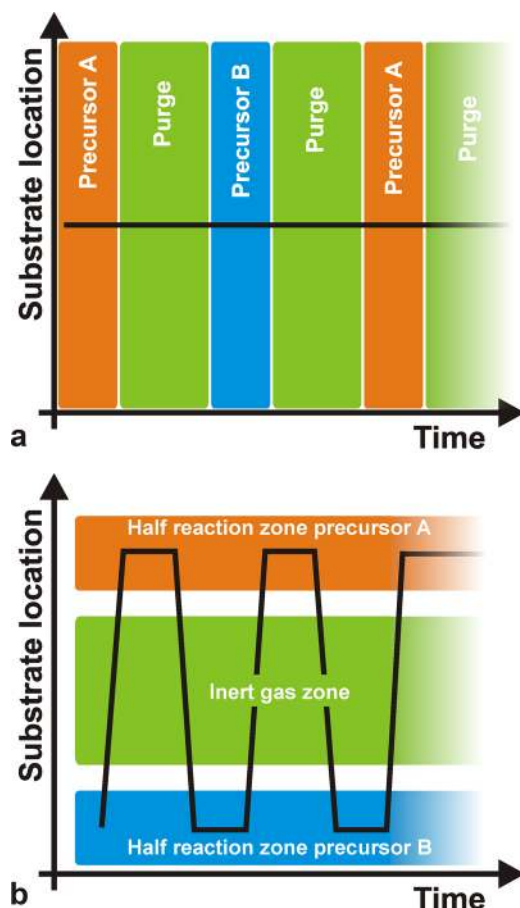


FIG. 1. (Color online) Schematic representation of the ALD process where the variation in time and position of the substrate (black line) and precursors (colored blocks) are given. (a) In conventional ALD the substrate is at a fixed position, and the precursors are dosed sequentially, separated by a purge step. (b) In spatial ALD, the precursors are dosed simultaneously and continuously, but at different half-reaction zones. The substrate moves between these zones where the half-reactions take place.

ond half-reaction zone, where the ALD cycle is completed to form one ALD monolayer. Alternatively the substrate position could be fixed and the gas supplies could be moved, or some combination of the two. To obtain thicker films, this sequence has to be repeated for the desired number of cycles.

The main advantage of spatially separating the half-reactions is that the purge steps between the precursor dosages in conventional ALD become obsolete. As a consequence, the available net deposition rate is no longer limited by the accumulated individual cycle step times, but by the time required to move between half-reactions zones. Of course the ultimate limit depends on the kinetics of the specific reaction involved, but this can be of the order of a few milliseconds when using planar substrates. For substrates with more complex topographies (e.g., trenches), required exposure times will scale dictated by transport limitations at the feature scale, similar to conventional ALD.⁶

The main essence of ALD lies in decoupling the (at least) two independent half-reactions, which requires a complete separation of the precursors with no intermixing precursors whatsoever. In conventional ALD this is achieved through the use of purge steps in between the precursor exposure steps. In spatial ALD, the half reactions are separated using a combination of physical barriers and continuously flowing purge streams to prevent the interdiffusion and mixing of the precursors.

In conventional ALD, deposition will not only take place on the substrates, but on the reactor walls as well. Furthermore, the purge steps in between the half reactions will remove any surplus precursor from the reactor, making it unavailable for incorporation into the deposited film. This results in an inherently poor precursor utilization efficiency, constraining the ability to employ costly precursors for large scale applications. In spatial ALD, the separation of the reaction zones combined with barriers to prevent mixing ensures that precursors never reside in the same reactor volume. As a consequence, the only place where deposition will take place is on the substrate and no parasitic deposition on the reactor walls will occur. A significant reduction of precursor consumption and consequently the cost of ownership can be achieved, depending on the specific design, mode of operation and desired deposition rate of the various spatial ALD concepts.

Summarizing, the three main characteristics of spatial ALD are: (1) physically separated half-reactions zone, (2) movement of the gas injector head and/or substrate, and (3) a barrier between the half-reaction zones to prevent precursor mixing.

A. Early developments

Although there have been several recent developments in spatial ALD, the original idea was already patented in 1977 by Suntola *et al.*^{7,8} (Fig. 2). They described the concept of spatially separating the half-reactions, with separate precursor inlets and exhausts. In their patents, the authors describe the use of shields of inert gas for separating the half-reaction zones, where the gas shield width should be designed wide enough to prevent any diffusion and cross-reactions between the precursors.

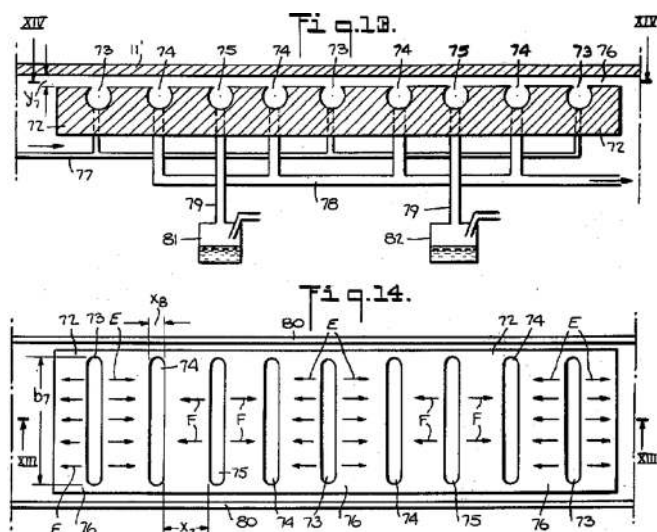


FIG. 2. Original design for the spatial ALD injector as taken from Ref. 7. Precursors A and B are dosed through slits (number 75 in drawing), distributed sideways and removed through exhausts (No. 74). The half-reaction zones are separated by gas curtains (No. 73). Reprinted from T. Suntola, J. Antson, U.S. Patent No. 4,058,430 (15 Nov. 1977).

Jusung Engineering (Korea) also developed a spatial ALD reactor,⁹ quite similar to Suntola's concept. They used a rotating injector with, at least, four arms in a sort of wind-mill construction; two for supplying each precursor separately and two arms in between supplying inert gas acting as purge steps. This concept has been commercialized as their Cyclone reactor, but publicly available information is limited.

B. Lotus Applied Technology

Lotus Applied Technology (Hillsboro, USA) has developed a spatial ALD reactor for flexible substrates, operated in a roll-to-roll mode. In this approach the flexible substrate is woven back and forth between two precursor zones separated by an inert gas zone in a serpentine configuration (Fig. 3). Differential pressure and pumping is used to prevent precursor migration into the purge zone. Process pressure is generally about 2 mbar, similar to that used in conventional pulse-based ALD. This approach was originally published in 2007, with the first experiments beginning in 2008 in a 100 mm research reactor. Initial work was done using well known water-based thermal processes (Al_2O_3 from $\text{TMA} + \text{H}_2\text{O}$, TiO_2 from $\text{TiCl}_4 + \text{H}_2\text{O}$). The first results, including results for full roll-to-roll processing were published in 2009.¹⁰ To overcome processing speed limitations related to desorption of excess physisorbed water following water precursor exposure,¹¹ plasma-based processes were employed beginning in 2009, and this has remained the focus of technology development.

C. ASTRaL

At the Advanced Surface Technology Research Laboratory (ASTRaL) of Lappeenranta University of Technology (Finland), another spatial ALD concept for depositing on flexible substrates has been developed. They have chosen to pass the flexible web around a large rotating drum adjacent

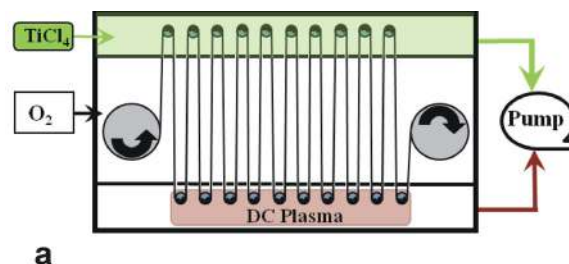


FIG. 3. (Color online) (a) Schematic drawing and (b) photograph of the Lotus Applied Technology spatial ALD reactor. The flexible substrate is woven back and forth between two precursor zones separated by an inert gas zone, in a serpentine configuration.

to the various gas inlet and purge zones. A schematic of this process is shown in Fig. 4(a).

As a vehicle to explore the issues concerning the operation of such a system, such as uniformity, gas flows, gas separation, etc., they have carried out initial experimental work using a system based on the same design ideas except that, for simplicity, a continuous web is not used. Instead, a flexible substrate is fixed to the surface of a rotating drum and sequentially passes through the precursor and purge gas zones which are separated by exhaust zones. One rotation of the cylinder is equivalent to one ALD cycle. This system is the Beneq TFS200R ALD tool and a photograph of the reaction chamber is shown in Fig. 4(b). A more detailed description is given in Ref. 12. Experimental depositions with this system have shown that the process behaves as a conventional ALD process in that the deposition rate per cycle saturates as the precursor flow rate is increased, reaching approximately $1 \text{ \AA}/\text{cycle}$ for the TMA/water process for Al_2O_3 at a temperature of $100 \text{ }^\circ\text{C}$.

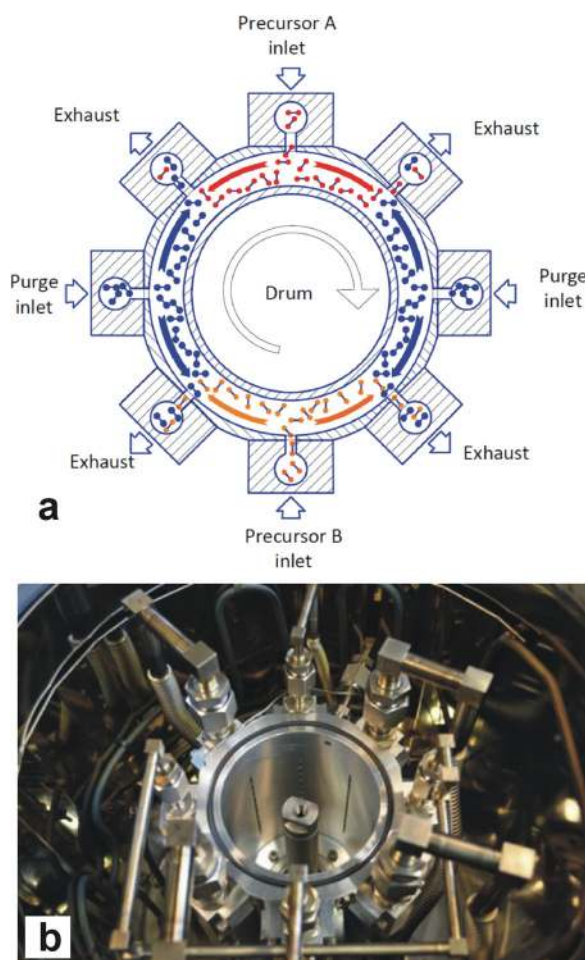


FIG. 4. (Color online) (a) Schematic drawing and (b) photograph of the Astral spatial ALD reactor. The flexible substrate is mounted on a central drum and rotated past several precursor injectors, separated by inert gas and exhausts. Reprinted with permission from Chem. Eng. J. **171**, 345 (2011).

D. Cambridge NanoTech

Cambridge NanoTech (Cambridge, USA) is developing their own spatial ALD concept, referred to as zone separated ALD. This concept is based on a manifold/cell design [see Figs. 5(a) and 5(b)] involving the operation of the cell under choked flow conditions. Under these conditions gases are supplied via the manifold to the input side of the cell under choked flow operation at atmospheric process pressure. Choked flow operation is beneficial in that variations in the process pressure will have no impact on the flow through the cell.

Aside from the design of the cell, this zone separated has involved integrating the cell into a prototype architecture to facilitate characterizing the performance of the unit [Fig. 5(c)]. The cell and manifold are mounted on top of the pressure vessel and are covered in insulation material. The precursor and exhaust lines are similarly insulated. The yellow cabinet, shown on the left in Fig. 5(c), houses the precursors, flow controllers, pressure gauges and a variety of manual and pneumatic valves that enable system operation.

III. CLOSE PROXIMITY SPATIAL ALD

In the aforementioned applications, the separation between the precursor half-reaction zones is ensured by a

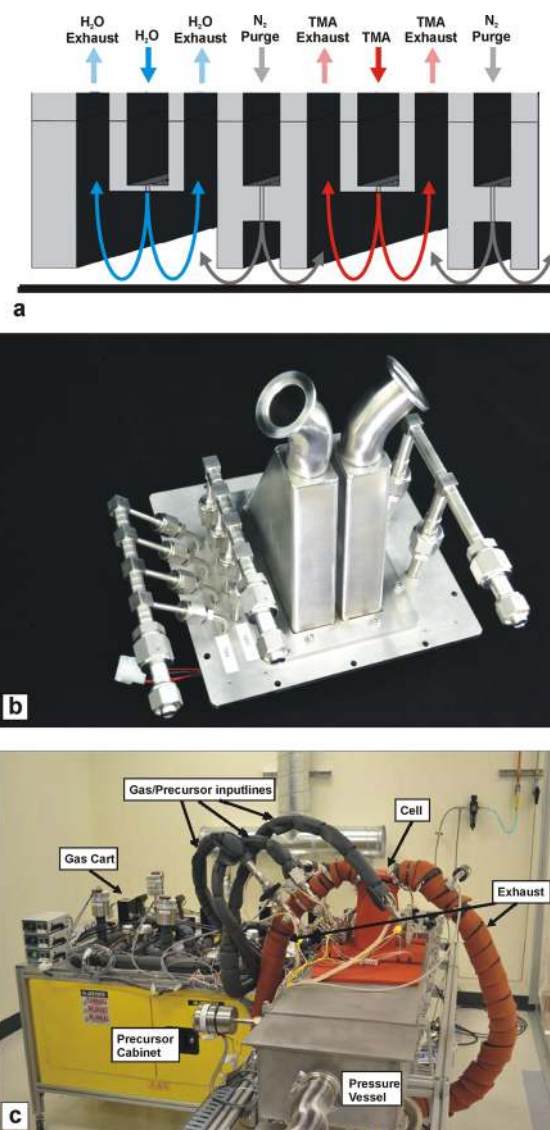


FIG. 5. (Color online) (a) Schematic drawing and (b) photograph of Cambridge Nanotech's zone separated ALD manifold and cell. (c) Photograph of a prototype 150 mm zone separated ALD cell integrated into a system.

relatively large physical distance between the half-reaction zones, in combination with zones of an inert gas as a diffusion barrier. An alternative approach to ensure precursor separation is by using a very close proximity of the substrate to the precursor injector ($<100\ \mu\text{m}$). In such a concept, illustrated schematically in Fig. 6, the reactor has separate zones exposing the precursors one by one to a substrate that moves underneath the reactor. Between and around the reaction zones, there are shields of inert gas again to separate the precursor flows. When operated properly, these gas shields can act as gas bearings, facilitating virtually frictionless movement between reactor and substrate. When a gas-bearing is used, the distance between the reactor and substrate can be minimized and made very accurately and controllable of the order of several tens of micrometers. These small gap heights combined with the resulting high flow rates in the gaps form excellent diffusion barriers with relatively narrow widths. An additional benefit of using gas-bearings is that they completely seal off the reaction zones, making the

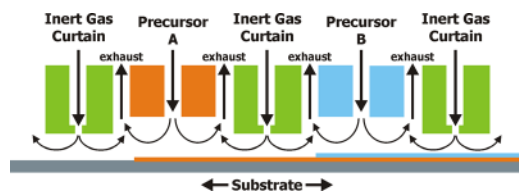


FIG. 6. (Color online) Principle of a close proximity spatial ALD reactor concept, where the precursor half-reaction zones are separated by inert gas curtains. By moving the substrate horizontally underneath the injector, the two half-reactions will take place sequentially to form an ALD monolayer. The close proximity between the reactor and the substrate combined with the inert gas flows gives an excellent separation between the precursors.

reactor completely independent of the environment, enabling operation under atmospheric pressure conditions.

In and around 2008 several groups have independently filed patents on gas bearing based spatial ALD concepts. These groups are Eastman Kodak (Rochester, USA),¹³ ASM International (Almere, The Netherlands),¹⁴ and TNO (Eindhoven, The Netherlands).¹⁵ However, Levy and co-workers from Eastman Kodak were the first to present their results.¹⁶

A. Eastman Kodak

Eastman Kodak first started with a transverse flow spatial ALD head without gas bearings. However, precursor gas separation was not optimal.¹⁷ In an improved design, the substrate is allowed to approach the head until the pressure field resulting from the gas flow hydrodynamically supports the substrate (Fig. 7). In this case, the system operates like a gas bearing and the substrate is maintained in close proximity to the coating head without the need for extremely high tolerance mechanical fixtures. The main application this group has been working on is thin film transistors where Al_2O_3 and ZnO are deposited with their setup.¹⁸

B. University of Colorado

The University of Colorado at Boulder (USA) has made a spatial ALD reactor similar to the Kodak design (Fig. 8). The gas source head is composed of a series of rectangular channels that spatially separate the process sequence. The substrate sits below the gas source head and is mounted to a frictionless translation table. A linear translation stepper motor was programmed to translate the substrate back-and-forth underneath the gas source head. The spacing between the gas source head and substrate is fixed and can be controlled with micron precision using micrometers. This apparatus can test how continuous atmospheric ALD depends on the gap spacing, substrate translation speed, gas flow rates, and the pressure difference between reactant and purge channels. Tests have been performed using helium as a probe gas to demonstrate that operating conditions exist where the two reactant streams do not mix.¹⁹ Reactant isolation could be maintained with a gap spacing as large as $100\ \mu\text{m}$. Al_2O_3 ALD films were also grown by translating the substrate relative to the gas source head.¹⁹ These model experiments revealed that fixed gap spacings using a mechanical spacer are very difficult to maintain during substrate translation.

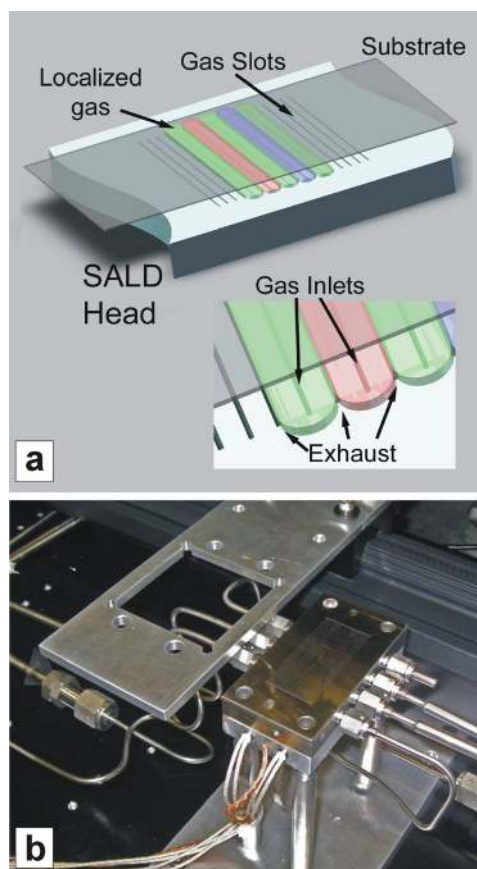


FIG. 7. (Color online) (a) Perspective view of the spatial ALD coating head from Eastman Kodak, showing the gas inlet and exhaust slots as well as a substrate floating on the head. The inset shows the desired gas isolation regions for two inert channels adjacent to a central reactive gas channel. (b) Photograph of the actual reactor.

These experiments have led to the conclusion that defining the gap spacing using a gas bearing may be required for reliable ALD during sample translation.

C. TNO

For the development of their spatial ALD concept TNO combined their gas bearing technology with atmospheric pressure gas phase deposition technology. They started development on their rotary proof-of-principle reactor in 2009 and presented their results soon after.^{20,21} The half-reaction

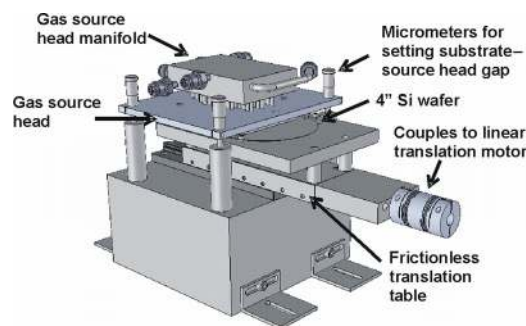


FIG. 8. (Color online) Schematic of the University of Colorado's test apparatus showing the gas source head manifold, gas source head, micrometers for setting the gap between the substrate and the gas source head, the coupling to the linear translation motor and the frictionless translation table.

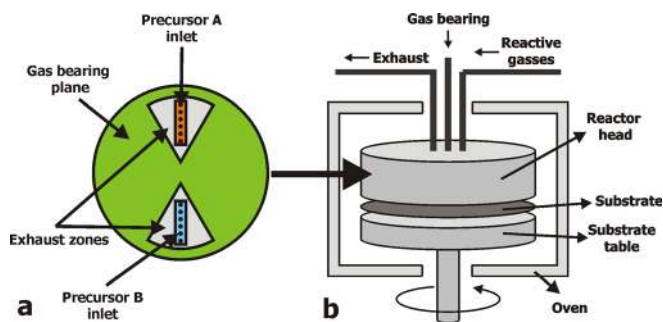


FIG. 9. (Color online) (a) Schematic of the bottom side of TNO's rotating spatial ALD reactor head, where the half-reaction zones are integrated into inlets surrounded by exhaust zones and gas bearing planes. (b) Schematic drawing and (c) photograph of the reactor. The reactor head and rotating substrate table with the substrate in between are placed in an oven. The substrate table is rotated by a servo-motor, connected by a drive shaft. The process- and waste gas lines are connected to the reactor head through an opening in the top of the oven.

zones are incorporated in a round reactor head surrounded by exhaust zones [Fig. 9(a)]. These zones are surrounded by gas bearing planes. The reactor head is mounted on top of a rotating substrate table [Fig. 9(b)]. The entire construction is mounted in a convection oven. Round substrates with diameters of 150 or 200 mm and (semi-)square $15.6 \times 15.6 \text{ cm}^2$ wafers can be mounted. Their main focus was ALD of alumina as surface passivation layers on crystalline silicon solar cells,²⁰ an activity that has been transferred to the spin-off company SoLayTec (see the next section). TNO is currently developing new tools and processes for spatial ALD for applications such as encapsulation, photovoltaics, and solid state lighting.

D. ASM International

ASM International patented a spatial ALD reactor combined with the floating wafer and conductive heating technology they developed earlier for their rapid thermal processing tools (Levitor RTP).²² This technology was further developed by ASMI's spin-off Levitech, aiming at solar cell applications which will be discussed in more detail below.

IV. FIRST INDUSTRIAL APPLICATION: PASSIVATION LAYERS FOR SILICON SOLAR CELLS

An obvious application for spatial ALD is backside passivation of crystalline silicon solar cells. Silicon solar cell

efficiency can be increased by applying thin Al_2O_3 films by ALD for surface passivation.^{23–25} Such films combine excellent chemical passivation with field effect passivation caused by a high intrinsic negative charge density fixed at the Si- Al_2O_3 interface. This implies a breakthrough in the production of high-efficiency solar cells, but also opens the way to using thinner silicon wafers, where surface passivation is of utmost importance. Here, the requirement called for is the availability of an ALD deposition technique meeting the industrial throughput requirements of ~ 3000 wafers/h.

The demand for a high-throughput ALD deposition tool has led to the formation of two companies developing and commercializing spatial ALD machines dedicated for depositing Al_2O_3 passivation layers on crystalline silicon solar cells: Levitech and SoLayTec. Both use a double-sided gas bearing configuration, sandwiching the solar cell wafer (Fig. 10). This concept allows for a near-frictionless movement of the wafer through ALD half-reaction zones. Both spatial ALD tools were recently compared with batch ALD and PECVD in a market survey on aluminum oxide deposition systems for passivation layers.²⁶

A. Levitech

Levitech, a spin-off from ASM International, developed a spatial ALD tool for the passivation of silicon solar cells with Al_2O_3 layers based on the ASM International concept as shown in Fig. 10(a). Silicon solar cell wafers are transported in a linear track crossing areas with precursor gas curtains. The wafers are transported between two gas bearings, levitating in between a narrow track. Figure 10(a) shows the cells of the Levitrack system. Wafers pass such a cell in ~ 1 s resulting in a throughput of 3600 wafers/h. The total number of ALD cells determines the final layer thickness. For the deposition of a 10 nm layer the tool length will be ~ 10 ms [Fig. 10(c)].

Extensive modeling was used to optimize the track performance. The track dimensions and gas flow rate are chosen such that no gas mixing occurs in the track when empty or during processing of $(156 \times 156 \text{ mm}^2)$ wafers.²⁷ Heated top and bottom walls transfer heat efficiently to the wafers: heating up and cooling down takes place in the first and the last 60 cm of the track, respectively. The TMA evaporation rate is tuned to just reach wafer surface saturation; consequently, almost no TMA goes to the exhaust. A high-speed N_2 flow protects the back side of the wafer against Al_2O_3 deposition.

The tool has been manufactured and tested for passivation quality of atmospheric ALD layers, throughput and reliability. The results have demonstrated that the electrical charge in the deposited Al_2O_3 layer is sufficient to provide field-effect passivation. Measurements of the minority carrier lifetime show good passivation levels.²⁸ Marathon runs at throughputs rates of 4000 wafers per hour showed a reliable system performance. Complete solar cells with ALD Al_2O_3 passivation layers deposited in the Levitrack reactor on $156 \times 156 \text{ mm}^2$ wafers have been fabricated following existing industrial technologies (optimized for SiN_x and SiO_x passivation). Here, superior results were obtained with the ALD Al_2O_3 layers with thicknesses in the range 5–10 nm.

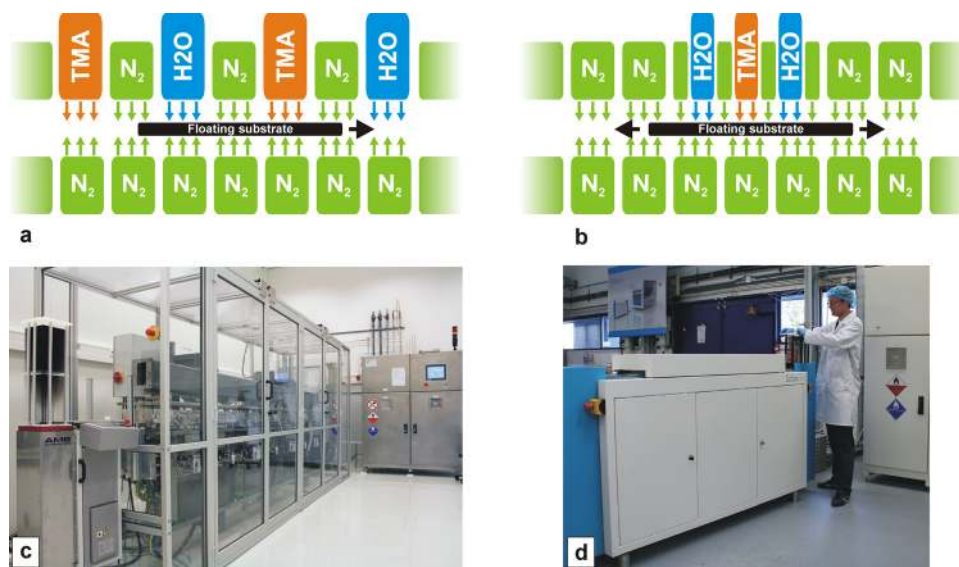


FIG. 10. (Color online) Two double-sided gas bearing spatial ALD concepts used by (a) Levitech and (b) SoLayTec. In both cases, a gas bearing above and below the substrate, forming a narrow slit, makes the substrate float virtually frictionless through the reactor. In the Levitech concept, the substrate is moved along a linear row of repeating half-reaction zones (called “cells”) where the total number of cells determines the overall layer thickness. In the SoLayTec concept, the substrate is moved back and forth in a reciprocating manner under a spatial ALD injector, where the overall thickness is determined by the number of passages underneath the injector. (c) Photograph of the Levitrack spatial ALD reactor from Levitech. (d) Photograph of the Process Development Tool spatial ALD reactor from SoLayTec.

The highest efficiency obtained for n-type crystalline silicon cell was 18.3%.²⁹

B. SoLayTec

SoLayTec, a spin-off company from TNO, have developed a spatial ALD reactor for depositing Al_2O_3 passivation layers where substrates are moved back and forth all the way underneath a spatial ALD injector head with the same width as the substrate.^{30,31} By using a double-sided gas bearing [below and above the substrate; Fig. 10(b)], the substrate floats virtually frictionlessly underneath the injector zones. By pulsing flows of inert gas into the substrate drive-in and drive-out zones next to the injector, the wafer can be moved back and forth without utilizing carriers. The film thickness is tuned by simply selecting the number of passages of the substrate.

The total deposition rate depends on the number of TMA and water slots that are integrated in the injector head, in combination with the number of passages of the substrate per second. Currently, each substrate passes 4 times per second through the injector head containing a single TMA slot, resulting in an effective deposition rate of ~ 0.45 nm/s. This allows for throughput numbers of the order of 100 wafers/h, based on a passivation layer thickness of 10 nm. However, integrating 2 or 3 TMA and water half-reaction zones is possible; thus increasing the throughput accordingly. With this tool [Fig. 10(d)], homogeneous deposits ($30 \text{ nm} \pm 2\text{--}4\%$ thickness variation) can be obtained over the entire area of $156 \times 156 \text{ mm}^2$ solar cell wafers, with minimal backside deposition with low effective recombination velocities (~ 30 cm/s) for $180 \mu\text{m}$ thick p-type CZ Si-wafers, $1\text{--}5 \Omega\text{cm}$), but highly dependent on type, quality, pretreatment and posttreatment of the Si-wafer).^{32,33} Further up-scaling to

industrial scale can be achieved by incorporating 10 or up to 15 injectors in parallel use, as a modular system. In this way throughput numbers over 3000 wafers/h can be obtained while keeping a relatively small footprint.

V. OUTLOOK

Several potentially new applications for spatial ALD will be discussed in this section. Some have seen already quite some development but await industrialization, others are still in a very early stage of development.

A. Spatial ALD for flexible electronics

1. Thin film encapsulation and roll-to-roll ALD

A new field of applications for spatial ALD is in flexible electronics, including flexible displays, flexible OLEDs, and flexible (organic) solar cells. Flexible electronics are slowly but surely evolving from lab-scale to industrial production. This opens up new possibilities for spatial ALD as a production technique for functional layers where conventional ALD already showed its potential in the lab, such as transparent oxide (semi)conductors [e.g., ZnO [Ref. 34]] and moisture barriers [e.g., Al_2O_3 (Ref. 4)]. A major obstacle to this development is the sensitivity of these devices towards O_2 and H_2O . For OLEDs, water vapor transmission rates (WVTRs) as low as $10^{-6} \text{ g/m}^2/\text{day}$ are required. The requirements for thin film solar cells are expected to be less stringent (by one or two orders of magnitude) but still beyond simple polymer encapsulation. Excellent diffusion barriers have been made from alumina by ALD, either as a single film or as part of a multilayer stack, where WVTR values below $10^{-5} \text{ g/m}^2/\text{day}$ have been reported.⁴ The excellent barrier properties of alumina deposited with ALD

combined with the flexibility of the spatial ALD concept opens up the opportunity to develop a roll-to-roll spatial ALD tool for depositing barrier layer on flexible substrates for applications such as flexible OLEDs, solar cells, and displays.

Lotus Applied Technology (Fig. 3), ASTRaL (Fig. 4) and Cambridge Nanotech (Fig. 5) all have designed their spatial ALD tools specifically for roll-to-roll purposes. For Lotus Applied Technology, the near-term application target for the technology is transparent gas diffusion barriers on polymer substrates, for flexible packaging and encapsulation of flexible electronics, including thin-film photovoltaic modules and OLED displays and lighting. For the longer term, the technology lends itself well to roll-to-roll processing applications that benefit from the unique attributes of ALD, and require low cost, such as surface functionalization, active layers in flexible electronics such as gate dielectrics and semiconductors, and thin transparent conductive oxides, such as ZnO:Al. A pilot scale system, capable of coating substrates 300 mm wide by 1000 m long, and up to 100 ALD cycles per pass, is currently under construction.

ASTRaL is currently awaiting delivery of a complete roll-to-roll ALD system based on their technology. ASTRaL will have the capability of coating 500 mm width webs and is intended to prove the technology in a pilot-scale industrial process. Their initial aim is to study the application of ALD to paper-based products such as food packaging and labeling and also for application to plastic electronics.

Cambridge NanoTech's zone-separated ALD work began in 2010 when they became a recipient of a FlexTech Alliance (a consortium of Flexible Electronics companies) award, to develop a Roll-to-Roll ALD system for use within the flexible electronics sector. A beta system is planned to be installed at the Flexible Display Centre at Arizona State University in 2012.

TNO is developing a roll-to-roll spatial ALD reactor consisting of a central drum that contains one or more combinations of TMA and water half-reaction zones³⁵ (Fig. 11). These zones are separated and surrounded by nitrogen gas bearings. The foil to be coated is pulled over this drum, where the gas bearing ensures that the foil is kept at a fixed distance from the surface of the drum (typically tens to hundreds of micrometers). If the foil is then moved over the drum, ALD deposition will take place, where the total thickness is determined by the number of half-reaction zone pairs that the foil passes. Higher deposition rates can be achieved if the drum is rotated in the opposite direction of the movement direction of the foil. In this manner, the total thickness is determined by the rotation frequency of the drum in combination with the translation speed of the foil. One main benefit of this roll-to-roll concept is that there is no mechanical contact between the deposition side of the foil and the reactor and that there is a minimum of moving parts. This minimizes contamination of the foil with particles that would lead to pinholes in the deposited films and; consequently, to deterioration of the barrier function. A small-scale prototype reactor is planned to be operational spring 2012, aimed at web widths of maximum 30 cm.

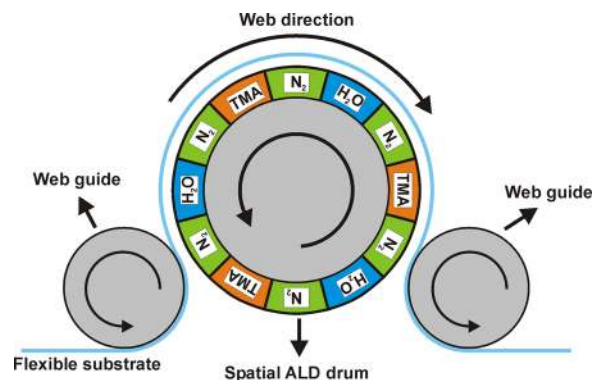


Fig. 11. (Color online) Schematic drawing of the roll-to-roll concept currently under development at TNO, consisting of a central drum that contains one or more combinations of TMA and water half-reaction zones. These zones are separated and surrounded by nitrogen gas bearings. The foil to be coated is pulled over this drum, where the gas bearing ensures that the foil is kept at a fixed distance from the surface of the drum outer surface. If the foil is then moved typically clockwise over the drum, ALD deposition will take place, where the total thickness is determined by the rotation frequency of the drum (typically rotating counterclockwise) in combination with the translation speed of the foil.

2. Oxide based thin-film transistors

The field of oxide electronics has shown significant growth in past years, fueled to a large part by demonstrations of thin-film transistors (TFTs) based upon zinc oxide (ZnO).³⁶ At Eastman Kodak, ZnO-based TFTs have been made by spatial ALD.¹⁸ Their spatial ALD system was used to deposit an Al₂O₃ film of approximately 50 nm thickness. The ZnO semiconductor layer is deposited on top of the Al₂O₃ dielectric using the same equipment. A final passivation layer of 30 nm thick Al₂O₃ is deposited under the same conditions as for the gate dielectric. These ZnO-based thin-film transistors show mobilities approaching 20 cm²/Vs and stabilities as good as any published data on this material system. The deposition system is also capable of producing very good uniformity of devices, with threshold uniformities over the area of their 5 cm wide coater of approximately 50 mV.

B. Deep reactive ion etching

Deep reactive ion etching (DRIE) of silicon is a highly anisotropic etch process used to create high aspect ratio (20:1) features or more. DRIE was developed for the manufacture of microelectromechanical systems (MEMS) and to excavate trenches for high-density capacitors for DRAM and passive integration, and for through-silicon-vias (TSVs) in advanced 3D wafer-level packaging technology. The DRIE process of choice today is the Bosch process^{37,38} (Fig. 12). The Bosch process is a near-room temperature, low pressure (~100 mTorr) plasma etch process, where the etch step is divided in two separate half-reactions. The first step is etching with SF₆ which is an isotropic etch. In order to minimize the lateral etching component, the etch steps are interrupted by a second step, being a sidewall passivation step by depositing a thin Teflonlike layer from C₄F₈. Etch or passivation

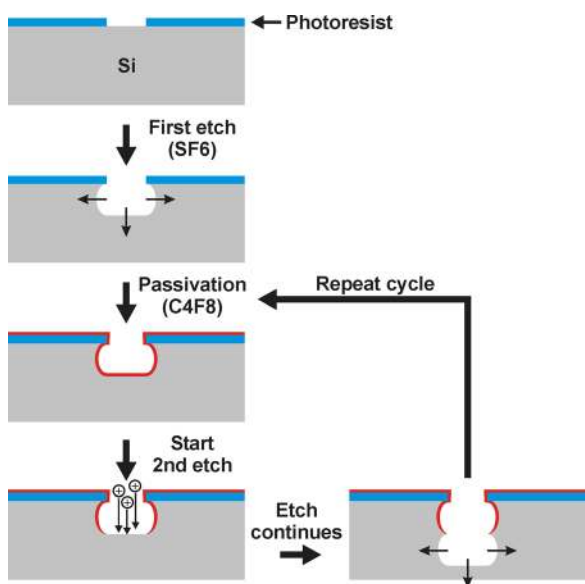


Fig. 12. (Color online) Schematic representation of the current (temporal) “Bosch” deep reactive ion etching process. The etching cycle includes alternating cycles of an isotropic plasma etch and deposition of a chemically inert passivation layer.

subcycle times are typically 1–10 s with 0.1–1 μm etched depth per cycle.

The similarity of this process with ALD might allow the conversion of this process from the time-divided into the spatially divided regime, similar to spatial ALD, to significantly reduce the total cycle time.^{39,40} A second improvement in the spatially-divided approach is the replacement of the CVD-based C_4F_8 passivation steps by ALD-based oxide (e.g., SiO_2) deposition cycles. In contrast to the C_4F_8 case the ALD-based oxide layer is self-limiting and chemisorptive of nature and less complex in its layer thickness control and thus in the anisotropy control of the total DRIE process.

C. Light management for photovoltaics and lighting

In order to increase the performance of devices such as solar cells and LEDs, functional layers can be applied on top of or below the active layers of these devices to enhance the incoupling or outcoupling of light.⁴¹ Examples can be mirror layers, textured front or back contacts, multilayer antireflection layers and plasmonics, typically using materials as oxides and metals. These applications often require manufacturing techniques with feature sizes of the order of tens of nanometers, for which ALD is excellently suited. However, they should be applied at low cost and industrial throughput scales (e.g., km^2/yr). Here, spatial ALD can be a potential manufacturing technique as spatial ALD can cope with both aspects.

D. Textile functionalization

Fibrous materials, including natural and synthetic textiles, are important for protective and decorative function. Because of their high surface area, flexibility, ease of manufacture and low cost, research interest is growing in textile

materials for biomedical systems and for applications in photovoltaic cells, transistors, or sensors.^{42–45} For many applications, chemically modifying the fiber surface is important to improve appearance, chemical durability, and mechanical properties. Several processes such as sol-gel or plasma treatments are known to alter surface properties of woven and nonwoven fiber mats for use in hygiene, bandages, or oil recovery functions.^{46–48} In addition, exposing fibers to reactive vapors is also known to alter the surface properties of fibers, especially their wetting characteristics.^{49,50}

Although less well known than other methods in the industry, vapor phase atomic layer deposition (ALD) is known to penetrate inside complex fibrous structures to create conformal and uniform coatings of inorganic or inorganic-organic thin films that modify the surface finish.^{51,52} ALD is particularly advantageous compared to wet chemical processes since ALD is highly efficient, it leaves no liquid chemical waste, and it requires no expensive thermal drying steps after coating. Recent reports by researchers at North Carolina State University show that ALD can promote wetting transitions on polypropylene and cotton fibers,⁵³ and this ALD treatment can then enable more advanced functionalization.⁵⁴

A key factor that will limit ALD as a viable surface modification technique for textiles will be the process throughput. Most textile manufacturing proceeds via roll-to-roll, so making ALD compatible with textile processes will require movement away from batch processes. The advent of spatial ALD will be a critical enabler to help this transition happen. A recent report shows that surface-saturated Al_2O_3 and ZnO ALD can be achieved on textile materials in a reactor operating at ambient pressure,⁵⁵ indicating that textiles can be compatible with ambient spatial ALD processes. However, we note that most of the spatial ALD tool designs developed to date are primarily focused on coating flat planar substrates (primarily flexible polymer web). For textiles the tools may require unique design features to be compatible with porous substrates where more of a “flow through” reactant delivery will likely be preferred.⁵³

E. New processes

Other processes than thermal ALD [e.g., plasma ALD (Ref. 56)] are possible as well with spatial ALD. Lotus Applied Technology uses an oxygen containing plasma in their reactor. However, the close proximity spatial ALD discussed earlier in this review operates at atmospheric pressure, and conventional low-pressure plasmas cannot be used. Yet, plasma ALD should be possible by allowing an atmospheric plasma source to replace one of the reactant channels in Fig. 6.⁵⁷ Photon-assisted ALD could also be implemented by allowing radiation to pass through one of the reactant channels. A continuous flow of reactants, plasma or photons can be defined much more easily in the spatial ALD format than in the temporal ALD format as the latter is confined to a vacuum chamber. Having the substrate at atmospheric pressure and the gas source head open to the atmosphere allows much more flexibility. In addition, low vapor pressure

reactants could also be introduced very closely to the reactant channel in the gas source head thus enabling deposition rates unattainable in the temporal ALD case. The closer proximity to the substrate and the much smaller volume in the reactant channel could allow a much wider range of low vapor pressure precursors as, for instance, used in molecular layer deposition.⁵⁸

VI. SUMMARY AND CONCLUSIONS

Although the basic concept of spatial ALD is already known for decades, the most major developments occurred in the past two to three years. A major driver for these developments is the growing demand from industry for high throughput, large area ALD equipment and processes. Surface passivation of crystalline silicon solar cells is an important example that led to the launch of dedicated spatial ALD equipment. Another upcoming application is the encapsulation of thin-film solar cells and OLED devices, both rigid and flexible. Especially the possibility of roll-to-roll spatial ALD holds promise to make ALD an established manufacturing technique for flexible electronics.

Several potentially new applications for spatial ALD have been discussed. Most mentioned applications fall outside the more traditional application domains of ALD in, e.g., microelectronics. This is probably because the throughput limitations of conventional ALD are less of an issue in the microelectronics industry. For spatial ALD to successfully enter these new application domains, new materials, processes and reactor concepts need to be developed that fit the requirements of these applications. There is a risk; however, that for more complex materials and applications there will be significant challenges for spatial ALD to overcome. For example, so far only processes for which high vapor pressure precursors are available have been developed for spatial ALD (e.g., for Al₂O₃ and ZnO), but for many precursors the vapor pressure is limited. Furthermore, freeform and highly textured substrates might impose limitations on the applicability of spatial ALD, especially for close-proximity spatial ALD concepts.

Nevertheless, spatial ALD has experienced a strong development in the last few years and smart tailor-made solutions have been developed to overcome the technical and economic obstacles that make volume manufacturing with conventional ALD impossible. Although much more research and development is required, spatial ALD has the potential to become a disruptive manufacturing technique for a wide number of applications. The future of spatial ALD promises to be very exciting.

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- ¹S. George, *Chem. Rev.* **110**, 111 (2010), and references therein.
- ²F. Lee, S. Marcus, E. Shero, G. Wilk, J. Swerts, J. W. Maes, T. Blomberg, A. Delabie, M. Gros-Jean, and E. Deloffre, 2007 IEEE/SEMI Advanced Semiconductor Manufacturing, 2007 (unpublished), p. 359.
- ³J. R. Bakke, K. L. Pickrahn, T. P. Brennan, and S. F. Bent, *Nanoscale* **3**, 3482 (2011)
- ⁴P. F. Carcia, R. S. McLean, M. D. Groner, A. A. Dameron, and S. M. George, *J. Appl. Phys.* **106**, 023533 (2009).
- ⁵E. Granneman, P. Fischer, D. Pierreux, H. Terhorst, and P. Zagwijn, *Surf. Coat. Technol.* **201**, 8899 (2007).
- ⁶R. G. Gordon, D. Hausmann, E. Kim, and J. Shepard, *Chem. Vapor Depos.* **9**, 73 (2003).
- ⁷T. Suntola and J. Antson, U.S. Patent No. 4,058,430 (15 November 1977).
- ⁸T. Suntola, A. Pakkala, and S. Lindfors, U.S. Patent No. 4,389,973 (28 June 1983).
- ⁹C. J. Hwang and K. S. Shim, U.S. Patent No. 2002/0043216 (18 April 2002).
- ¹⁰E. R. Dickey and W. A. Barrow, 52nd Annual Technical Conference Proceedings of the Society of Vacuum Coaters, 2009 (unpublished), pp. 720–726.
- ¹¹W. A. Barrow and E. R. Dickey, proceedings of the Fall Conference of the Association of Industrial Metallizers, Coaters and Laminators (AIMCAL), 2009 (unpublished).
- ¹²P. S. Maydannik, T. O. Kääriäinen, and D. C. Cameron, *Chem. Eng. J.* **171**, 345 (2011).
- ¹³D. H. Levy, R. S. Jerr and J. T. Carey, U.S. Patent No. 2009/0217878 (3 September 2009).
- ¹⁴E. H. A. Granneman and S. E. van Nooten, U.S. Patent No. 2011/0124199 (26 May 2011).
- ¹⁵D. J. Maas, B. van Someren, A. S. Lexmond, C. I. M. A. Spee, A. E. Duisterwinkel, and A. J. P. M. Vermeer, W.O. Patent 2010/024671 (4 March 2010).
- ¹⁶D. H. Levy, D. C. Freeman, S. F. Nelson, and P. J. Cowdery-Corvan, 8th International Conference Atomic Layer Deposition, Bruges, Belgium, 2008 (unpublished).
- ¹⁷D. H. Levy, D. C. Freeman, S. F. Nelson, P. J. Cowdery-Corvan, and L. M. Irving, *Appl. Phys. Lett.* **92**, 192101 (2008).
- ¹⁸D. H. Levy, S. F. Nelson, and D. Freeman, *J. Display Technol.* **5**, 484 (2009).
- ¹⁹P. R. Fitzpatrick, Z. M. Gibbs, and S. M. George, *J. Vac. Sci. Technol. A* **30**, 01A136 (2011).
- ²⁰P. Poodt, A. Lankhorst, F. Roozeboom, V. Tiba, K. Spee, D. Maas, and A. Vermeer, 10th International Conference on Atomic Layer Deposition, Seoul, R. South Korea, 2010 (unpublished).
- ²¹P. Poodt, A. Lankhorst, F. Roozeboom, K. Spee, D. Maas, and A. Vermeer, *Adv. Mater.* **22**, 3564 (2010).
- ²²E. Granneman, *Mater. Sci. Forum* **573**, 375 (2008).
- ²³B. Hoex, J. Schmidt, M. C. M. van de Sanden, and W. M. M. Kessels, *J. Appl. Phys.* **104**, 44903 (2008).
- ²⁴J. Schmidt, A. Merkle, R. Brendel, B. Hoex, M.C.M. van de Sanden, and W. M. M. Kessels, *Prog. Photovoltaics* **16**, 461 (2008).
- ²⁵J. Benick, B. Hoex, M. C. M. van de Sanden, W. M. M. Kessels, O. Schultz, and S. W. Glunz, *Appl. Phys. Lett.* **92**, 253504 (2008).
- ²⁶Photon International, March edition 146, 2011 (unpublished).
- ²⁷E. H. A. Granneman, V. Kuznetsov, and K. Vanormelingen, Proceedings of the 25th EU PVSEC, Valencia, September, 2010 (unpublished).
- ²⁸I. Cesar, I. Romijn, A. Mewe, E. Granneman, P. Vermont, and A. Weeber, Proceedings of the 37th IEEE Photovoltaic Specialist Conference, Seattle, June, 2011 (unpublished).
- ²⁹P. Brand, Y. Veschetti, V. Sanzone, R. Cabal, X. Parges, K. Vanormelingen, and P. Vermont, Proceedings of the 37th IEEE Photovoltaic Specialist Conference, Seattle, June, 2011 (unpublished).
- ³⁰A. Vermeer and G. P. Janssen, E.P. Patent No. 2281921 (9 February 2011).
- ³¹A. Vermeer, F. Roozeboom, P. Poodt, and R. Görtzen, MRS spring meeting, San Francisco, 2011 (unpublished).
- ³²B. Vermang, A. Rothschild, A. Racz, J. John, J. Poortmans, R. Mertens, P. Poodt, V. Tiba, and F. Roozeboom, *Prog. Photovoltaics* **19**, 733 (2011).

- ³³F. Werner, B. Veith, V. Tiba, P. Poodt, F. Roozeboom, R. Brendel, and J. Schmidt, *Appl. Phys. Lett.* **97**, 162103 (2010).
- ³⁴P. F. Carcia, R. S. McLean, and M. H. Reilly, *Appl. Phys. Lett.* **88**, 123509 (2006).
- ³⁵A. J. P. M. Vermeer, F. Roozeboom, and J. van Deelen, EP2360293 (11 February 2010).
- ³⁶R. L. Hofmann, B. J. Norris, and J. F. Wager, *Appl. Phys. Lett.* **82**, 733 (2003).
- ³⁷F. Lärmer and A. Schilp, U.S. Patent 5,498,312 (12 March 1996).
- ³⁸F. Roozeboom *et al.*, in *Handbook of 3D Integration; Technology and Applications of 3D Integrated Circuits*, edited by P. Garrou, C. Bower, and P. Ramm (Wiley, New York, 2008), pp. 47–91.
- ³⁹F. Roozeboom, A. M. Lankhorst, G. Winands, N. B. Koster, P. Poodt, A. Vermeer, G. Dingemans, and W. M. M. Kessels, 11th International Conference on Atomic Layer Deposition, Cambridge, MA, 2011 (unpublished).
- ⁴⁰F. Roozeboom, A. Lankhorst, P. Poodt, N. Koster, H. Winands, and A. Vermeer, WO 2011105908 (28 February 2010).
- ⁴¹H. A. Atwater and A. Polman, *Nat. Mater.* **9**, 205 (2010).
- ⁴²B. O'Connor, K. P. Pipe, and M. Shtein, *Appl. Phys. Lett.* **92**, 193306 (2008).
- ⁴³M. Hamed, R. Forchheimer, and O. Inganäs, *Nat. Mater.* **6**, 357 (2007).
- ⁴⁴J. B. Lee and V. Subramanian, *IEEE Trans. Electron Dev.* **52**, 269 (2005).
- ⁴⁵S. D. Hart, G. R. Maskaly, B. Temelkuran, P. H. Prideaux, J. D. Joannopoulos, and Y. Fink, *Science* **296**, 510 (2002).
- ⁴⁶G. Deschamps, H. Caruel, M.-E. Borredon, C. Bonnin, and C. Vignoles, *Environ. Sci. Technol.* **37**, 1013 (2003).
- ⁴⁷A. Vilcnik, I. Jerman, A. S. Vuk, M. Kozelj, B. Orel, B. Tomsic, B. Simonic, and J. Kovac, *Langmuir* **25**, 5869 (2009).
- ⁴⁸K. T. Meilert, D. Laub, and J. Kiwi, *J. Mol. Catal. A: Chem.* **237**, 101 (2005).
- ⁴⁹A. G. Cunha, C. Freire, A. Silvestre, C. P. Neto, A. Gandini, M. N. Belgacem, D. Chaussy, and D. Beneventi, *J. Colloid Interface Sci.* **344**, 588 (2010).
- ⁵⁰W. E. Tenhaeff and K. K. Gleason, *Adv. Funct. Mater.* **18**, 979 (2008).
- ⁵¹M. Knez, K. Nielsch, and L. Niinistö, *Adv. Mater.* **19**, 3425 (2007).
- ⁵²Q. Peng, X. Y. Sung, J. C. Spagnola, G. K. Hyde, R. J. Spontak, and G. N. Parsons, *Nano Lett.* **7**, 719 (2007).
- ⁵³G. K. Hyde, G. Scarel, J. C. Spagnola, Q. Peng, K. Lee, B. Gong, K. G. Roberts, K. M. Roth, C. A. Hanson, C. K. Devine, S. M. Stewart, D. Hojo, J. S. Na, J. S. Jur, and G. N. Parsons, *Langmuir* **26**, 2550 (2010).
- ⁵⁴Q. Peng, B. Gong, and G. N. Parsons, *Nanotechnology* **22**, 155601 (2011).
- ⁵⁵J. S. Jur and G. N. Parsons, *ACS Appl. Mater. Interface* **3**, 299 (2011).
- ⁵⁶W. M. M. Kessels, H. B. Profijt, S. E. Potts, and M. C. M. van de Sanden, *ALD of Nanostructured Materials*, edited by M. Knez and N. Pinna (Wiley-VCH, Weinheim, 2011) pp. 131–159.
- ⁵⁷P. Poodt, B. Kniknie, A. Branca, H. Winands, and F. Roozeboom, *Phys. Status Solidi (RRL)* **5**, 165 (2011).
- ⁵⁸S. M. George, A. A. Dameron, and B. Yoon, *Acc. Chem. Res.* **42**, 498 (2009).