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# Spin nano-oscillator-based wireless communication

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Spin-torque nano-oscillators (STNOs) have outstanding advantages of a high degree of compactness, high-frequency tunability, and good compatibility with the standard complementary metal-oxidesemiconductor process, which offer prospects for future wireless communication. There have as yet been no reports on wireless communication using STNOs, since the STNOs also have notable disadvantages such as lower output power and poorer spectral purity in comparison with those of LC voltage-controlled oscillators. Here we show that wireless communication is achieved by a proper choice of modulation scheme despite these drawbacks of STNOs. By adopting direct binary amplitude shift keying modulation and non-coherent demodulation, we demonstrate STNO-based wireless communication with 200-kbps data rate at a distance of 1 m between transmitter and receiver. It is shown, from the analysis of STNO noise, that the maximum data rate can be extended up to 1.48 Gbps with 1-ns turn-on time. For the fabricated STNO, the maximum data rate is 5 Mbps which is limited by the rise time measured in the total system. The result will provide a viable route to real microwave application of STNOs.

obile terminals, an indispensable gadget of modern life, are becoming more compact and power efficient while serving multiple functions and operating in different standards of wireless communication. One of the main issues associated with multi-standard transceivers is how to design a compact, low-power, low-phase-noise, and wideband voltage-controlled oscillator (VCO). The rapid progress in spintronics technology suggests that the spin-torque nano-oscillator (STNO), owing to its small chip size and high tunability, can be a new solution not only for memory and but also for radio-frequency (RF) devices<sup>1-2</sup>. Compared to the LC tank-based VCO, which is generally used for conventional RF transceivers, STNOs are remarkably small (100 nm or less in diameter) without an LC resonance tank because they are based on the spin-transfer torque on nano-magnetic structures. Moreover, for reconfigurable communication, the high tunability of the STNO makes an STNO-based RF transceiver superior to an LC VCO-based RF transceiver, which requires multiple VCOs and thus consumes much more power than a single LC VCO. In one example, an STNO<sup>3</sup> featured an ultra-wideband tuning ratio that exceeds 100% at a center frequency of oscillation of 5 GHz.

However, several issues remain that hinder the realization of an STNO-based RF transceiver. The STNOs have critical disadvantages such as lower output power and poorer spectral purity in comparison with LC voltage-controlled oscillators (VCOs). Many research groups have tried to improve the output power and spectral purity of a single or multiple STNOs<sup>4-17</sup>. For example, to increase the output power associated with STNOs, the synchronization of multiple STNOs<sup>4-5</sup> as well as structural optimizations<sup>6-15</sup> has all been attempted; however, these devices require a sizable amplification for wireless communication. Even with multiple amplifications of the STNO output<sup>2</sup>, modulation which uses a mixer for up– and down–conversion of the frequency cannot be realized due to the poor spectral purity, as evidenced by several MHz of the linewidth  $\Delta f$  and numerous spurious signals, mode hopping<sup>16</sup>, and the frequency–amplitude nonlinearity<sup>17</sup>.

Here we take an alternative approach to perform wireless communication by adopting direct modulation with binary amplitude shift keying (ASK) despite major drawbacks of STNOs. This modulation scheme enables us to demonstrate STNO-based wireless communication with a decent data rate at a distance between the transmitter  $(T_x)$  and receiver  $(R_x)$  of 1 m. We analyze the noise caused by the STNO to extract the maximum data rate theoretically and present the measured data rate experimentally.



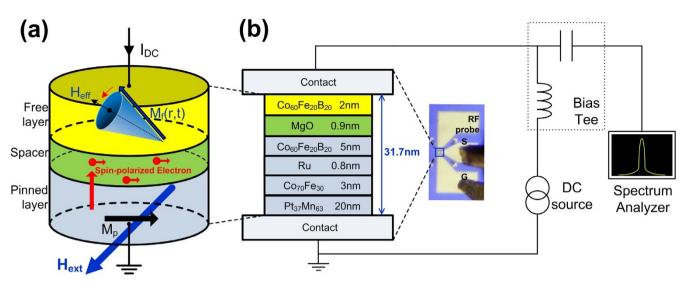


Figure 1 | Operation and test setup of the STNO. (a) Schematic of the STNO sample with a free layer (FL), a barrier, and a pinned layer (PL).  $M_p$  denotes the magnetization of the pinned layer,  $M_f(r,t)$  is the magnetization of the free layer, and  $H_{eff}$  is the local effective magnetic field. (b) Schematic of the test setup for the STNO.  $H_{ext}$  is the external magnetic field. Chip photo of the STNO (middle).

#### Results

Operational principle and properties of the STNO. The operation of the STNO is based on the transfer of spin angular momentum from spin-polarized current to the local magnetization of a thin magnetic layer<sup>18–20</sup>. As shown in Fig. 1a, when DC current  $I_{DC}$  is supplied to the STNO, the conduction electrons are polarized by the magnetization  $M_p$  of the pinned layer; the magnetization vector  $M_f(r,t)$  in the free layer continuously oscillates around the local effective magnetic field  $H_{eff}$ . The spin torque oscillation is observed in a nanoscale structure with a high current density  $(\sim 10^7 \text{ A/cm}^2)$ . The oscillation occurs around static equilibrium at Gigahertz frequencies, which can be tuned by choosing the material parameters and device structure. The direction and magnitude of  $H_{ext}$  also mainly influences the oscillation frequency. Fig. 1b shows that, to measure the STNO oscillation,  $I_{DC}$  is injected via the inductive branch and the generated voltage oscillation is extracted via RF microprobes and the capacitive branch of the bias tee. A spectrum analyzer with an operating bandwidth in the range of 10 kHz-50 GHz was used in this case to extract the output spectrum of the STNO.

We have measured the DC and RF properties of an STNO based on MgO magnetic tunnel junction (MTJ) (See Methods for the details of the sample structure). Fig. 2a shows the magneto-resistance (MR) values of the STNO at both antiparallel (AP) and parallel (P) states between  $M_p$  and  $M_f(r,t)$  when  $I_{DC}$  varies from -3 mA to 3 mA. When  $I_{DC}$  is increased from 0 mA to 3 mA,  $R_{AP}$  decreases from 83  $\Omega$  to 77  $\Omega$ . We have observed a microwave oscillation in this AP state at  $I_{DC} = 1 \sim 3$  mA and  $H_{ext} = 55$  Oe; as shown in Fig. 2b, when  $I_{DC}$  is increased from 1.0 mA to 3 mA, the output peak power of the STNO  $(P_{peak})$  is increased from -94.0 dBm to -85.7 dBm, and the oscillation frequency of the STNO  $(f_{osc})$  is decreased from 2.62 GHz to 2.51 GHz. When  $I_{DC}$  is increased further to 3.5 mA,  $P_{peak}$  is slightly decreased to -86.1 dBm. Fig 2c shows the magnetic field dependence of microwave oscillation from the STNO; both  $f_{osc}$ and  $P_{peak}$  are changed notably when  $H_{ext}$  is applied in a range of 20 Oe to 400 Oe. The frequency is tuned from 1.870 GHz to 5.350 GHz, and  $P_{peak}$  ranges from  $-96.4~\mathrm{dBm}$  to  $-85.7~\mathrm{dBm};$  the linewidth  $\Delta f$  of the oscillation output spectrum ranges from 200 MHz to 245 MHz with variation of  $I_{DC}$ , as shown in Fig. 2d. With an  $I_{DC}$  value of 3 mA and a  $H_{ext}$  value of 55 Oe, the STNO generates a maximum  $P_{peak}$  of -85.7 dBm with a minimum  $\Delta f$  of 200 MHz and resistance near 50  $\Omega(77 \ \Omega)$  at 2.5 GHz; the STNO

under these conditions will be used for wireless communication in this work.

**Modulation for STNO–based communication.** For wireless modulation and demodulation, a local oscillator (LO), which generates the carrier frequency, requires enough power (~0 dBm) to obtain the intermodulation signal using an RF mixer to perform frequency upand down–conversion. However, the output power levels of STNOs are much lower than 0 dBm; the STNOs in the previous reports<sup>2-15</sup> have an output power lower than -45 dBm, and the STNO in this work has an  $P_{peak}$  value of -85.7 dBm. In addition, the STNO generates numerous spurious signals with the STNO output signal. Due to the low output power and the spurious signals of the STNO, wireless communication by intermodulation, which uses the STNO as an LO, cannot be performed successfully. Thus, we consider amplitude shift keying (ASK) and frequency shift keying (FSK) for STNO–based modulation, as these strategies do not require an RF mixer for wireless communication.

The wired binary FSK (BFSK) modulation or analog frequency modulation using STNOs by controlling  $I_{DC}$  has already been demonstrated<sup>21-26</sup>. The demodulation of BFSK, however, leads to another challenge. In the BFSK demodulation, there are two types of the BFSK demodulation with no RF mixer; one is using bandpass filter (BPF) pairs, and the other is using a frequency discriminator<sup>27</sup>. The BPF pairs should separate two different frequencies clearly. However, given the oscillation characteristics of STNOs, it is difficult to separate the two different frequencies by BPF pairs due to the bandwidth limitation of BPF; a frequency discriminator increases the noise of high frequency components of the STNOs. Therefore, the demodulation through IDC-controlled BFSK is very difficult to be used with STNOs. Moreover, the STNO fabricated in this work has a broad minimum linewidth of 200 MHz as shown in Fig. 2d. In order to use the BFSK, a sufficient frequency separation between two frequencies more than 200 MHz is required, but the frequency range of the STNO that can be controlled by bias current  $(I_{DC})$  is only 110 MHz shown in Fig. 2b. The  $I_{DC}$ -controlled BFSK based on this type of STNO cannot distinguish the on-state from the off-state in the demodulated STNO signal. Instead, we may consider the BFSK controlled by external field  $(H_{ext})$  as an alternative option for our STNO of which frequency can be controlled over a wide frequency band (1.87  $\sim$  5.35 GHz) by  $H_{ext}$ . Unfortunately, the  $H_{ext}$ -controlled BFSK additionally requires a circuit such as coplanar wave guide (CPW) for generating  $H_{ext}$  changed continuously by the current of

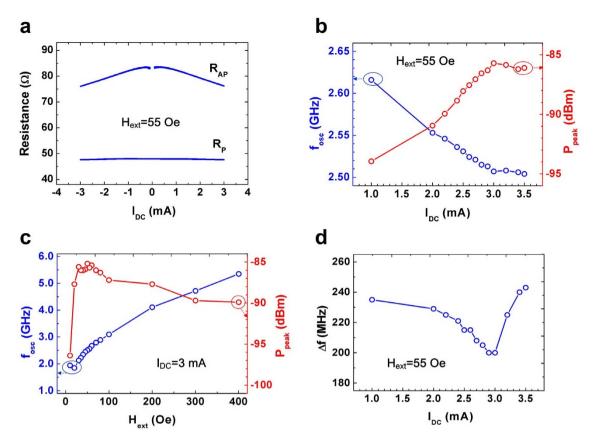


Figure 2 | Characteristics of the measurements for the fabricated STNO. (a) Magneto–resistance of the STNO as a function of  $I_{DC}$  ( $H_{ext} = 55$  Oe). P indicates the parallel (P) state between  $M_p$  and  $M_f$  AP denotes the antiparallel (AP) state between  $M_p$  and  $M_f$  (b) The oscillation frequency  $f_{osc}$  and peak output power  $P_{peak}$  level of the STNO as a function of  $I_{DC}$  ( $H_{ext} = 55$  Oe). (c)  $f_{osc}$  and the  $P_{peak}$  level of the STNO as a function of  $H_{ext}$  ( $I_{DC} = 3$  mA). (d) The linewidth  $\Delta f$  of the STNO as a function of  $I_{DC}$  ( $H_{ext} = 55$  Oe).

pulsed input<sup>28–29</sup>. As a consequence, the integrated chip size for wireless communication system becomes larger, and more DC current may be consumed in comparison with the  $I_{DC}$ -controlled modulation.

For the binary ASK, digital data is simply represented as the presence or absence of STNO oscillation. Because the demodulation is performed by detecting the envelope of the modulated signal without BPF pairs and a frequency discriminator, it is also related to neither  $\Delta f$  nor the maximum frequency difference of the STNO, in contrast to BFSK. Moreover. The BFSK signal requires two frequencies representing the symbol states with a sufficient separation. In contrast, the binary ASK signal requires only one carrier frequency. Because the required bandwidth of the BFSK signal has twice larger than the bandwidth of the ASK signal under same data rate, the binary ASK has a higher data rate that can be transmitted over a given bandwidth (or higher bandwidth efficiency) than the BFSK<sup>27</sup>. Thus, we decided to take advantage of binary ASK modulation instead of BFSK for wireless communication.

There are two types of circuit structures for binary ASK modulation which do not use intermodulation; one is modulation by turning on and off the oscillator directly, and the other is the modulation by switching on and off the on–state oscillator to the antenna. By utilizing the fast turn–on time ( $\sim$ 1 ns) of the STNO<sup>30</sup>, the direct binary ASK modulation was adopted here instead of switching binary ASK modulation in order to have low power consumption with simple circuitry<sup>31</sup>. For the demodulation stage, a simple envelope detector was implemented to recover the data from the received RF signal. Demodulation using an envelope detector avoids the poor spectral purity and weak output power of the STNO because it is noncoherent demodulation, which does not require a RF mixer for the frequency down–conversion process.

Figs. 3a and 3b show a schematic block diagram of the STNObased direct binary ASK wireless communication system and Fig. 3c shows the measurement set-up of the system. For  $T_x$  shown in Fig. 3a, the signal generator modulates the STNO directly and the modulated signal is amplified by as much as 50 dB by two cascaded RF amplifiers. Because the amplified modulation signal (-35.7 dBm) is transmitted through the  $T_x$  antenna (-33.7 dBm)with 2-dBi Tx antenna gain), which is a 2.5-GHz 2-dBi dipole antenna identical to  $R_x$  and given that the path loss of air is -40 dB (see Methods) with a distance of 1 m between  $T_x$  and  $R_x$ , the received signal power through the  $R_x$  antenna is -71.7 dBm [= -85.7 dBm (STNO output) + 50 dB (amplifier gain) + 2 dB ( $T_x$ antenna gain) – 40 dB (the air path loss) + 2 dB ( $R_x$  antenna gain)]. For  $R_x$  shown in Fig. 3b, the total gain and noise figure NF of  $R_x$  are 39 dB and 1.4 dB, respectively, as calculated by adding each RF block's gain and NF (see Eq. (1) in Methods). Fig. 3d shows the RF block-by-block signal power flow of the received signal. The received signal power right before the demodulator is -32.7-dBm, which is within the operating range of -60 dBm to -5 dBm of the envelope detector. We designed  $R_x$  such that it has a 1.4–dB NF and a gain of 39 dB, thus satisfying the minimum sensitivity of the envelope detector (-60 dBm).

**Demonstration of binary ASK modulation and demodulation.** The square-pulsed signal of 230 mV<sub>p-p</sub> generates an  $I_{DC}$  value of 3 mA to turn on and off the STNO, where  $V_{p-p}$  is the peak-to-peak voltage of a signal. The modulated signal is measured at node a in Fig. 3a, right before the antenna, and the demodulated signal is measured at node b in Fig. 3b on the sampling oscilloscope.

Fig. 4 reveals the successful wireless communication between  $T_x$  and  $R_x$  at 200 kbps. Figs. 4a and 4b show the modulated signal with a

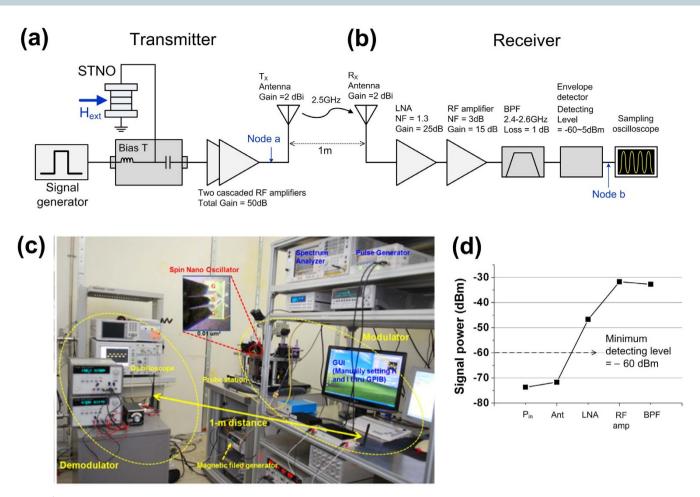


Figure 3 | Configuration of the STNO-based binary ASK transceiver. (a) Schematic of the STNO-based binary ASK transmitter ( $T_x$ ). (b) Schematic of the receiver ( $R_x$ ) and the gain and noise figure (*NF*) of the  $R_x$  chain. (c) The measurement set–up of the STNO–based binary ASK system. (d) The received signal power flow, where  $P_{in}$  is the received signal power of  $R_x$  and Ant, LNA, RF amp, and BPF are the measurement positions before the  $R_x$  antenna, the LNA, the RF amplifier, BPF, and the envelope detector, respectively. The signal level right after BPF is -32.7 dBm, which is within the detectable range of the demodulator ( $-60 \sim 5$  dBm).

pulsed input frequency of 100 kHz and a demodulated signal at a distance of 1 m between  $T_x$  and  $R_x$ , respectively. The 170–mV<sub>p-p</sub> value in Fig. 4b is much larger than the noise voltage level after the demodulation process. One remarkable feature in Fig. 4b is that there is a difference in the noise voltage between the on–and–off states: ±45 mV for the on–state ( $V_{n, on}$ ) and ±20 mV for the off–state ( $V_{n, off}$ ). The only cause of this difference is whether the STNO is on or off. This is used to extract the STNO noise voltage and finally to determine the maximum data rate with the STNO in the forthcoming discussion section.

#### Dicussion

**Analysis of the STNO noise.** The STNO–caused noise,  $V_{n, STNO}$ , can be extracted from the measurement of the on–state and off–state noise voltages with the assumption that the STNO noise does not have any correlation with other noise sources in the communication system in Fig. 4b.

The total noise voltage of a system and the noise voltage from each noise source are related by the following equation,

$$V_{n,tot}{}^2 = V_{n1}{}^2 + V_{n2}{}^2 + \dots + V_{ni}{}^2,$$
(2)

where  $V_{n,tot}$  is the total noise voltage of the system and  $V_{ni}$  is the n–th noise voltage from the n–th noise source. All noise voltages are independent of each other in Eq. (2). Thus, the noise voltage caused by only the STNO is calculated by the equations below.

$$V_{n,on}{}^2 = V_{n,STNO}{}^2 + V_{n,off}{}^2 \tag{3}$$

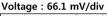
$$V_{n,STNO} = \sqrt{V_{n,on}^2 - V_{n,off}^2} \tag{4}$$

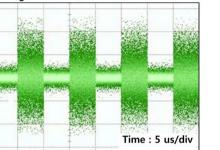
Here,  $V_{n, STNO}$  is the noise voltage amplified from the only STNOcaused noise,  $V_{n, on}$  is the noise voltage of the system when the STNO is turned on, and  $V_{n, off}$  is the noise voltage of the system when the STNO is turned off.

Using Eqs. (3) and (4),  $V_{n, STNO}$  is determined to be ±40.3 mV, which is observed as the largest noise source in the system, where  $V_{n, on} = \pm 45$  mV and  $V_{n, off} = \pm 20$  mV. The only STNO-caused noise power  $P_{n, STNO}$  is determined to be 16.25  $\mu$ W, or -17.90 dBm from the root-mean-square (RMS) value of  $V_{n, STNO}$  and the 50- $\Omega$  impedance  $[P_{n, STNO} = V_{n, STNO}/2\sqrt{2})^2/50]$ .

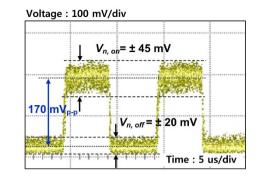
The noise generated only by the STNO is  $V_{n, STNO}$  which is amplified as much as the total gain of the system because we analyze  $V_{n, STNO}$  in the demodulator. The total gain of 80.3 dB is calculated by adding the subtotal gain of 53 dB before the envelope detector and the envelope detector gain is calculated by subtracting the input signal power of -32.7 dBm from the output signal power of -5.4 dBm, as calculated from a RMS of 170 mV<sub>p-p</sub> and the 50- $\Omega$  impedance [the output signal power =  $(170 \text{ mV}_{p-p}/2\sqrt{2})^2/50 = 5.4 \text{ dBm}$ ]. Therefore, the noise generated only by the STNO itself is  $-98.2 \text{ dBm} [-17.90 \text{ dBm} (P_{n, STNO}) - 80.3 \text{ dBm}$  (total gain)], and the signal-to-noise ratio (*SNR*) of the

### (a) Modulated signal





#### (b) Demodulated signal



**Figure 4** | **Modulated and demodulated signal in the time domain.** (a) The measured time trace of the modulated signal when the pulse frequency is 100 kHz at node a in Fig. 3a. (b) The measured time trace of the demodulated signal of 100 kHz at node b in Fig. 3b.

signal power to the STNO noise power is determined to be 12.5 dB [-85.7 dBm - (-98.2 dBm)], where the signal power of the STNO is -85.7 dBm, as shown in Fig. 2b. For ASK modulation with the STNO, because the amplitude stability of the STNO mainly determines the *SNR* of the system, we can improve the *SNR* and the data rate by eliminating the mode hopping at  $f_{osc}$  and/or maintaining the constant oscillation amplitude of the STNO.

**Extraction of the maximum data rate.** The maximum data rate  $D_{max}$  can be extended with the available bandwidth and the number of modulation levels which can be increased with *SNR* from Shannon's theorem given as

$$D_{max} = B \log_2(SNR + 1), \tag{5}$$

where *B* is the 3-dB bandwidth of the signal. The rise time  $(T_r)$  is typically defined as the time for the signal to rise from 10% to 90% of the step size, and *B* is given by  $0.35/T_r^{32}$ . The *B* is closely related to the transitional processes in STNOs<sup>30,33-36</sup>. If the turn–on of the STNO is approximately 1 ns<sup>30</sup>, the rise time  $T_r$  is approximately 1 ns, and thus *B* is calculated as 350 MHz. The total noise power in the *SNR* calculation for a communication system is the arithmetic sum of the system noise power, which results from the circuits of the signal generator, power amplifiers, antennas, LNA, and the envelope detector, and the STNO noise power. Here, the maximum date rate, where it is assumed that all circuits do not produce any noise apart from the STNO, can be extracted as 1.48 Gbps from the 12.5–dB *SNR* of the STNO itself by Eq. (5). From Table 1, it can be seen that the parameters and possible maximum date rate for the different types of STNOs.

In the modulation and demodulation test of Fig. 5, the pulsed signal generated by the pulse generator has approximately only 50–ns rise time and 60–ns fall time by itself. The modulated signal

has 130-ns  $T_{r, mod}$  and 150-ns  $T_{f, mod}$  in the usage of 1-MHz pulsed input signal in Fig 5a. The 80-ns increase of  $T_r$  and 90-ns increase of  $T_f$  in the modulated signals is involved in the by-pass capacitor which is parallel-connected to the inductive branch of bias tee. In Fig. 5b and 5c, the rise time of the demodulated signal by the transceiver  $(T_{r, TxRx})$  and the fall time of the demodulated signal by the transceiver (T f, TxRx) are measured into 350 ns and 190 ns after the envelope detector, which are shorter than the pulse width of 500 ns corresponding to 1-MHz pulsed input frequency. Thus, the signal power decreased by the  $V_{p-p}$  reduction does not occur and does not degrade SNR because the  $V_{p-p}$  is 168  $mV_{p\text{-}p}$  from 1–MHz pulsed input compared with 170  $mV_{p\text{-}p}$ of 100-kHz pulsed input (only 2-mV<sub>p-p</sub> decrease). If the pulsed input frequency increases to more than 1 MHz, 350-ns  $T_{r, TxRx}$  is much longer than the pulse width and the SNR is degraded. Moreover, because the 120-ns Tr, mod is increased to 350-ns  $T_{f_t TxRx}$  and the 150-ns  $T_{f_t mod}$  is increased to 190-ns  $T_{f_t TxRx}$ , the  $T_r$  of only demodulator  $(T_{r, demod})$  is 230 ns and the  $T_f$  of only demodulator  $(T_{f, demod})$  is 40 ns. Due to the total rise time of the system is 350 ns, the maximum data rate using the fabricated STNO is 5 Mbps.

In conclusion, STNO-based wireless communication was demonstrated for the first time successfully at a distance of 1 m at 200 kbps with the direct binary ASK modulation and demodulation. By analyzing the STNO noise, a *SNR* of STNO is 12.5 dB, and maximum data rate could be, theoretically, increased up to 1.48 Gbps with the STNO having the fastest turn-on time of 1 ns. The maximum data rate using the fabricated STNO is 5 Mbps which is limited by rise time and fall time of the system. Further improvement of the data rate is possible by reducing the turn-on time, the rise time, and the fall time of the system and by stabilizing and linearizing the STNO oscillation amplitude.

Table 1 | Comparison of possible maximum data rate for the different types of STNOs. Here F is the oscillation frequency,  $LW_{min}$  the minimum linewidth,  $P_{out}$  the maximum output power,  $D_{max}$  the maximum data rate. For all STNOs,  $D_{max}$  is evaluated with SNR (12.5 dB) through the modulator and demodulator of binary ASK modulation. All the STNOs have the 50-ohm connection to the sampling oscilloscope

STNOs	F (GHz)	LW <sub>min</sub> (MHz)	P <sub>out</sub> (dBm)	Turn–on time (ns)	D <sub>max</sub> (Mbps)
Ref. 30	3–5.2	-	-73.0	1	1480
Ref. 33	10.48	10	-73.2	15	99
Ref. 34	25.3	-	-	16*	93
Ref. 35	0.007-0.04	-	-	500	3
This work	1.87-5.35	200	-85.7	350**	5

\*The turn-on time of Ref. 34 is assumed to 16 ns from the pulse repetition period in the measurement. \*\*The total rise time of the system is 350 ns which include the turn-on time of the fabricated STNO.

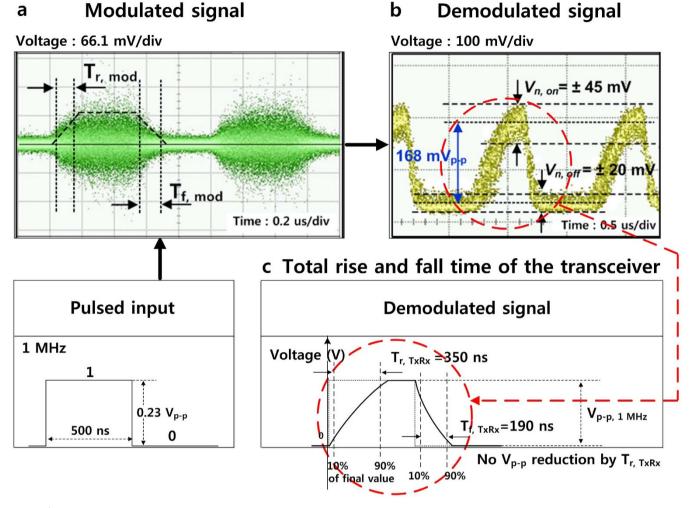


Figure 5 | Rise and fall time of modulated and demodulated signal. (a) The rise and fall time from the measured time trace of the modulated signal when the pulse frequency is 1 MHz at node a in Fig. 3a. (b) The measured time trace of the demodulated signal of 1 MHz at node b in Fig. 3b. (c) Total rise and fall time of the transceiver from the demodulated signal.

#### Methods

**STNO fabrication**. We fabricated STNOs using magnetic tunnel junctions (MTJs) with an MgO barrier. As shown in Fig. 1b, the MTJ stack consists of a buffer/Pt<sub>37</sub>Mn<sub>63</sub> (20)/Co<sub>70</sub>Fe<sub>30</sub> (3)/Ru (0.8)/Co<sub>60</sub>Fe<sub>20</sub>B<sub>20</sub> (5) (pinned layer)/MgO (~0.9) (barrier)/Co<sub>60</sub>Fe<sub>20</sub>B<sub>20</sub> (2) (free layer)/cap layer, where the subscript denotes the composition in atomic percent and the numbers in parentheses denote the layer thickness. Nanopillars with area of 0.053  $\mu$ m<sup>2</sup> were formed by Ar ion milling and e–beam lithography.

**Numerical.** The path loss is the power attenuation when an electromagnetic wave propagates through air space. The power at receiving antenna is given by Friis transmission equation as follows:

$$P_r = P_t + G_t + G_r + L(\text{in dB})$$

where  $P_r$  is the input power of the  $R_x$  antenna,  $P_t$  is the output power of the  $T_x$  antenna,  $G_t$  and  $G_r$  are the antenna gains of the  $T_x$  and  $R_x$  respectively, L is the free–space path loss,  $20\log_{10} (4\pi d/\lambda)$ ,  $\lambda$  is the wavelength, and d is the distance between the  $T_x$  and  $R_x$  in the same units as the wavelength). The oscillation frequency of the STNO in this experiment is 2.5 GHz and the calculated path loss of the free space is -40 dB.

For the calculation of total *NF*, if several devices are cascaded, the total noise factor can be obtained by the Friis formula for the noise factor, as follows:

$$F = F_{1+}(F_2 - I)/G_1 + (F_3 - 1)/(G_1G_2) + \dots + (F_{n-1})/(G_1G_2G_3\cdots G_{n-1})$$
(1)

Here,  $F_n$  is the noise factor for the n–th device and  $G_n$  is the power gain (linear, not in dB) of the n–th device. Related to this equation, NF in the text refers to F expressed in dB.

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#### Author contributions

H.S.C. and C.S.P. designed the experiments and performed the analysis of the data. C.J. and B.C.M. fabricated the devices. S.Y.K., S.J.C., S.I.K. and S.Y.P. supported the experiments and I.Y.O., M.S. and H.P. supported the analysis of the data. H.S.C. wrote the manuscript. All authors discussed the results and commented on the manuscript.

#### Additional information

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